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(71) Applicant: ATMEL CORPORATION [US/US]; 2325 Orchard Parkway, San Jose, CA 95131 (US).

(72) Inventors: MAALEJ, Khaled; 9, rue de la Ville l'Evêque, F-75008 Paris (FR). HAMMAN, Emmanuel; 14, rue Nelaton, F-75015 Paris (FR). DEMOL, Amaury; 5, rue des Reculettes, F-75013 Paris (FR). LEVY, Yannick; 54, rue des Meuniers, F-75012 Paris (FR).

(74) Agent: SCHNECK, Thomas; Law Offices of Thomas Schneck, P.O. Box 2-E, San Jose, CA 95109-0005 (US).

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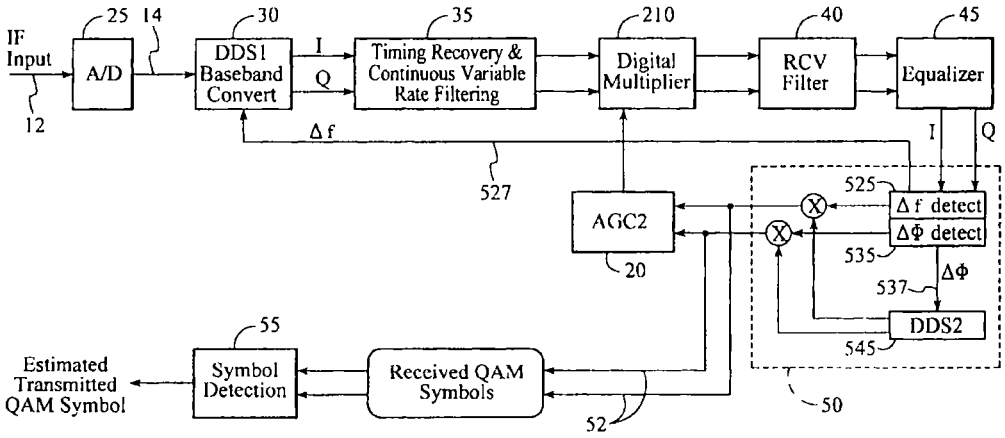
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(54) Title: DIRECT DIGITAL SYNTHESIS IN A QAM DEMODULATOR



(57) Abstract: A QAM demodulator is disclosed, which comprises two direct digital synthesizer circuits, for the provision of carrier synchronisation. The first DDS is located upstream of the receivers root-raised-cosine filter, and ensures frequency synchronisation, whilst the second DDS is downstream of the filter and ensures phase synchronisation.

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