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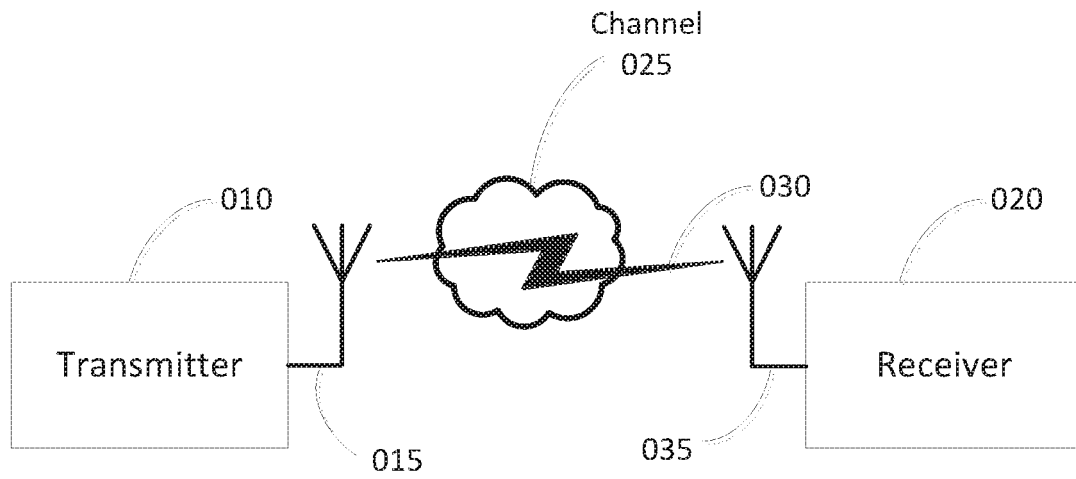


Figure-1a
(Prior Art)

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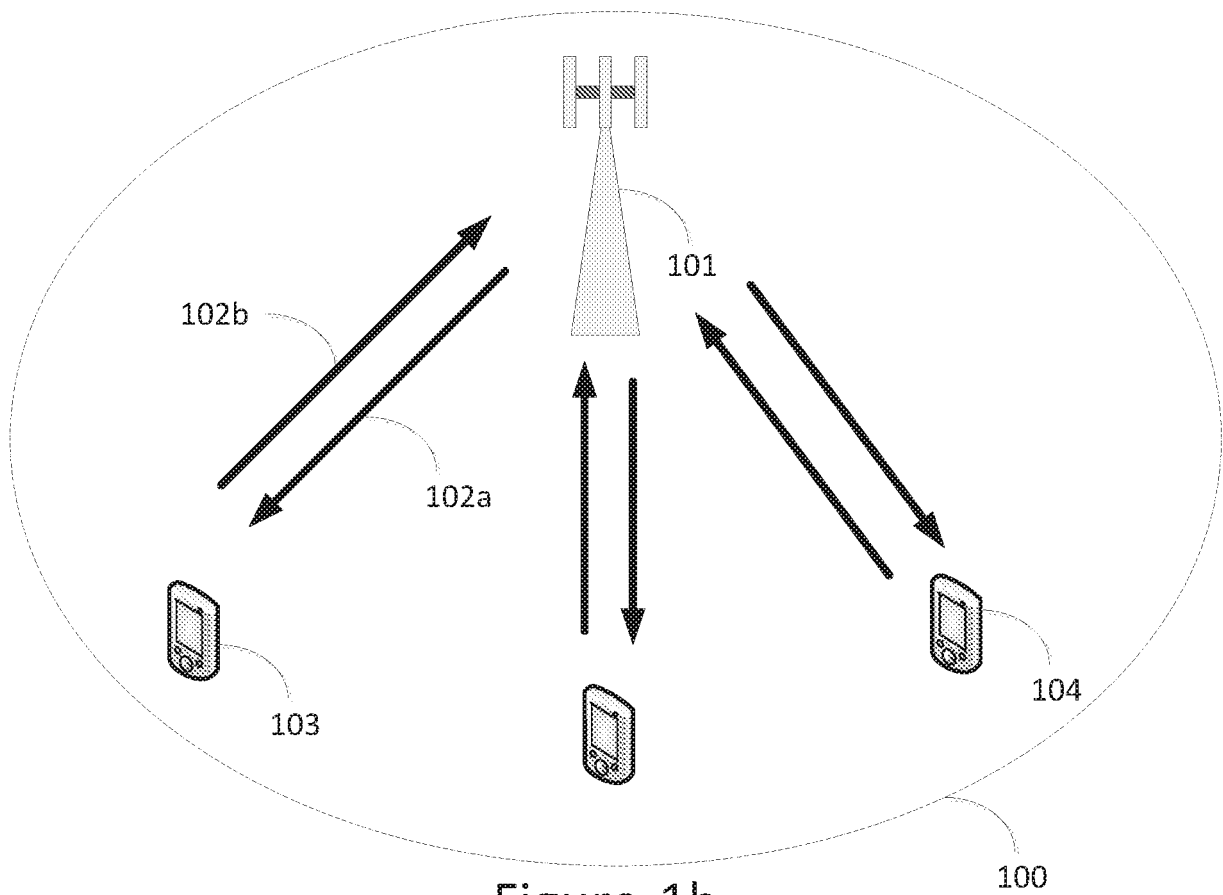


Figure-1b
(Prior Art)

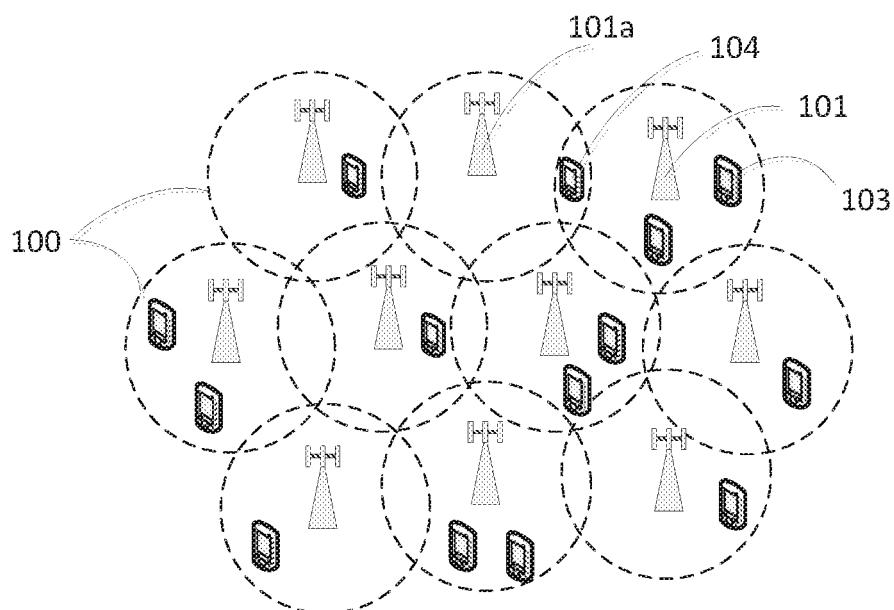


Figure-1c
(Prior art)

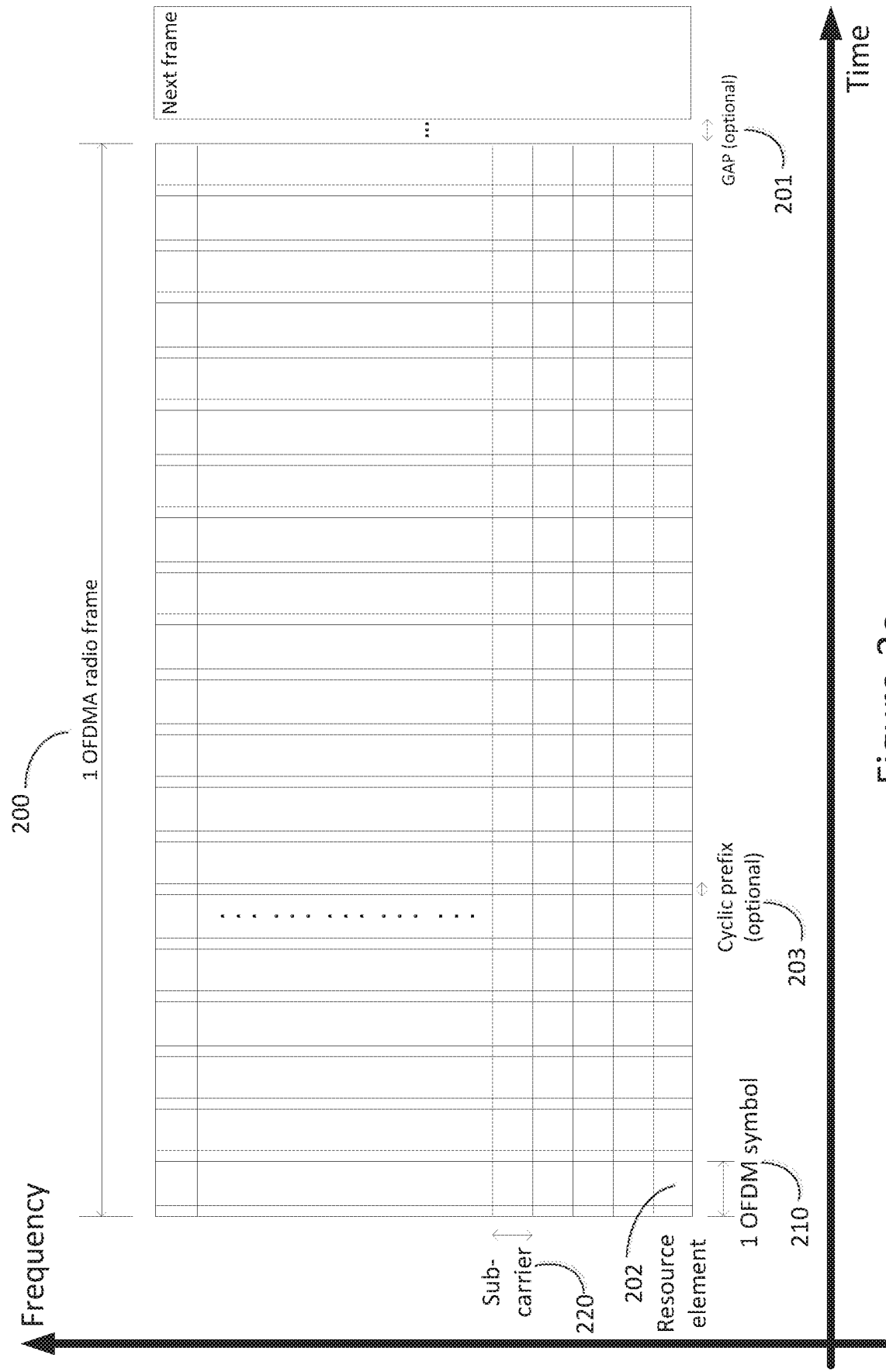


Figure-2a

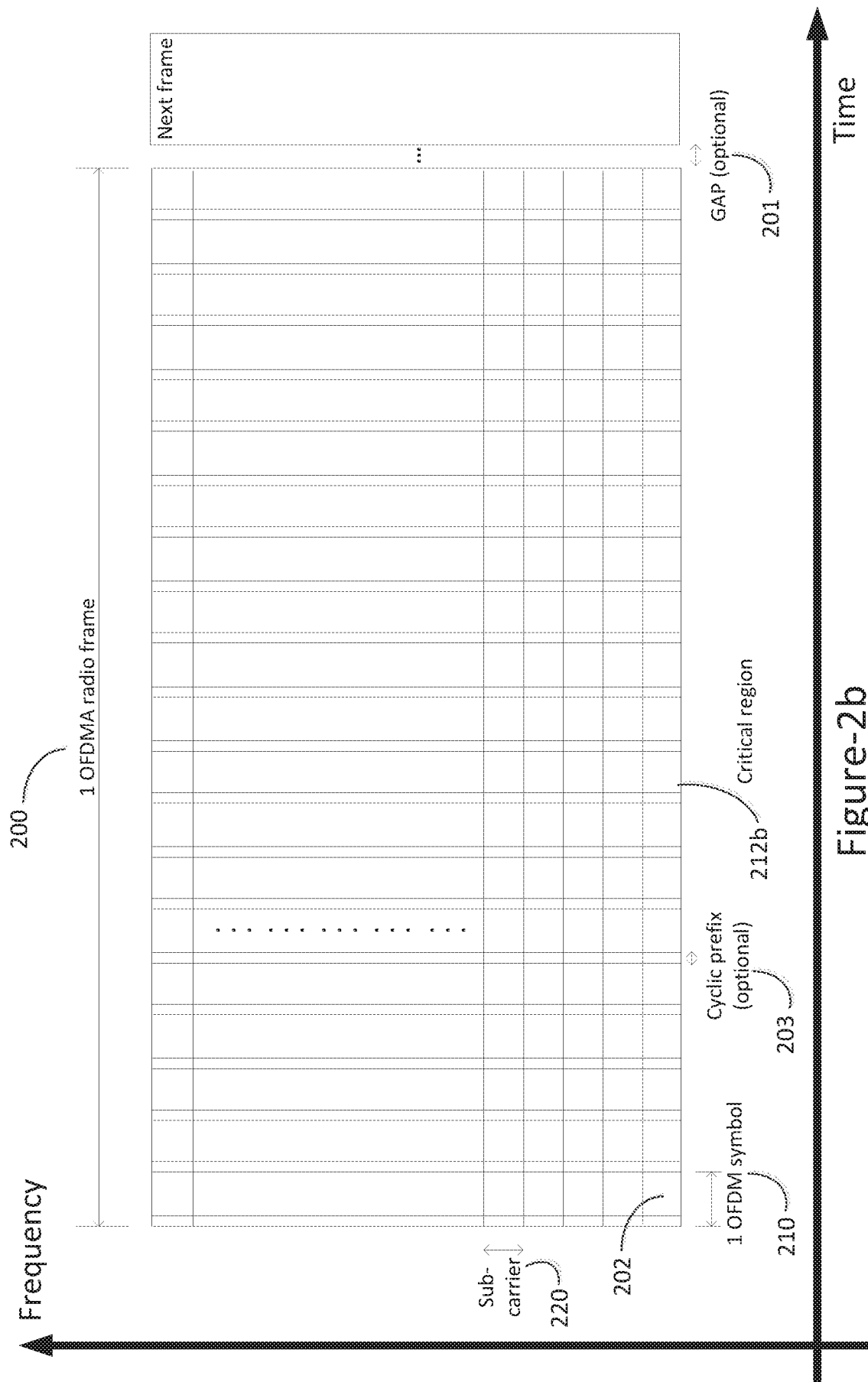
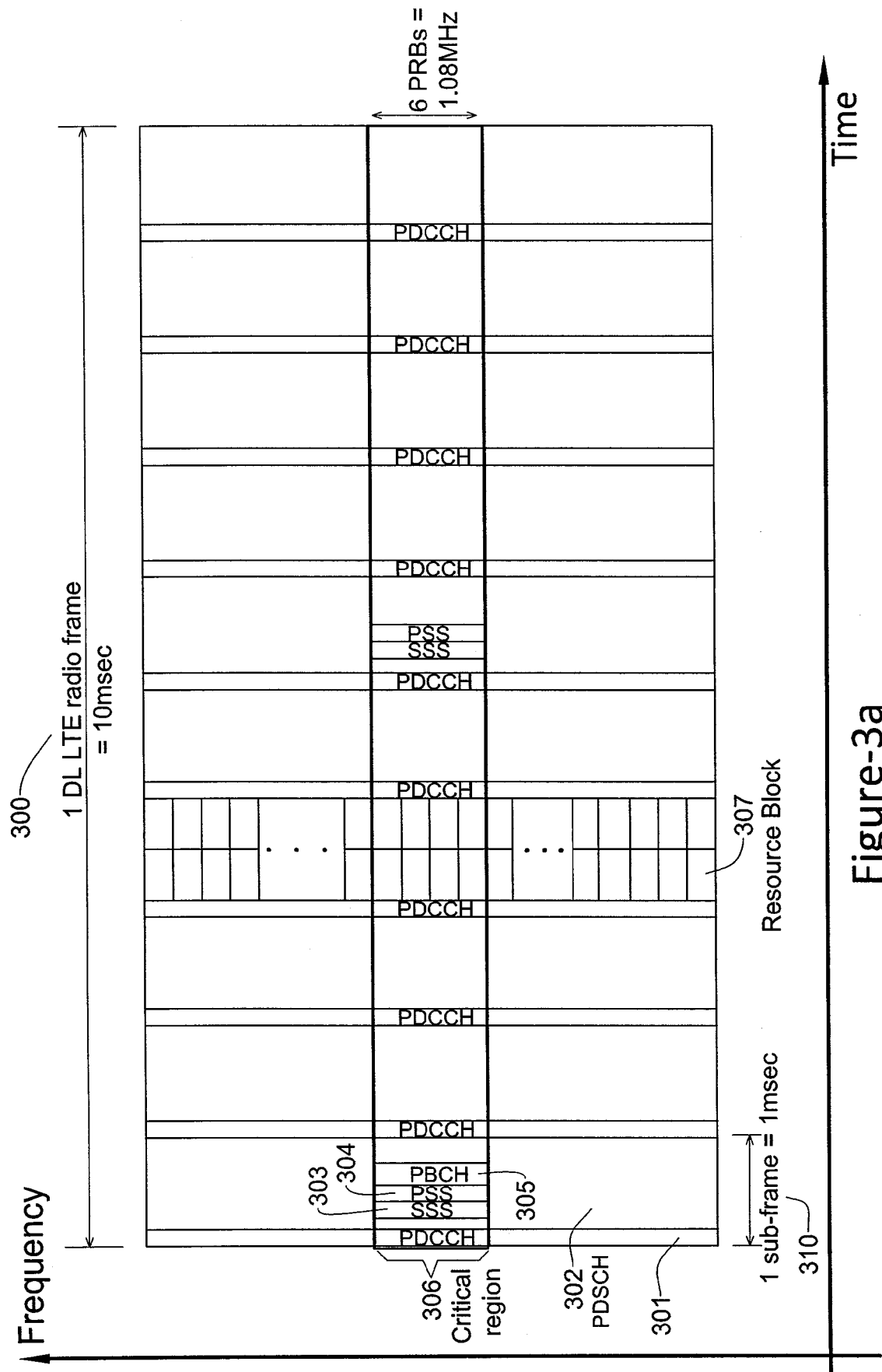
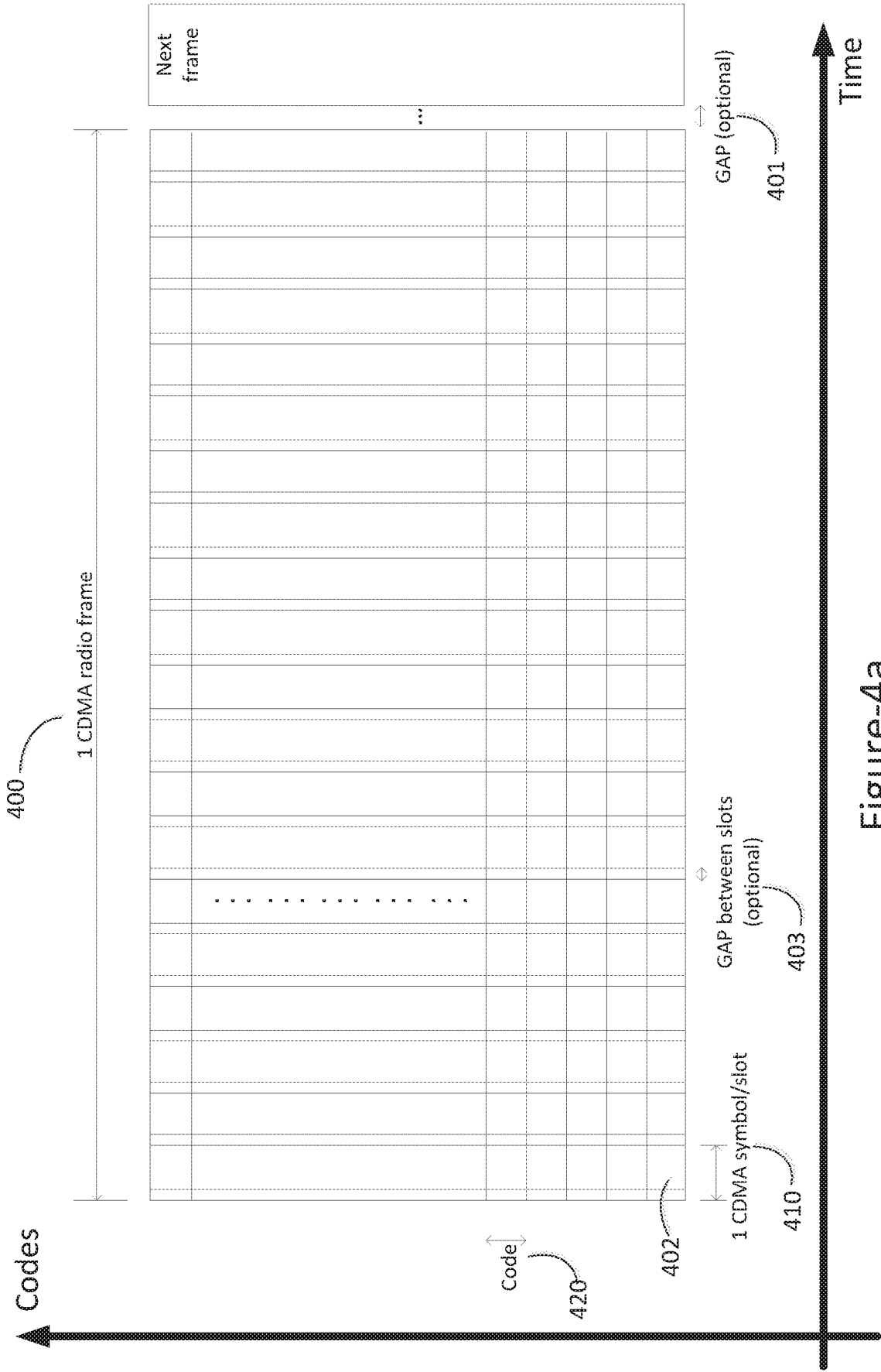


Figure 2b

[[add OF DVA TDD]]





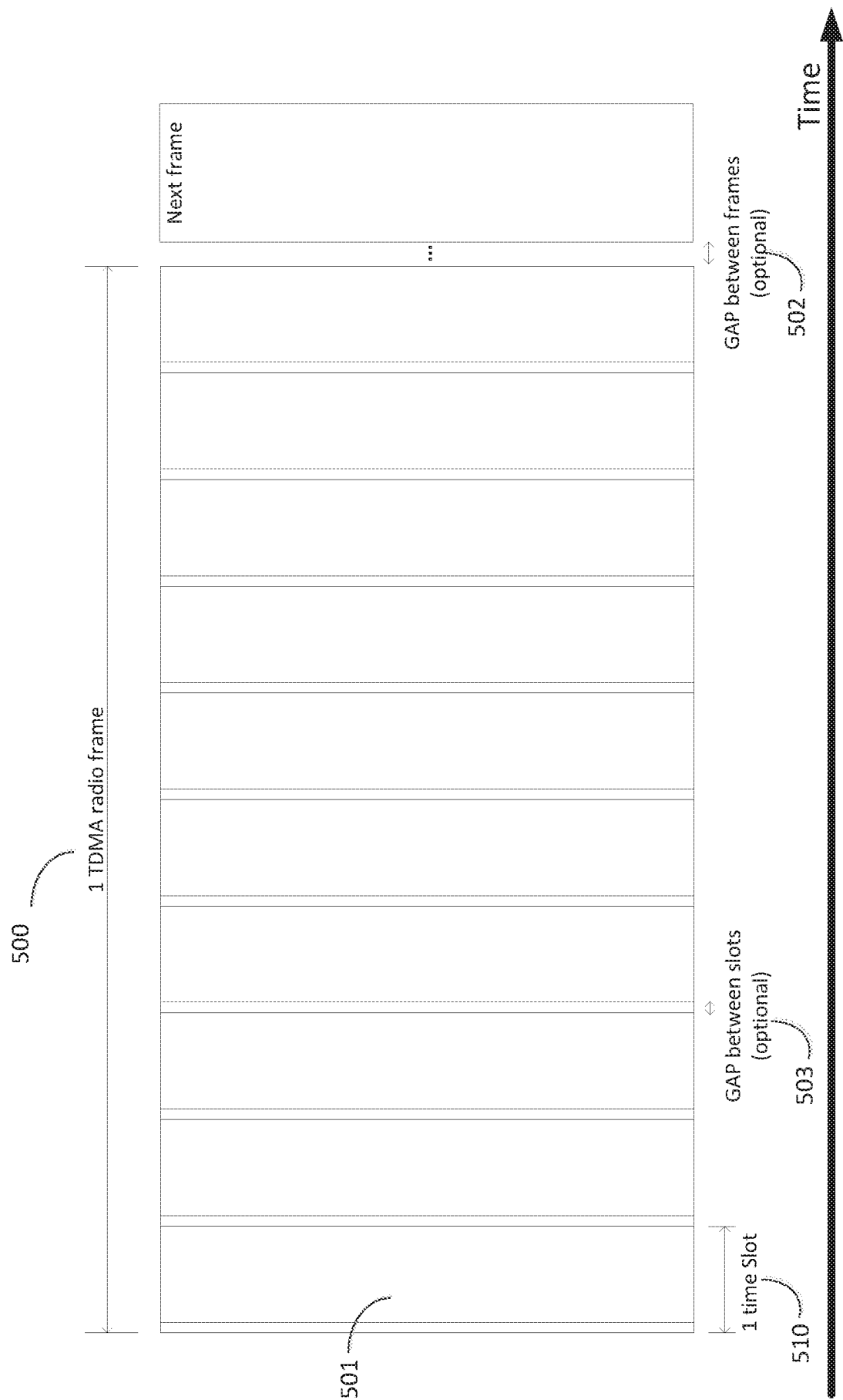


Figure-5

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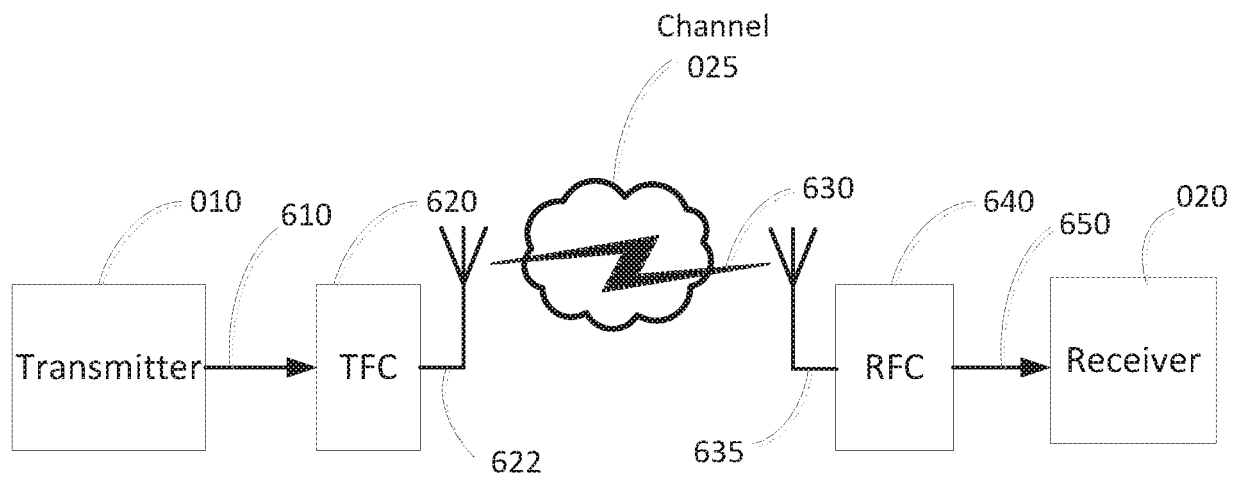


Figure-6

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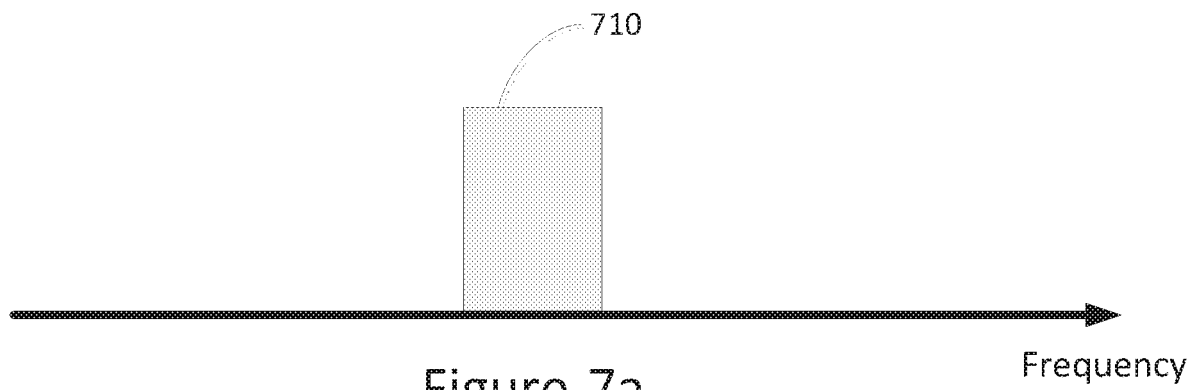


Figure-7a
(Prior art)

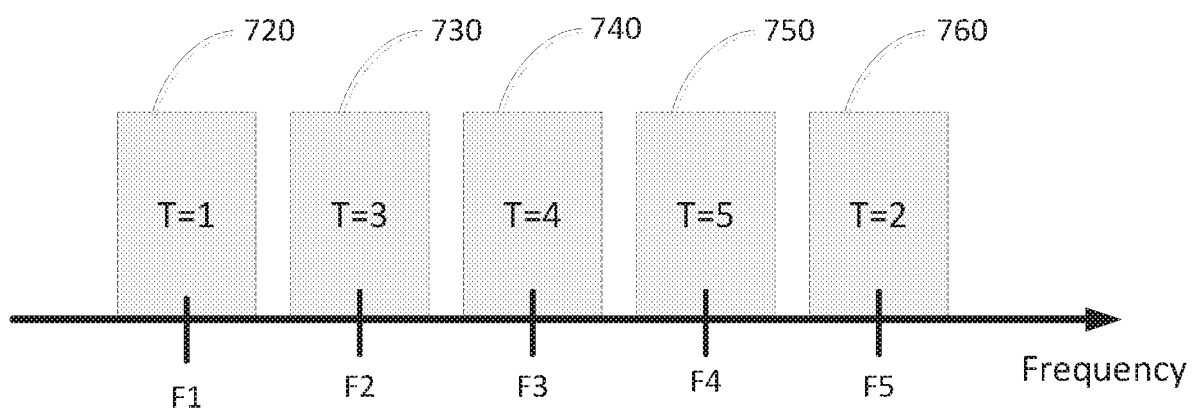
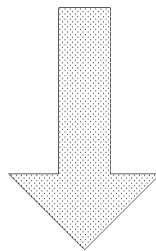


Figure-7b

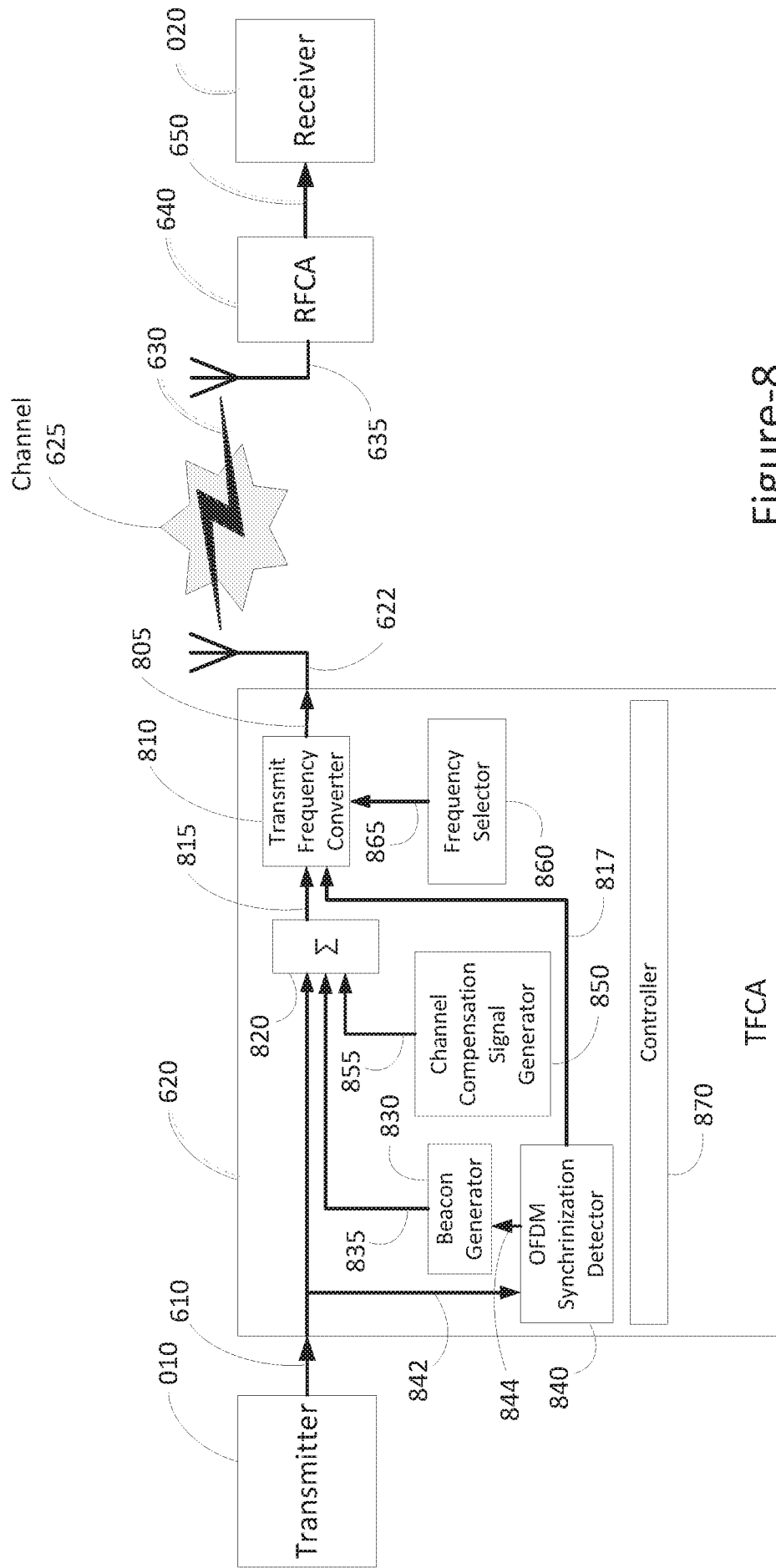


Figure-8

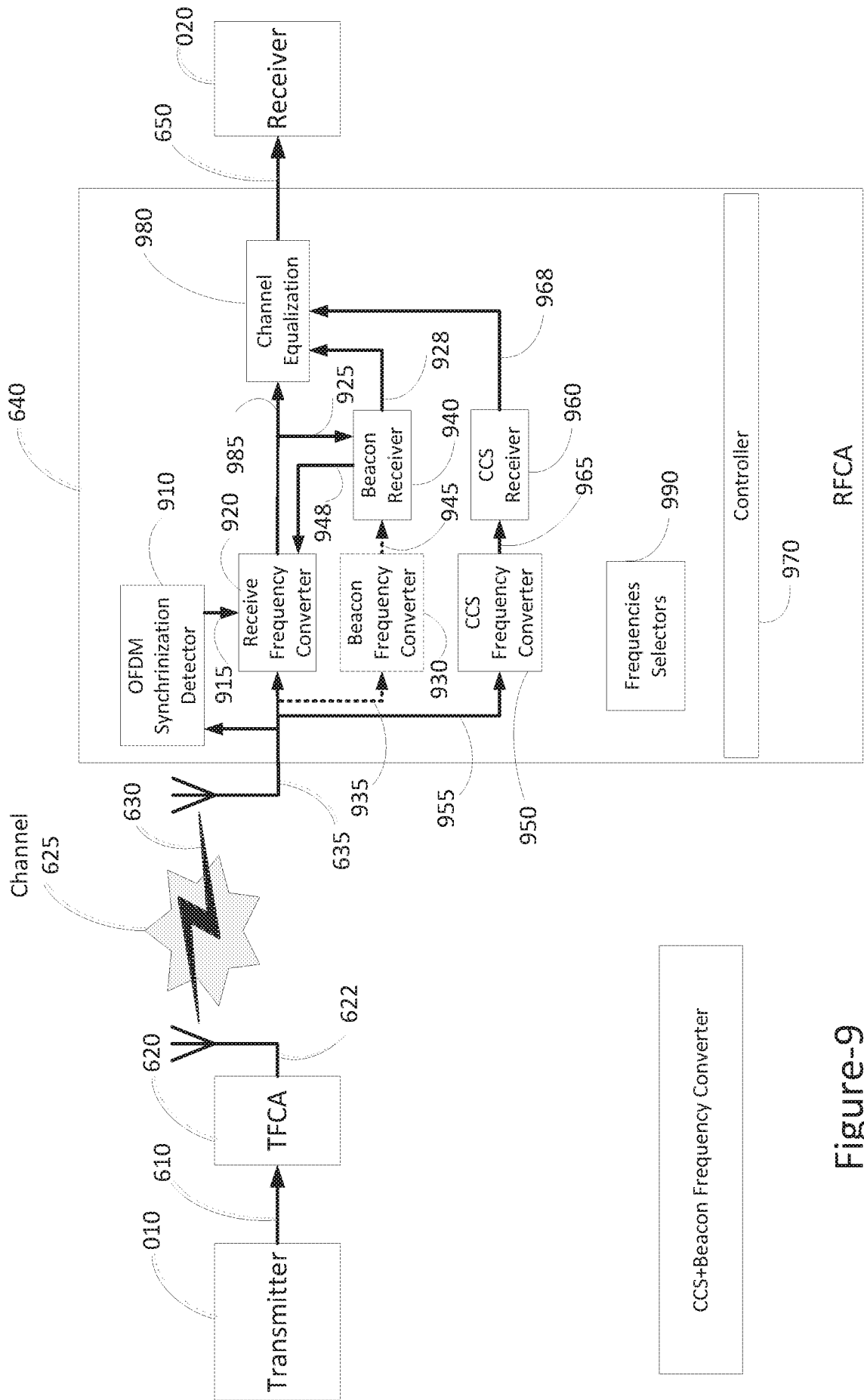


Figure-9

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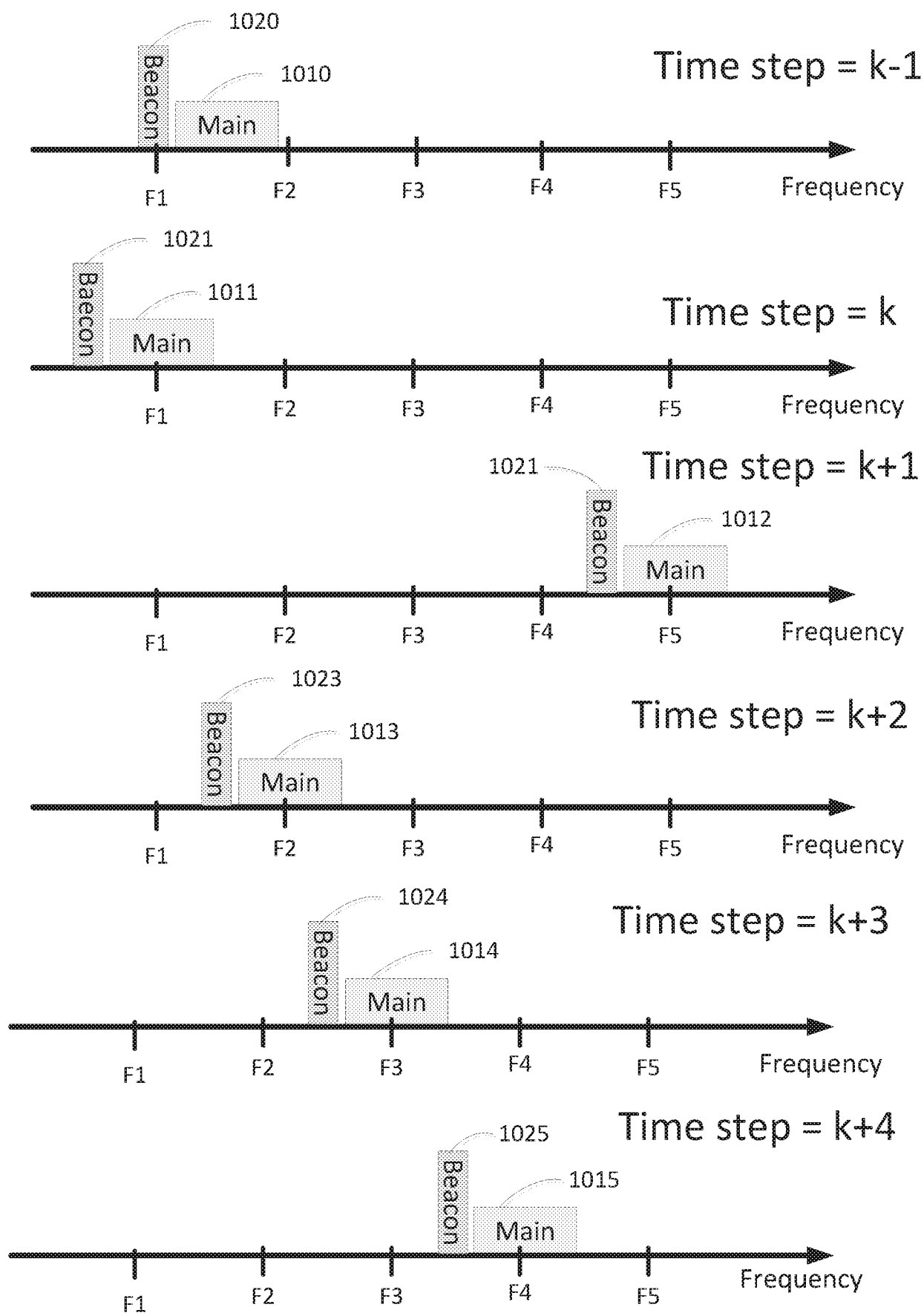


Figure-10a

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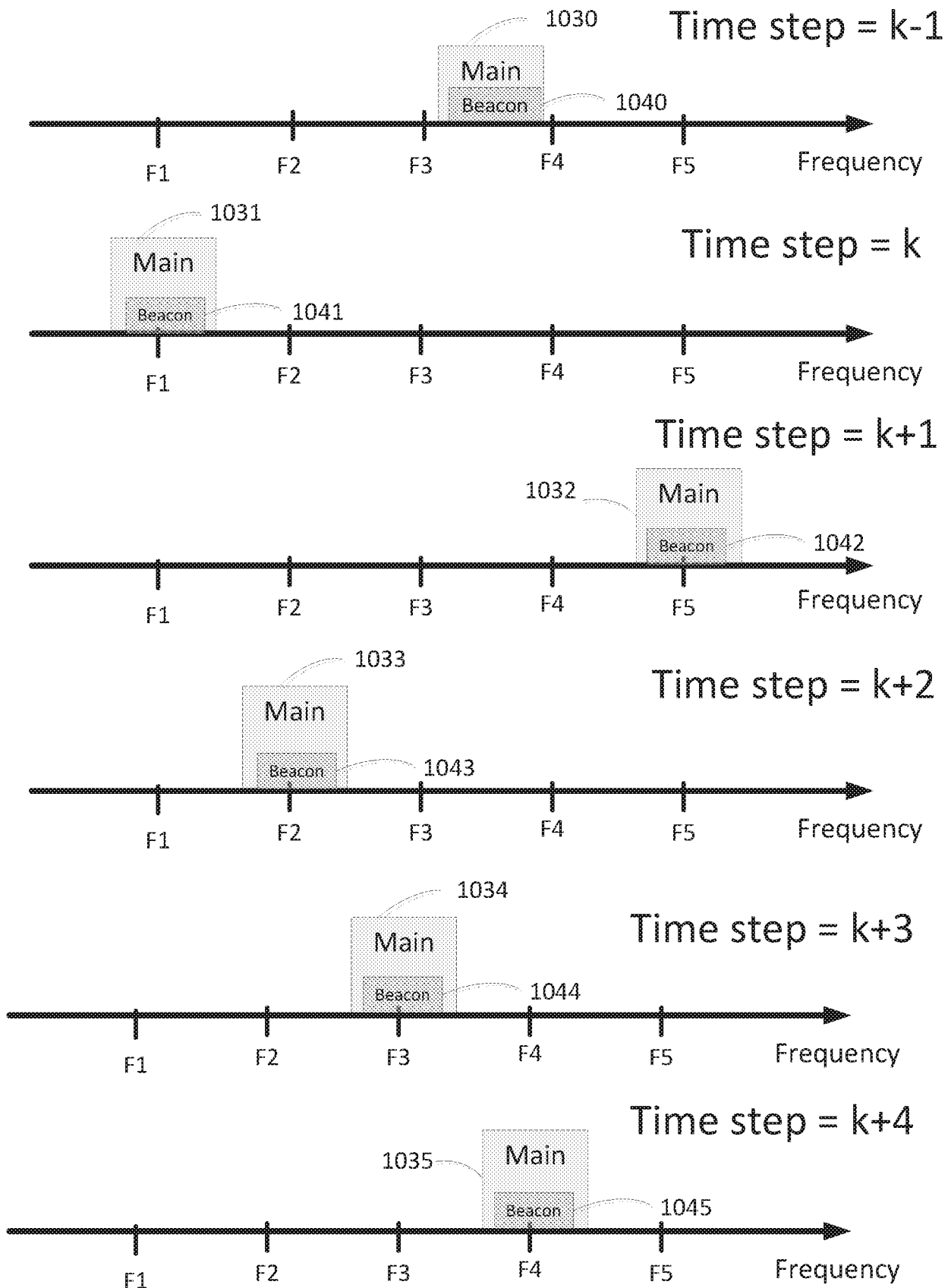


Figure-10b

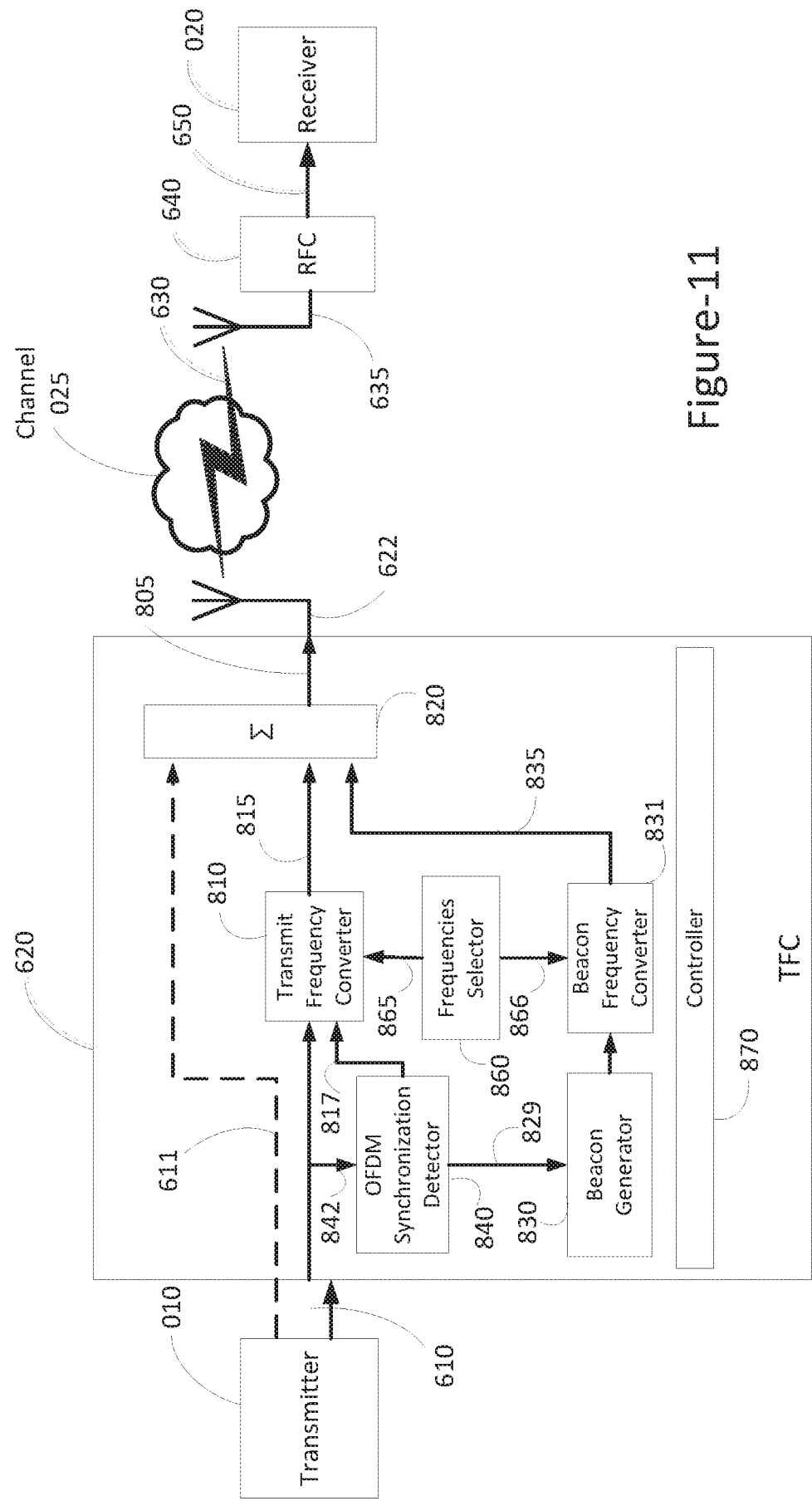


Figure-11

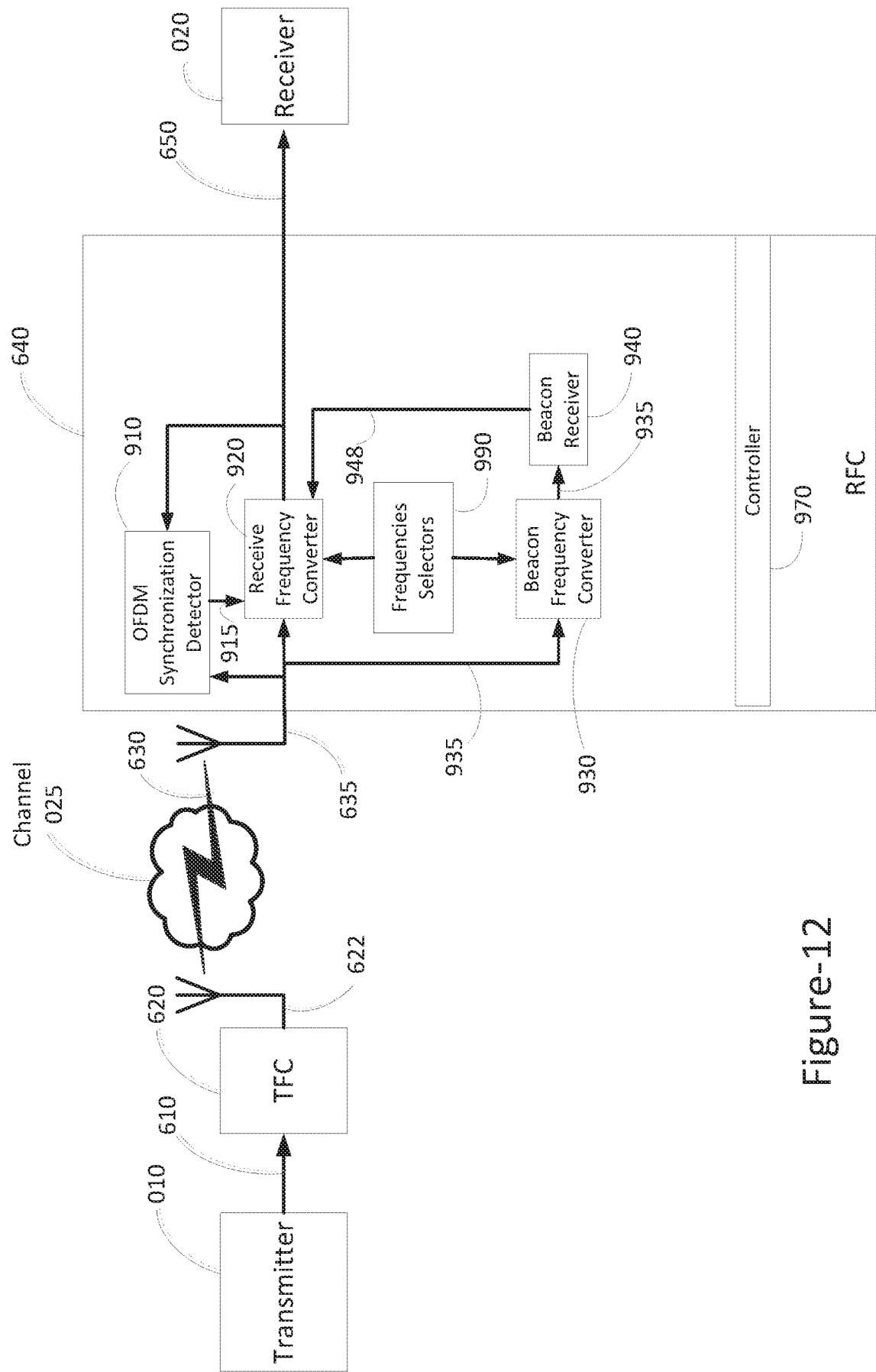


Figure-12

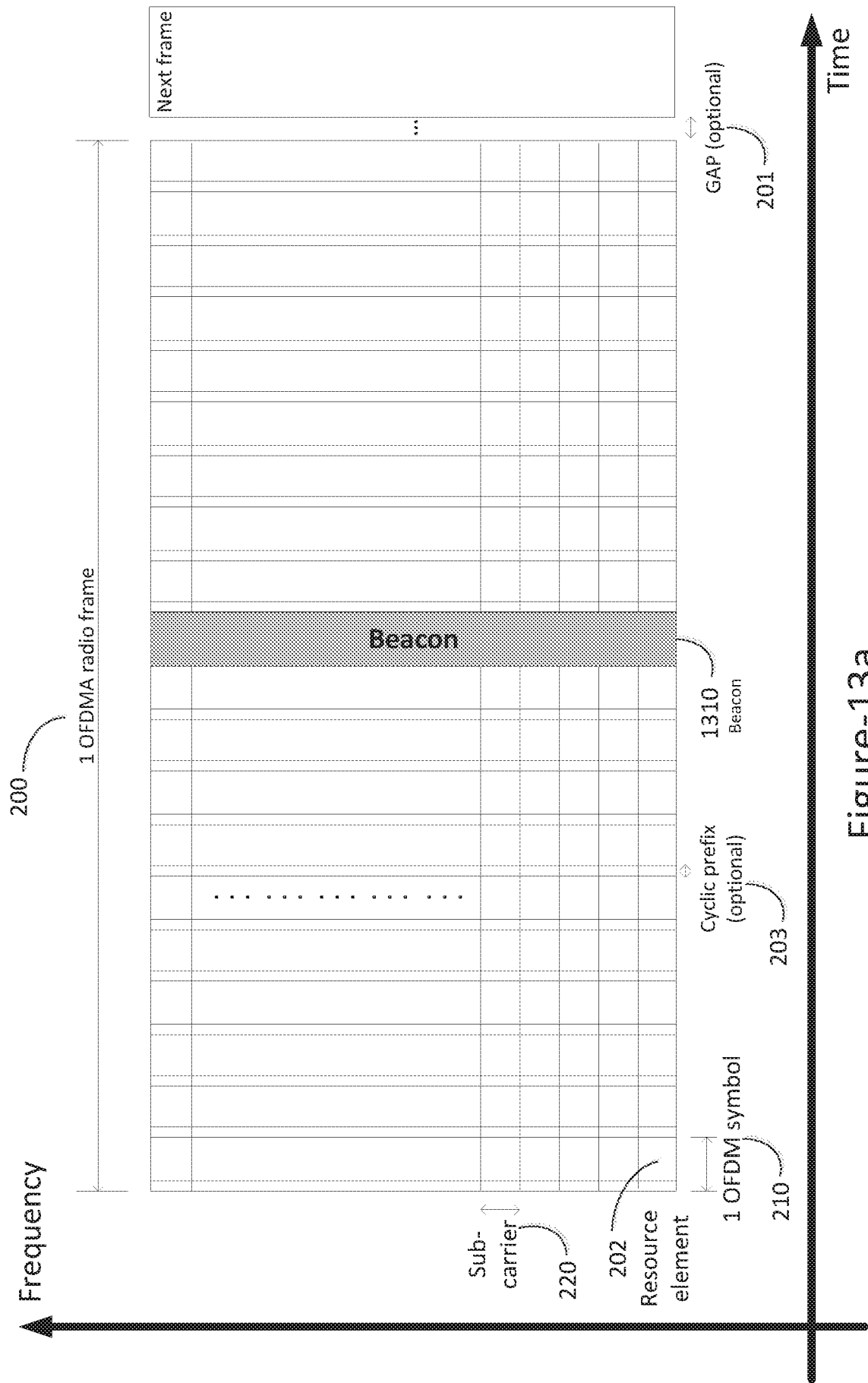


Figure-13a

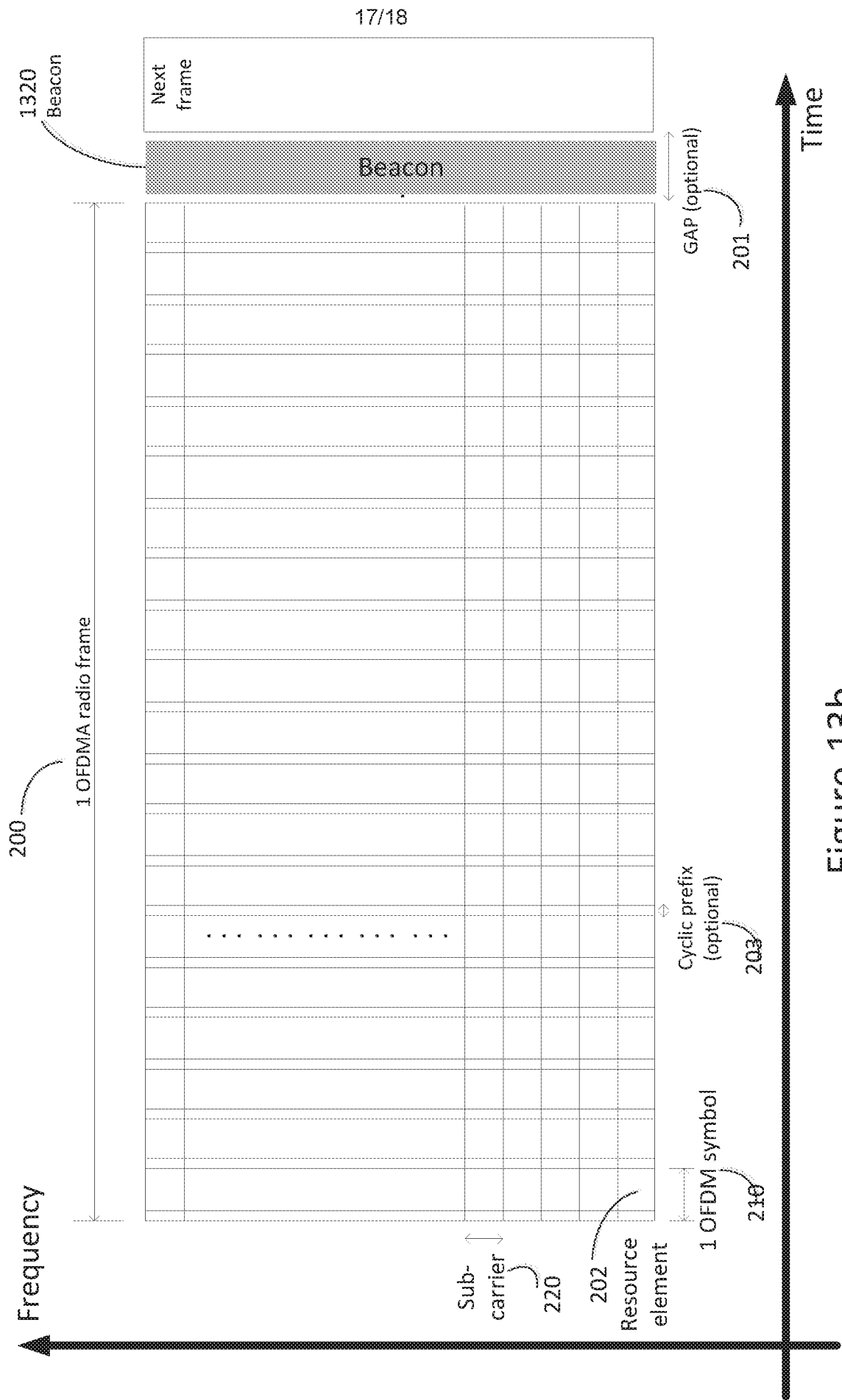


Figure-13b

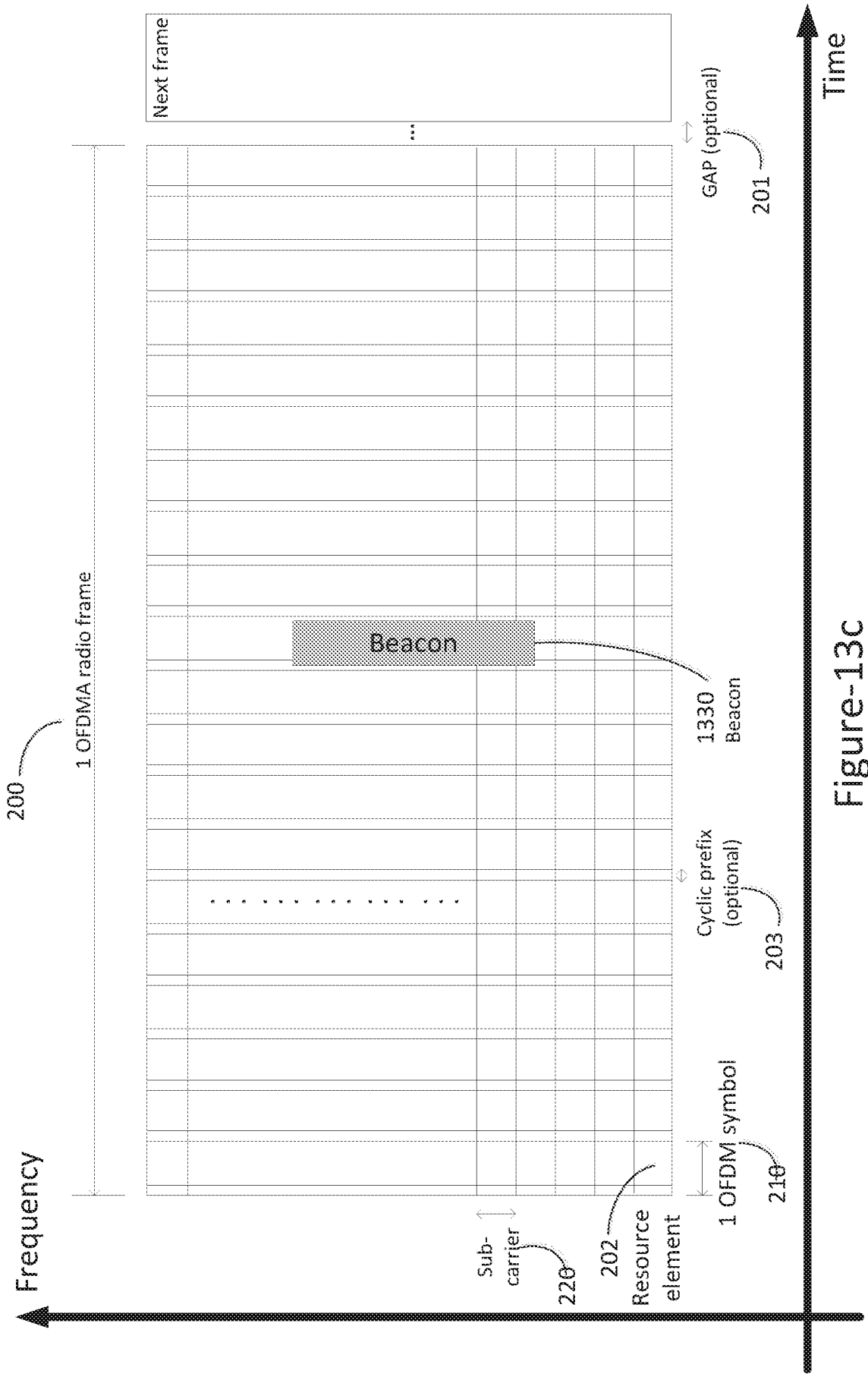


Figure-13c