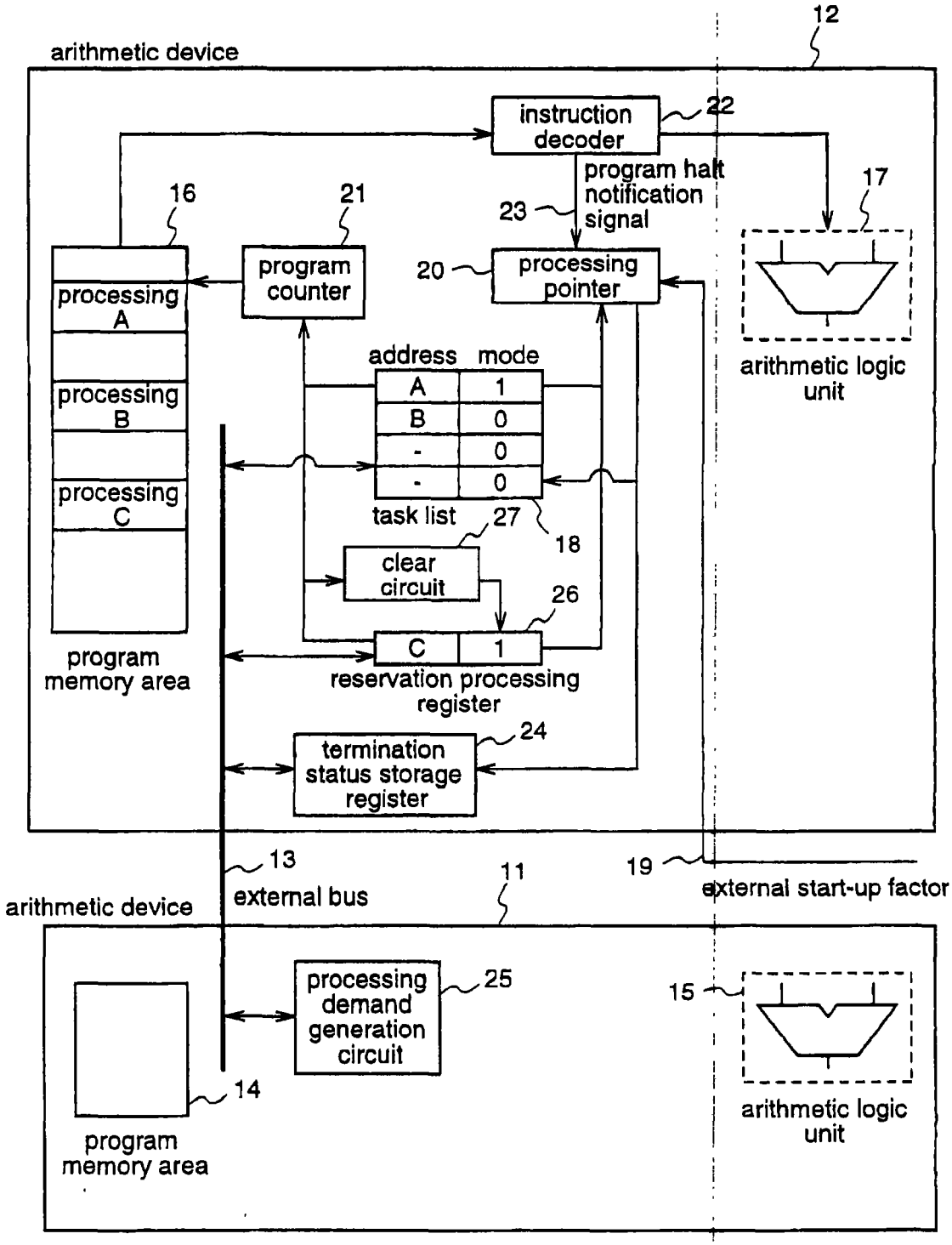
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Fig.1



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Fig.2

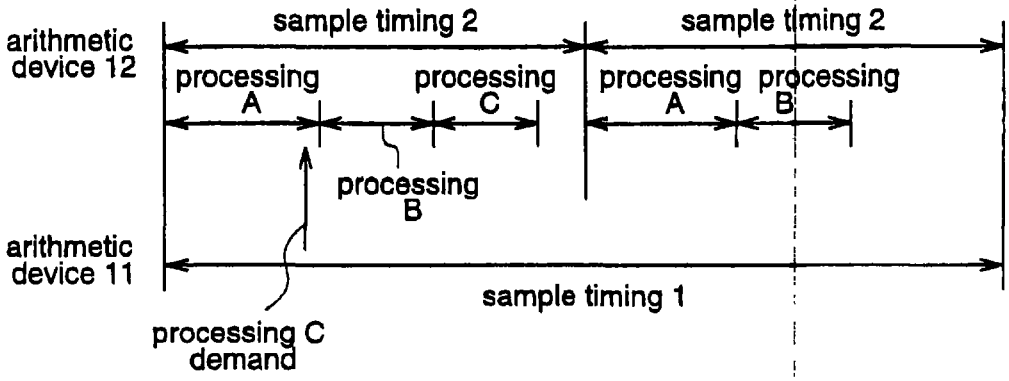


Fig.3

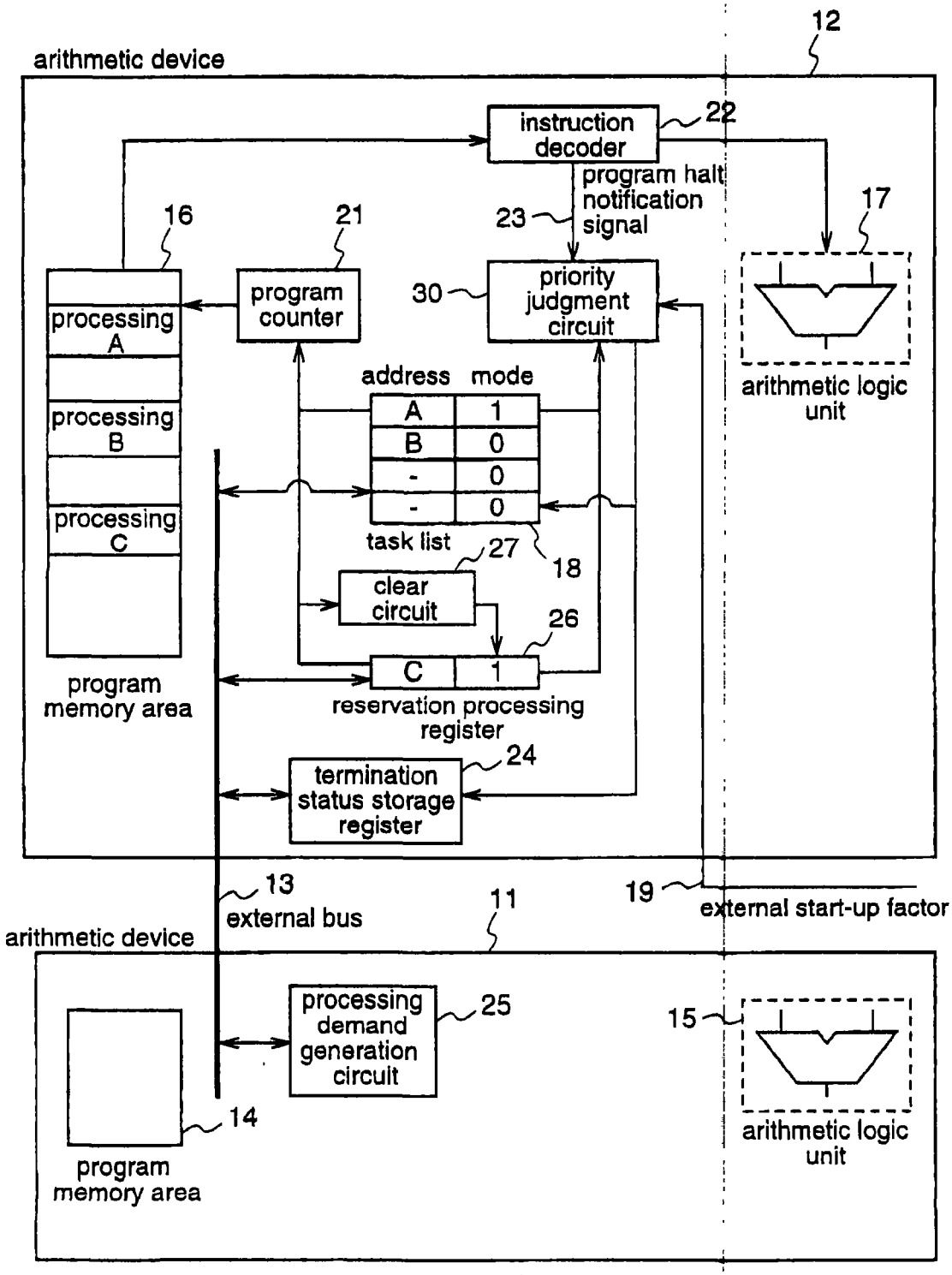

12
arithmetic device
instruction decoder
22
program halt notification signal
23
17
16
21
30
program counter
priority judgment circuit
processing A
processing B
processing C
address
mode
A
1
B
0
-
0
-
0
arithmetic logic unit
task list
27
18
clear circuit
26
C
1
program memory area
reservation processing register
24
termination status storage register
13
external bus
11
19
external start-up factor
arithmetic device
processing demand generation circuit
25
15
14
program memory area
arithmetic logic unit

Fig.4

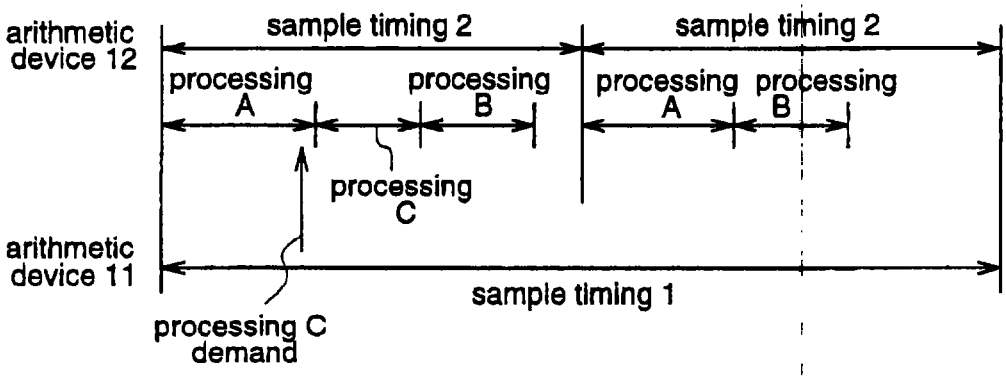

arithmetic device 12
sample timing 2
sample timing 2
processing A
processing B
processing A
processing B
processing C
arithmetic device 11
sample timing 1
processing C demand

Fig.5

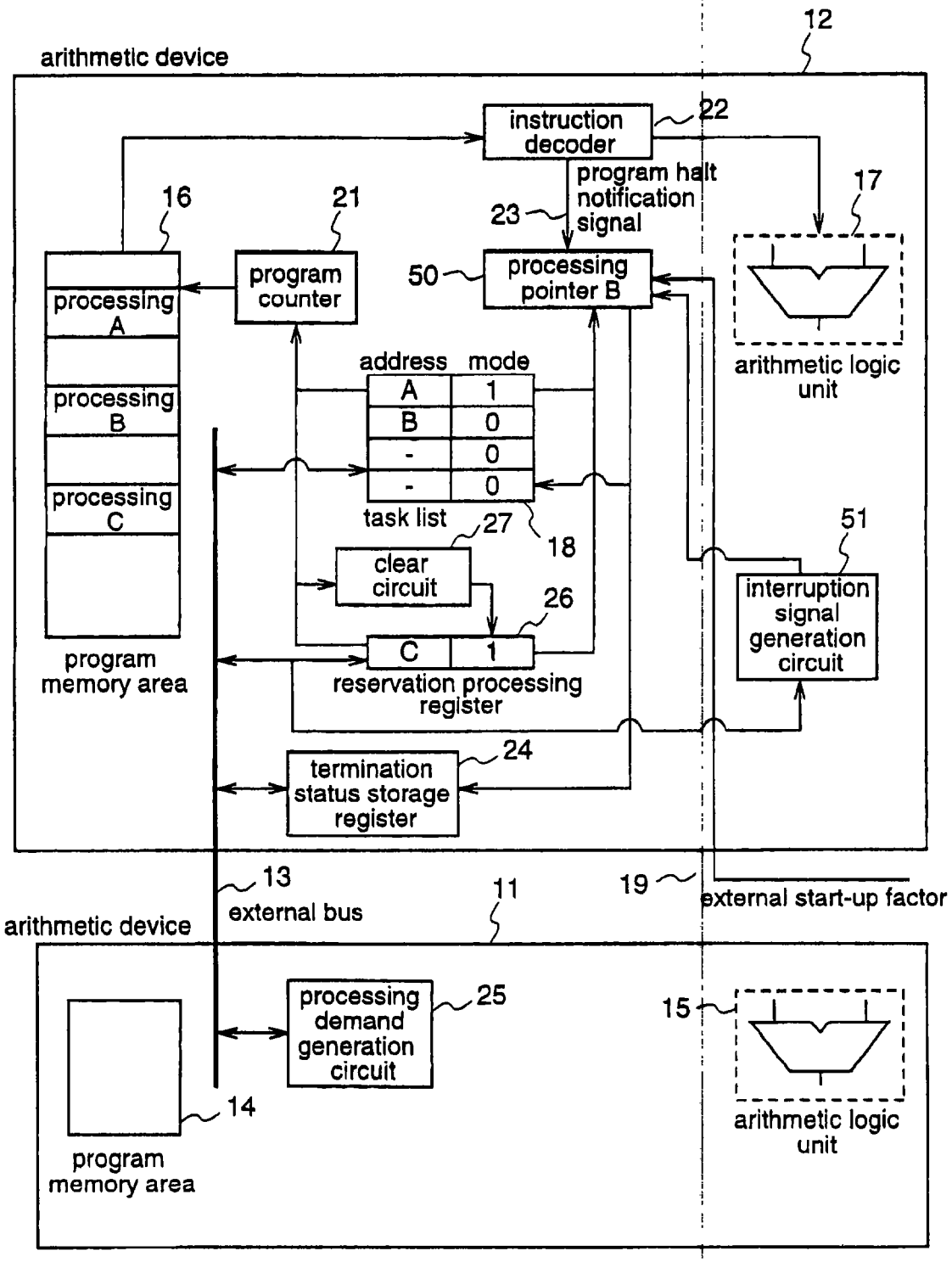

12
arithmetic device
instruction decoder
22
program halt notification signal
23
17
16
21
program counter
50
processing pointer B
processing A
processing B
processing C
address mode
A 1
B 0
- 0
- 0
task list
27
18
arithmetic logic unit
51
clear circuit
26
interruption signal generation circuit
C 1
reservation processing register
program memory area
24
termination status storage register
13
external bus
11
19
external start-up factor
arithmetic device
processing demand generation circuit
25
15
14
program memory area
arithmetic logic unit

Fig.6

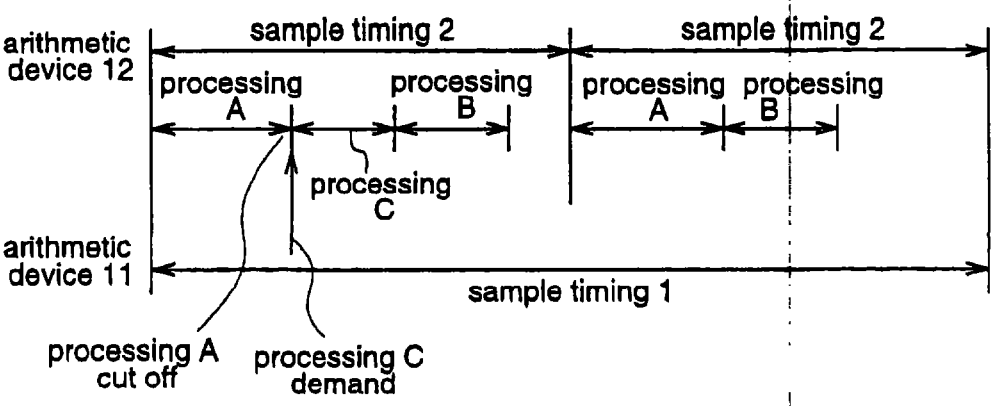
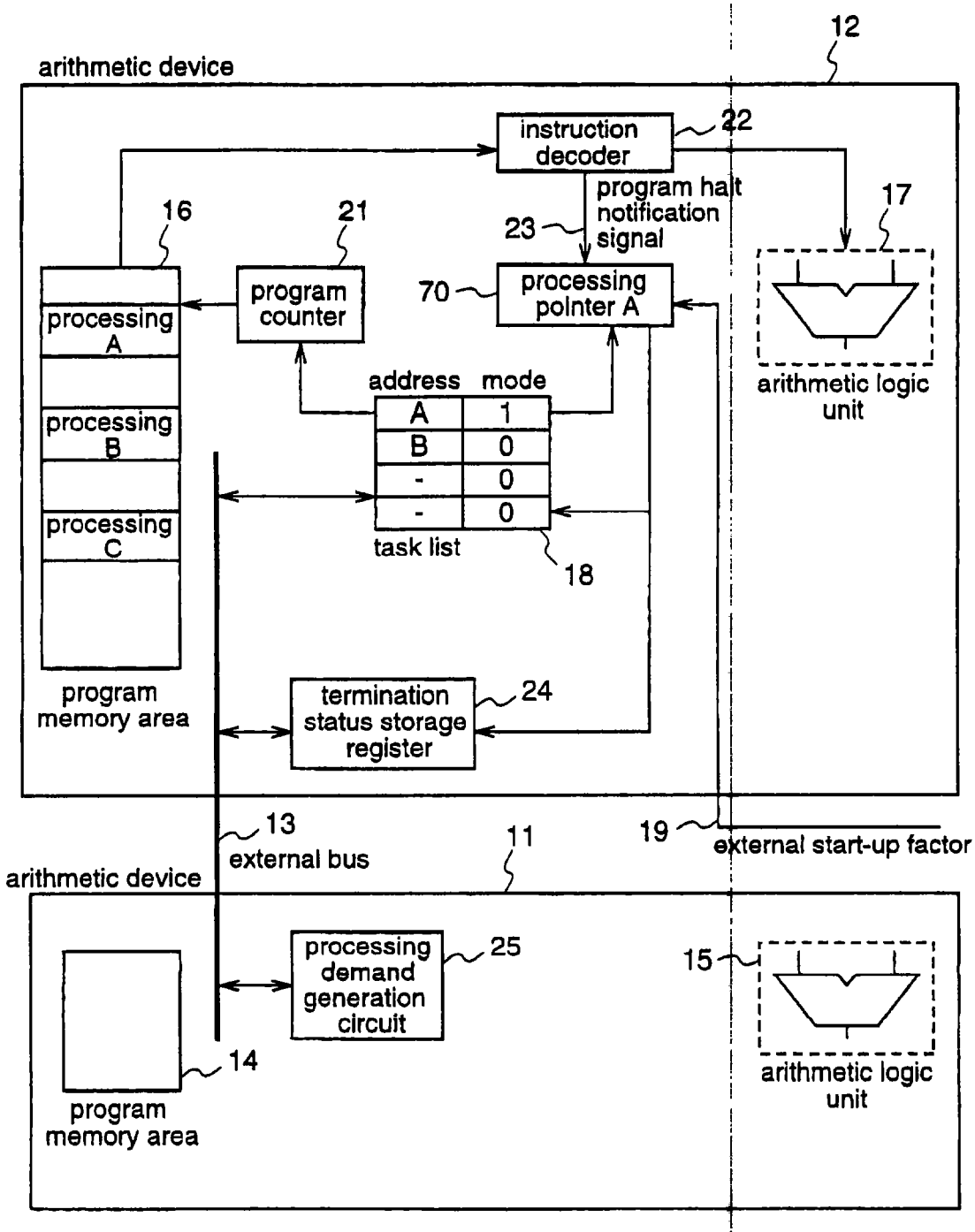
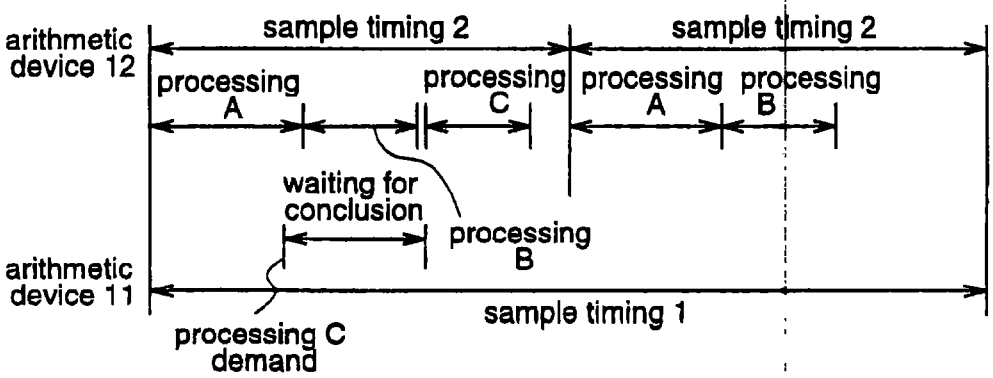

arithmetic device 12
sample timing 2
sample timing 2
processing A
processing B
processing A
processing B
processing C
arithmetic device 11
sample timing 1
processing A cut off
processing C demand

Fig.7


12
arithmetic device
instruction decoder
22
program halt notification signal
23
17
16
21
70
processing pointer A
program counter
processing A
processing B
processing C
address mode
A 1
B 0
- 0
- 0
task list
18
arithmetic logic unit
program memory area
termination status storage register
24
13
external bus
11
19
external start-up factor
arithmetic device
processing demand generation circuit
25
15
14
program memory area
arithmetic logic unit

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Fig.8



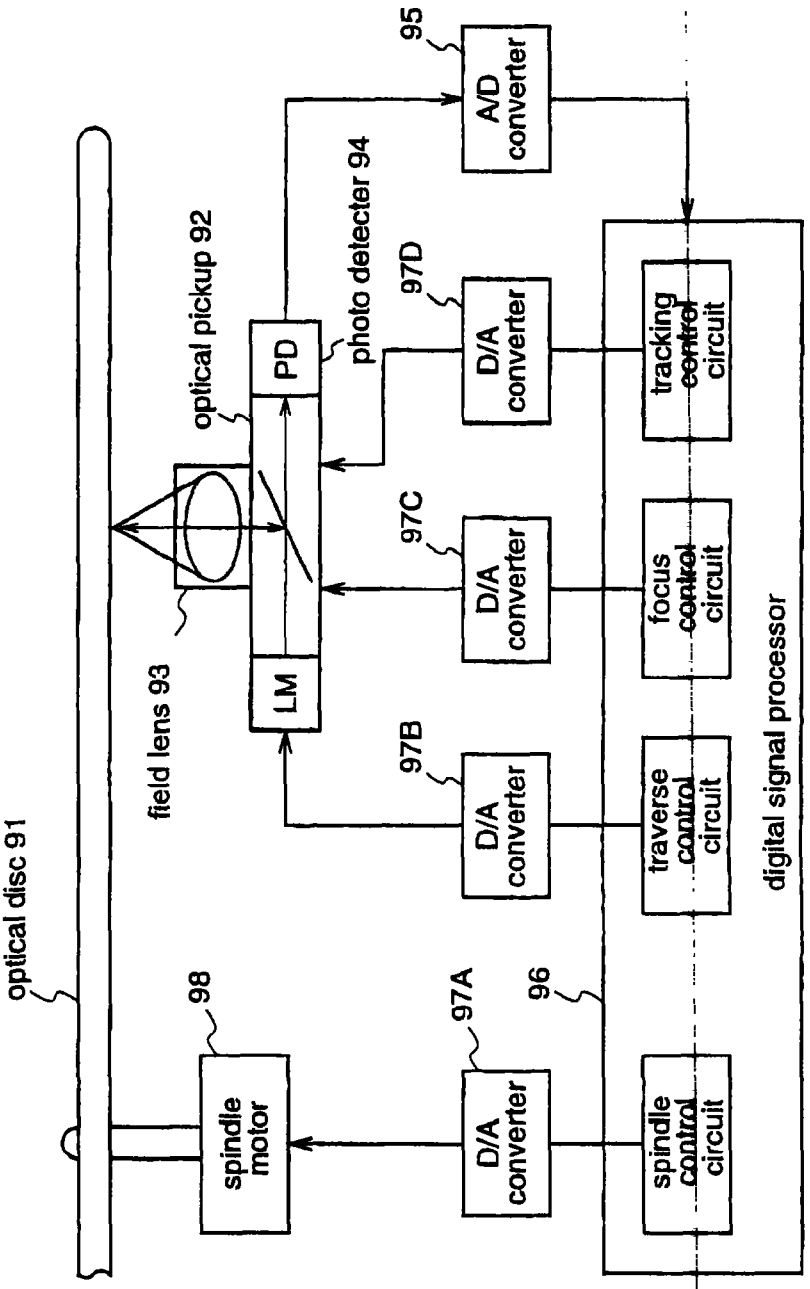

optical disc 91
field lens 93
optical pickup 92
LM
PD
photo detecter 94
95
A/D converter
97D
D/A converter
97C
D/A converter
97B
D/A converter
97A
D/A converter
98
spindle motor
96
digital signal processor
tracking control circuit
focus control circuit
traverse control circuit
spindle control circuit

Fig.9