

Description

Title of Invention: ELECTRIC CONTACT AND SOCKET FOR ELECTRICAL PART

Technical Field

[0001]

The present invention relates to an electric contact electrically connected to an electrical part such as a semiconductor device (hereinafter referred to as "IC package"), and a socket for electrical part in which this electric contact is disposed.

Background Art

[0002]

Conventionally, probe pins arranged in an IC socket for use as a socket for electrical part have been known as this type of electric contacts. This IC socket is disposed on a wiring board and configured to house an IC package, which is an inspection object. Terminals of this IC package are electrically connected to electrodes of the wiring board through the probe pins.

[0003]

The probe pins have a structure in which a foundation layer and a surface layer are formed on a base material. On the other hand, some of the terminals of the IC package include a layer of so-called lead-free solder (i.e., solder composed primarily of tin and not containing lead) formed on surfaces of each terminal. In a continuity test or the like of the IC package, the surface layer of each probe pin and the lead-free solder layer of each IC package terminal are electrically connected to each other as the result of being brought into contact with each other (see, for example, Patent Literature 1).

Citation List

Patent Literature

[0004]

Patent Literature 1: Domestic Re-publication No. 2007/034921

Summary of Invention

Problems to be solved by The Invention

[0005]

In such a conventional probe pin as described above, however, a terminal of the IC package sticks to the probe pin in some cases when the IC package is detached from the IC socket after a continuity test is conducted. If the terminal of the IC package is unstuck under this condition, the leading end of the probe pin is liable to become damaged. Thus, there arises the problem of degrading the durability of the probe pin. This sticking is remarkable, in particular, after a continuity test is conducted at high temperature (for example, 150°C or higher) and is, therefore, significantly problematic.

[0006]

Hence, it is an object of the present invention to provide an electric contact (probe pin) and a socket for electrical part capable of preventing a terminal of an electrical part (IC package) and the electric contact from sticking to each other after a continuity test and thereby improving durability of the probe pin.

Means for solving The Problem

[0007]

As the result of keen examination in order to achieve such an object, the inventors et al. of the present invention have found the following. That is, the inventors et al. have found that it is possible to prevent a terminal of an electrical part and an electric contact from sticking to each other by further disposing an outermost layer composed of a specific material on a surface layer of the electric contact which is stick to the terminal of the electrical part.

[0008]

An electric contact according to the present invention includes an electrically-conductive base material; and an outermost layer composed primarily of Sn or Au formed on the base material.

[0009]

In the present invention, a surface layer composed primarily of a material into which Sn melts and diffuses upon application of heat is preferably formed on the base material, and the outermost layer is preferably formed so as to have direct contact with a surface of the surface

layer.

[0010]

In the present invention, the surface layer is preferably composed primarily of Pd and Ag.

[0011]

In the present invention, the Ag is preferably heavier in weight than the Pd.

In the present invention, the surface layer preferably includes a Pd-Ag plated layer.

[0012]

In the present invention, the surface layer preferably includes a laminate of a Pd-Ag plated layer and an Ag plated layer or a laminate of a Pd-Ag plated layer and a Pd plated layer.

[0013]

In the present invention, the surface layer preferably includes a laminate of an Ag plated layer and a Pd plated layer.

[0014]

In the present invention, a foundation layer composed primarily of Ni is preferably formed on the base material, and the surface layer is preferably formed on the foundation layer.

[0015]

A socket for electrical part according to the present invention includes a socket body; a housing portion in which an electrical part provided with Sn-containing terminals is housed; and electric contacts according to the present invention disposed in the socket body and brought into contact with the terminals.

Advantageous Effects of Invention

[0016]

According to the present invention, since electric contacts including a base material composed of an electrically-conductive material and an outermost layer made of an Sn or Au material are adopted, it is possible to prevent terminals of an electrical part and electric contacts from sticking to each other after a continuity test and thereby improve the durability of a socket for electrical part.

Brief Description of Drawings

[0017]

[Figure 1] Figure 1 is an enlarged cross-sectional view of the vicinity of a probe pin in an IC socket according to Embodiment 1 of the present invention.

[Figure 2] Figure 2 is an enlarged cross-sectional view illustrating a state of an IC package and a wiring board being attached to the IC socket of Figure 1.

[Figure 3] Figure 3 is a schematic cross-sectional view illustrating the laminar structure of the contacting portion of the probe pin according to Embodiment 1.

[Figure 4A] Figure 4A is a photograph showing a state of the contacting portion of the probe pin according to Embodiment 1 after a high-temperature test.

[Figure 4B] Figure 4B is a partially-enlarged photograph of Figure 4A.

[Figure 4C] Figure 4C is a photograph of a graph showing the result of measuring a cross-sectional shape corresponding to Figure 4B.

[Figure 5A] Figure 5A is a photograph showing a state of the contacting portion of a probe pin according to a comparative example of Embodiment 1 after a high-temperature test.

[Figure 5B] Figure 5B is a partially-enlarged photograph of Figure 5A.

[Figure 5C] Figure 5C is a photograph of a graph showing the result of measuring a cross-sectional shape corresponding to Figure 5B.

[Figure 6] Figure 6 is a schematic cross-sectional view illustrating the laminar structure of the contacting portion of a probe pin according to Embodiment 2 of the present invention.

[Figure 7A] Figure 7A is a photograph showing a state of the contacting portion of the probe pin according to Embodiment 1 after a high-temperature test.

[Figure 7B] Figure 7B is a partially-enlarged photograph of Figure 7A.

[Figure 7C] Figure 7C is a photograph of a graph showing the result of measuring a cross-sectional shape corresponding to Figure 7B.

[Figure 8A] Figure 8A is a photograph showing a state of the contacting portion of a probe pin according to a comparative example of Embodiment 1 after a high-temperature test.

[Figure 8B] Figure 8B is a partially-enlarged photograph of Figure 8A.

[Figure 8C] Figure 8C is a photograph of a graph showing the result of

measuring a cross-sectional shape corresponding to Figure 8B.

[Figure 9] Figure 9 is a schematic cross-sectional view illustrating the laminar structure of the contacting portion of a probe pin according to a modified example of the present invention.

[Figure 10] Figure 10 is a schematic cross-sectional view illustrating the laminar structure of the contacting portion of a probe pin according to a modified example of the present invention.

[Figure 11] Figure 11 is a schematic cross-sectional view illustrating the laminar structure of the contacting portion of a probe pin according to a modified example of the present invention.

[Figure 12] Figure 12 is a schematic cross-sectional view illustrating the laminar structure of the contacting portion of a probe pin according to a modified example of the present invention.

[Figure 13] Figure 13 is a schematic cross-sectional view illustrating the laminar structure of the contacting portion of a probe pin according to a modified example of the present invention.

[Figure 14] Figure 14 is a schematic cross-sectional view illustrating the laminar structure of the contacting portion of a probe pin according to a modified example of the present invention.

Description of Embodiments

[0018]

Hereinafter, embodiments of the present invention will be described.

[Embodiment 1 of Invention]

Figures 1 to 5 illustrate Embodiment 1 of the present invention.

[0019]

An IC socket 10 of this Embodiment 1 (corresponding to "socket for electrical part" of the present invention) is fixed on a wiring board 1, as illustrated in Figures 1 and 2. An IC package 2 (corresponding to "electrical part" of the present invention) is attached to the upper portion of the IC socket 10. When the IC package 2 is attached, an electrode 1a of the wiring board 1 and a solder ball 2a of the IC package 2 serving as a terminal are electrically connected to each other. The IC socket 10 is used in testing equipment or the like for a continuity test, such as a burn-in test, on the IC package 2, for example.

[0020]

In the IC package 2 of this Embodiment 1, a plurality of solder balls 2a is disposed on the bottom surface of a substantially rectangular package body 2b. These solder balls 2a are formed from so-called lead-free solder (i.e., solder composed primarily of Sn and not containing lead).

[0021]

The IC socket 10 is provided with a socket body 13 comprising an upper plate 11 and a lower plate 12. In addition, the IC socket 10 is provided with a plurality of probe pins 14 serving as electric contacts disposed in this socket body 13 into a matrix-like shape, so as to penetrate through the socket body 13 in the longitudinal direction thereof. The socket body 13 is positioned in place on the wiring board 1 by a positioning pin (not illustrated) with the upper plate 11 and the lower plate 12 fixed with fixing screws (not illustrated). Note that a floating plate 15 (corresponding to "housing portion" of the present invention) movable upward and downward with the IC package 2 housed is disposed on the upper plate 11.

[0022]

In this Embodiment 1, the layout pitch of the solder balls 2a disposed on the IC package 2 is the same as the layout pitch of the electrodes 1a disposed on the wiring board 1 and electrically connected to the solder balls 2a. The layout pitch of the probe pins 14 is also the same as these layout pitches.

[0023]

As illustrated in Figures 1 and 2, each probe pin 14 includes a first plunger 20, a second plunger 30, and a cylindrical member 40 disposed along a lengthwise axis line L. A contacting portion 20a to have electrical contact with the electrode 1a of the wiring board 1 is formed at the leading end of the first plunger 20. A contacting portion 30a to have electrical contact with the solder ball 2a of the IC package 2 is formed at the leading end of the second plunger 30. The cylindrical member 40 is disposed between the first plunger 20 and the second plunger 30. A coil spring 50 for urging the first plunger 20 and the second plunger 30 in directions in which the plungers move away from each other along the axis line L is housed inside this cylindrical member 40.

[0024]

The first plunger 20 is provided with a protruding portion 20b at the leading end of which the substantially conical contacting portion 20a to have

contact with the electrode 1a of the wiring board 1 is disposed; and an insertion portion 20c thicker than the protruding portion 20b. The insertion portion 20c of these portions is slidably housed inside the cylindrical member 40 in a state of having contact therewith. The movement of the first plunger 20 in the projecting direction (downward direction) thereof is restricted by a locking part 40a formed at the lower end of the cylindrical member 40. In addition, a receiving portion 20d for stopping the coil spring 50 is formed into a conical shape with the axis line L in the center at an end of the insertion portion 20c and integrally therewith. Yet additionally, the protruding portion 20b is slidably inserted through a through-hole 12a of the lower plate 12.

[0025]

The second plunger 30 is provided with a protruding portion 30b at the leading end of which a substantially crown-shaped contacting portion 30a to have contact with the solder ball 2a of the IC package 2 is disposed; and an insertion portion 30c thicker than the protruding portion 30b. The insertion portion 30c of these portions is housed inside the cylindrical member 40 in a state of slidably having contact with the inner side surface of the cylindrical member 40. The movement of the second plunger 30 in the projecting direction (upward direction) thereof is restricted by a locking part 40b formed at the upper end of the cylindrical member 40. In addition, a receiving portion 30d for stopping the coil spring 50 is formed into a conical shape with the axis line L in the center at the lower end of the insertion portion 30c and integrally therewith. Yet additionally, the protruding portion 30b projects out of the upper plate 11. A contacting portion 30a is disposed at the upper end of this protruding portion 30b. This contacting portion 30a is slidably inserted through a through-hole 15a of the floating plate 15 capable of housing the solder ball 2a of the IC package 2.

[0026]

Next, the material of the probe pin 14 in this Embodiment 1 will be described using Figures 3 to 5. Note that a description will be made of the material of the second plunger 30 of the probe pin 14 in particular here.

[0027]

As illustrated in Figure 3, the second plunger 30 of the probe pin 14 in this Embodiment 1 has a structure in which a base material 31, a foundation layer 32, a surface layer 33, and an outermost layer 34 are laminated.

[0028]

Among the material and the layers, the base material 31 has electrical conductivity and is formed from, for example, brass.

[0029]

As the foundation layer 32, it is possible to use, for example, a plated layer composed primarily of Ni. The film thickness of the foundation layer 32 is, for example, 2 to 3 μm .

[0030]

As a primary constituent, the surface layer 33 contains a material into which Sn melts upon application of heat. In this Embodiment 1, a Pd-Ag plated layer having a thickness of approximately 1 μm is adopted as the surface layer 33. Pd may be higher in weight ratio than Ag, or Ag may be higher in weight ratio than Pd. It is possible however to enhance the durability of the probe pin 14, compared with a conventional probe pin in which Au is plated on Ni, by making the ratio of Ag higher.

[0031]

This Pd-Ag plated layer can be formed using, for example, a process of manufacture by wet plating or a process of manufacture by ion plating.

[0032]

In the process of manufacture by wet plating, the foundation layer 32 (for example, an Ni plated layer having a thickness of 2 to 3 μm) is formed first, and then an adhesion layer (for example, an Au strike-plated layer) is formed on the foundation layer 32. Then, a Pd plated layer having a thickness of 0.5 μm and an Ag plated layer having a thickness of 0.5 μm , for example, are laminated in an alternate manner. Thereafter, Pd and Ag are thermally diffused at predetermined temperature using a thermostatic chamber to form the surface layer 33. The weight ratio between Pd and Ag at this time is, for example, Pd : Ag = 54 : 46. This ratio may be freely varied by adjusting the film thicknesses of the Pd plated layer and the Ag plated layer.

[0033]

On the other hand, in the process of manufacture by ion plating, the foundation layer 32 (for example, an Ni plated layer having a thickness of 2 to 3 μm) is formed first. Then, Pd and Ag are deposited on the foundation layer 32 by ion plating, so as to have a thickness of, for example, 1 μm , thereby forming the surface layer 33. In this case, the weight ratio between

Pd and Ag is Pd : Ag = 36 : 64, Ag being higher in weight ratio than Pd.
[0034]

The outermost layer 34 is a layer used to prevent the solder ball 2a of the IC package 2 and the second plunger 30 of the probe pin 14 from sticking to each other at the time of continuity test. In this Embodiment 1, an Sn plated layer having a thickness of approximately 0.3 μm is used as the outermost layer 34. This outermost layer 34 is formed on the surface layer 33. The outermost layer 34 can be formed by means of wet plating or ion plating in the same way as the abovementioned surface layer 33.
[0035]

When the IC socket 10 according to this Embodiment 1 (i.e., an IC socket 10 in which the second plunger 30 of the probe pin 14 is configured as described above) is used, a plurality of probe pins 14 is first plugged into the socket body 13. In addition, the IC socket 10 is disposed with the contacting portion 20a of the first plunger 20 projecting downward and the contacting portion 30a of the second plunger 30 facing the through-hole 15a of the floating plate 15, as illustrated in Figure 1. Then, this IC socket 10 is positioned and fixed on the wiring board 1, and the contacting portion 20a of the first plunger 20 is brought into contact with the electrode 1a of the wiring board 1, as illustrated in Figure 2. At this time, the coil spring 50 is compressed by the receiving portion 20d of the insertion portion 20c of the first plunger 20 inside the cylindrical member 40.
[0036]

Thereafter, the IC package 2 is housed in the floating plate 15, and the solder ball 2a is fitted in the through-hole 15a. When the floating plate 15 is brought down under that condition, the contacting portion 30a of the second plunger 30 comes into contact with the solder ball 2a. If the floating plate 15 is brought down further, the coil spring 50 is compressed by the receiving portion 30d of the second plunger 30 inside the cylindrical member 40, as illustrated in Figure 2.
[0037]

The coil spring 50 is compressed by the first plunger 20 and the second plunger 30 in this way to urge the contacting portion 20a of the first plunger 20 and the contacting portion 30a of the second plunger 30 by the coil spring 50 in directions in which the contacting portions move away from each other. Thus, it is possible to bring the electrode 1a of the wiring board 1 and the

solder ball 2a of the IC package 2 into contact with each other. Then, a continuity test, such as a burn-in test, is conducted on the IC package 2 under such a condition.

[0038]

In a conventional IC socket, the solder ball 2a of the IC package 2 and the second plunger 30 of the probe pin 14 stick to each other in such a continuity test when the IC package 2 is detached from the IC socket 10 after the continuity test. In contrast, the IC socket of this Embodiment 1 can prevent the solder ball 2a and the second plunger 30 from sticking to each other and thereby improve the durability of the probe pin 14 and the IC socket 10. Hereinafter, the reason for this will be described in detail.

[0039]

In the conventional IC socket, the Pd-Ag plated surface layer 33 is formed on the outermost side of the second plunger 30 formed in the probe pin 14. Accordingly, Sn in the solder ball 2a melts into the probe pin 14 when a continuity test is conducted on the IC package 2 including a terminal (solder ball 2a) formed from lead-free solder, thus forming a Pd-Ag-Sn alloy. As a result, there arises the problem in which the solder ball 2a and the probe pin 14 stick to each other through this Pd-Ag-Sn alloy after the continuity test. If an attempt is made to forcibly unstick the solder ball 2a and the probe pin 14 from each other under such a condition, a large amount of solder remains on the surface layer 33 of the probe pin 14. Consequently, the surface layer 33 of the probe pin 14 becomes damaged, thus degrading the durability of the probe pin 14.

[0040]

In contrast, in this Embodiment 1, a plated layer serving as the outermost layer 34 and composed primarily of Sn is disposed on the probe pin 14, so as to have direct contact with a surface of the surface layer 33 (i.e., a plated layer composed primarily of Pd and Ag). Consequently, the probe pin 14 is in a state in which a specific amount of Sn in the outermost layer 34 has previously melted into a Pd-Ag alloy of the surface layer 33 prior to the continuity test. Accordingly, Sn in the solder ball 2a disposed on the IC package 2 is less likely to melt into the probe pin 14 even in a burn-in environment (for example, at a temperature of 125°C to 180°C). As a result, a Pd-Ag-Sn alloy is hardly formed between the solder ball 2a of the IC package 2 and the second plunger 30 of the probe pin 14. Accordingly, there

does not arise the problem of the solder ball 2a and the probe pin 14 sticking to each other through this Pd-Ag-Sn alloy after a continuity test.

[0041]

Thus, according to this Embodiment 1, a large amount of solder is less likely to remain on the surface layer 33 of the probe pin 14 after a continuity test. In addition, the surface layer 33 of the probe pin 14 is less likely to become damaged. Consequently, it is possible to improve the durability of the probe pin 14.

[0042]

Next, a description will be made of evaluation tests that confirm the advantageous effects of the present invention.

[0043]

Here, the amount of solder in the solder ball of the IC package formed into an alloy and stuck to the probe pin at the time of a high-temperature test was compared between a conventional probe pin (i.e., a probe pin provided with a second plunger including a surface layer composed of a Pd-Ag plated layer on the outermost side of the probe pin) and a probe pin of this Embodiment 1 (i.e., a probe pin provided with a second plunger in which an outermost layer composed primarily of Sn is plated on a surface layer composed primarily of Pd and Ag).

(1) Test Contents

First, a simultaneous high-temperature test was conducted using an IC socket including the probe pin according to this Embodiment 1 and an IC socket including a conventional probe pin (i.e., a probe pin not provided with an Sn plated layer serving as an outermost layer).

[0044]

The specifications of the probe pins and the solder balls of IC packages used in this test were as follows:

(2) Specifications of Probe Pin Including Sn Plated Layer

Brass was used as a base material.

[0045]

Like the contacting portion 30a illustrated in Figure 1, the contacting portion of the probe pin was formed into a substantially crown-like shape.

[0046]

A laminated film was formed on a surface of this contacting portion using wet plating.

[0047]

That is, an Ni plated layer having a thickness of 2 to 3 μm was first formed on a surface of the base material. Then, an Au strike-plated layer to serve as an adhesion layer was formed on the Ni plated layer. In addition, a Pd plated layer having a thickness of 0.5 μm and an Ag plated layer having a thickness of 0.5 μm were laminated in an alternate manner. Thereafter, Pd and Ag were thermally diffused at predetermined temperature using a thermostatic chamber. The weight ratio between Pd and Ag at this time was Pd : Ag = 54 : 46. Yet additionally, an Sn plated layer having a thickness of 0.3 μm was formed on the layer stack by a wet plating method.

(3) Specifications of Probe Pin Not Including Sn Plated Layer

Brass was used as a base material.

[0048]

Like the contacting portion 30a illustrated in Figure 1, the contacting portion of the probe pin was formed into a substantially crown-like shape.

[0049]

A laminated film was formed on a surface of this contacting portion using wet plating.

[0050]

That is, an Ni plated layer having a thickness of 2 to 3 μm was first formed on a surface of the base material. Then, an Au strike-plated layer to serve as an adhesion layer was formed on the Ni plated layer. In addition, a Pd plated layer having a thickness of 0.5 μm and an Ag plated layer having a thickness of 0.5 μm were laminated in an alternate manner. Thereafter, Pd and Ag were thermally diffused at predetermined temperature using a thermostatic chamber. The weight ratio between Pd and Ag at this time was Pd : Ag = 54 : 46.

(4) Specifications of Solder Balls of IC Package

Sn-3Ag-0.5Cu (percent by mass) lead-free solder was used as a material for forming the solder balls.

(5) Test Method

- Method for measuring the amount of deposited solder: A shape measurement was conducted using a shape-measuring laser microscope (VK-9500 made by Keyence Corporation).
- Test conditions were as follows:

[0051]

Ambient temperature: Room temperature to 150°C

Test time: 24 hours

• A test procedure was as follows:

- a. Attach the IC package to the IC socket.
- b. Raise the temperature of the IC socket and the IC package up to 150°C.
- c. Keep the temperature of the IC socket and the IC package at 150°C for 24 hours.
- d. Lower the temperature of the IC socket and the IC package down to room temperature.
- e. Detach the IC package from the IC socket.
- f. Measure the amount of solder deposited on the contacting portion of the probe pin.

(6) Test Results

The above-described test was conducted to analyze the contacting portion of each probe pin.

[0052]

Figure 4A is a photograph showing a state of the probe pin including an Sn plated layer after the evaluation test. In Figure 4A, white areas represent Sn that separated from the solder balls of the IC package and stuck to the surface of the probe pin. Figure 4B is a partially-enlarged photograph of the region shown by reference character A in Figure 4A. Figure 4C is a photograph showing the result of measuring deposited Sn using the shape-measuring laser microscope, where the axis of abscissas represents the position, whereas the axis of ordinates represents the thickness.

[0053]

Figure 5A is a photograph showing a state of the probe pin not including an Sn plated layer after the evaluation test. In Figure 5A, white areas represent Sn that separated from the solder balls of the IC package and stuck to the surface of the probe pin. Figure 5B is a partially-enlarged photograph of the region shown by reference character B in Figure 5A. Figure 5C is a photograph showing the result of measuring the stuck Sn using the shape-measuring laser microscope, where the axis of abscissas represents the position, whereas the axis of ordinates represents the thickness.

[0054]

As is understood from Figures 4A to 4C, the amount of solder sticking after the test is small in the probe pin according to this Embodiment 1 (i.e., a probe pin on which an Sn plated layer serving as an outermost layer is formed). Consequently, it can be said that the solder ball and the probe pin are less likely to stick to each other. In contrast, as is understood from Figures 5A to 5C, the amount of solder sticking after the test is large in the conventional probe pin (a probe pin on which an Sn plated layer is not formed), compared with the probe pin according to this Embodiment 1. Consequently, it can be said that the solder ball and the probe pin are more likely to stick to each other in the conventional probe pin.

[Embodiment 2 of Invention]

Figures 6 to 8 illustrate Embodiment 2 of the present invention.
[0055]

Note that Embodiment 2 of the present invention is the same as Embodiment 1 except the constituent elements to be discussed hereinafter, and therefore, constituent elements other than those different from the constituent elements of Embodiment 1 described above will be denoted by like reference numerals and characters and will be excluded from the discussion.

[0056]

Hereinafter, the material of a probe pin 14 of this Embodiment 2 will be described using Figures 6 to 8. Note that the material of the second plunger 30 of the probe pin 14 will be described in particular here.

[0057]

As illustrated in Figure 6, the second plunger 30 of the probe pin 14 of this Embodiment 2 has a structure in which a base material 31, a foundation layer 32, a surface layer 33, and an outermost layer 35 are laminated.

[0058]

Among the material and the layers, the base material 31, the foundation layer 32 and the surface layer 33 are the same as those of Embodiment 1 described above, and therefore, will not be discussed here.

[0059]

The outermost layer 35 is a layer used to prevent the solder ball 2a of the IC package 2 and the second plunger 30 of the probe pin 14 from sticking to each other at the time of continuity test. In this Embodiment 2, an Au plated layer having a thickness of approximately 0.1 μm is used as the

outermost layer 35. This outermost layer 35 is formed on the surface layer 33. The outermost layer 35 can be formed by means of wet plating or ion plating in the same way as the abovementioned surface layer 33.

[0060]

The continuity test is conducted by bringing the probe pin 14 according to this Embodiment 2 (i.e., a probe pin 14 in which the second plunger 30 is structured as described above) into contact with the electrode 1a of the wiring board 1 and the solder ball 2a of the IC package 2. In this case, the solder ball 2a of the IC package 2 and the second plunger 30 of the probe pin 14 stick to each other in a conventional IC socket when the IC package 2 is detached from the IC socket 10 after the continuity test. In contrast, in the IC socket of this Embodiment 2, it is possible to prevent the solder ball 2a and the second plunger 30 from sticking to each other and thereby improve the durability of the probe pin 14 and the IC socket 10. Hereinafter, the reason for this will be described in detail.

[0061]

In the conventional IC socket, the Pd-Ag plated surface layer 33 is formed on the outermost side of the second plunger 30 formed in the probe pin 14. Accordingly, Sn in the solder ball 2a melts into the probe pin 14 when a continuity test is conducted on the IC package 2 including a terminal (solder ball 2a) formed from lead-free solder, thus forming a Pd-Ag-Sn alloy. As a result, there arises the problem in which the solder ball 2a and the probe pin 14 stick to each other through this Pd-Ag-Sn alloy after the continuity test. If an attempt is made to forcibly unstick the solder ball 2a and the probe pin 14 from each other under such a condition, a large amount of solder remains on the surface layer 33 of the probe pin 14. Consequently, the surface layer 33 of the probe pin 14 becomes damaged, thus degrading the durability of the probe pin 14.

[0062]

In contrast, in this Embodiment 2, a plated layer serving as the outermost layer 35 and composed primarily of Au is disposed on the probe pin 14, so as to have direct contact with a surface of the surface layer 33 (i.e., a plated layer composed primarily of Pd and Ag). Consequently, Sn in the solder ball 2a and Au in the outermost layer 35 form an Sn-Au alloy in a burn-in environment (at a temperature of, for example, 125°C to 180°C). This Sn-Au alloy has the nature of being brittle and easy to delaminate. As

a result, the solder ball 2a and the probe pin 14 are easy to unstick from each other through this Sn-Au alloy after a continuity test. Accordingly, there does not arise the problem of the Pd-Ag alloy plated layer (i.e., the surface layer 33) underneath the outermost layer 35 forming a Pd-Ag-Sn alloy along with the solder ball 2a and sticking to the solder ball 2a.

[0063]

Thus, according to this Embodiment 2, a large amount of solder is less likely to remain on the surface layer 33 of the probe pin 14 after a continuity test. In addition, the surface layer 33 of the probe pin 14 is less likely to become damaged. Consequently, it is possible to improve the durability of the probe pin 14.

[0064]

Next, a description will be made of evaluation tests that confirm the advantageous effects of the present invention.

[0065]

Here, the amount of solder in the solder ball of the IC package formed into an alloy and stuck to the probe pin after a high-temperature test was compared between a conventional probe pin (i.e., a probe pin provided with a second plunger including a surface layer composed of a Pd-Ag plated layer on the outermost side of the probe pin) and a probe pin of this Embodiment 2 (i.e., a probe pin provided with a second plunger in which an outermost layer composed primarily of Au was plated on a surface layer composed primarily of Pd and Ag).

(1) Test Contents

First, a simultaneous high-temperature test was conducted using an IC socket including the probe pin according to this Embodiment 2 and an IC socket including a conventional probe pin (i.e., a probe pin not provided with an Au plated layer serving as an outermost layer).

[0066]

The specifications of the probe pins and the solder balls of IC packages used in this test were as follows:

(2) Specifications of Probe Pin Including Au Plated Layer

Brass was used as a base material.

[0067]

Like the contacting portion 30a illustrated in Figure 1, the contacting portion of the probe pin was formed into a substantially crown-like shape.

[0068]

A laminated film was formed on a surface of this contacting portion using wet plating.

[0069]

That is, an Ni plated layer having a thickness of 2 to 3 μm was first formed on a surface of the base material. Then, an Au strike-plated layer to serve as an adhesion layer was formed on the Ni plated layer. In addition, a Pd plated layer having a thickness of 0.5 μm and an Ag plated layer having a thickness of 0.5 μm were laminated in an alternate manner. Thereafter, Pd and Ag were thermally diffused at predetermined temperature using a thermostatic chamber. The weight ratio between Pd and Ag at this time was Pd : Ag = 54 : 46. Yet additionally, an Au plated layer having a thickness of 0.1 μm was formed on the layer stack by a wet plating method.

(3) Specifications of Probe Pin Not Including Au Plated Layer

Brass was used as a base material.

[0070]

Like the contacting portion 30a illustrated in Figure 1, the contacting portion of the probe pin was formed into a substantially crown-like shape.

[0071]

A laminated film was formed on a surface of this contacting portion using wet plating.

[0072]

That is, an Ni plated layer having a thickness of 2 to 3 μm was first formed on a surface of the base material. Then, an Au strike-plated layer to serve as an adhesion layer was formed on the Ni plated layer. In addition, a Pd plated layer having a thickness of 0.5 μm and an Ag plated layer having a thickness of 0.5 μm were laminated in an alternate manner. Thereafter, Pd and Ag were thermally diffused at predetermined temperature using a thermostatic chamber. The weight ratio between Pd and Ag at this time was Pd : Ag = 54 : 46.

(4) Specifications of Solder Balls of IC Package

Sn-3Ag-0.5Cu (percent by mass) lead-free solder was used as a material for forming the solder balls.

(5) Test Method

- Method for measuring the amount of deposited solder: A shape measurement was conducted using a shape-measuring laser microscope

(VK-9500 made by Keyence Corporation).

- Test Conditions

Ambient temperature: Room temperature to 125°C

Test time: 24 hours

- A test procedure was as follows:

[0073]

a. Attach the IC package to the IC socket.

[0074]

b. Raise the temperature of the IC socket and the IC package up to 125°C.

[0075]

c. Keep the temperature of the IC socket and the IC package at 125°C for 24 hours.

[0076]

d. Lower the temperature of the IC socket and the IC package down to room temperature.

[0077]

e. Detach the IC package from the IC socket.

[0078]

f. Measure the amount of solder deposited on the contacting portion of the probe pin.

(6) Test Results

The above-described test was conducted to analyze the contacting portion of each probe pin.

[0079]

Figure 7A is a photograph showing a state of the probe pin including an Au plated layer after the evaluation test. In Figure 7A, white areas represent Sn that separated from the solder balls of the IC package and stuck to the surface of the probe pin. Figure 7B is a partially-enlarged photograph of the region shown by reference character C in Figure 7A. Figure 7C is a photograph showing the result of measuring the stuck Sn using the shape-measuring laser microscope, where the axis of abscissas represents the position, whereas the axis of ordinates represents the thickness.

[0080]

Figure 8A is a photograph showing a state of the probe pin not

including an Sn plated layer after the evaluation test. In Figure 8A, white areas represent Sn that separated from the solder balls of the IC package and stuck to the surface of the probe pin. Figure 8B is a partially-enlarged photograph of the region shown by reference character D in Figure 8A. Figure 8C is a photograph showing the result of measuring the stuck Sn using the shape-measuring laser microscope, where the axis of abscissas represents the position, whereas the axis of ordinates represents the thickness.

[0081]

As is understood from Figures 7A to 7C, the amount of solder sticking after the test is small in the probe pin according to this Embodiment 2 (i.e., a probe pin on which an Au plated layer serving as an outermost layer is formed). Consequently, it can be said that the solder ball and the probe pin are less likely to stick to each other. In contrast, as is understood from Figures 8A to 8C, the amount of solder sticking after the test is large in the conventional probe pin (a probe pin on which an Au plated layer is not formed), compared with the probe pin according to this Embodiment 2. Consequently, it can be said that the solder ball and the probe pin are more likely to stick to each other in the conventional probe pin.

[0082]

Figures 9 to 14 respectively illustrate different modified examples of Embodiments 1 and 2 of the present invention. Note that the configurations of these modified examples are the same as those of Embodiments 1 and 2 described above, except the differences of materials to be described hereafter.

[0083]

In the second plunger 30 of the probe pin illustrated in Figure 9, a Pd plated layer 33b is formed between the Pd-Ag plated layer 33a and the foundation layer 32. In the second plunger 30 of the probe pin 14 illustrated in Figure 10, an Ag plated layer 33c is formed between the Pd-Ag plated layer 33a and the foundation layer 32. In the second plunger 30 of the probe pin 14 illustrated in Figure 11, a Pd plated layer 33b is formed on the Pd-Ag plated layer 33a. In the second plunger 30 of the probe pin 14 illustrated in Figure 12, an Ag plated layer 33c is formed on the Pd-Ag plated layer 33a. In the second plunger 30 of the probe pin 14 illustrated in Figure 13, a Pd plated layer 33b and an Ag plated layer 33c are laminated in order

on the foundation layer 32. In the second plunger 30 of the probe pin 14 illustrated in Figure 14, the Ag plated layer 33c and the Pd plated layer 33b are laminated in order on the foundation layer 32.

[0084]

These probe pins illustrated in Figures 9 to 14 can also prevent the solder ball and the second plunger 30 from sticking to each other after a continuity test and thereby improve the durability of the probe pin and the IC socket, since the probe pins are provided with the outermost layer 34 composed primarily of Sn or the outermost layer 35 composed primarily of Au.

[0085]

The present invention is also applicable to a probe pin including a surface layer 33 formed from other materials.

[0086]

For example, the surface layer 33 illustrated in Figure 3 may be an Ag-Sn (10% by weight of Ag) plated layer having a thickness of approximately 1 μm . At this time, Ag is preferably 80% or higher in the weight ratio between Ag and Sn.

[0087]

Alternatively, the surface layer 33 illustrated in Figure 3 may be formed from a composite material composed of Ag and an oxide of a metal element. For example, the surface layer 33 may be composed primarily of Ag and ZnO (zinc oxide). The weight ratio between Ag and ZnO may be, for example, 89.7 : 10.3, and thus the ratio of Ag is preferably 80% or higher.

[0088]

As the composite material composed of Ag and an oxide of a metal element, it is possible to adopt an Ag-SnO₂ (zinc dioxide) material, in addition to the Ag-ZnO material.

[0089]

As an example of the composite material other than a material composed of Ag and a metal, it is possible to adopt, for example, an Ag-C (carbon) material. The weight ratio between Ag and C may be, for example, 99 : 1, and thus the ratio of Ag is preferably 80% or higher.

[0090]

In Embodiments 1 and 2 described above, discussions have been made on the material of the second plunger 30 of the probe pin 14. Other portions

of the probe pin 14 may be composed of the same material, however.

[0091]

Also in Embodiments 1 and 2 described above, probe pins which are "electric contacts" have been applied to an IC socket. Without limitation to this application, however, the probe pins may be applied to other devices.

Reference Signs List

[0092]

- 1: Wiring board
- 1a: Electrode
- 2: IC package (electrical part)
- 2a: Solder ball (terminal)
- 10: IC socket (socket for electrical part)
- 13: Socket body
- 14: Probe pin (electric contact)
- 15: Floating plate (housing portion)
- 20: First plunger
- 30: Second plunger
- 30a: Contacting portion
- 31: Base material
- 32: Foundation layer
- 33: Surface layer
- 33a: Pd-Ag plated layer
- 33b: Pd plated layer
- 33c: Ag plated layer
- 34: Outermost layer (Sn)
- 35: Outermost layer (Au)
- 40: Cylindrical member
- 50: Coil spring

Claims

[Claim 1]

An electric contact comprising:
an electrically-conductive base material; and
an outermost layer composed primarily of Sn or Au formed on the base material.

[Claim 2]

The electric contact according to claim 1, wherein a surface layer composed primarily of a material into which Sn melts and diffuses upon application of heat is formed on the base material, and the outermost layer is formed so as to have direct contact with a surface of the surface layer.

[Claim 3]

The electric contact according to claim 2, wherein the surface layer is composed primarily of Pd and Ag.

[Claim 4]

The electric contact according to claim 3, wherein the Ag is heavier in weight than the Pd.

[Claim 5]

The electric contact according to claim 3 or 4, wherein the surface layer includes a Pd-Ag plated layer.

[Claim 6]

The electric contact according to claim 5, wherein the surface layer includes a laminate of a Pd-Ag plated layer and an Ag plated layer or a laminate of a Pd-Ag plated layer and a Pd plated layer.

[Claim 7]

The electric contact according to claim 3 or 4, wherein the surface layer includes a laminate of an Ag plated layer and a Pd plated layer.

[Claim 8]

The electric contact according to any one of claims 2 to 7, wherein a foundation layer composed primarily of Ni is formed on the base material, and the surface layer is formed on the foundation layer.

[Claim 9]

A socket for electrical part comprising:
a socket body;
a housing portion in which an electrical part provided with an

Sn-containing terminal is housed; and

an electric contact according to any one of claims 1 to 8 disposed in the socket body and brought into contact with the terminal.