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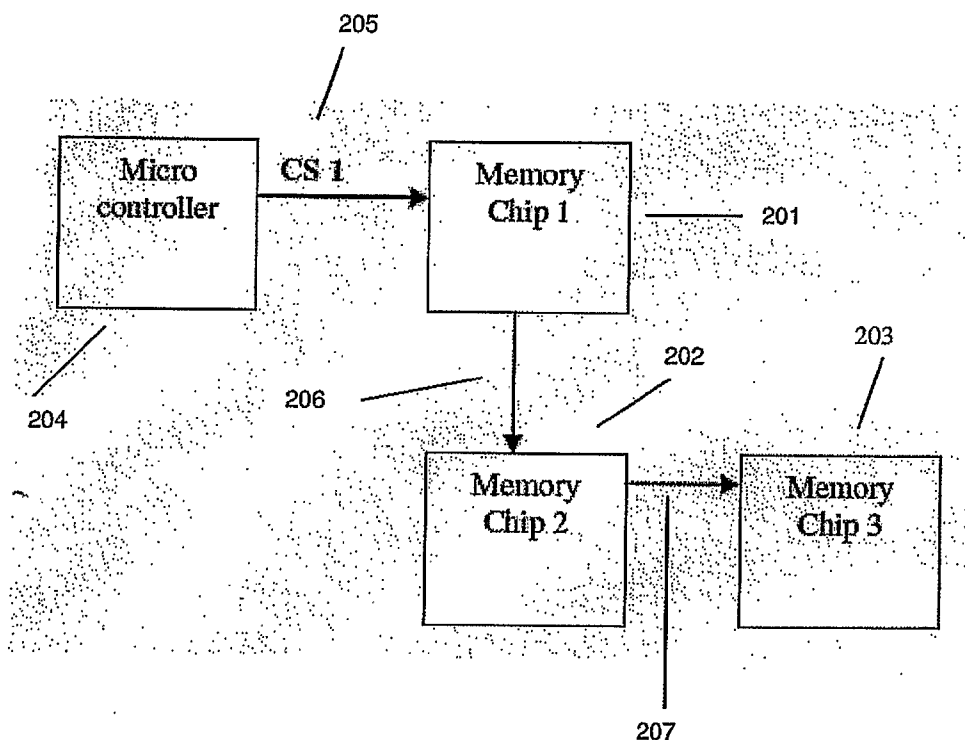
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(54) Title: METHOD AND APPARATUS FOR CASCADE MEMORY



(57) Abstract: A memory device having a memory cell and a memory controller operatively connected to the memory cell. A cascade circuit is provided for enabling a subsequent memory device in a cascade of memory devices. The cascade circuit is operatively connected to the memory controller. A corresponding method is also disclosed.

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