

プラズマ処理装置1

10
11
12

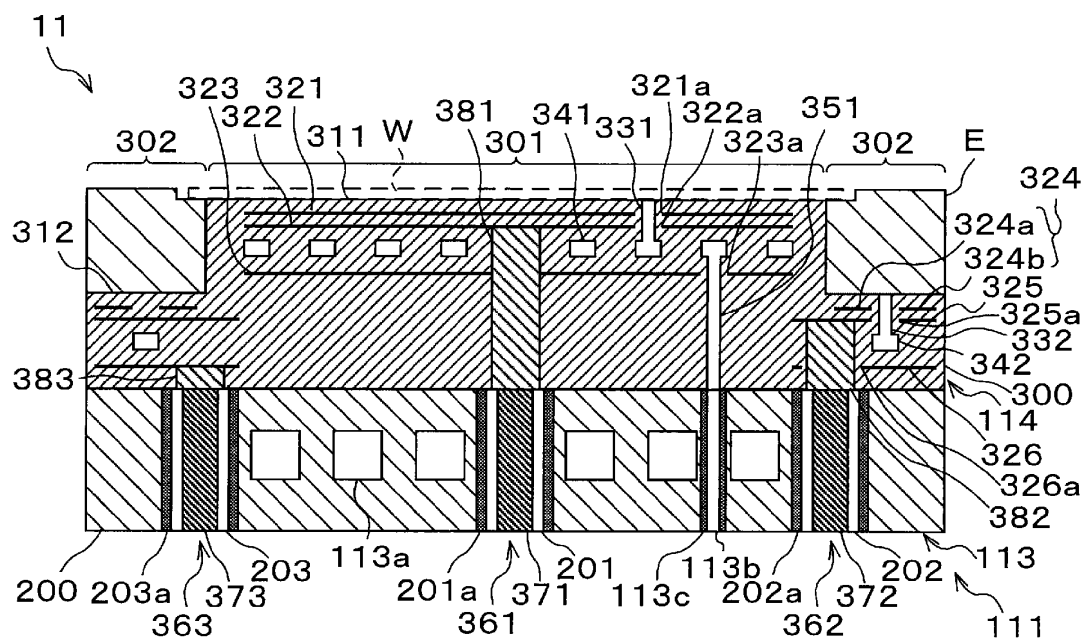
↑↓

制御部2

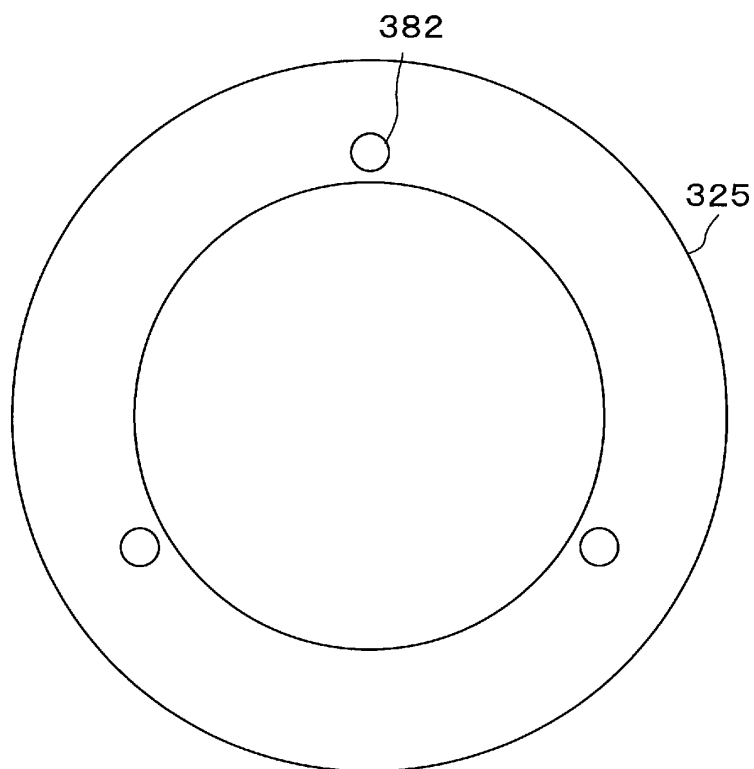
2a		
2a1	2a2	2a3

[illegible]

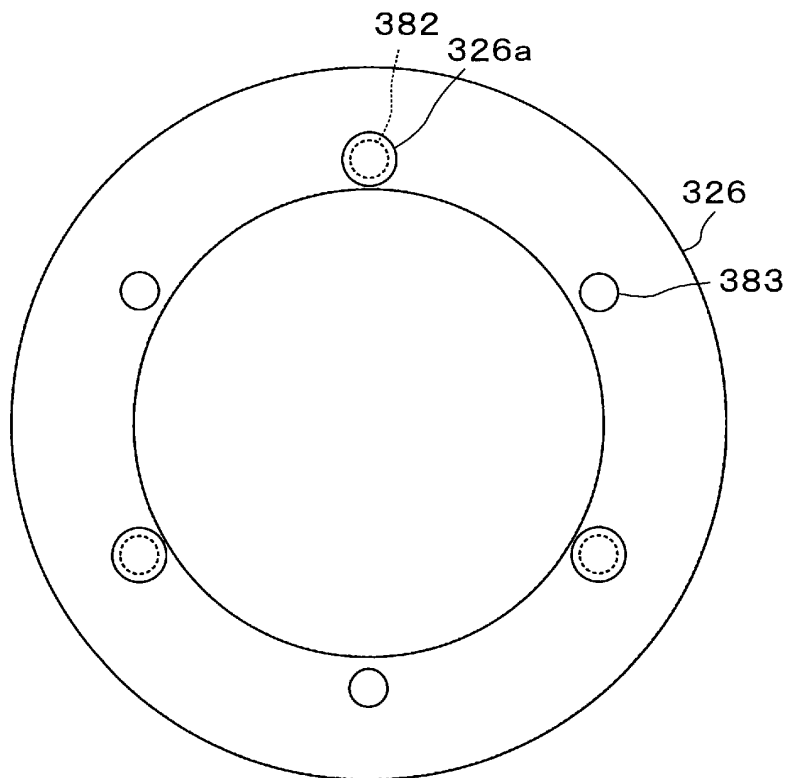
[図3]



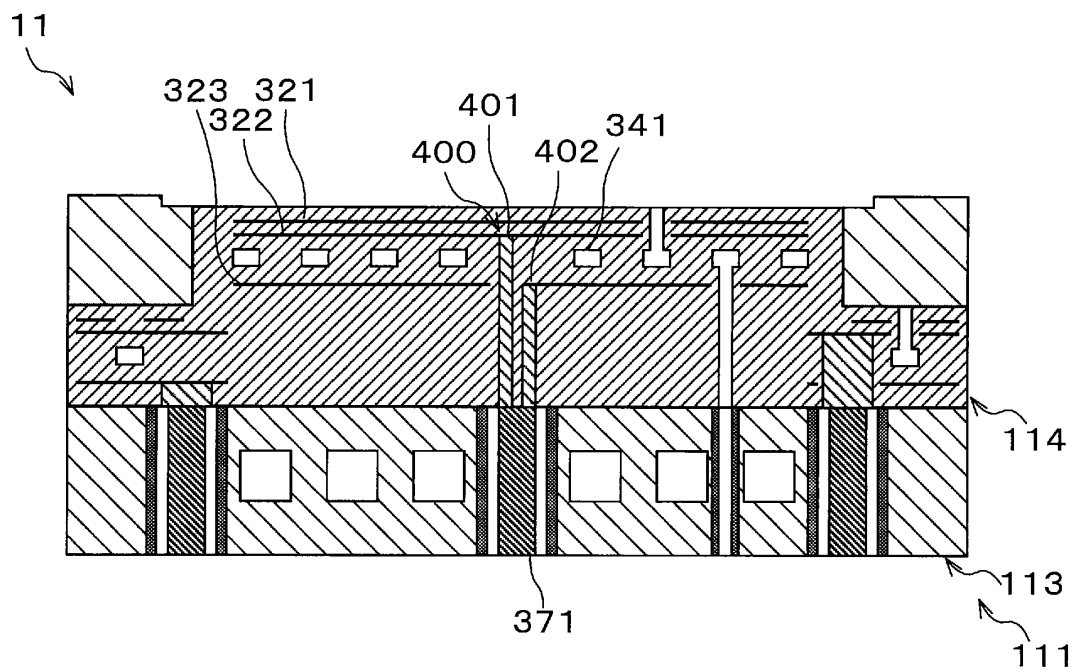
[図4]



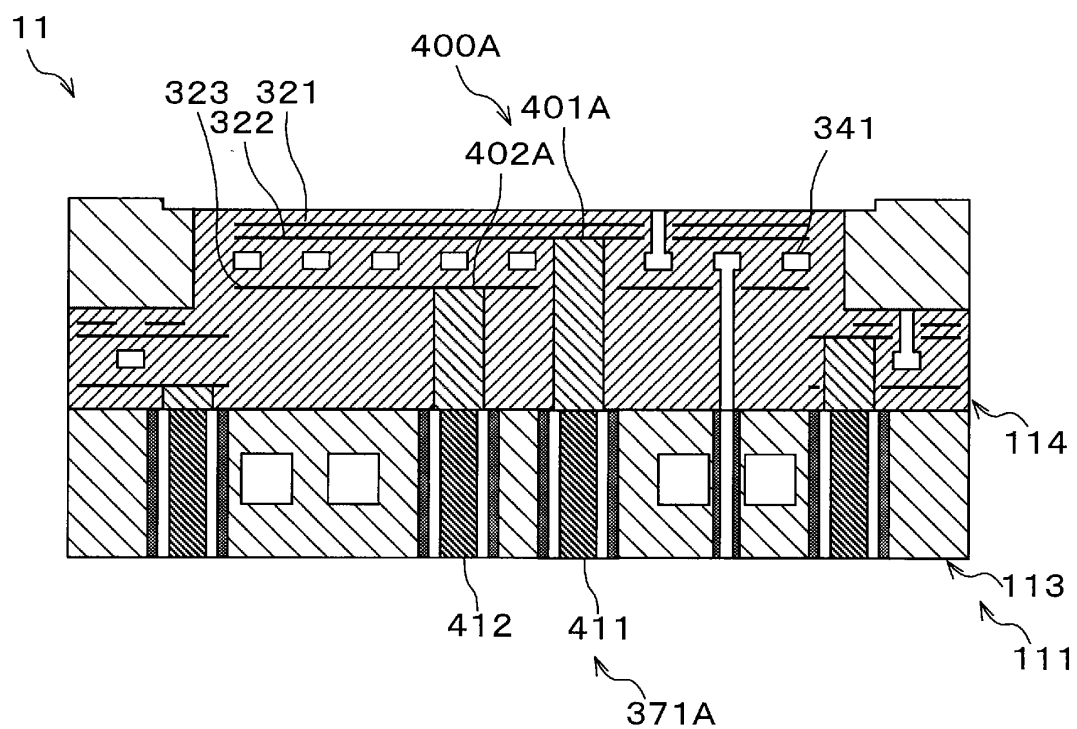
[図5]



[図6]



[図7]



[図8]

