

Memory Management Method and Apparatus, Control Program and Controller

ABSTRACT

Embodiments of the present disclosure provide a memory management method and apparatus, a control program, and a controller; the method comprises: an MMU identifies the type of a memory controller externally connected based on configuration information of a CPU; based on an address region corresponding to an address management sub-module and a memory controller type, determining an offset address in a manner of table look-up; calculating a logic address of the type of the memory controller based on the offset address; the numbers of external connection channels gated by different types of memory controllers are different. By means of the present disclosure, the problem of being compatible with different controllers under the same framework is solved, so that a plurality of controllers can be compatible under the same framework, and the storage efficiency is improved.

[Fig. 2]