


FIG. 1A

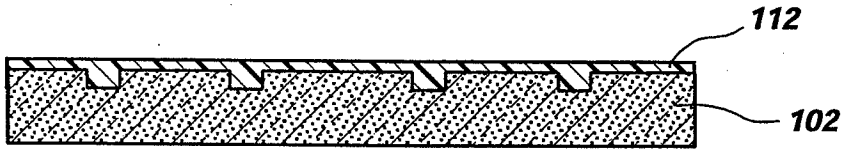



FIG. 1B

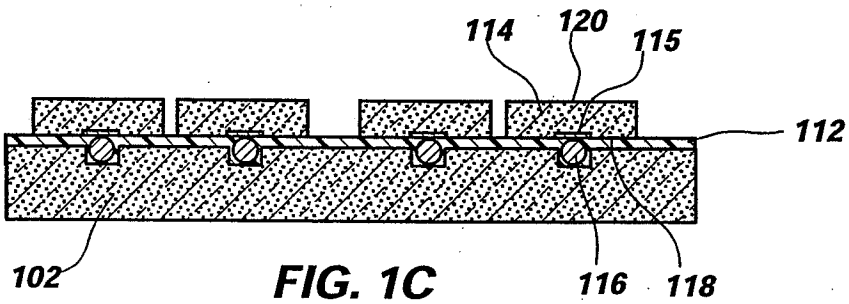



FIG. 1C

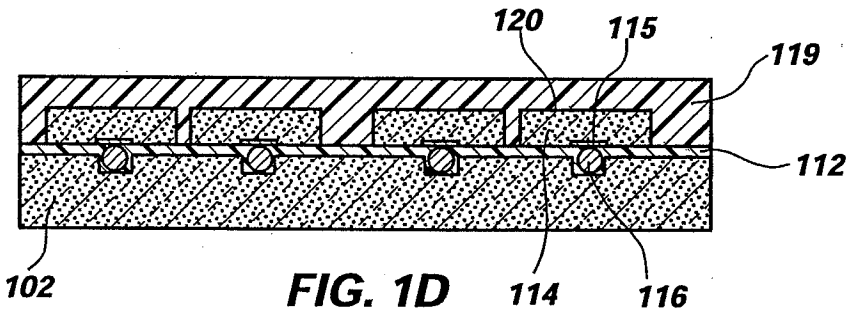



FIG. 1D

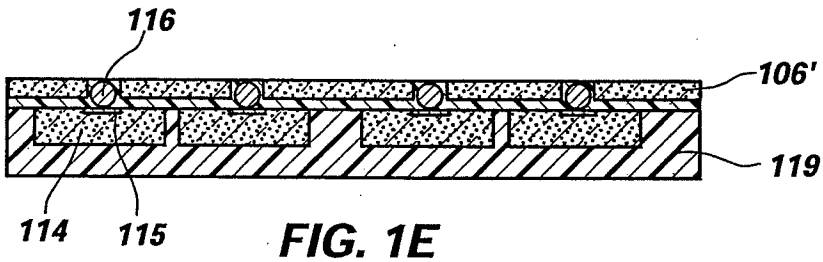



FIG. 1E

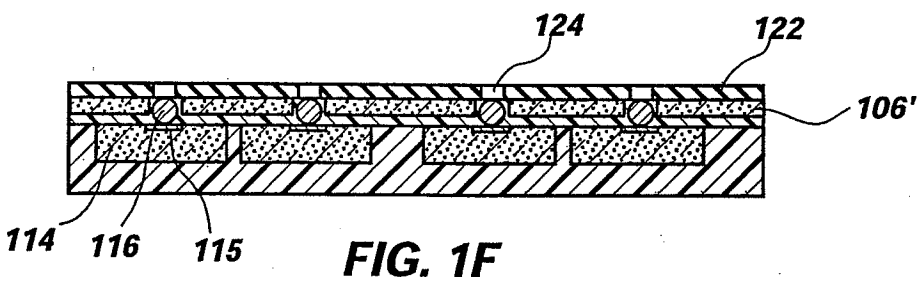



FIG. 1F

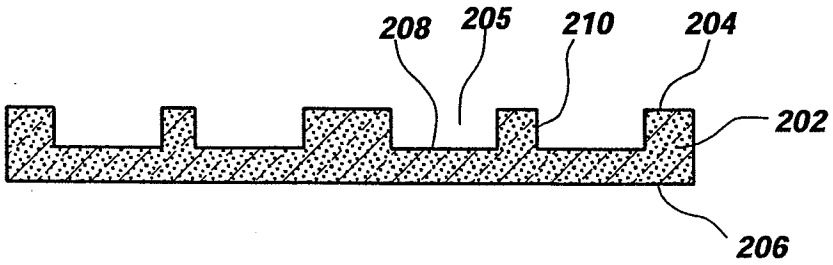



FIG. 2A

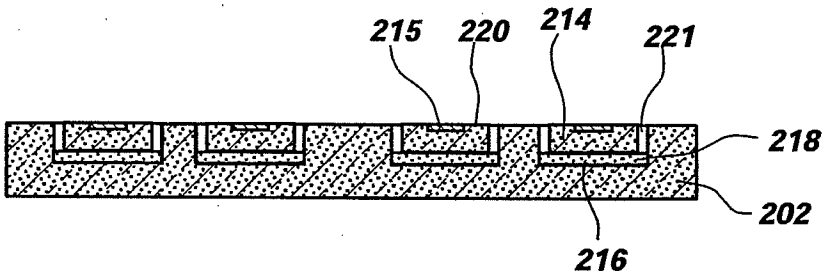



FIG. 2B

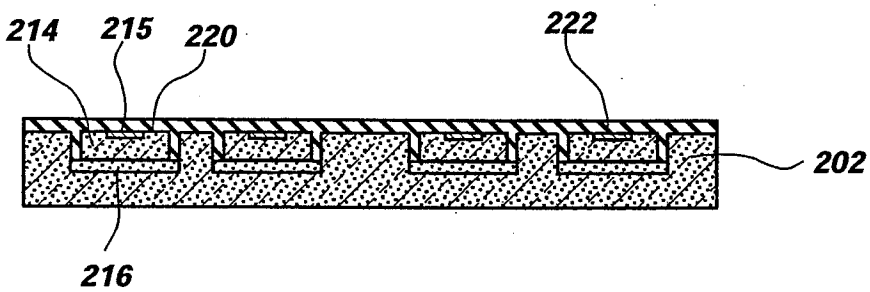



FIG. 2C

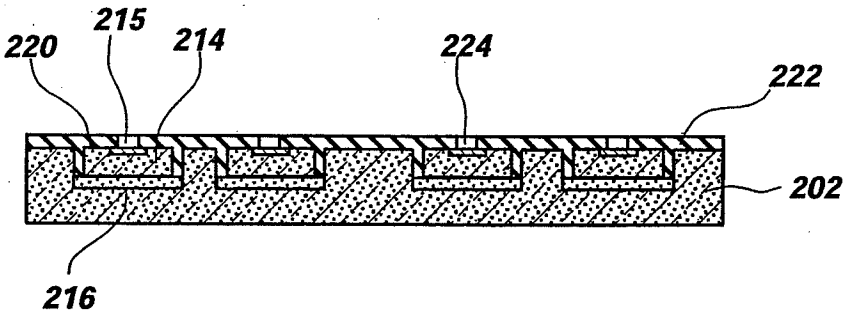



FIG. 2D

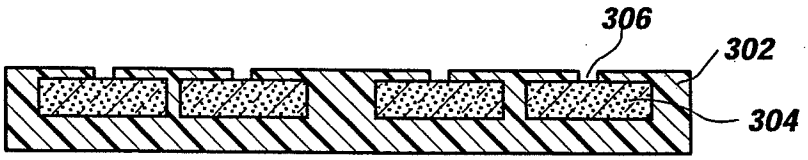



FIG. 3A

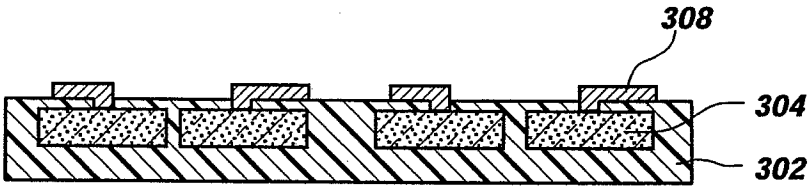



FIG. 3B

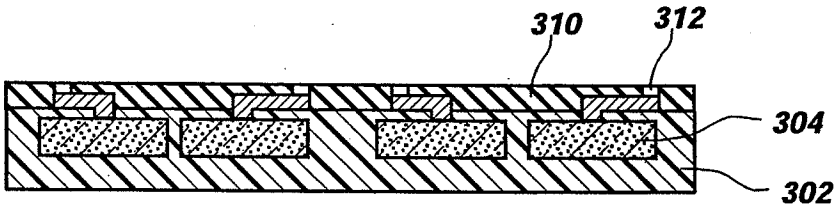



FIG. 3C

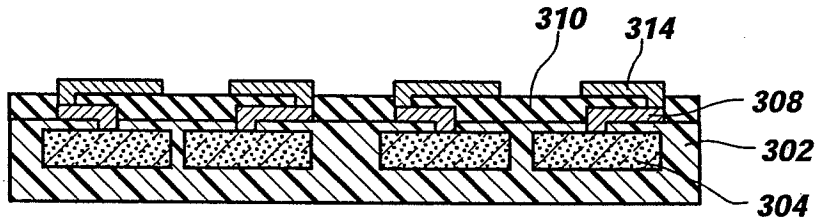



FIG. 3D

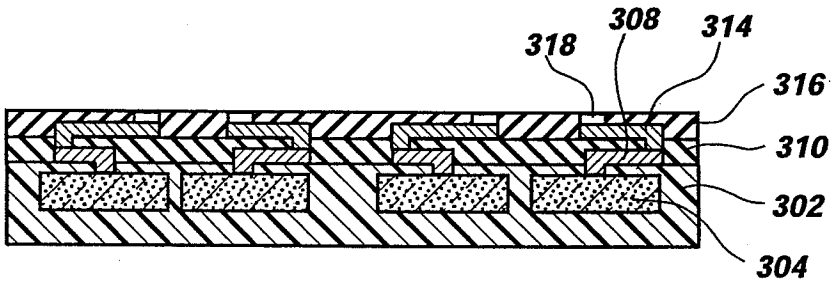



FIG. 3E

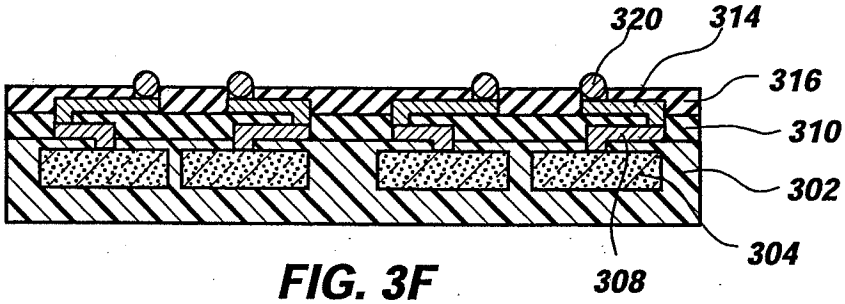



FIG. 3F

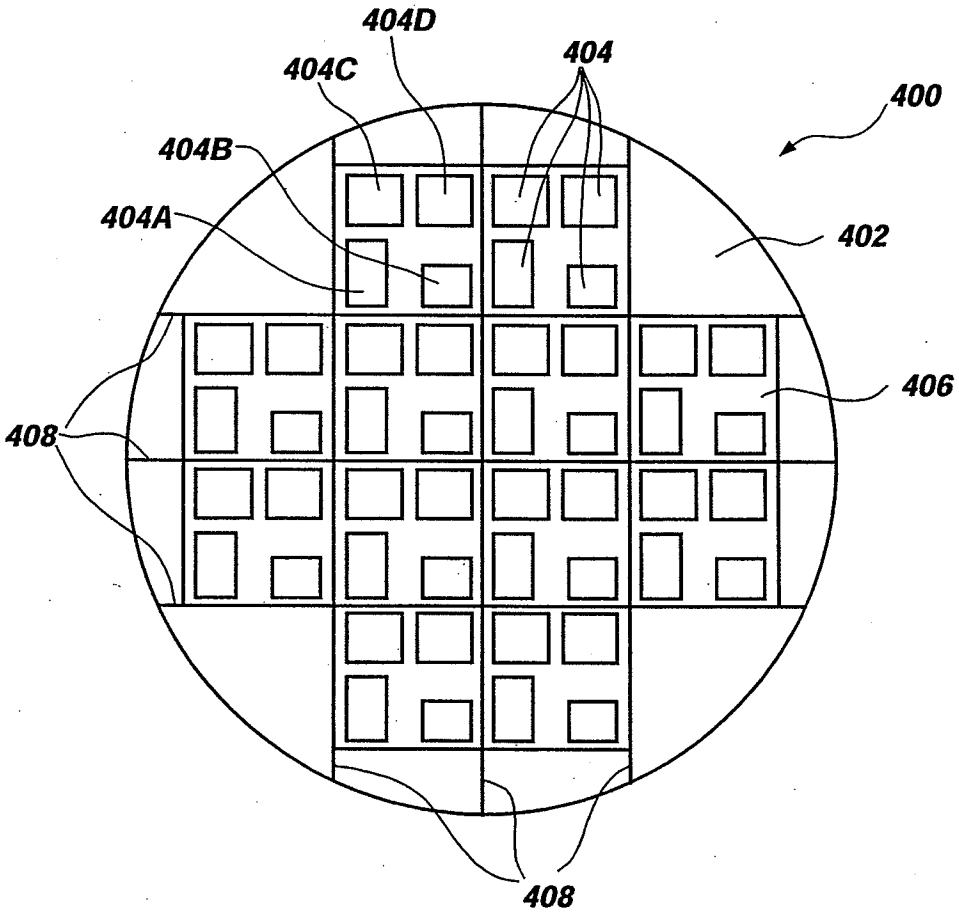



FIG. 4

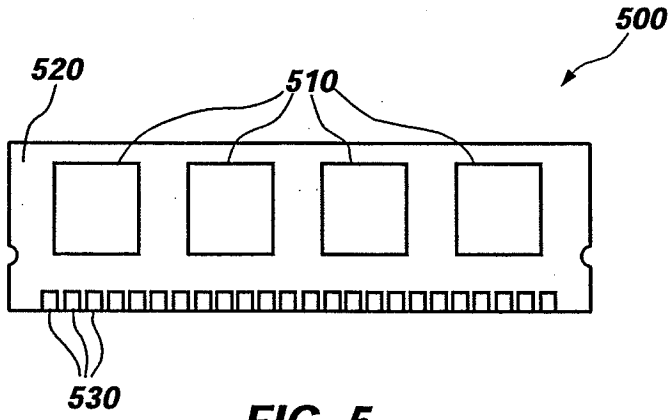



FIG. 5

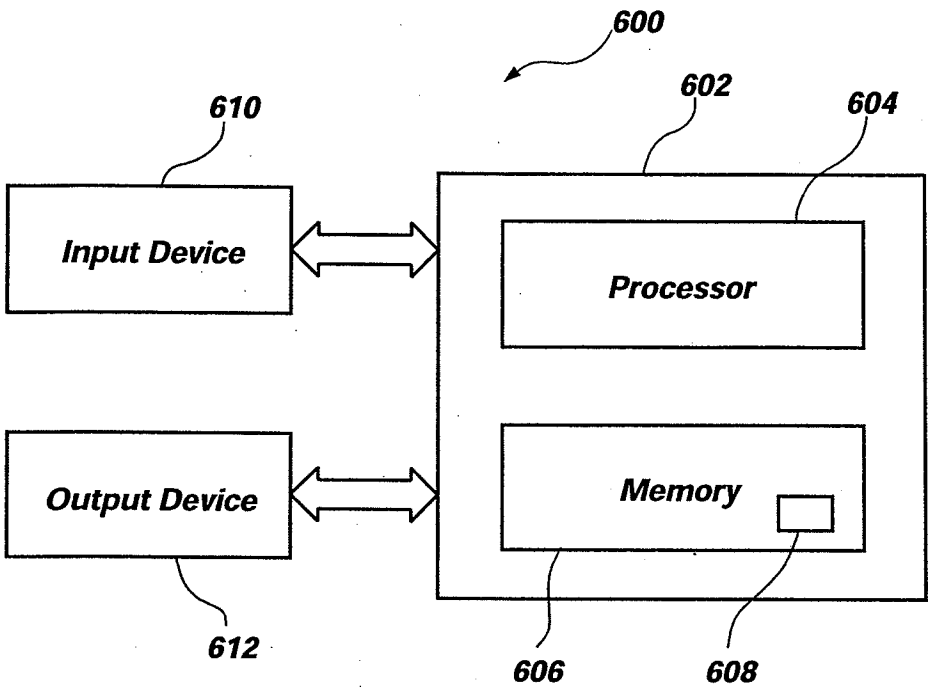

600
602
604
610
Input Device
Processor
Output Device
Memory
612
606
608

FIG. 6