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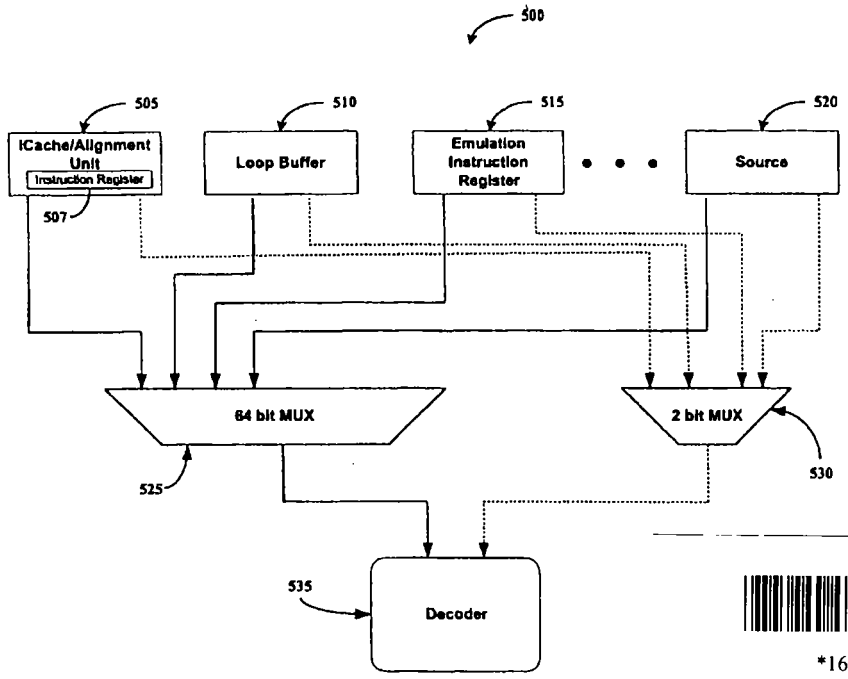
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ACTION

(54) Title: MULTIPLE SOURCES FOR INSTRUCTION DECODE



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(57) Abstract: In one embodiment, a processor contains multiple instruction sources and selects the proper source to provide an instruction to the decoder. Each of the instruction sources may provide an instruction to a multiplexer. The instruction sources also provide a signal to a second multiplexer indicative of the size of the instruction.

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