

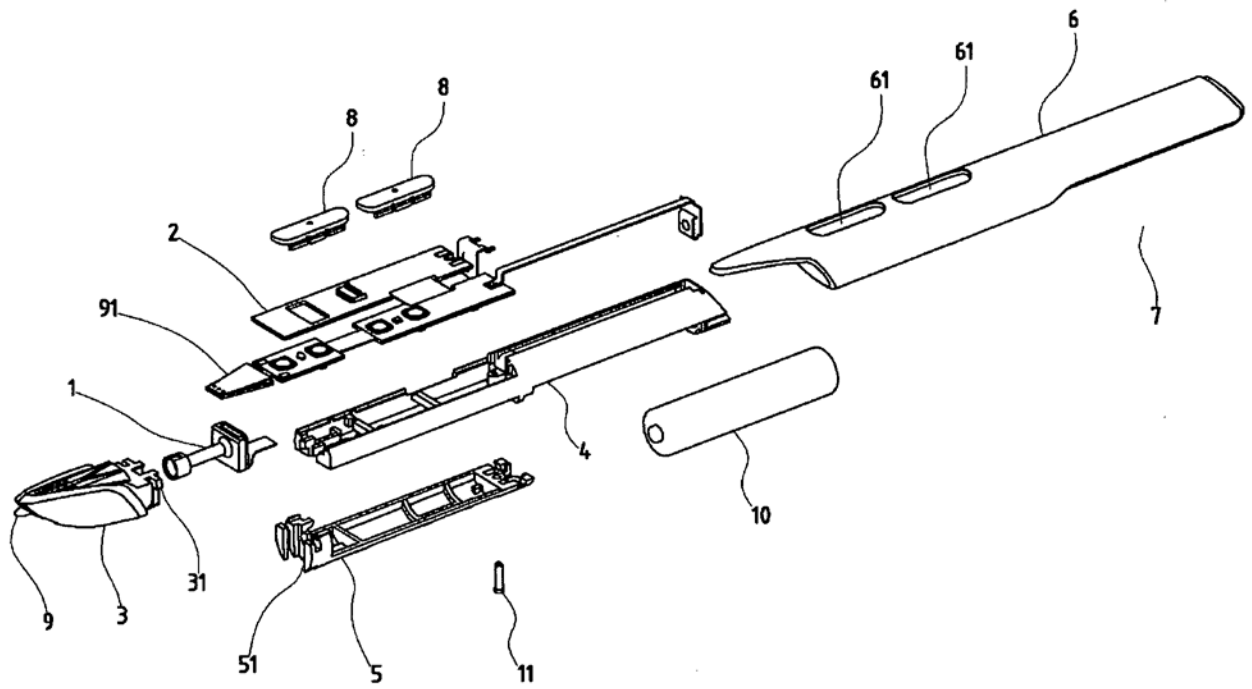


Fig. 1

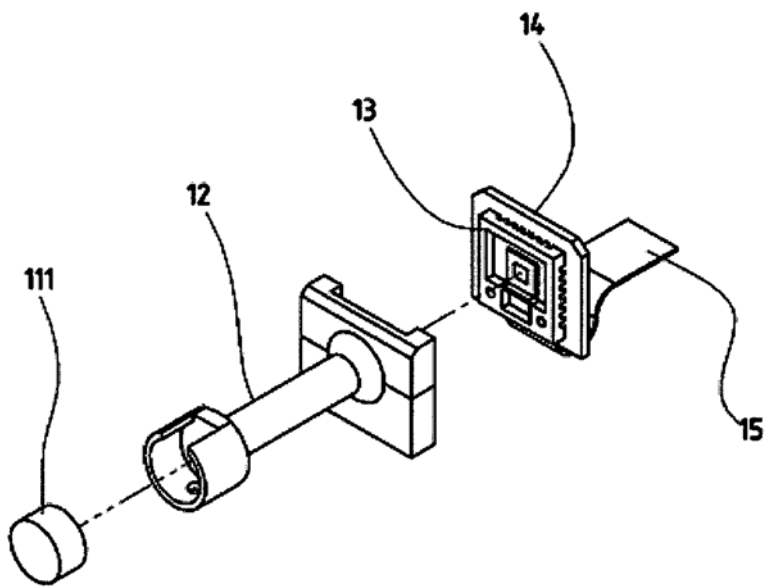


Fig. 2

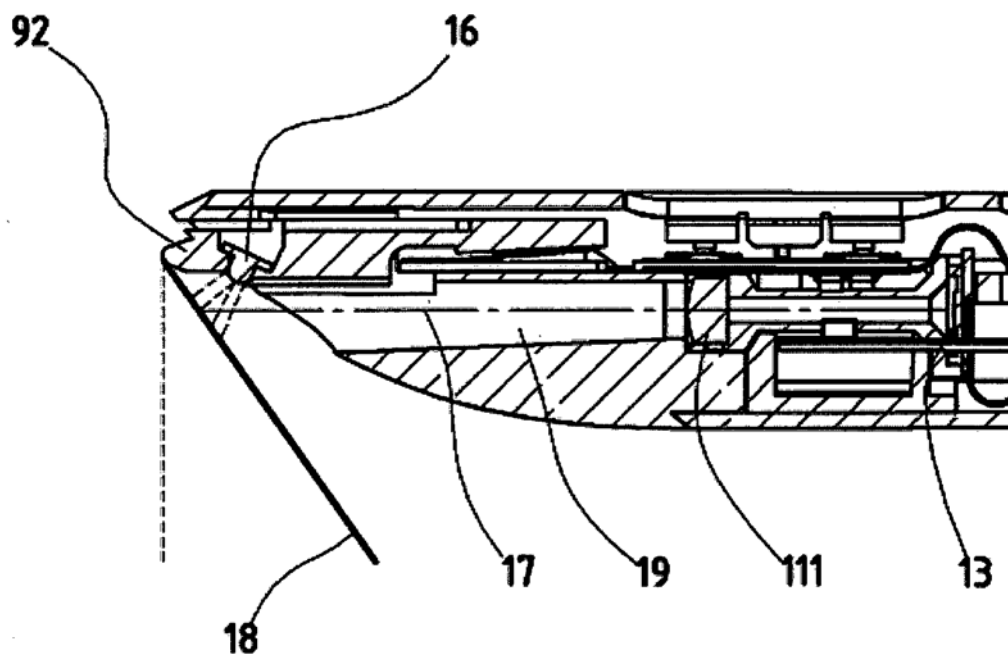


Fig. 3

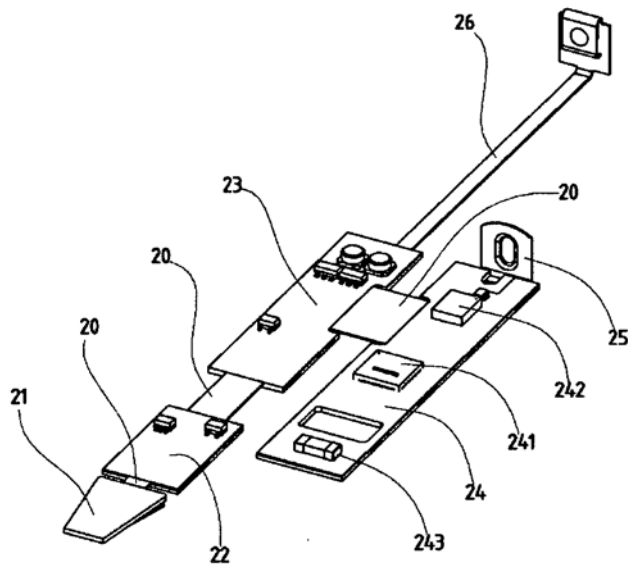


Fig. 4a

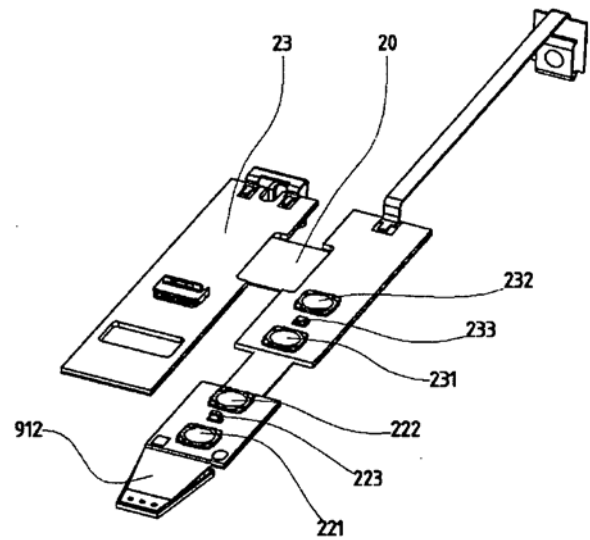


Fig. 4b

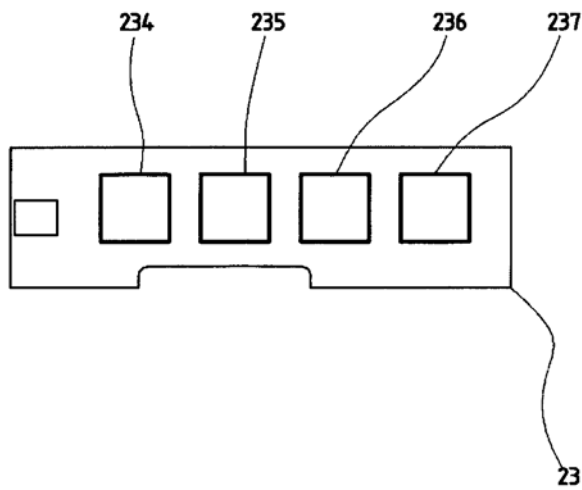


Fig. 4c

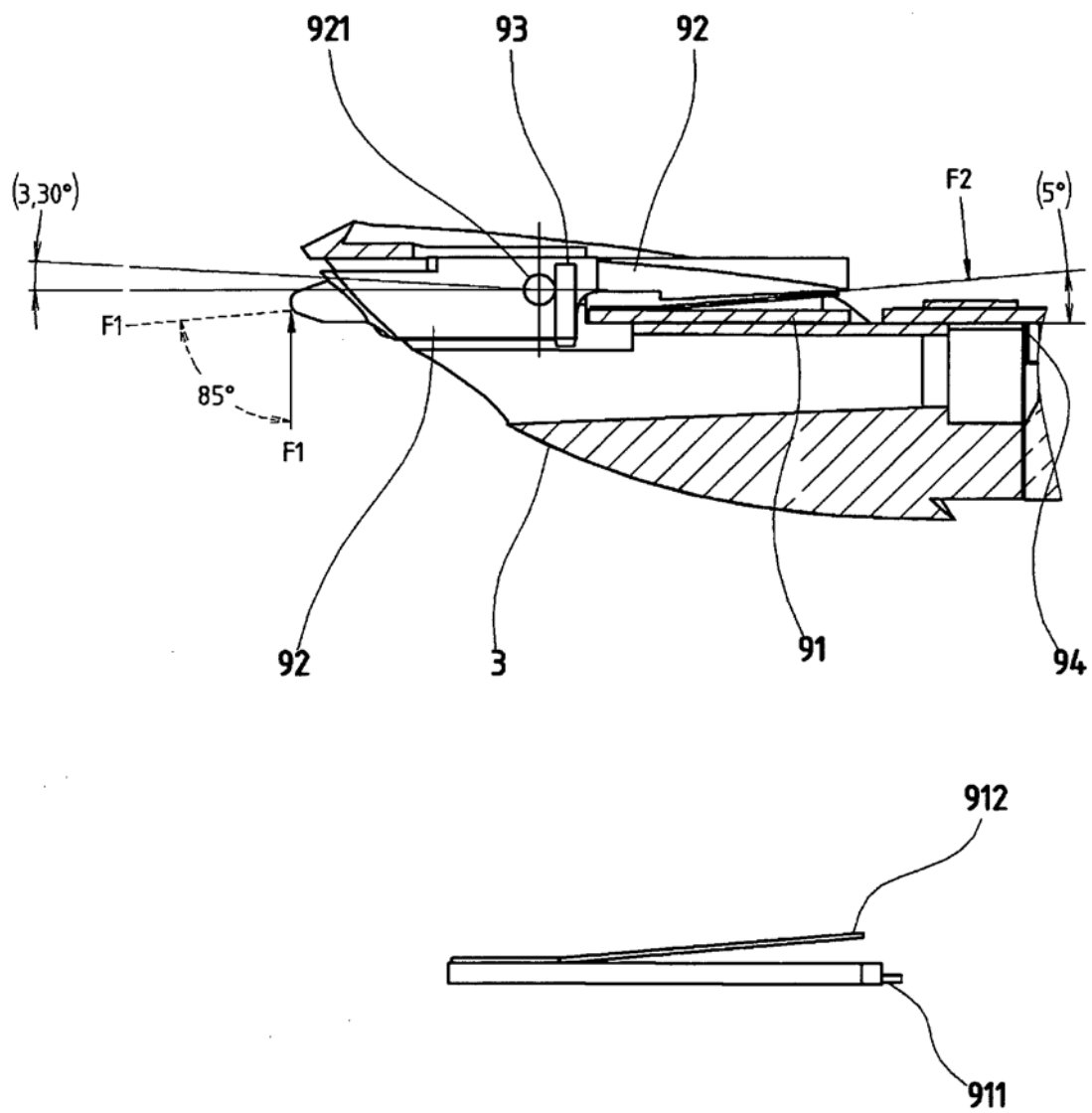


Fig. 5

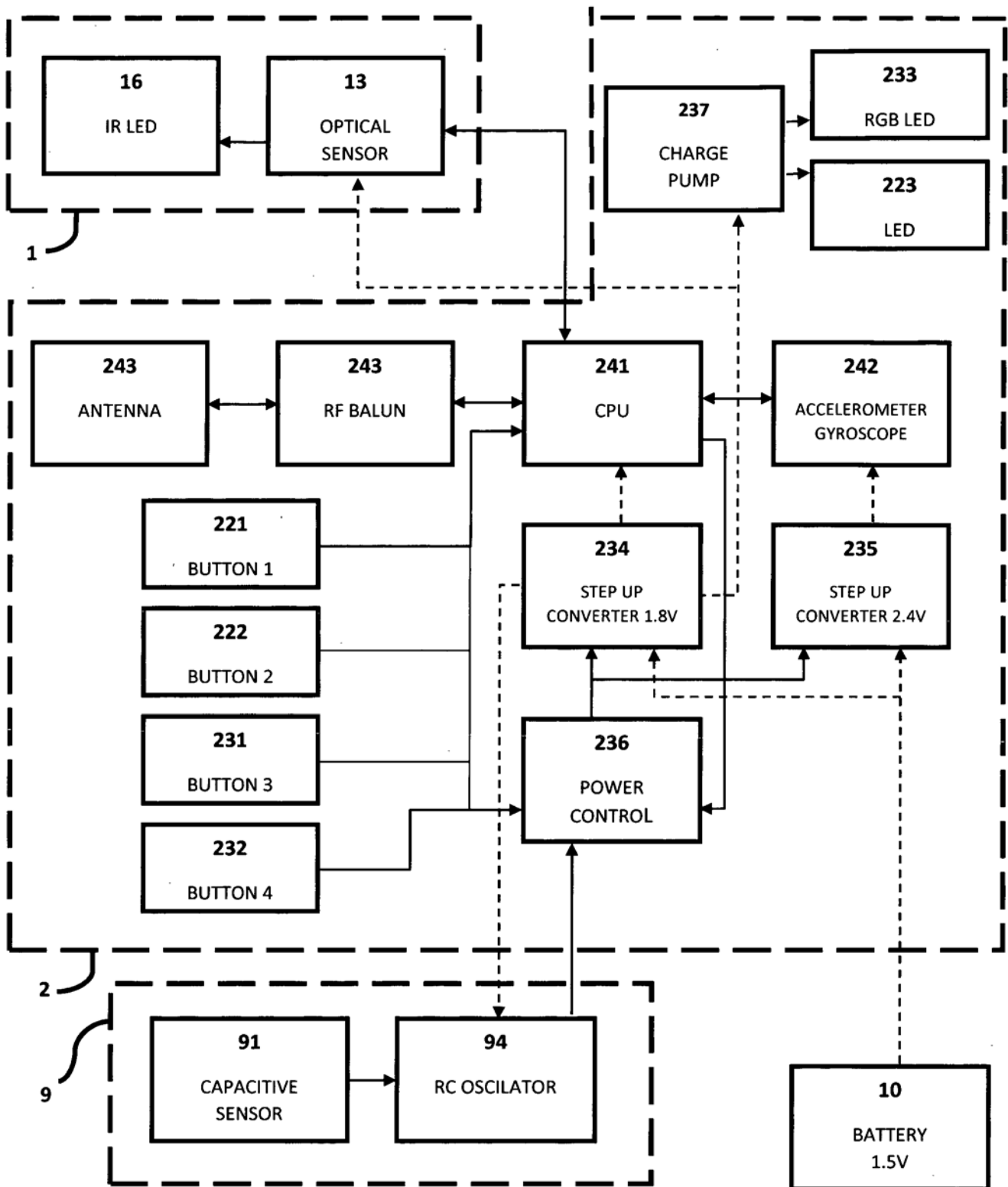


Fig. 6

(dashed line – power paths, solid line - signal paths)

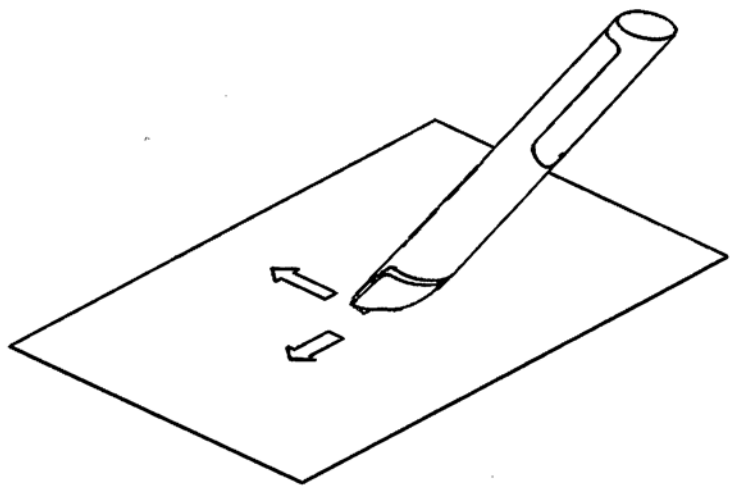
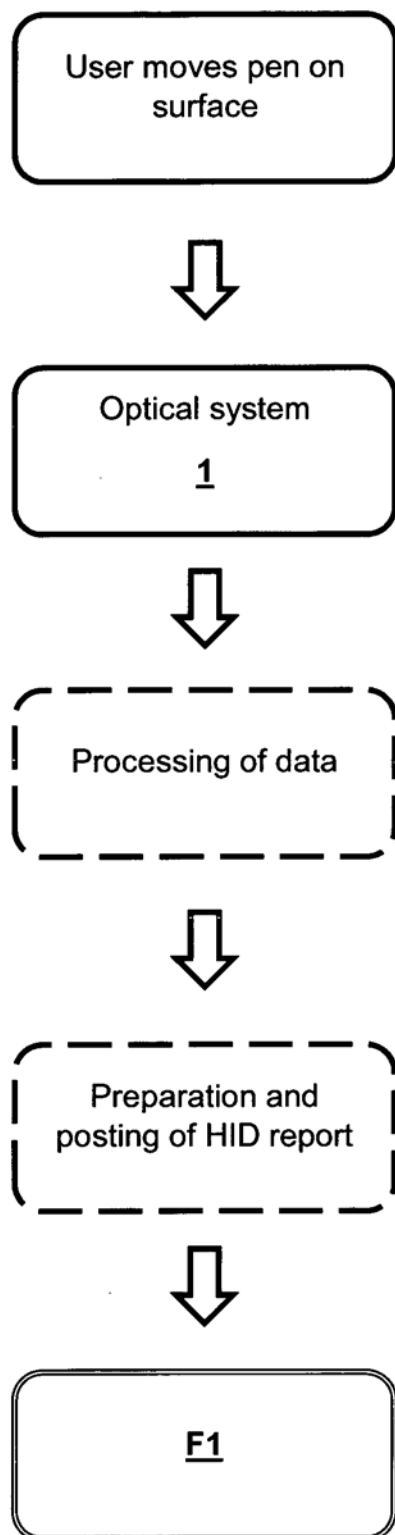


Fig.7

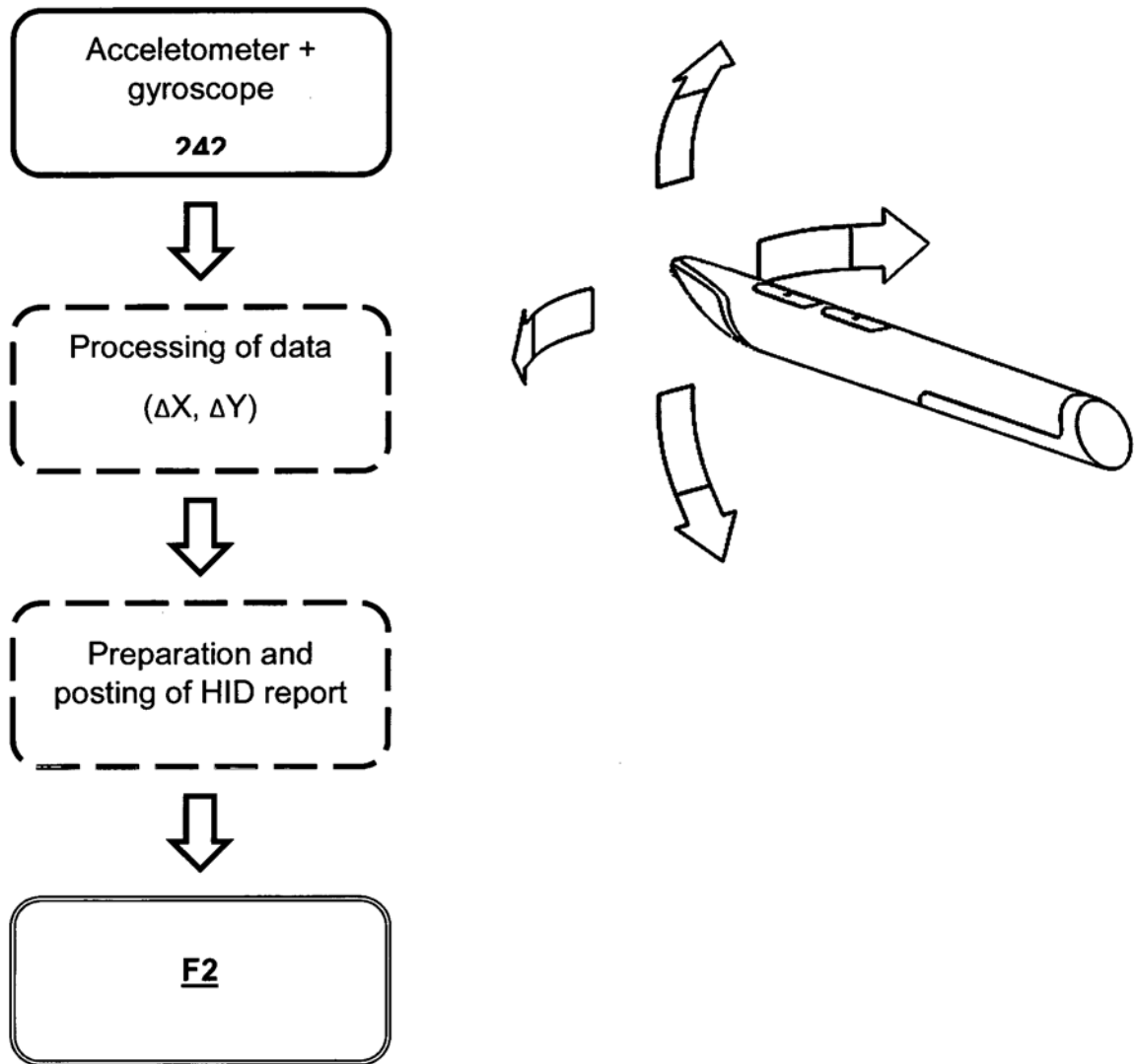


Fig. 8

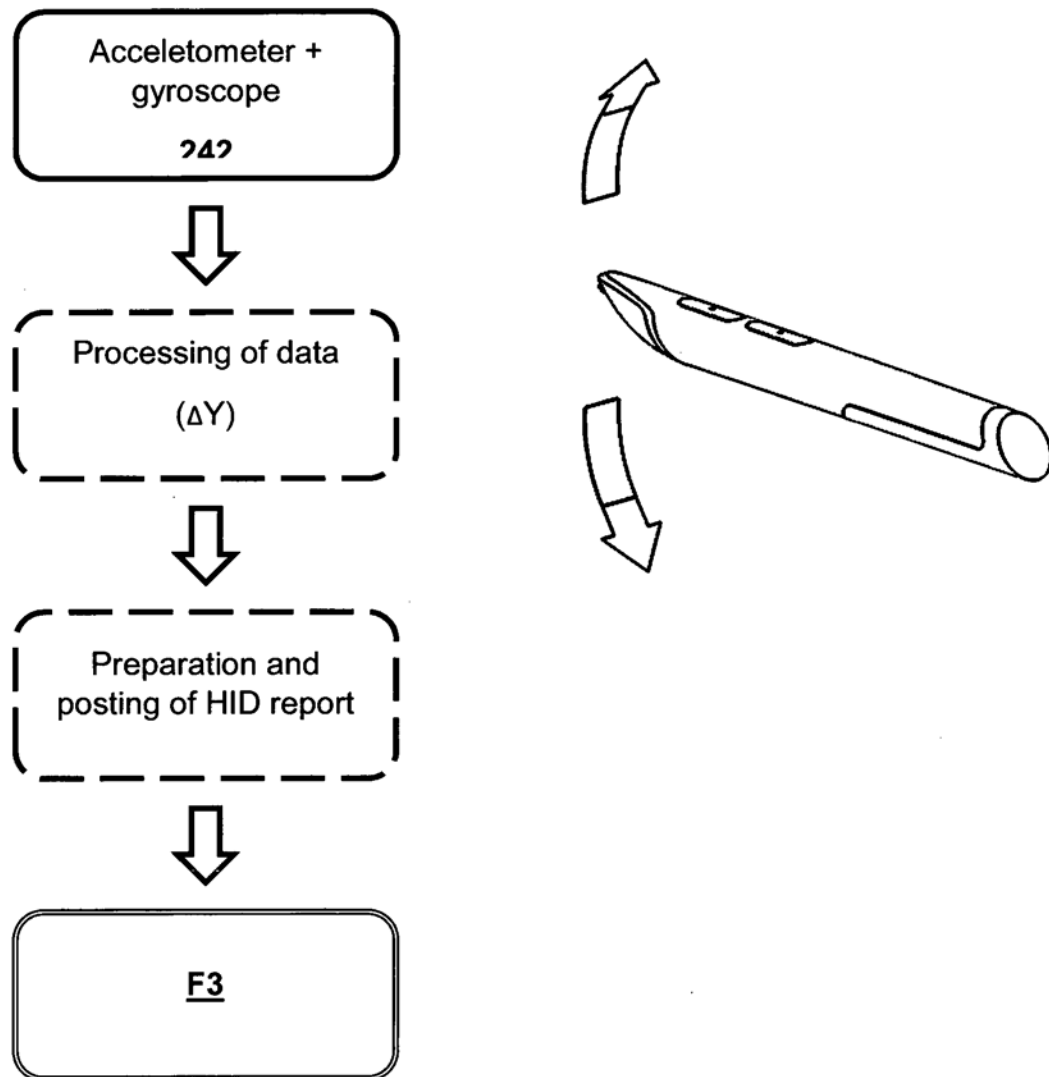


Fig. 9

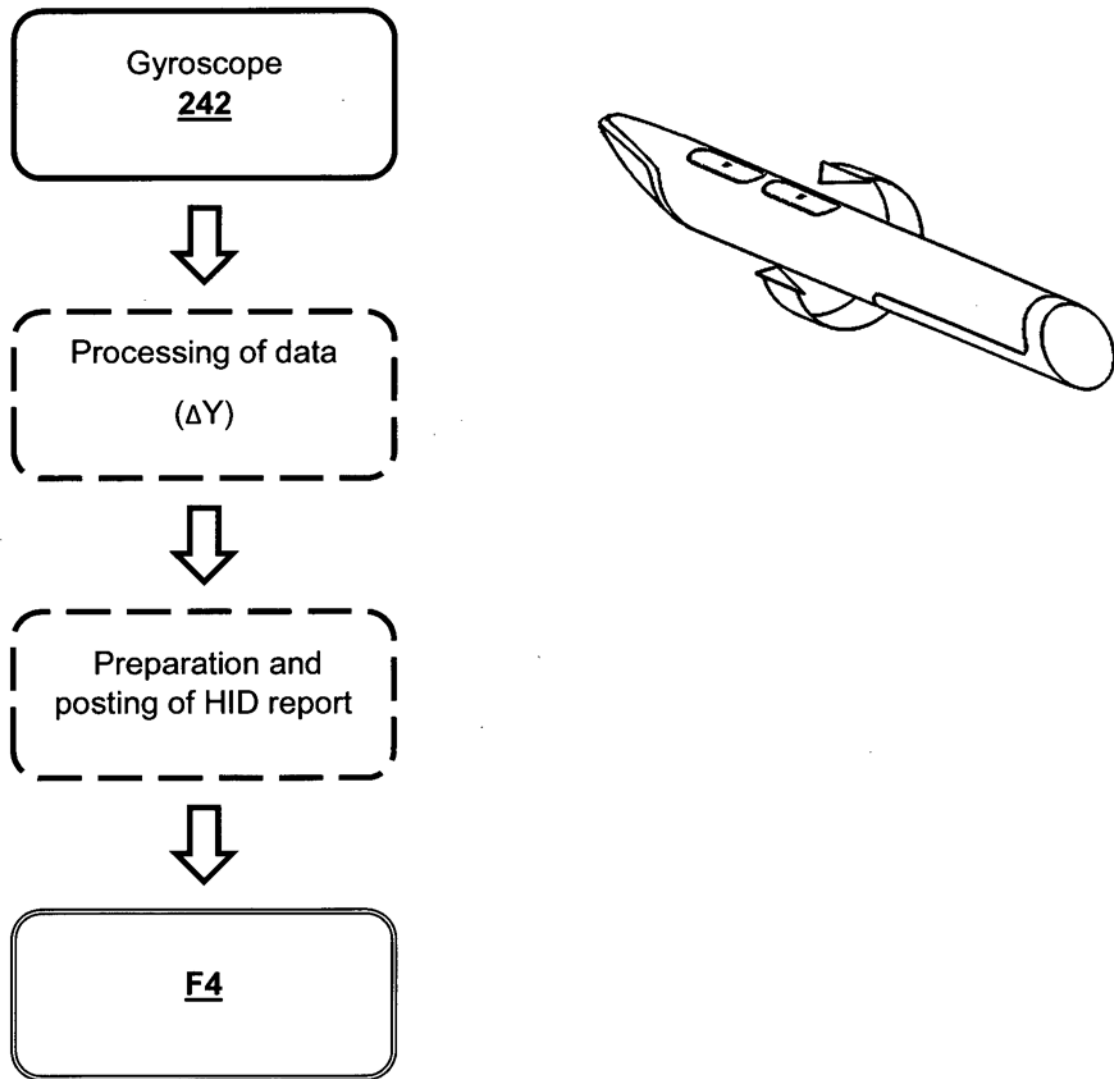


Fig. 10

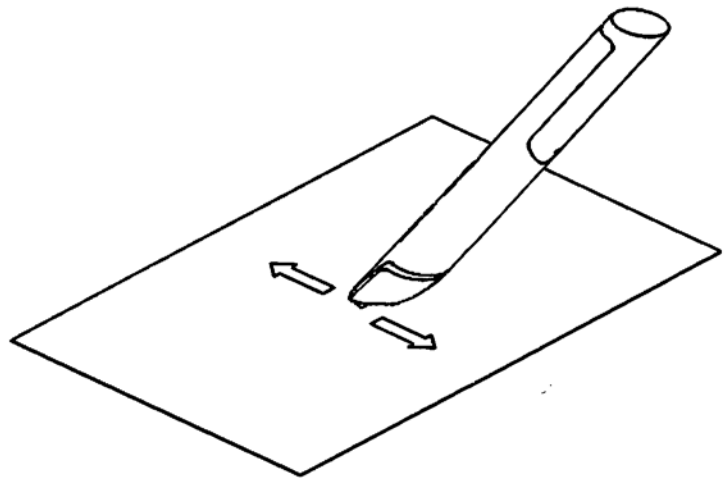
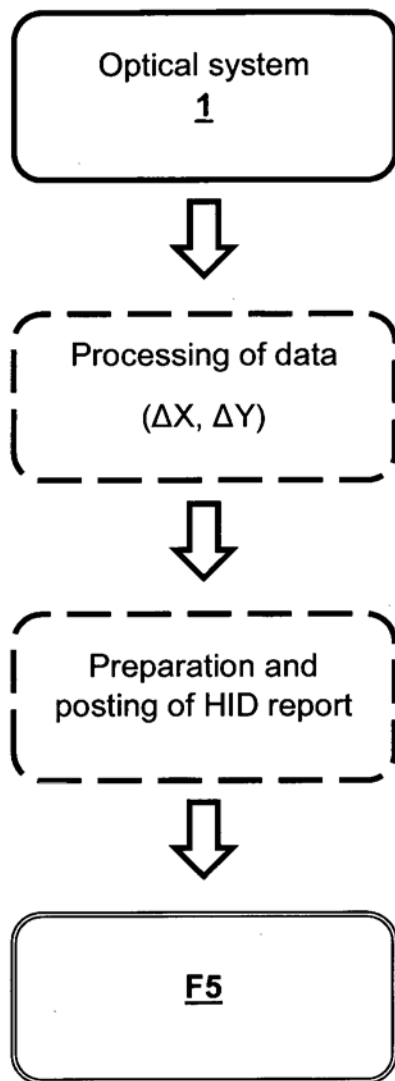


Fig. 11

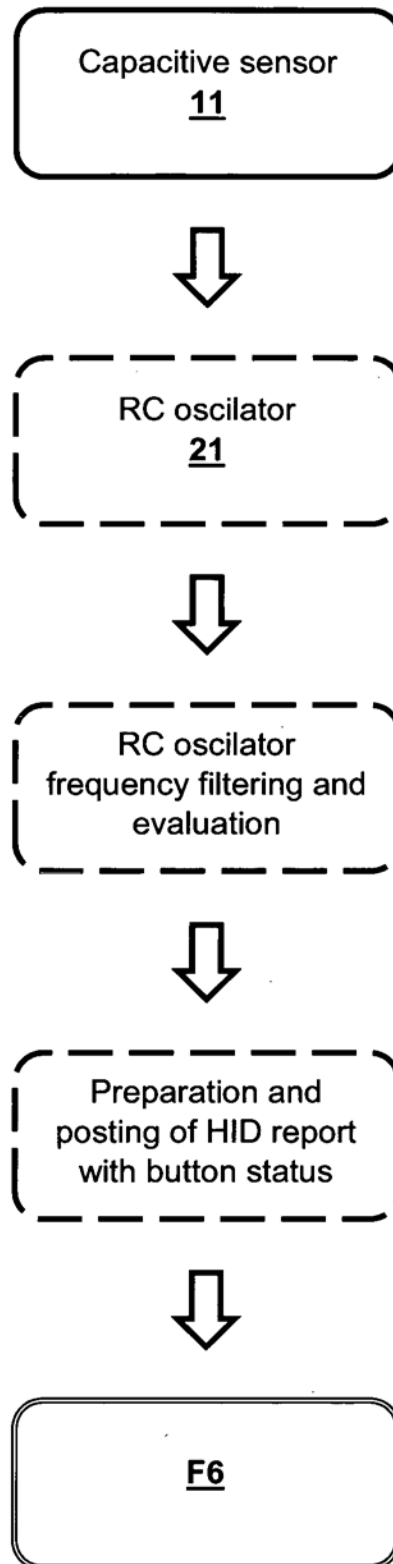


Fig. 12