ABSTRACT OF THE DISCLOSURE

In order to prevent a horizontal stripe and a flicker from taking place due to a gain difference between two circuit systems (channels), the solid state imager of the present invention includes a pilot signal generating means provided within a solid state image pickup element for allowing two output units to generate pilot signals of the same level and a gain correcting circuit provided outside of said solid state image pickup element for comparing levels of the pilot signals passed through the two circuit systems and correcting a gain of one circuit system if the two pilot signals have a level difference therebetween. Further, in a solid state imager of the present invention, a pilot signal for detecting gains of two circuit systems which process signals from two output units of a solid state image pickup element is inserted into a video signal without modifying the rule with respect to the signal timing so that signal charges are supplied from a pilot signal generating means to respective horizontal registers so as to output the pilot signal during the dummy bit output period.

This Sheet Has Been Replaced