

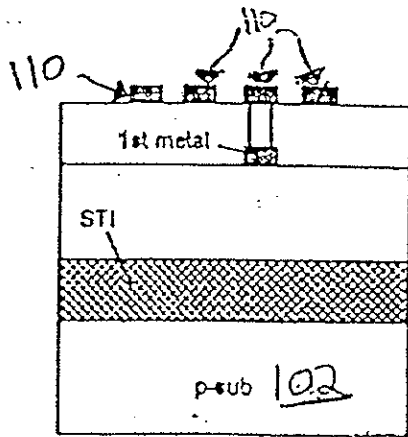


Fig. 1A
(Prior Art)

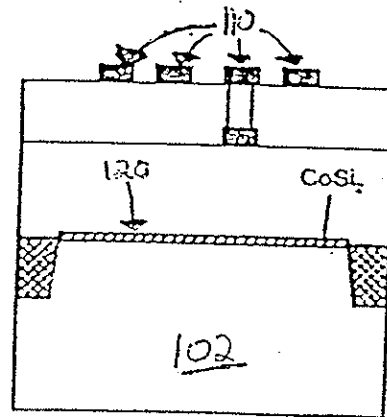


Fig. 1B
(Prior Art)

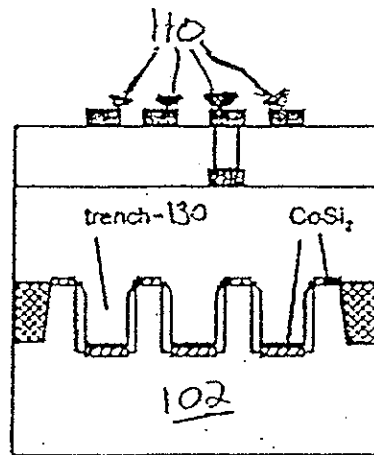


Fig. 1C
(Prior Art)

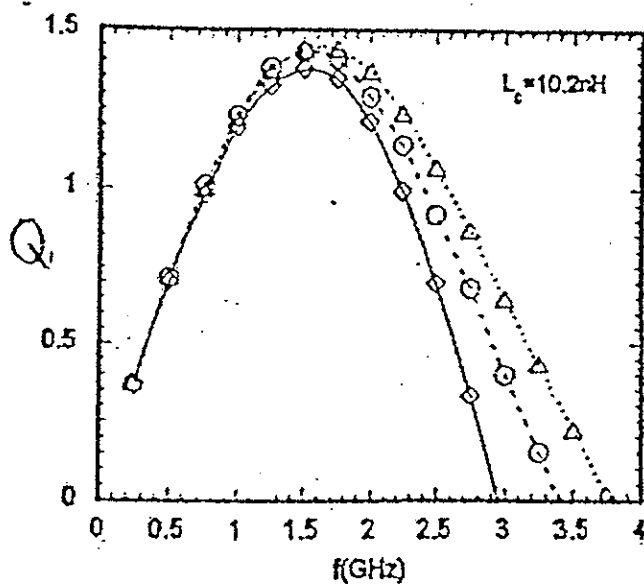


Fig. 2
(Prior Art)

Δ - unshielded
◇ - plate shield
O - trench shield

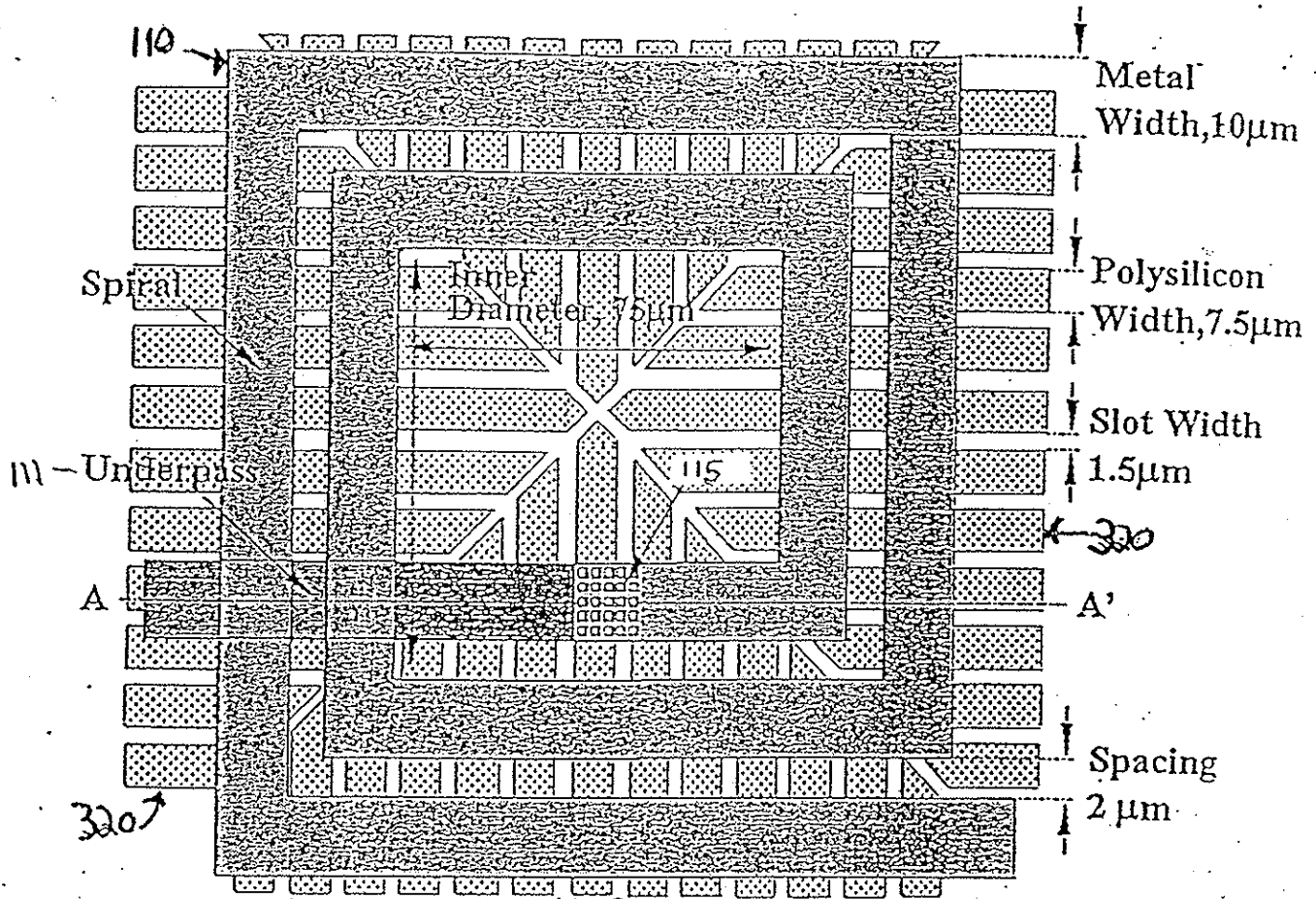


Fig. 3A (Prior Art)

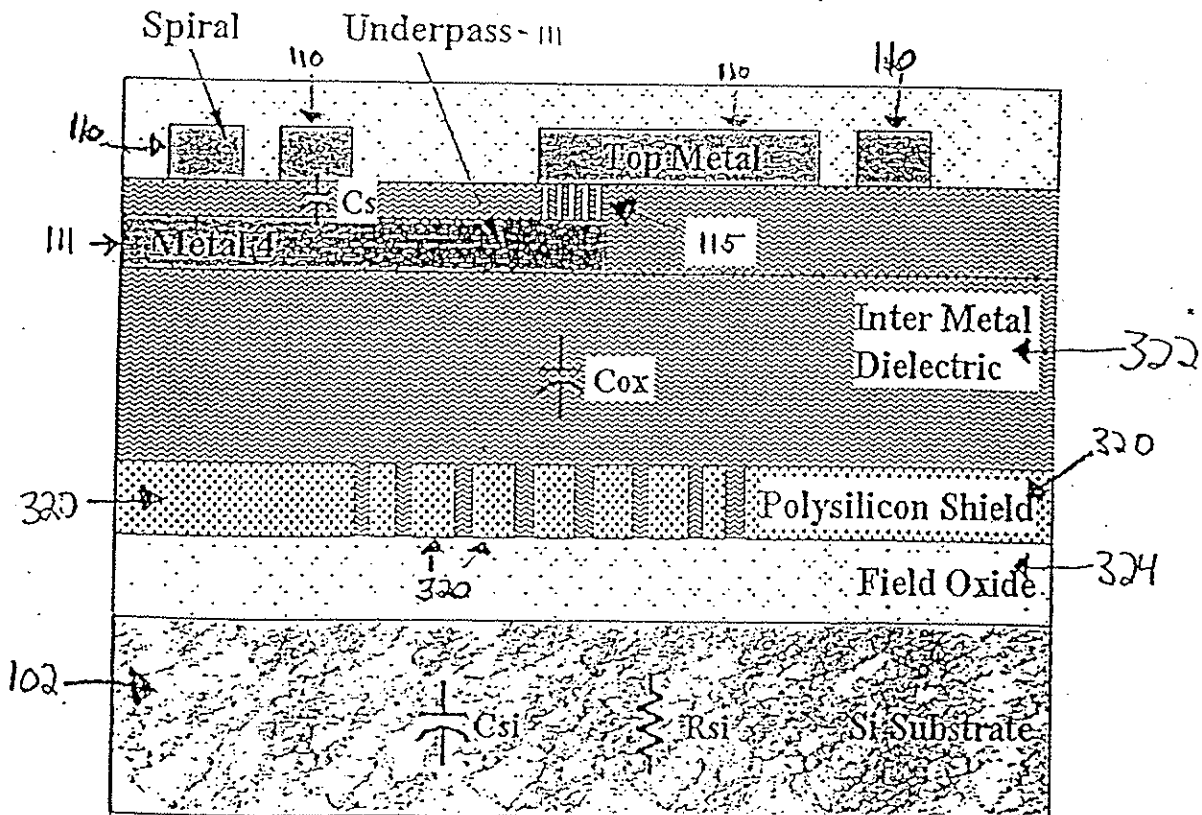
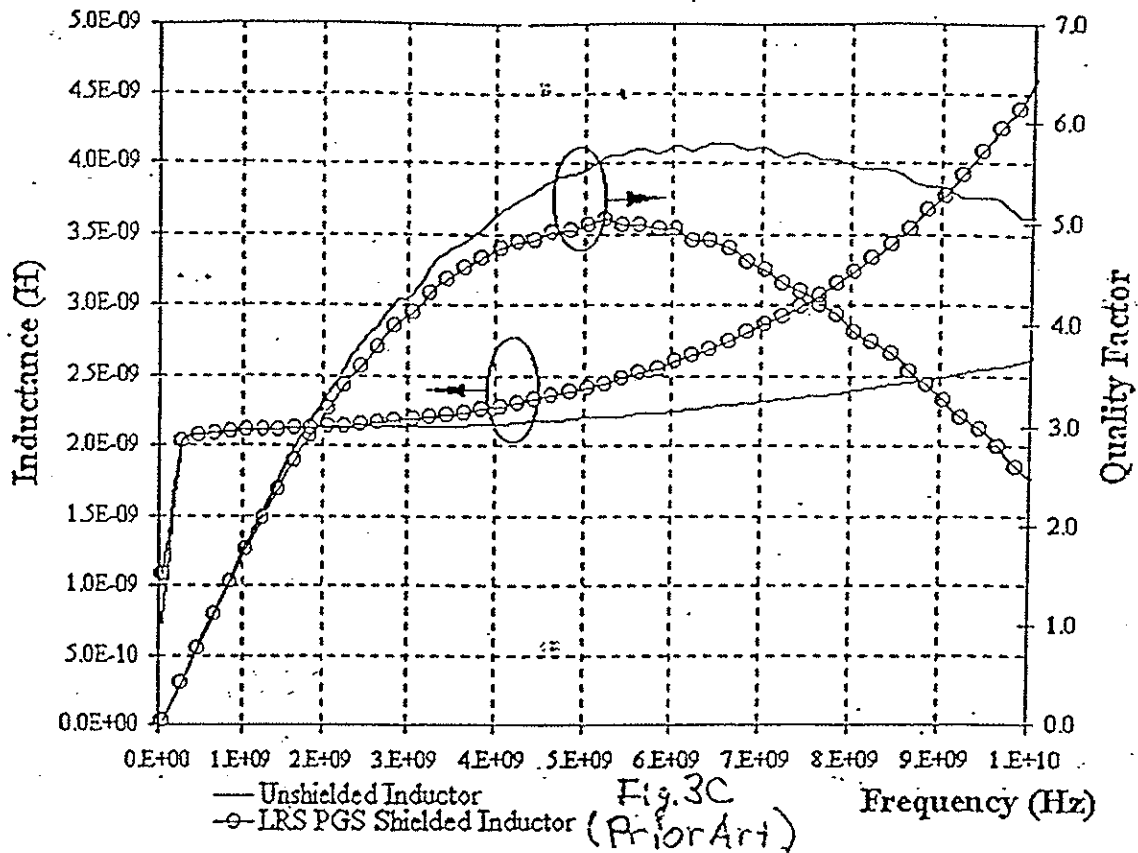


Fig. 3B
(Prior Art)



Quality Factor and Inductance Plots of Unshielded Inductor and Inductor with Low Sheet Resistance Patterned Polysilicon Shield.

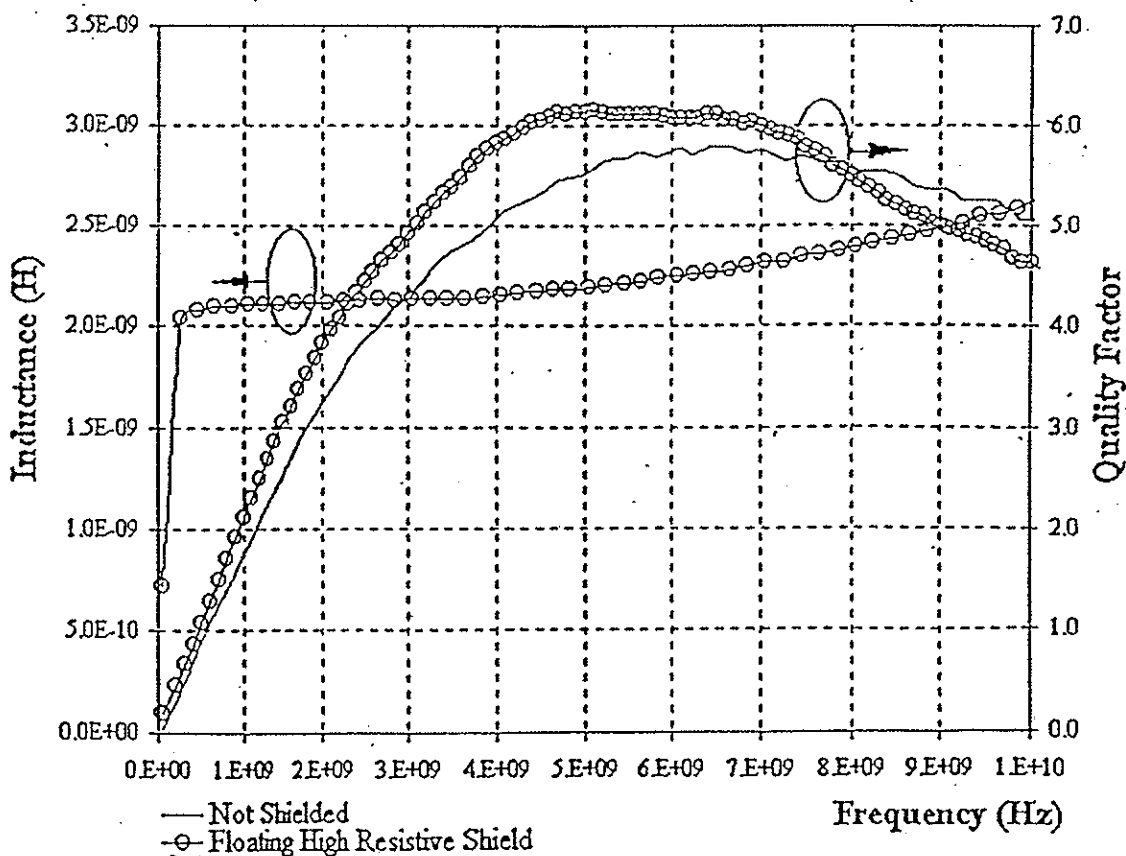
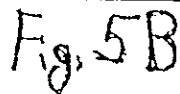
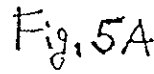
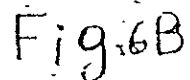


Fig. 4. Quality Factor and Inductance Plots of Unshielded Inductor and Inductor with High Sheet Resistance Floating Polysilicon Shield.





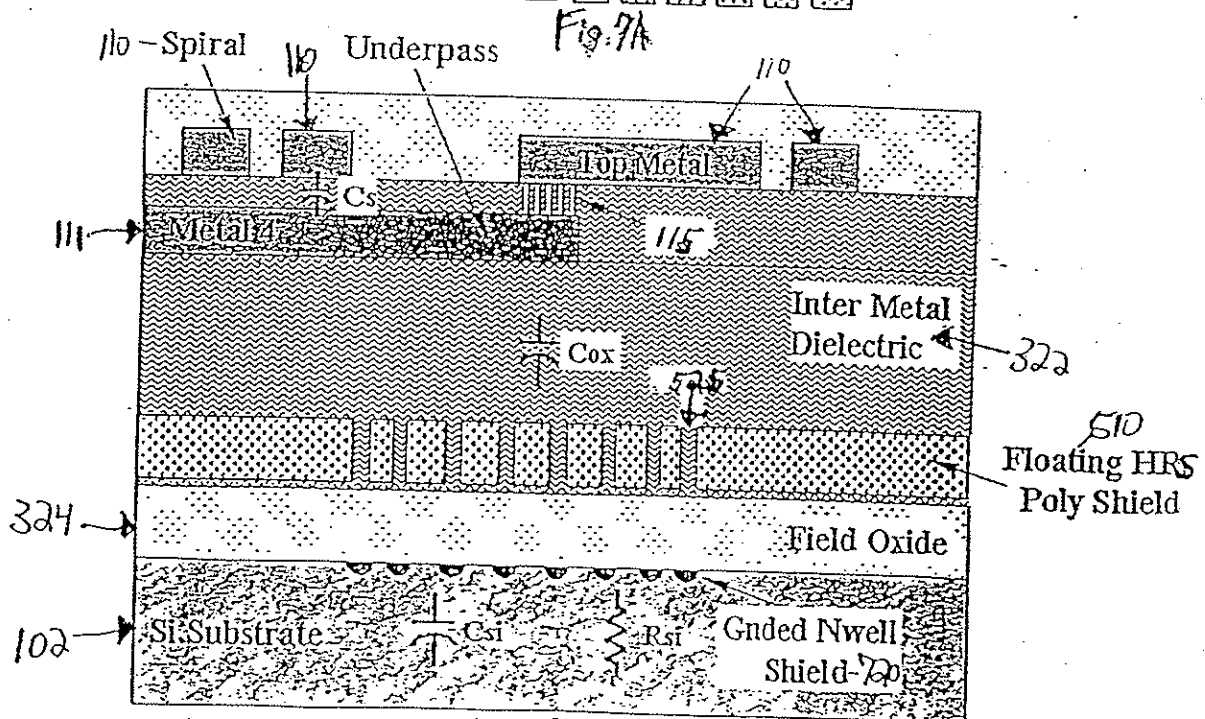
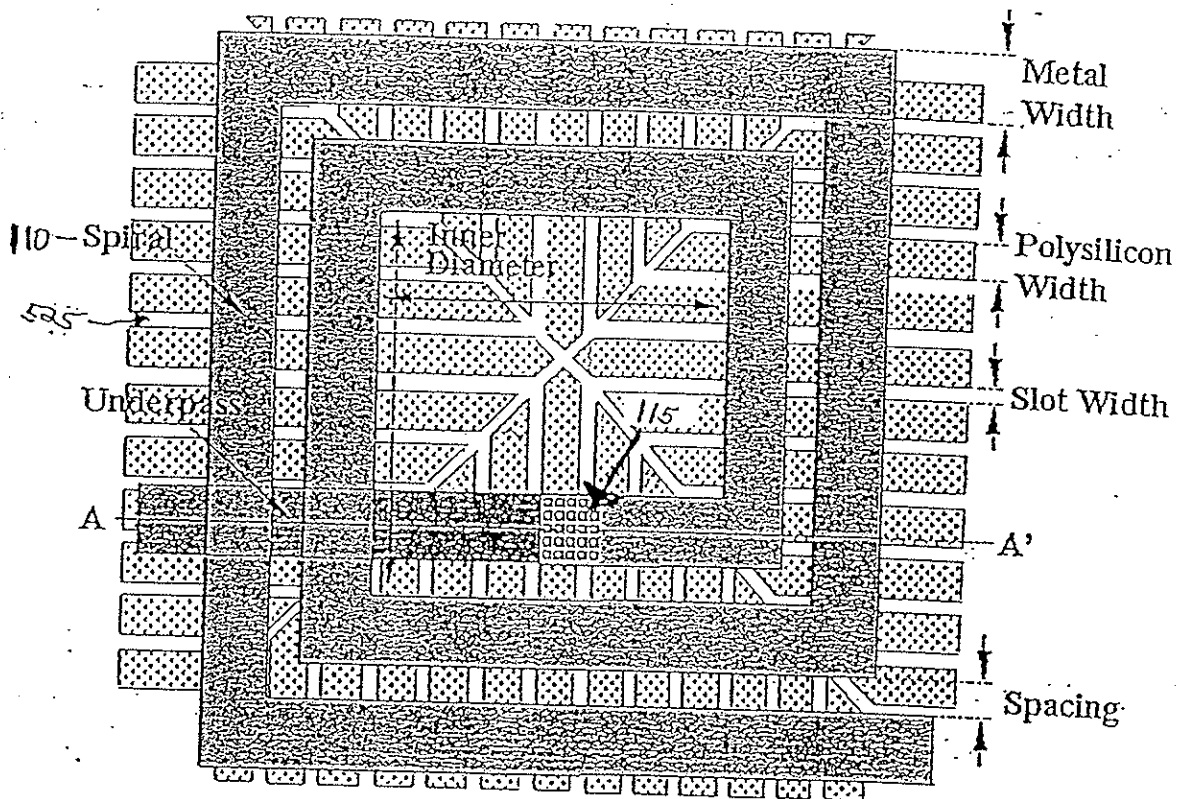


Fig. 7B

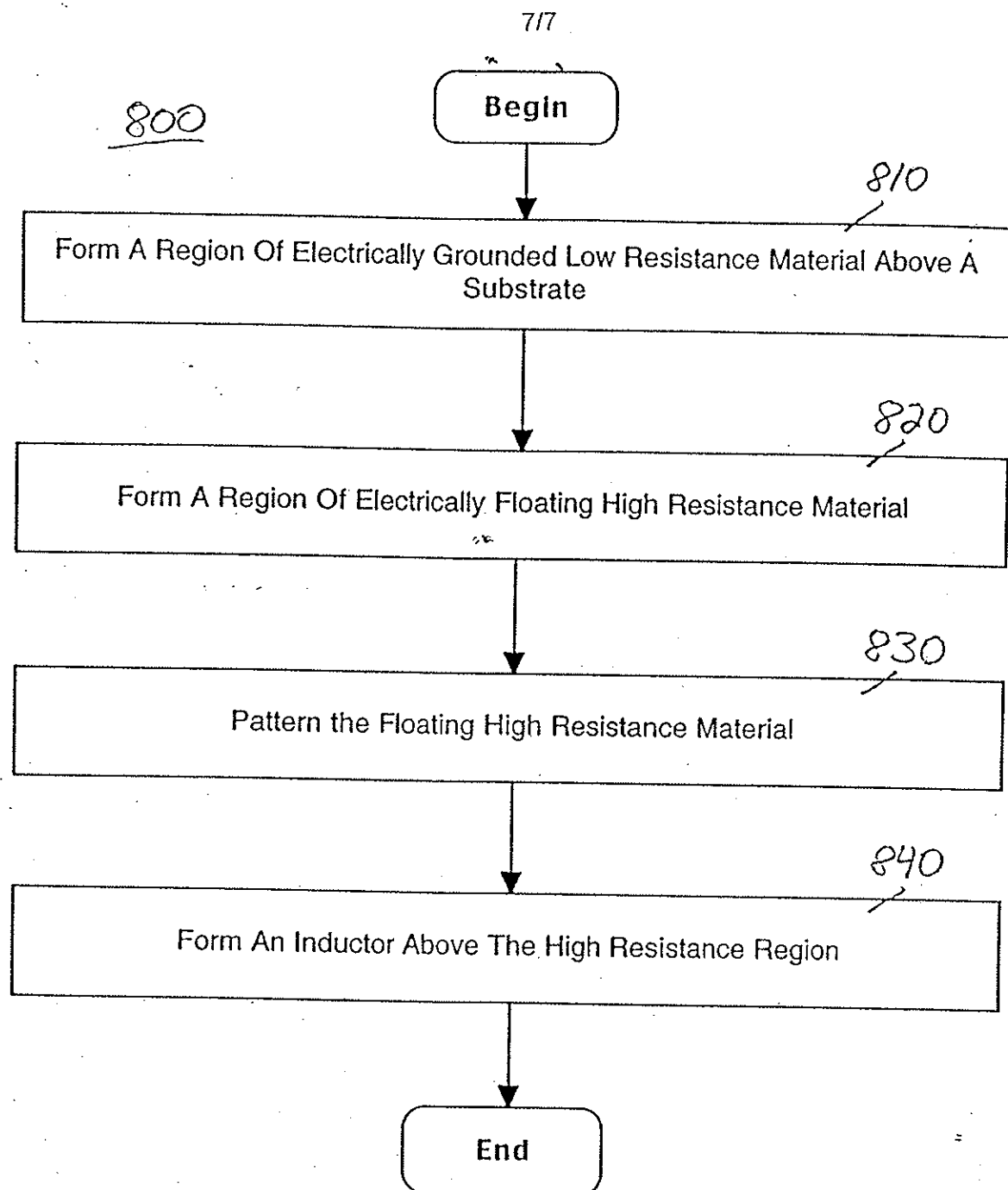


FIG. 8