

FIG. 1

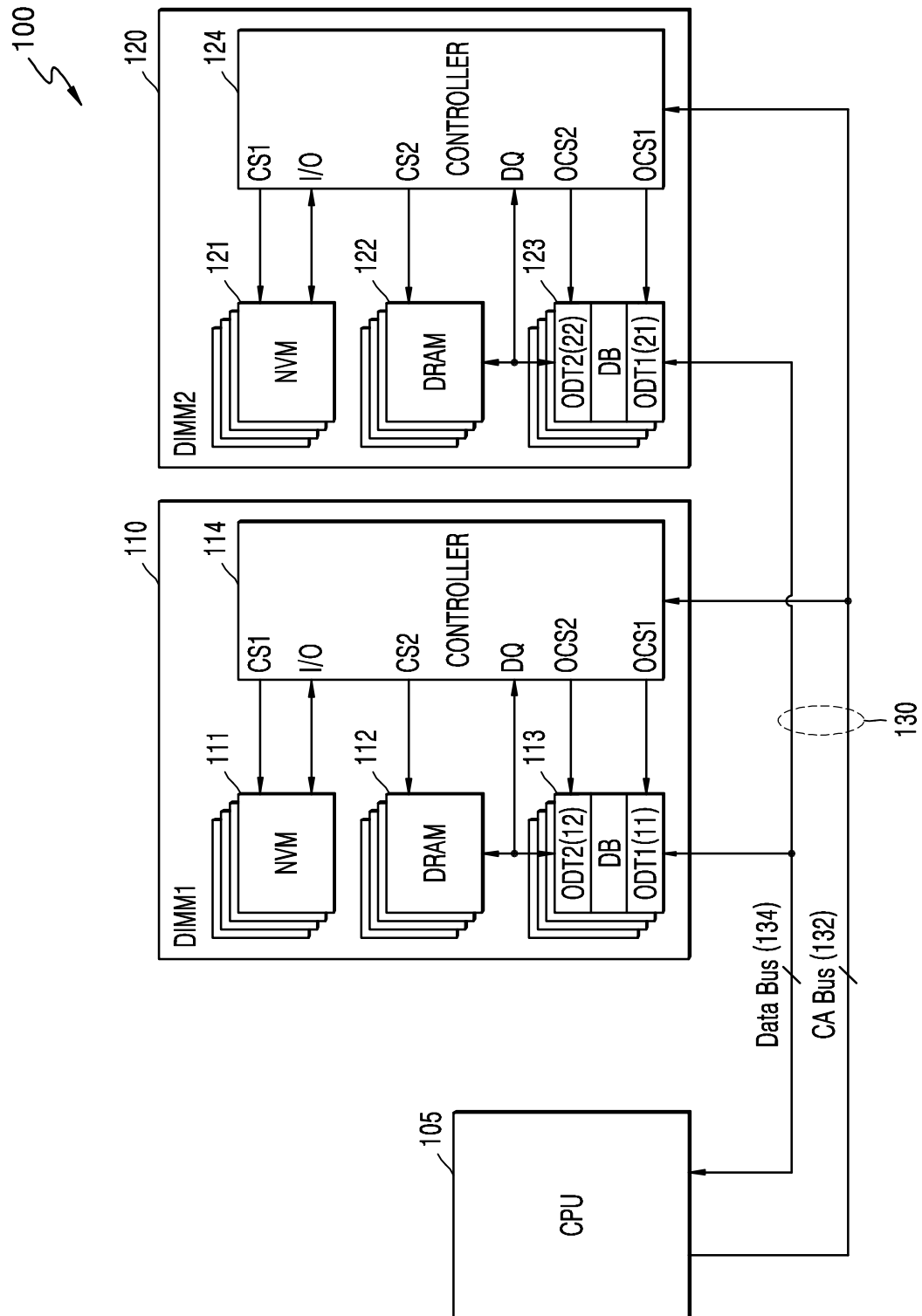


FIG. 2

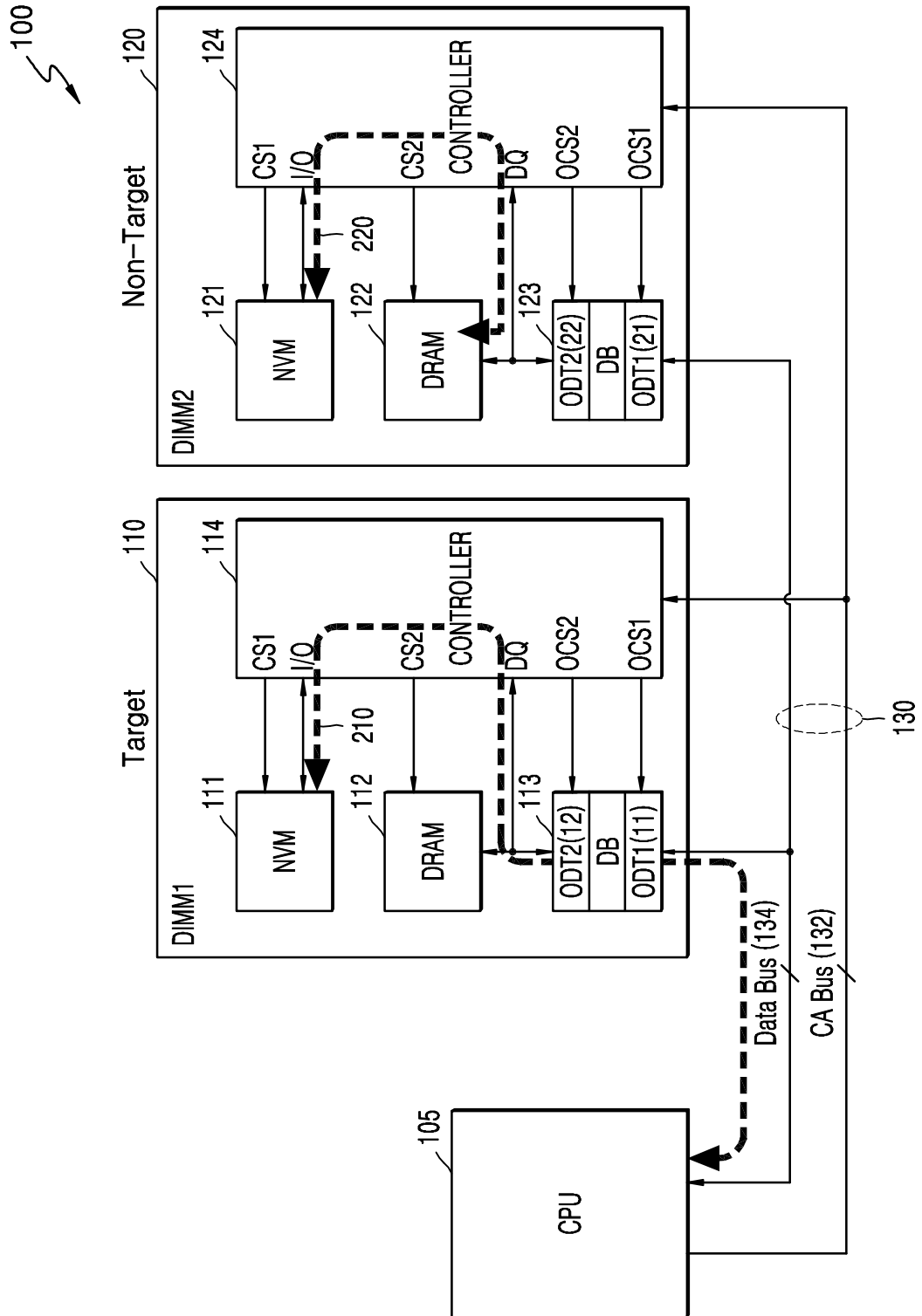


FIG. 3

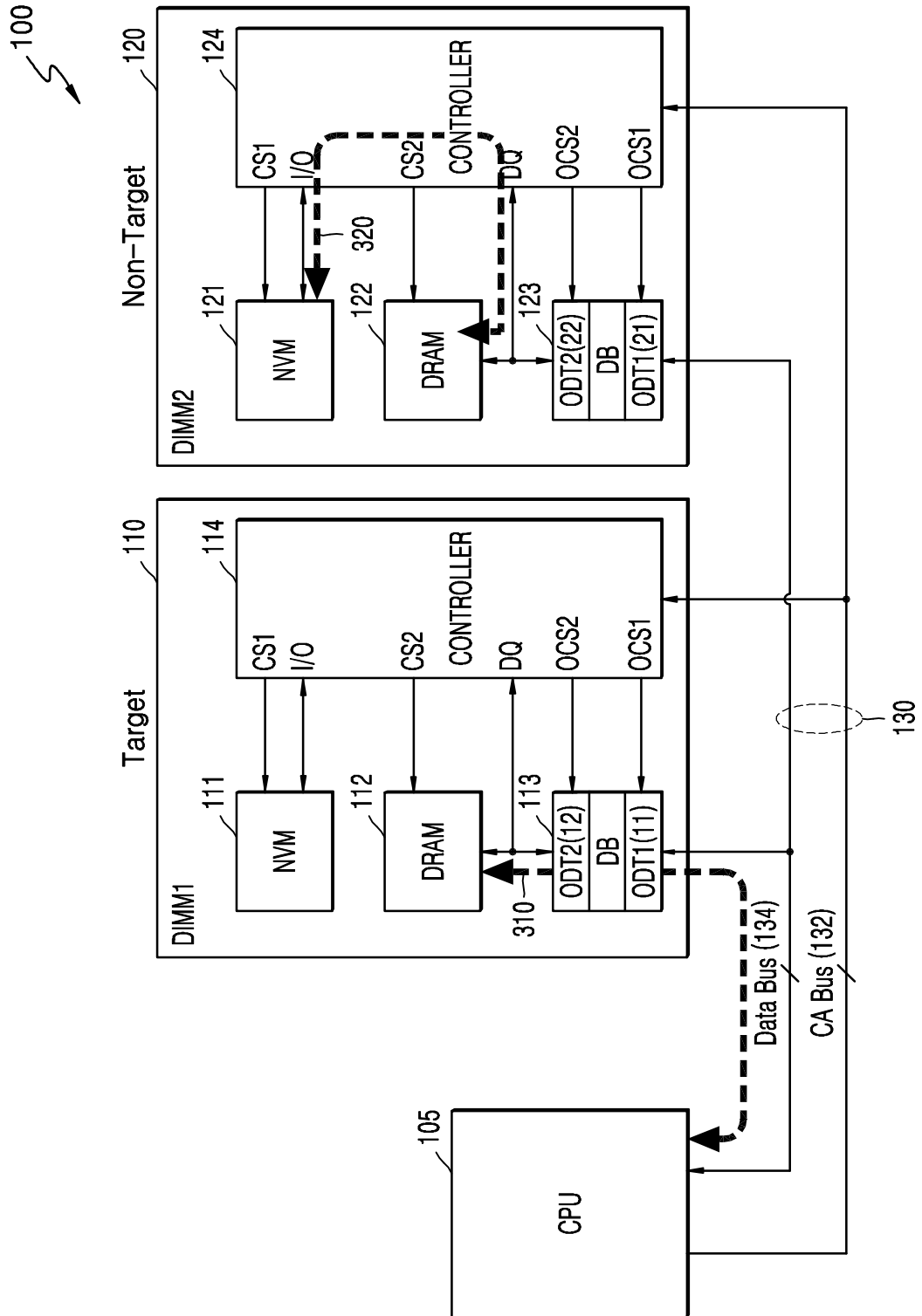


FIG. 4

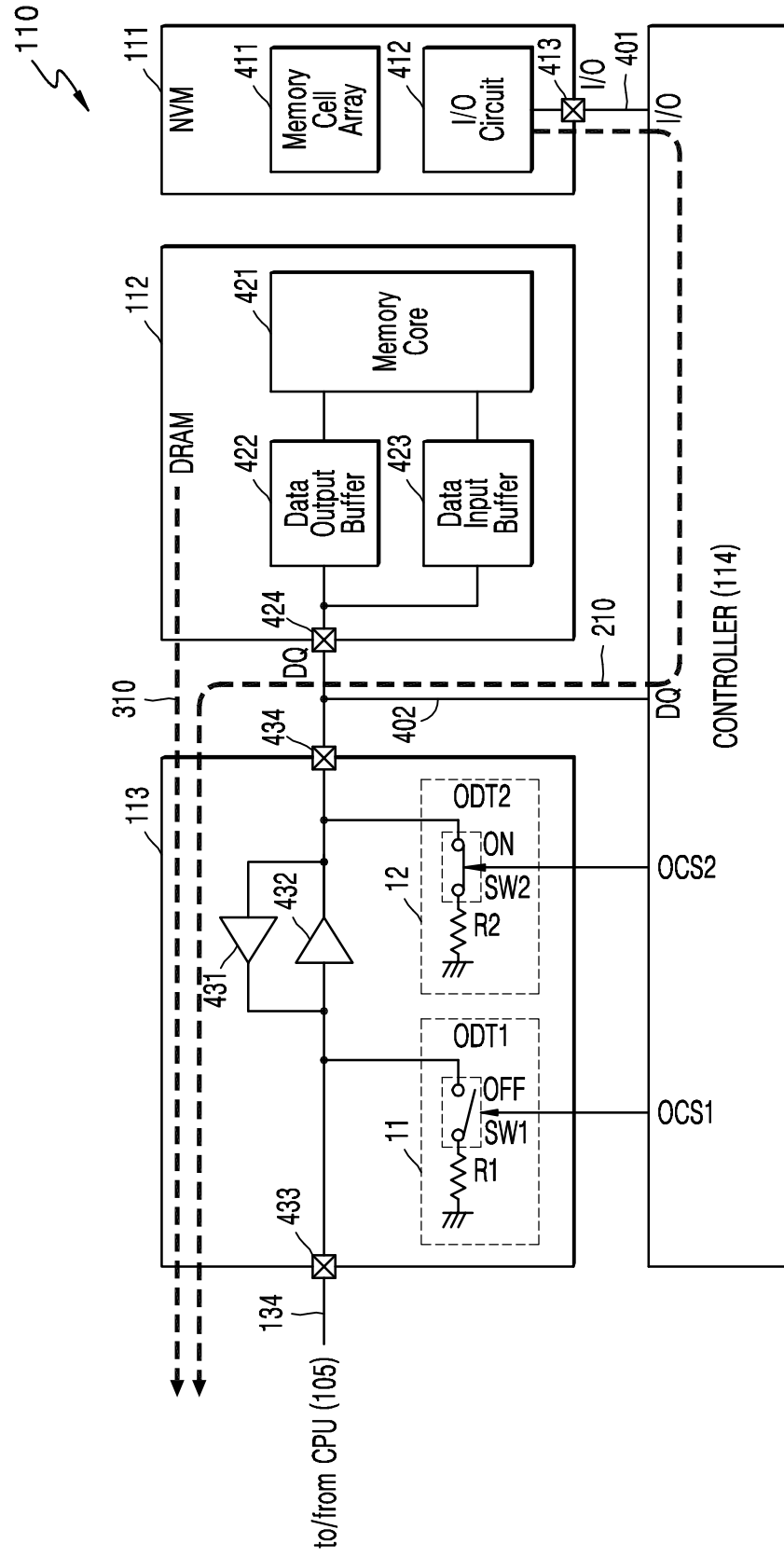


FIG. 5

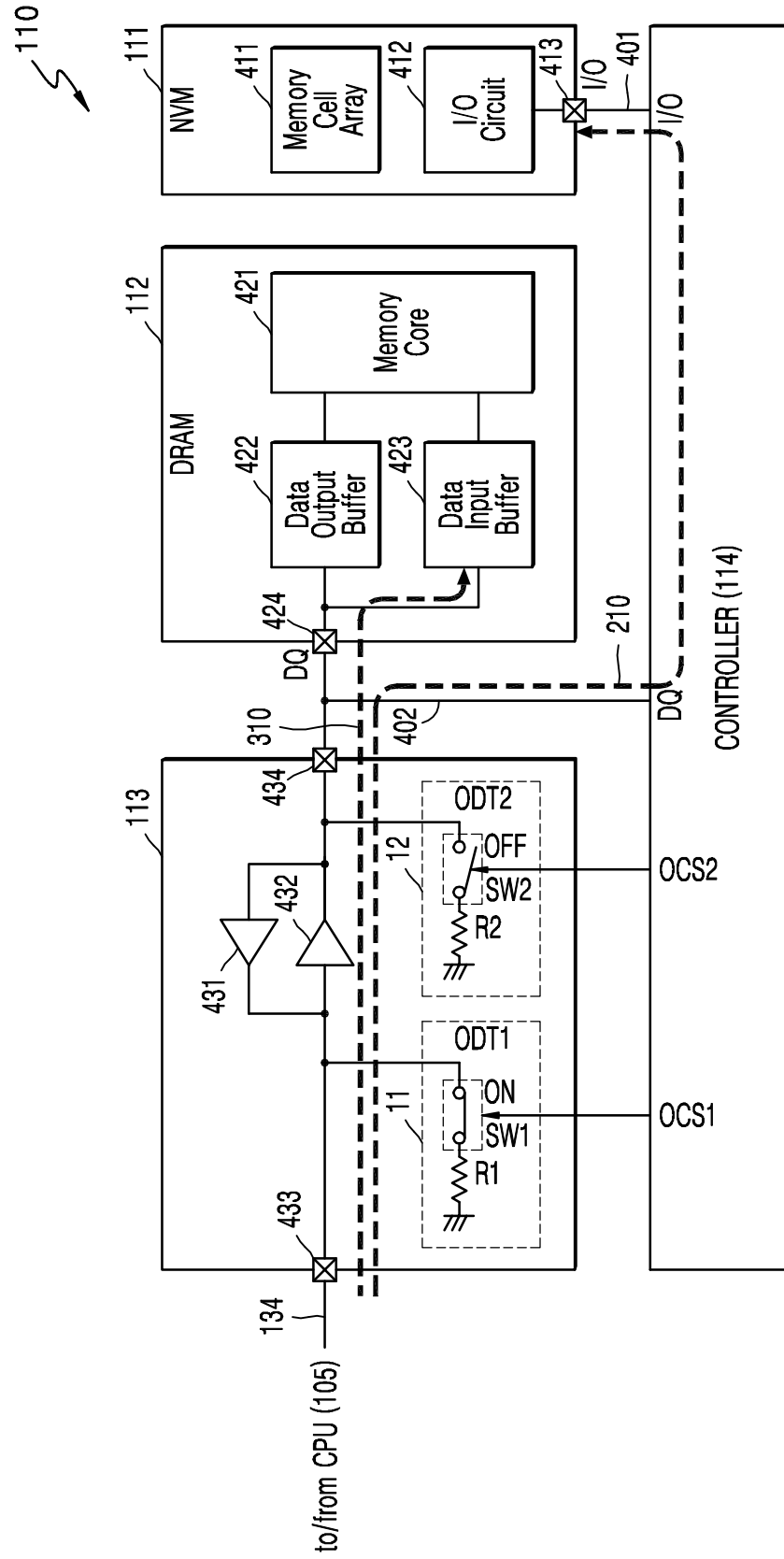


FIG. 6

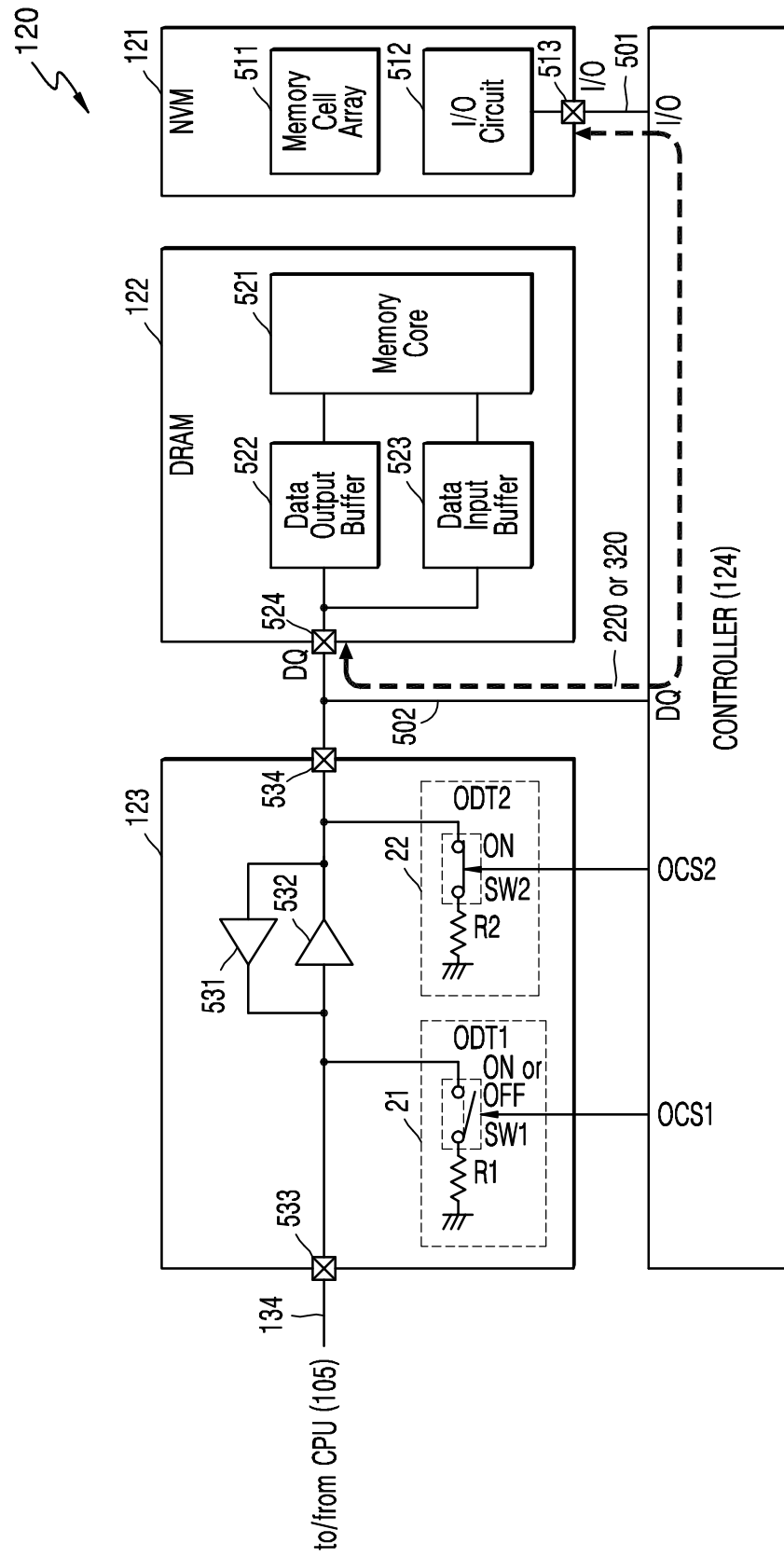


FIG. 7

Operation Mode	Target DIMM1		Non-Target DIMM2			
			Internal Operation Mode		Normal Mode	
	ODT1	ODT2	ODT1	ODT2	ODT1	ODT2
Read	OFF	ON	ON	ON	ON	OFF
	OFF	ON	OFF	ON	OFF	OFF
Write	ON	OFF	ON	ON	ON	OFF
	ON	OFF	OFF	ON	OFF	OFF

Internal DQ Termination

FIG. 8

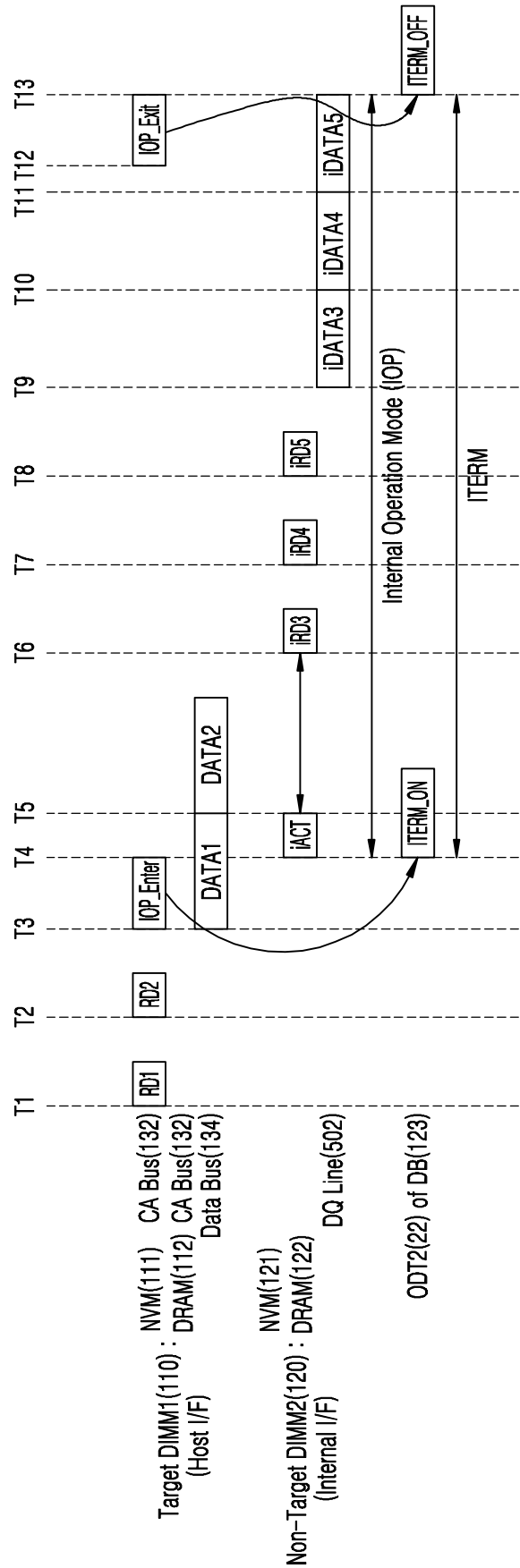


FIG. 9

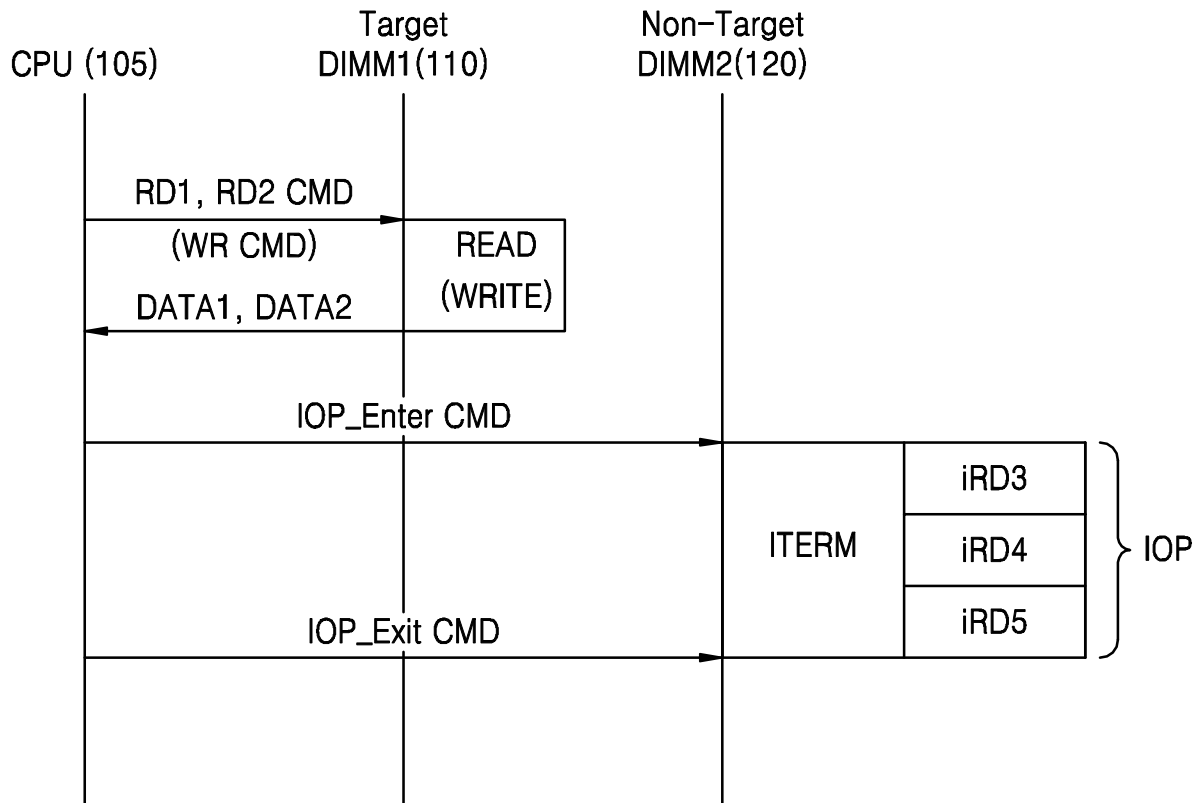


FIG. 10

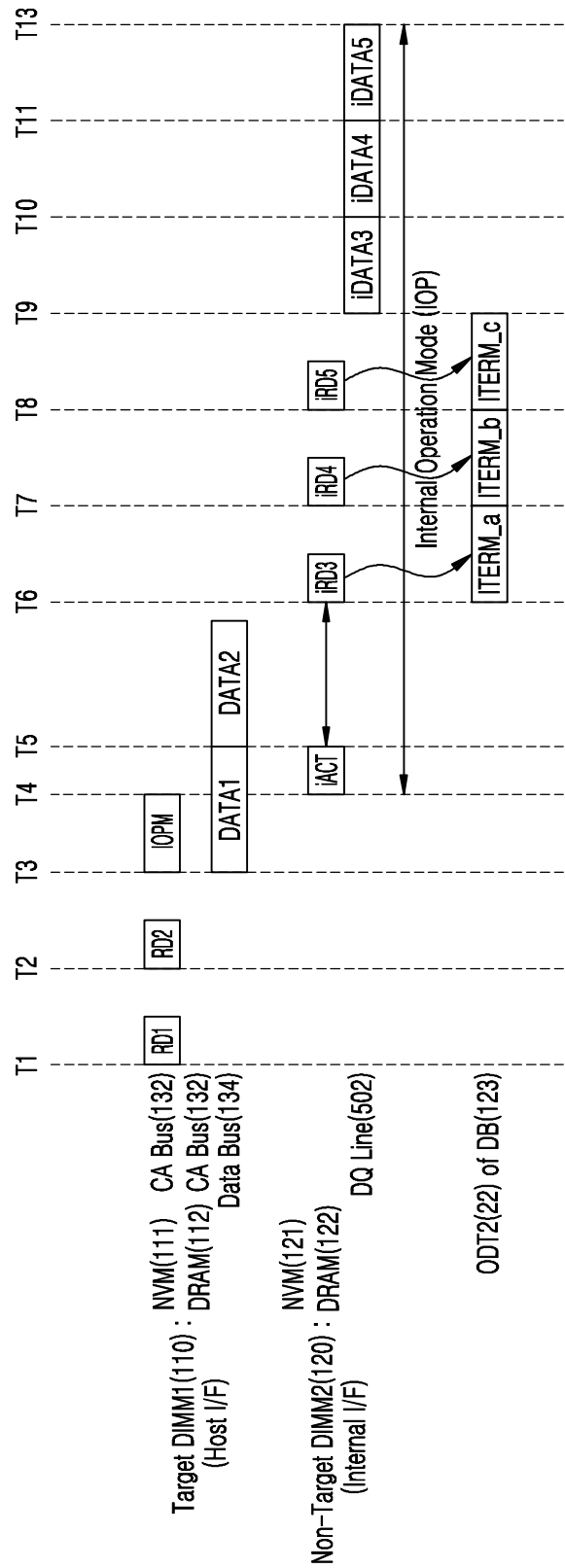


FIG. 11

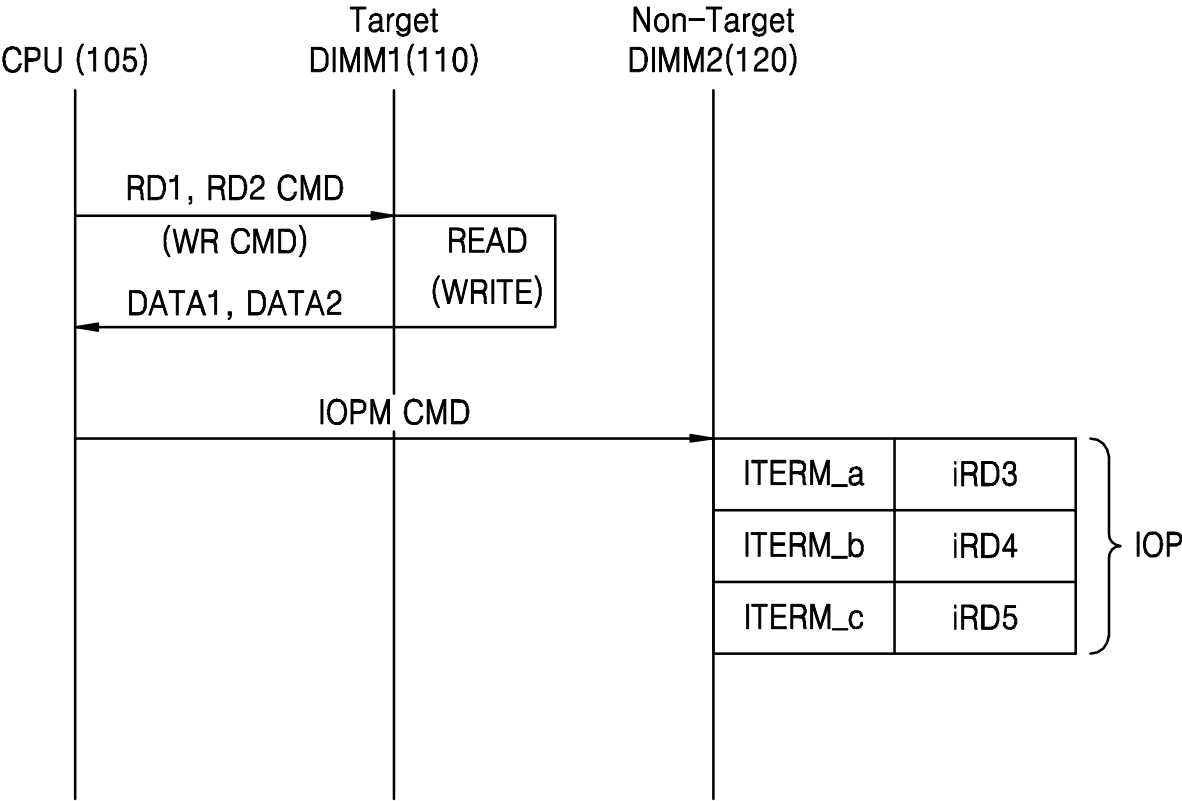


FIG. 12

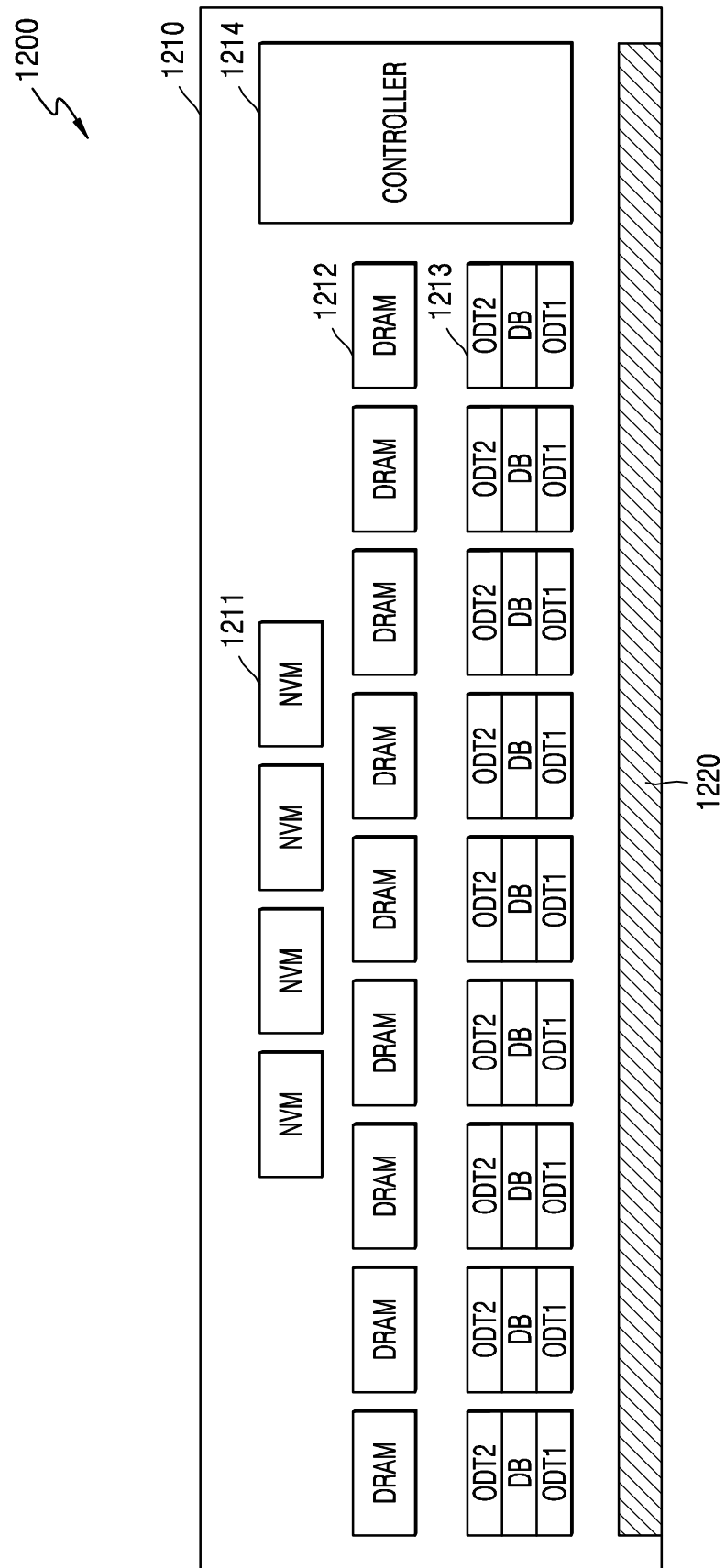


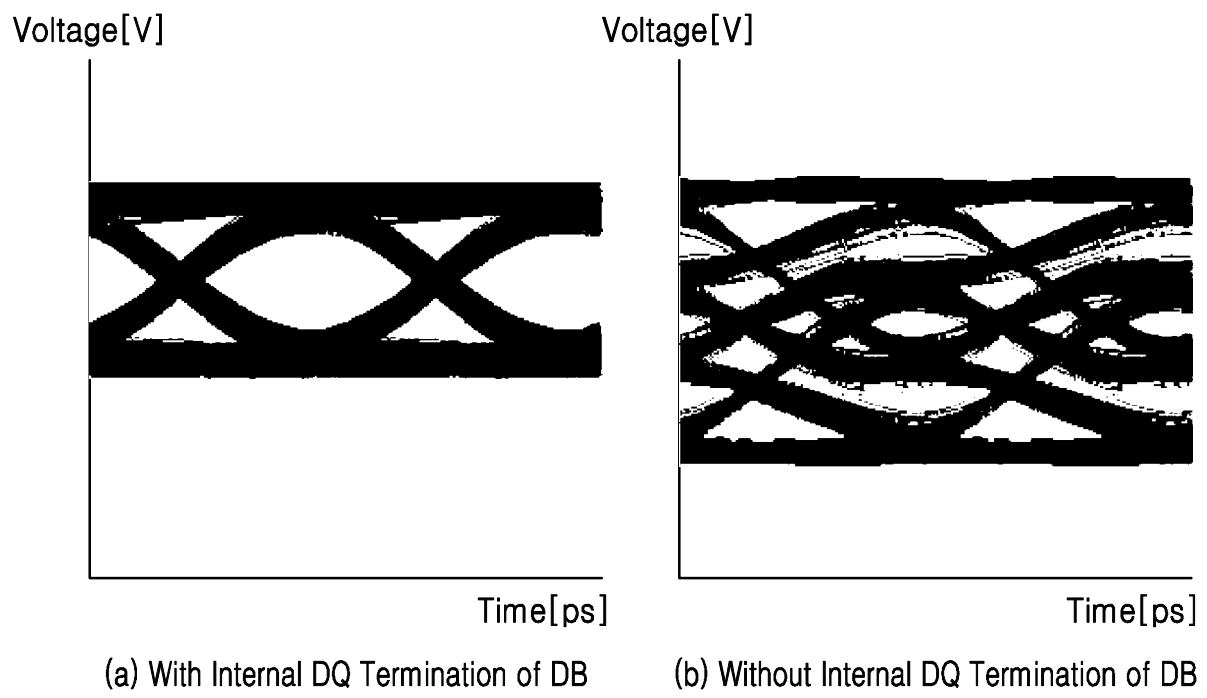
FIG. 13

FIG. 14

