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SELECTABLE CLAMPERS FOR AN S-SHAPING VOLTAGE OF A
VIDEO DISPLAY

ABSTRACT

In a horizontal deflection circuit output stage of a video display monitor capable of operating at multi-scan rates, a given S-capacitor (CS2), selected from a bank of S-capacitors, provide S-correction at a corresponding range of frequencies. A resistor-capacitor-diode clamp (DD2, RD2, CD2) selected from a bank of clamps (DD2, RD2, CD2, DD1, RD1, CD1) is coupled to the selected S-capacitor for reducing ringing by causing critical damping. The in-circuit selected resistor-capacitor-diode clamp is selectable, in accordance with the selected horizontal deflection frequency.

SOLE FIGURE



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