



RCA 87.369

VOLTAGE REGULATOR FOR CRT ELECTRODE SUPPLY

The invention relates to a power supply for a television apparatus. In particular, the invention relates to a high voltage power supply for generating a grid electrode voltage of a cathode ray tube (CRT) such as a screen grid electrode.

In a conventional television receiver, a horizontal flyback transformer is coupled between a collector of a horizontal output transistor and a DC power supply of a B+ supply voltage. The horizontal output transistor is coupled between the flyback transformer and ground, and conducts a current from the B+ power supply to energize the flyback transformer and produce a sawtooth horizontal deflection current in the horizontal deflection winding for repetitive horizontal scanning. The base of the horizontal output transistor is coupled to a horizontal oscillator, which controls a switching operation in the horizontal output transistor. A retrace capacitor is coupled between the collector of the horizontal output transistor and ground.

At the collector of the horizontal output transistor and on the retrace capacitor, a high voltage retrace pulse voltage of, for example 1000V is developed, during retrace. The flyback transformer is, typically, used for providing high voltage AC input needed for a high voltage DC power supply. By suitable turns ratios between primary and secondary windings of the flyback transformer, a high voltage is generated in the transformer and rectified to produce an ultor voltage, during retrace.

In one prior art arrangement, a rectifier coupled to the collector of the horizontal output transistor produces from the retrace pulse voltage a DC voltage that is coupled to the screen grid of the CRT. Increased beam loading may cause the peak voltage at the collector to increase and, hence, the DC voltage at the screen grid to increase.

In another prior art television receiver the screen grid voltage is developed at a terminal of a voltage divider that is responsive to a rectified high voltage produced from the high voltage winding of the flyback transformer and is approximately equal to one third of the ultor voltage. A portion of the rectified high voltage that is developed across the voltage divider provides the screen grid voltage.

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In a television receiver that does not have an ultor voltage regulator, an increase in beam current loading produces a decrease in the high voltage at the secondary winding of the flyback transformer and, hence in the screen grid voltage. It may
5 be desirable to regulate the screen grid voltage.

A television apparatus power supply, embodying an output of the invention includes a deflection winding adapted for mounting on a neck of a cathode ray tube and coupled to a retrace capacitance to form a retrace resonant circuit. A first switching
10 arrangement is responsive to a switching signal at a frequency that is related to a deflection frequency and coupled to the retrace resonant circuit and to a flyback transformer. A deflection current is generated in the deflection winding and a first flyback pulse voltage that is transformer-coupled via the transformer to a
15 high voltage winding of the transformer is generated. A first rectifier is coupled to the high voltage winding and is responsive to the transformer-coupled pulse voltage for generating a rectified, first voltage and a rectified, ultor voltage. A second rectifier is responsive to the first flyback pulse voltage for
20 generating a rectified, second voltage in a manner that bypasses the transformer. The first and second voltages are combined to develop an electrode voltage for the cathode ray tube.

The sole FIGURE is a schematic diagram illustrating a television power supply according to the invention.

25 As shown in the FIGURE, a horizontal deflection circuit, has a horizontal deflection winding LY for a CRT 100. A primary winding X1 of a horizontal output transformer T1 (also known as the flyback transformer) is coupled between a terminal where a DC supply voltage B+ is developed and a drain of a horizontal
30 output transistor Q1. A horizontal oscillator 20 is coupled to the base of horizontal output transistor Q1, through a driver stage 22. A retrace capacitor CR is coupled in parallel with horizontal output transistor Q1. A damper diode D1 clamps the minimum voltage at the collector of transistor Q1 to approximately ground potential.

35 Horizontal output transistor Q1 conducts current through winding X1 and through winding LY as the electron beam scans horizontally during the trace interval, and shuts off during retrace, thereby producing flyback or retrace pulses 10 on



capacitor CR, in winding X1 and on the drain of transistor Q1. An S-shaping or trace capacitor CT is coupled between the deflection winding and ground. In addition to the circuit components shown, the deflection circuits normally may include pincushion correction circuits; however these have not been illustrated in order to simplify the drawings. Corresponding portions of a secondary winding X2 of transformer T1 are coupled in series with six rectifying distributed diodes D4-D9 in a conventional manner.

10 An ultor voltage U is produced from secondary winding X2 of horizontal output transformer T1 via diode D4. A voltage HV/3 that is equal to approximately 1/3 of voltage U is developed across a lower voltage portion of winding X2.

A diode D2 has an anode that is coupled between winding LY and transistor Q1 and a cathode that is coupled to a filter capacitor CP to form a peak rectifier. Diode D2 conducts to charge capacitor CP during the retrace interval, when a given flyback pulse 10 is generated. An output voltage VOUT in capacitor CP is at its peak level during the retrace interval. Both DC voltage VOUT and voltage HV/3 are adjustably combined and coupled to the screen grid electrode via a voltage divider 200 including resistors R1, R2, R3, R4, R5 and R6.

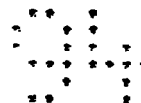
In accordance with an aspect of the invention, combining voltage HV/3 and voltage VOUT in voltage divider 200 provides improved regulation of a screen grid voltage VS that is developed at a terminal 200b of voltage divider 200. For example, an increase in a beam current iBEAM causes voltage HV/3 to decrease because of the well known effect of the output impedance of secondary winding X2 of transformer T1. On the other hand, the peak voltage of retrace pulses 10 tends to increase when beam current iBEAM increases. As a result of combining voltage VOUT and voltage HV/3 in voltage divider 200, the decrease in voltage HV/3 is compensated by the aforementioned tendency of voltage VOUT to increase in a manner to regulate the screen grid voltage.

The terminal of voltage divider 200 to which voltage VOUT is coupled is selected in accordance with the voltage range required for screen grid voltage VS. The coupling via a conductor

300, shown in the broken line in the FIGURE, illustrates an example of a range 750-1600 volts for screen grid VS; whereas, the alternative coupling via conductor 301 illustrates an example of a range of 240-600 volts for voltage VS. Advantageously, in
5 each of the alternative examples the voltage regulation of screen grid voltage VS is improved with respect to beam current changes.

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WHAT IS CLAIMED IS:

1. A television apparatus power supply, comprising:
a deflection winding (LY) adapted for mounting on a neck of a cathode ray tube (100) and coupled to a retrace
5 capacitance (CR) to form a retrace resonant circuit;
a flyback transformer (T1);
first switching means (Q1) responsive to a switching signal (at base of Q1) at a frequency that is related to a deflection frequency (f_H) and coupled to said retrace resonant circuit and to
10 said flyback transformer for generating a deflection current in said deflection winding and a first flyback pulse voltage (at D of Q1) that is transformer-coupled via said transformer to a high voltage winding (X2) of said transformer;
a first rectifier (D8) coupled to said high voltage
15 winding and responsive to said transformer-coupled pulse voltage for generating a rectified, first voltage (HV/3) and a rectified, ultor voltage (U);
a second rectifier (D2) responsive to said first flyback pulse voltage for generating a rectified, second voltage (VOUT) in
20 a manner that bypasses said transformer; characterized by means (200) for combining said first and second voltages to develop an electrode voltage (VS) for said cathode ray tube.
- 25 2. A power supply according to Claim 1, characterized in that said first (HV/3) and second (VOUT) voltages vary in opposite direction in a manner to regulate said electrode voltage (VS) when a beam current (i_{BEAM}) produced from said ultor voltage (U) varies.
- 30 3. A power supply according to Claim 1, characterized in that each of said rectifiers (D8, D2) operates as a peak voltage rectifier.
- 35 4. A power supply according to Claim 1, characterized in that said electrode voltage (VS) is developed at a screen grid of said cathode ray tube (100).



5. A power supply according to Claim 1, characterized in that said combining means comprises, a resistor (200) having a first terminal (200a) where said first voltage is developed, a second terminal, where said second voltage (VOUT) is developed and a third terminal (200b) where said electrode voltage (VS) is developed.

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