

SYSTEM AND METHOD FOR REDUCING POWER
CONSUMPTION IN AN ELECTRONIC CIRCUIT

ABSTRACT OF THE DISCLOSURE

While a set-associative cache memory operates in a first power mode, information is stored in up to N number of ways of the cache memory, where N is an integer number and $N > 1$. While the cache memory operates in a second power mode, the information is stored in up to M number of ways of the cache memory, where M is an integer number and $0 < M < N$.

Fig. 6

This Sheet Has Been Replaced



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REPLACED