

EXAMPLE OF ENTIRE CONFIGURATION OF TIME SYNCHRONIZATION SYSTEM 1

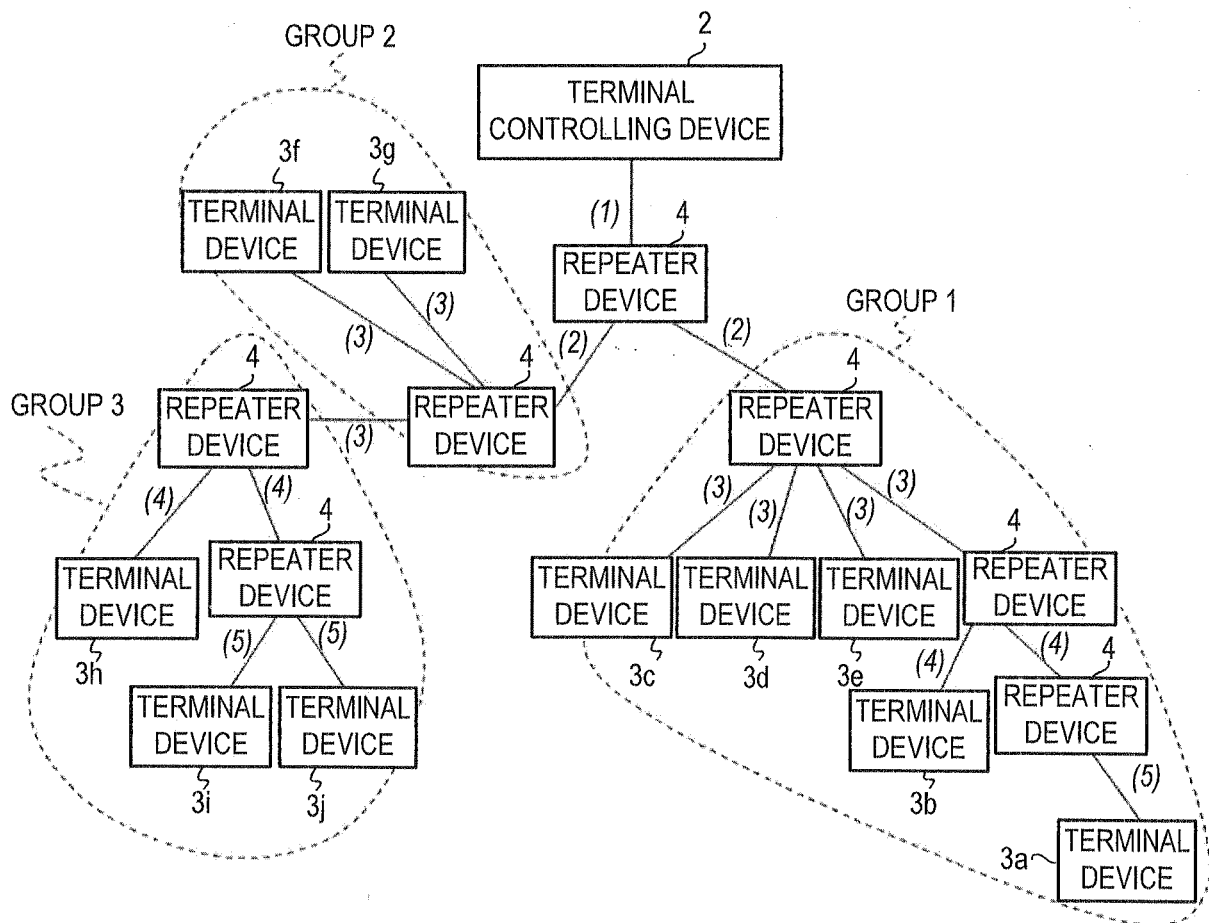


FIG. 1

EXAMPLE OF FUNCTIONAL BLOCK DIAGRAM OF TIME SYNCHRONIZATION SYSTEM

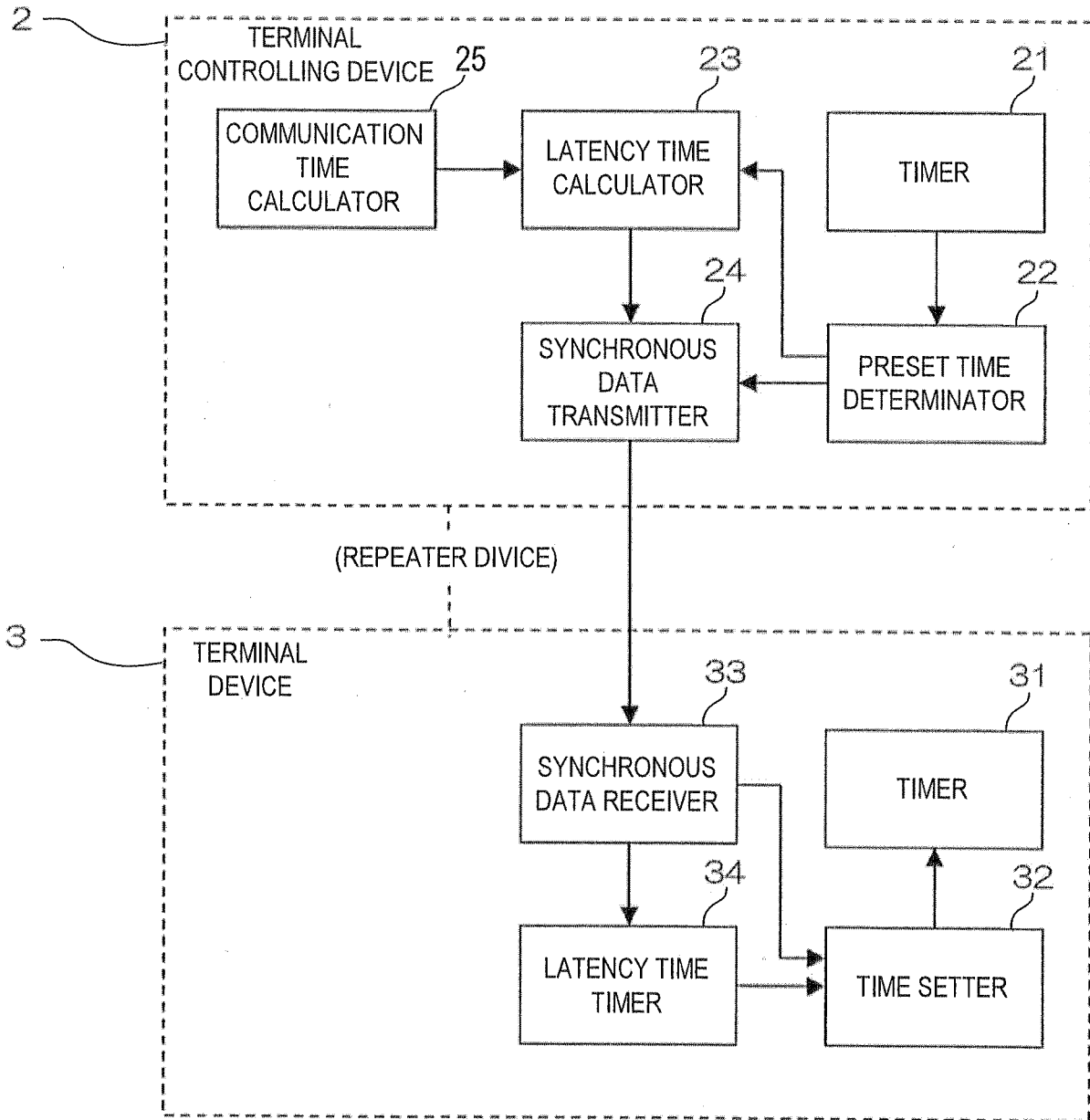


FIG. 2

EXAMPLE OF HARDWARE CONFIGURATION OF TERMINAL CONTROLLING DEVICE

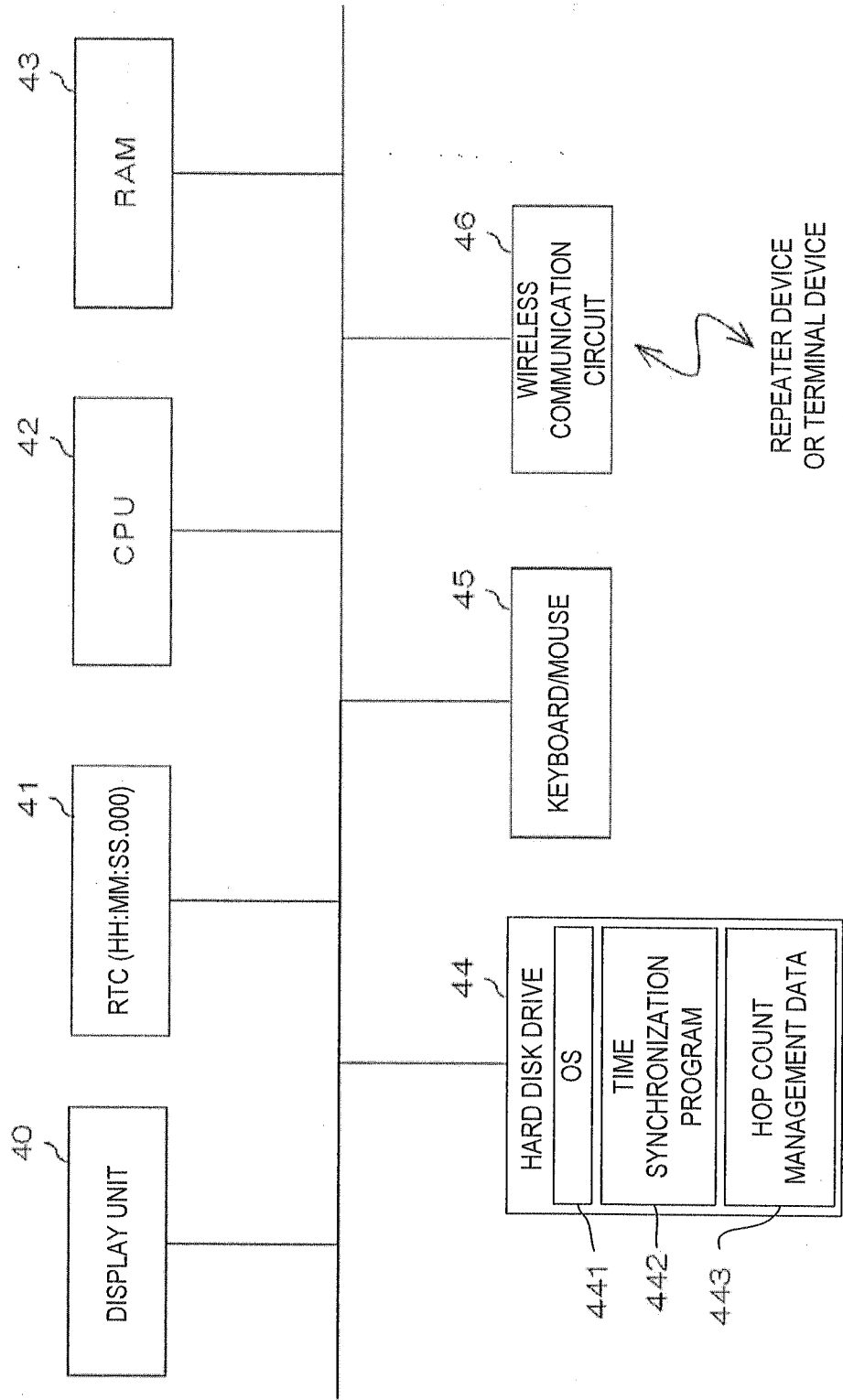


FIG. 3

EXAMPLE OF HARDWARE CONFIGURATION OF TERMINAL DEVICE

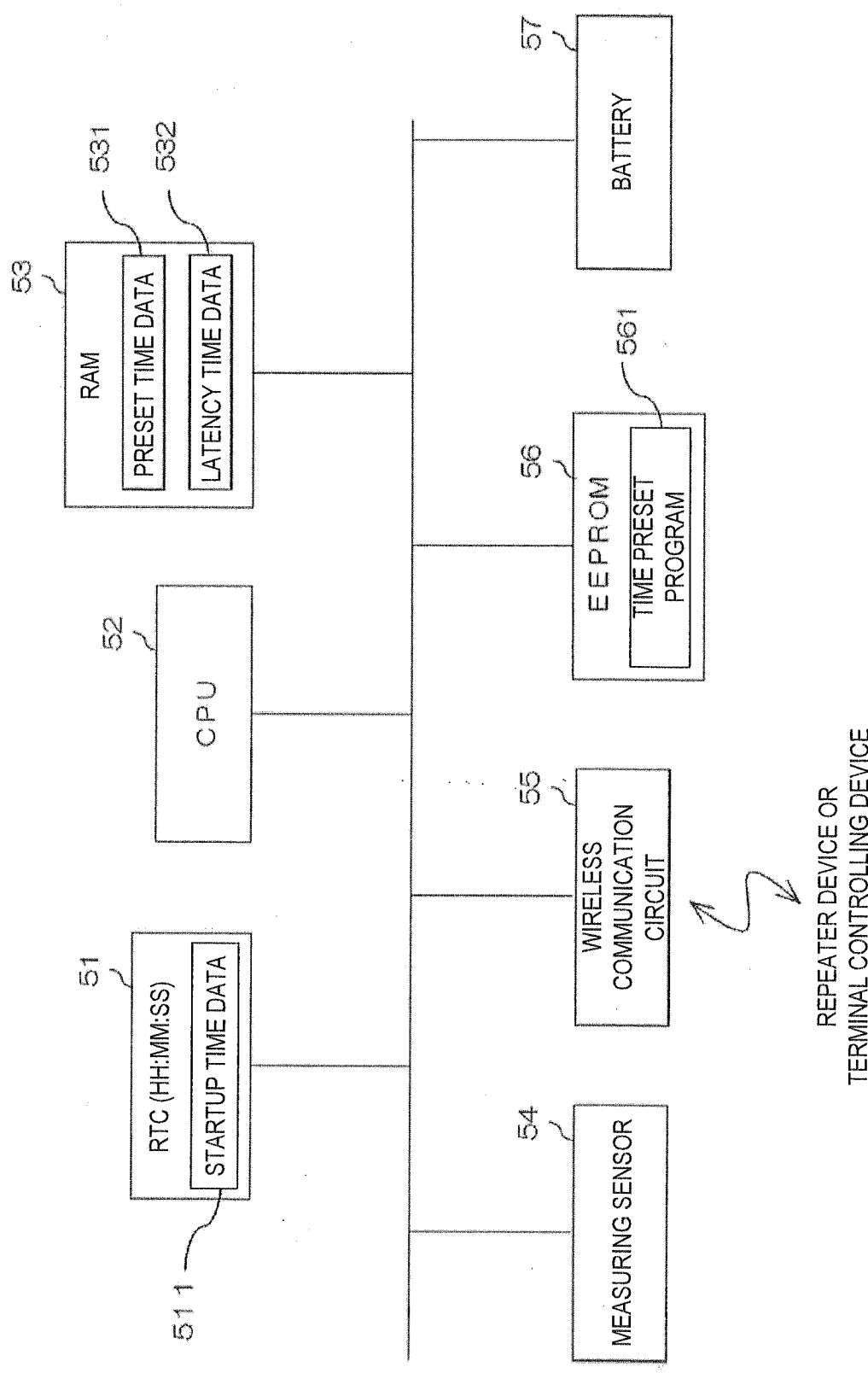


FIG. 4

EXAMPLE OF FLOWCHART OF TIME SYNCHRONIZATION PROCESSING

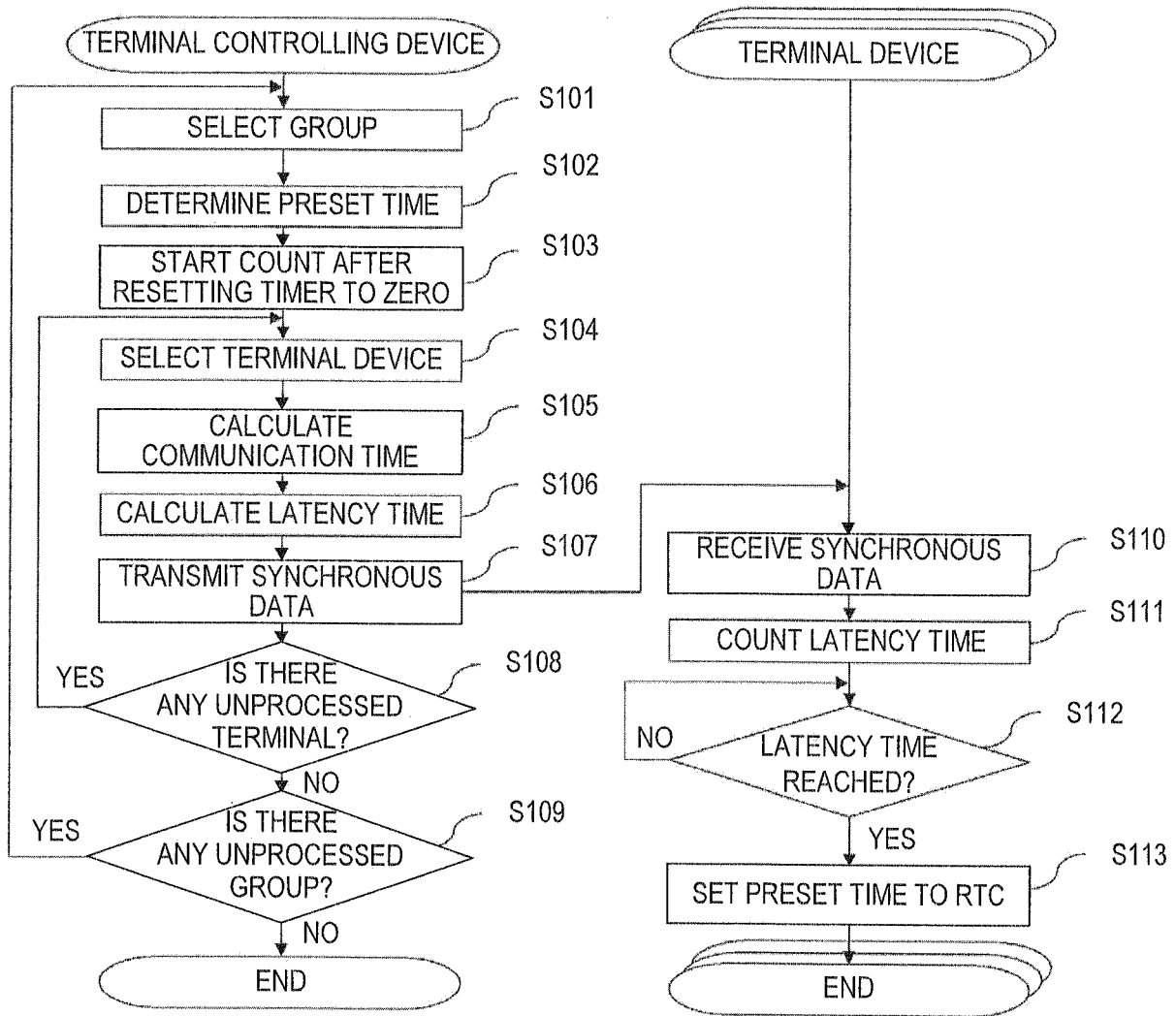


FIG. 5

EXAMPLE OF HOP COUNT MANAGEMENT DATA 443

	TERMINAL DEVICE	NUMBER OF HOPS
601 →	3 a	5
602 →	3 b	4
603 →	3 c	3
604 →	3 d	3
605 →	3 e	3
	:	:

FIG. 6

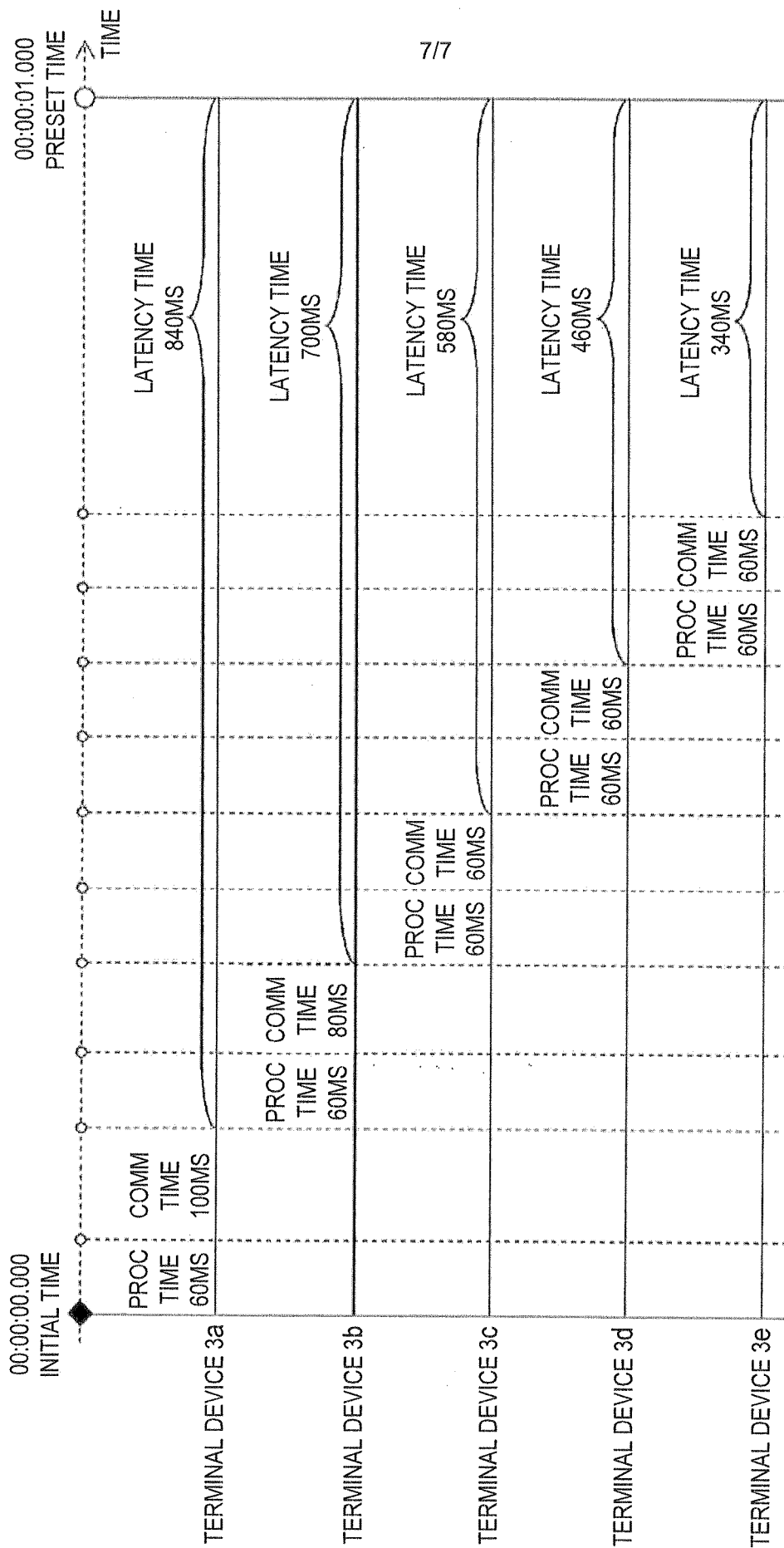


FIG. 7