

A CIRCUIT FOR REDUCING ALIASING NOISE

Abstract of the Invention

5 A toggle flip-flop (320) receives a square pulse signal from a horizontal synchronising circuit 300 and provides a modulation signal having half the frequency of the square pulse signal. A latch (330) stores a digital value which corresponds with a desired displacement between the positions of even and odd numbered horizontal scans on the screen (30). A
10 multiplexer (325) receives the modulation signal, a digital reference signal, and a digital ground. A digital to analogue converter (DAC) (335) receives the digital reference signal or the digital ground from the multiplexer in accordance with the modulation signal, and provides either a higher analogue reference level or a lower analogue reference level to shift ramp
15 voltages generated by the first and second ramp generators (340) and (345) to realise the displacement.

(FIGS. 8 accompanies this abstract)