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(54) Title: SWITCH MODE SINE WAVE DRIVER FOR POLYPHASE BRUSHLESS PERMANENT MAGNET MOTOR

(57) Abstract

A switch mode sine wave driver circuit selectively sources and sinks driving currents to and from a dc brushless motor. A signal waveform monitoring circuit (58) puts out motor phase reference signals. A phase clock generator (102) provides a motor-synchronized clock signal. A phase counter (62) is clocked by the motor clock signal and is reset by the phase reference signals. A motor state decoder (116) generates state control signals based on the phase reference signals. A pulse width modulator circuit (84) responds to the output and generates phase pulses in order to rotate the motor.

