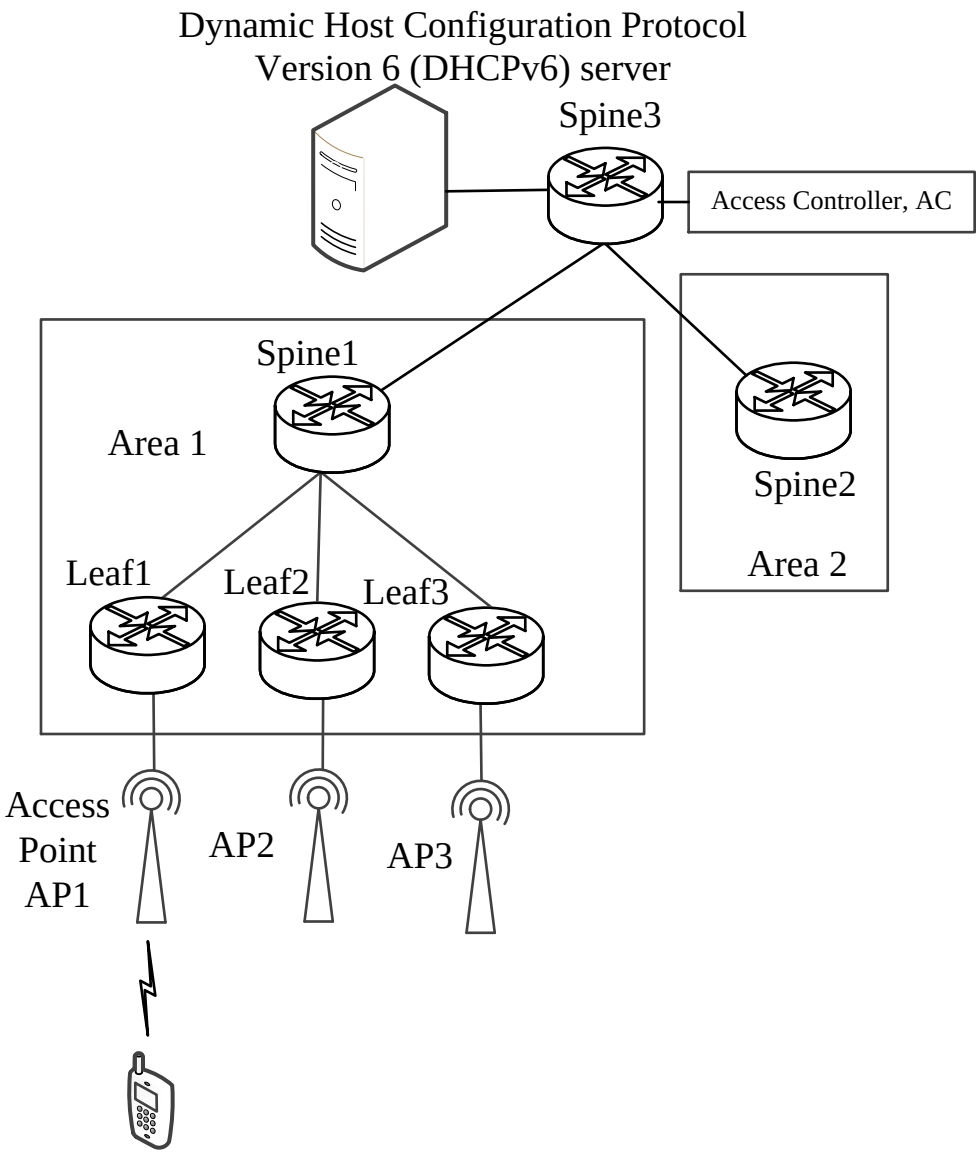

Dynamic Host Configuration Protocol Version 6 (DHCPv6) server
Spine3
Access Controller, AC
Spine1
Area 1
Spine2
Area 2
Leaf1
Leaf2
Leaf3
Access Point AP1
AP2
AP3

FIG. 1

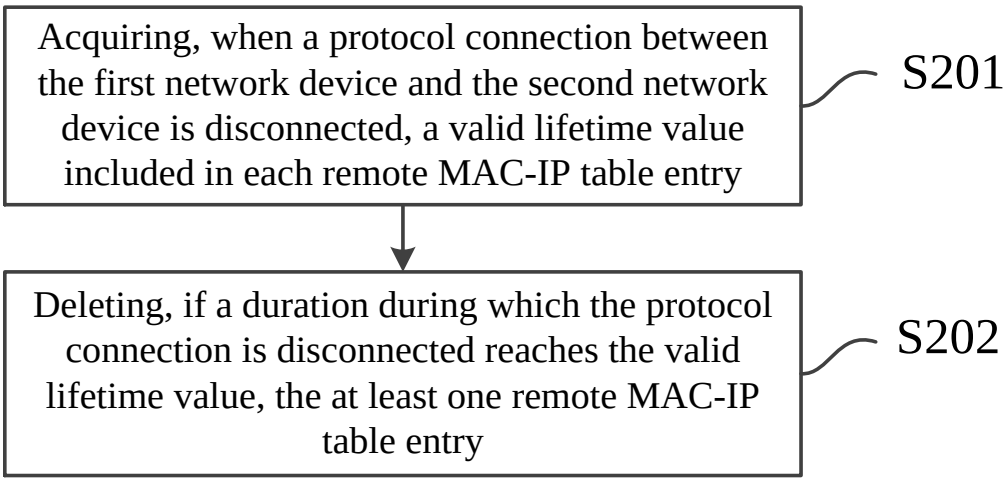

Acquiring, when a protocol connection between the first network device and the second network device is disconnected, a valid lifetime value included in each remote MAC-IP table entry
S201
Deleting, if a duration during which the protocol connection is disconnected reaches the valid lifetime value, the at least one remote MAC-IP table entry
S202

FIG. 2

Route Type（1 octet）
Length（1 octet）
Route Type specific（variable）

FIG. 3

Route Distinguisher, RD（8 octets）
Ethernet Segment Identifier（10 octets）
Ethernet Tag ID（4 octets）
MAC Address Length（1 octet）
MAC Address（6 octets）
IP Address Length（1 octet）
IP Address（0，4，or 16octets）

FIG. 4

Type=0x8d	Sub-Type=0x01	Reserved=0
Preferred lifetime		
Valid lifetime		

FIG. 5

Type=0x8e	Sub-Type=0x01	Reserved=0
Sequence Number		

FIG. 6

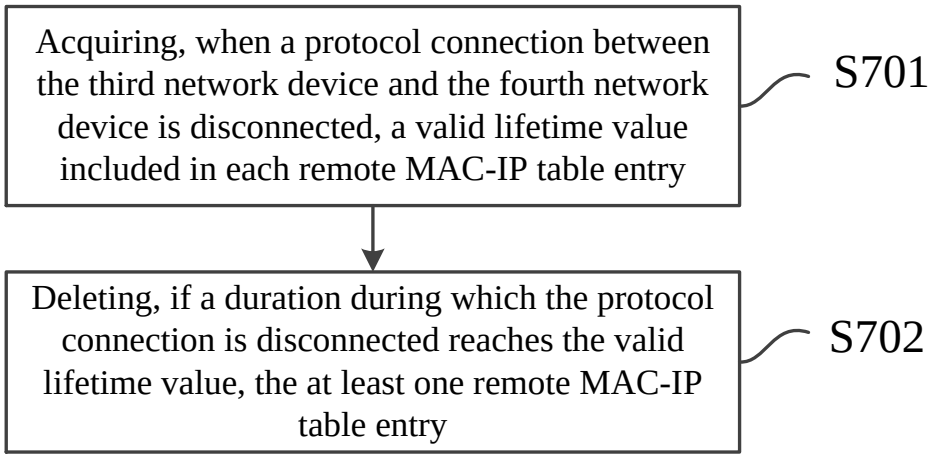

Acquiring, when a protocol connection between the third network device and the fourth network device is disconnected, a valid lifetime value included in each remote MAC-IP table entry
S701
Deleting, if a duration during which the protocol connection is disconnected reaches the valid lifetime value, the at least one remote MAC-IP table entry
S702

FIG. 7

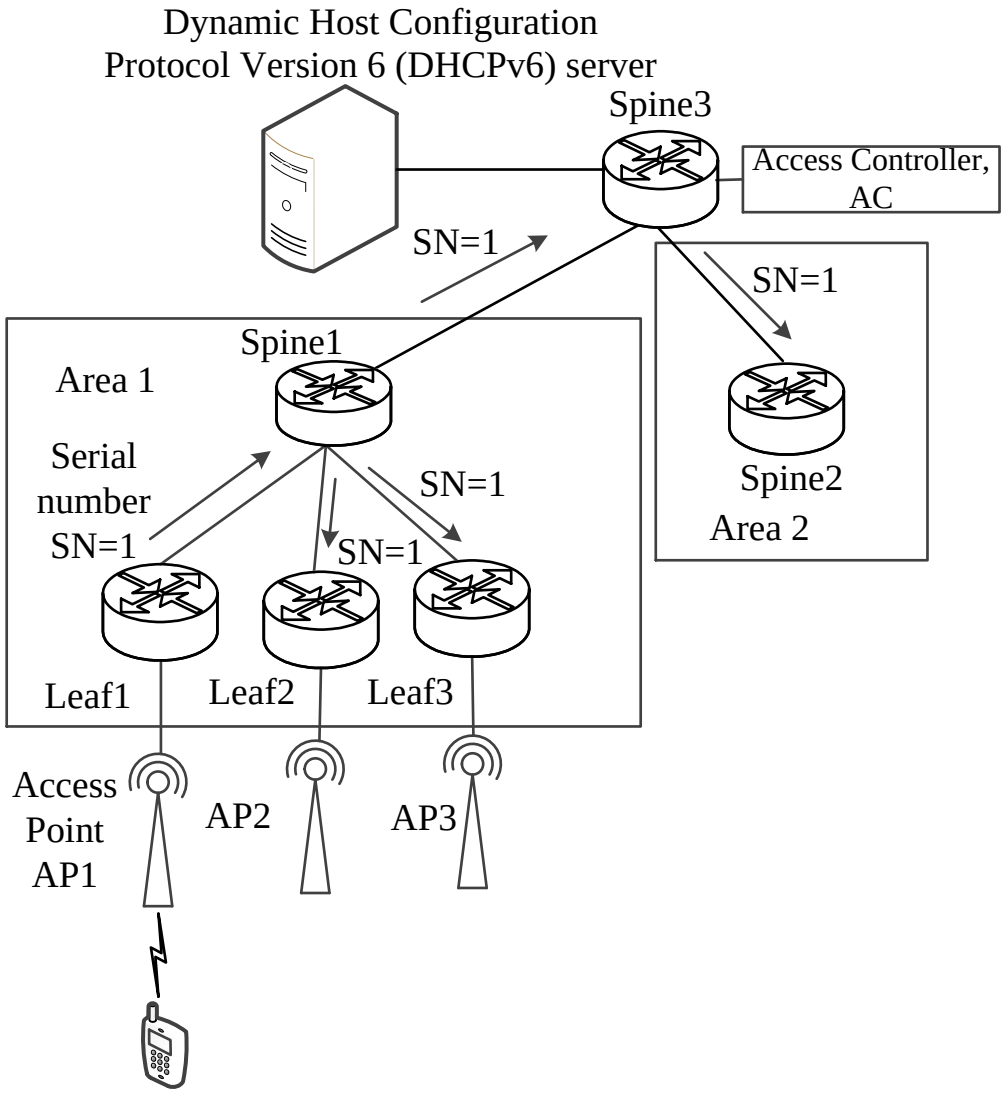

Dynamic Host Configuration Protocol Version 6 (DHCPv6) server
Spine3
Access Controller, AC
SN=1
SN=1
Spine1
Area 1
Serial number SN=1
SN=1
SN=1
Spine2
Area 2
Leaf1
Leaf2
Leaf3
Access Point AP1
AP2
AP3

FIG. 8

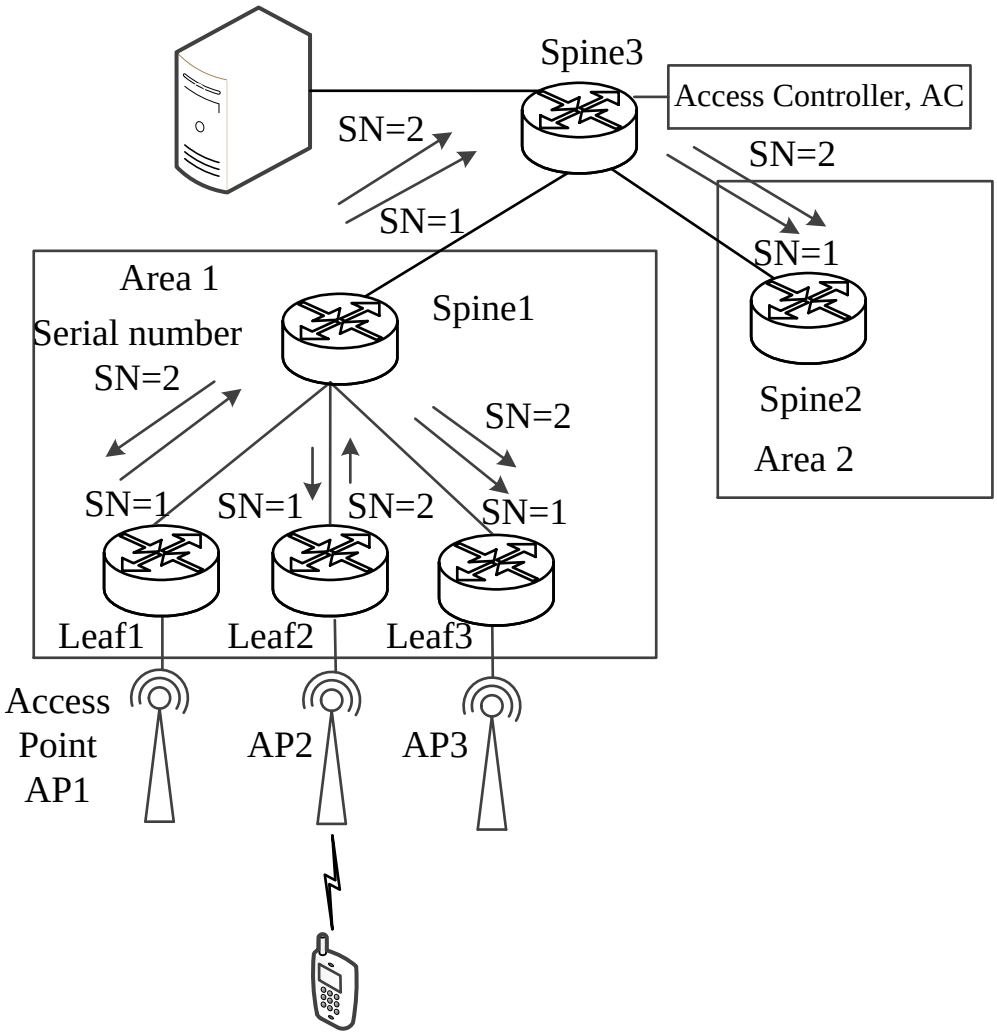

Dynamic Host Configuration Protocol
Version 6 (DHCPv6) server
Spine3
Access Controller, AC
SN=2
SN=1
SN=2
SN=1
Area 1
Serial number
Spine1
SN=2
SN=2
Spine2
Area 2
SN=1
SN=1
SN=2
SN=1
Leaf1
Leaf2
Leaf3
Access
Point
AP1
AP2
AP3

FIG. 9

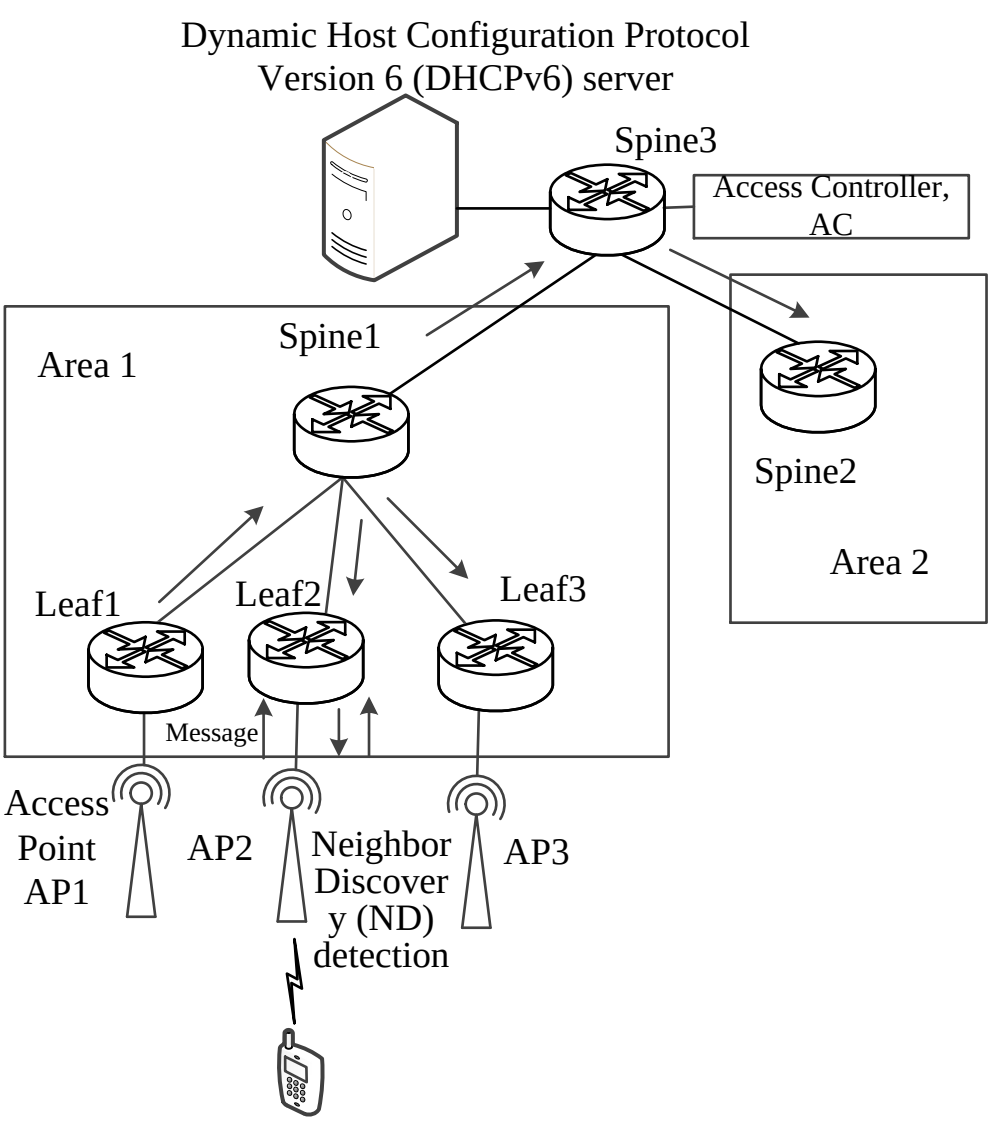


FIG. 10

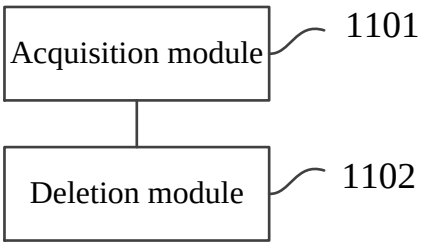


FIG. 11

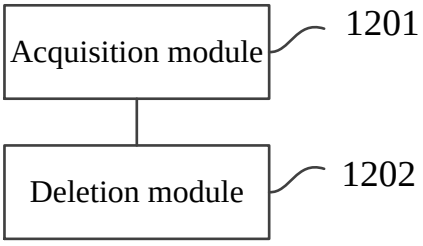


FIG. 12

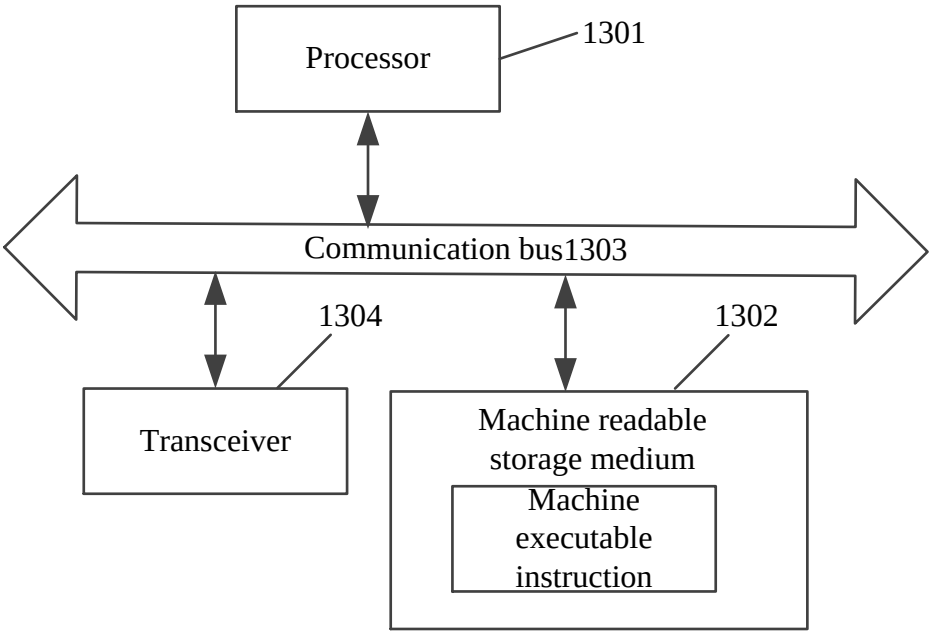

Processor
1301
Communication bus1303
1304
1302
Transceiver
Machine readable storage medium
Machine executable instruction

FIG. 13

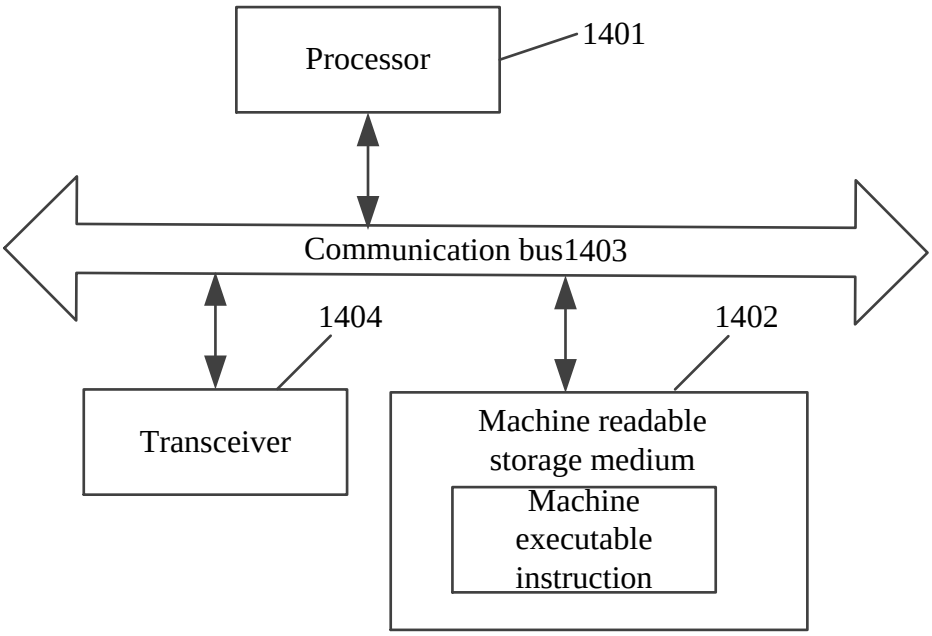

Processor
1401
Communication bus1403
1404
1402
Transceiver
Machine readable storage medium
Machine executable instruction

FIG. 14