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ABSTRACT OF THE DISCLOSURE

This specification discloses a signal processing apparatus for processing first and second periodic analog signals having the same period and having a fixed phase relation therebetween and having digitizing means for converting the first and second periodic analog signals into first and second digital signals in conformity with the respective amplitudes thereof, signal generating means responsive to the first and second periodic analog signals to generate a signal having a period shorter than the period of the first periodic analog signal, means for outputting the periodicity of the first periodic analog signal and first phase information of the first periodic analog signal from the signal from the signal generating means and processing means for producing second phase information of the first periodic analog signal from the first and second digital signals from the digitizing means, and correcting the periodicity from the output means on the basis of the first phase information and the second phase information.

