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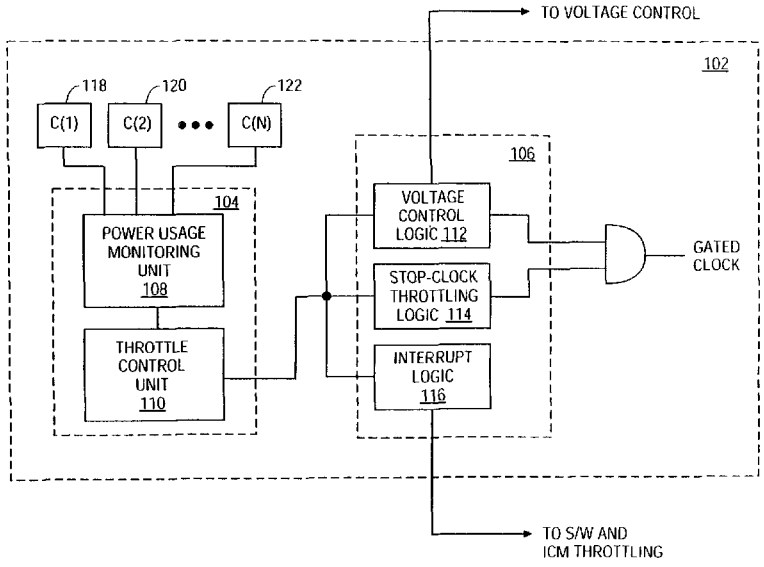
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(54) Title: DETERMINISTIC POWER-ESTIMATION FOR THERMAL CONTROL



(57) Abstract: A system is described that includes a microprocessor (102) and a thermal control subsystem (104). The microprocessor (102) includes execution resources to support processing of instructions and consumes power. The microprocessor (102) also includes at least one throttling mechanism (106) to reduce the amount of heat generated by the microprocessor (102). The thermal control subsystem (104) is configured to estimate an amount of power used by the microprocessor (102) and to control the throttling mechanism (106) based on the estimated amount of current power usage to ensure that junction temperature will not exceed the maximum allowed temperature.

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