

Fig. 1A

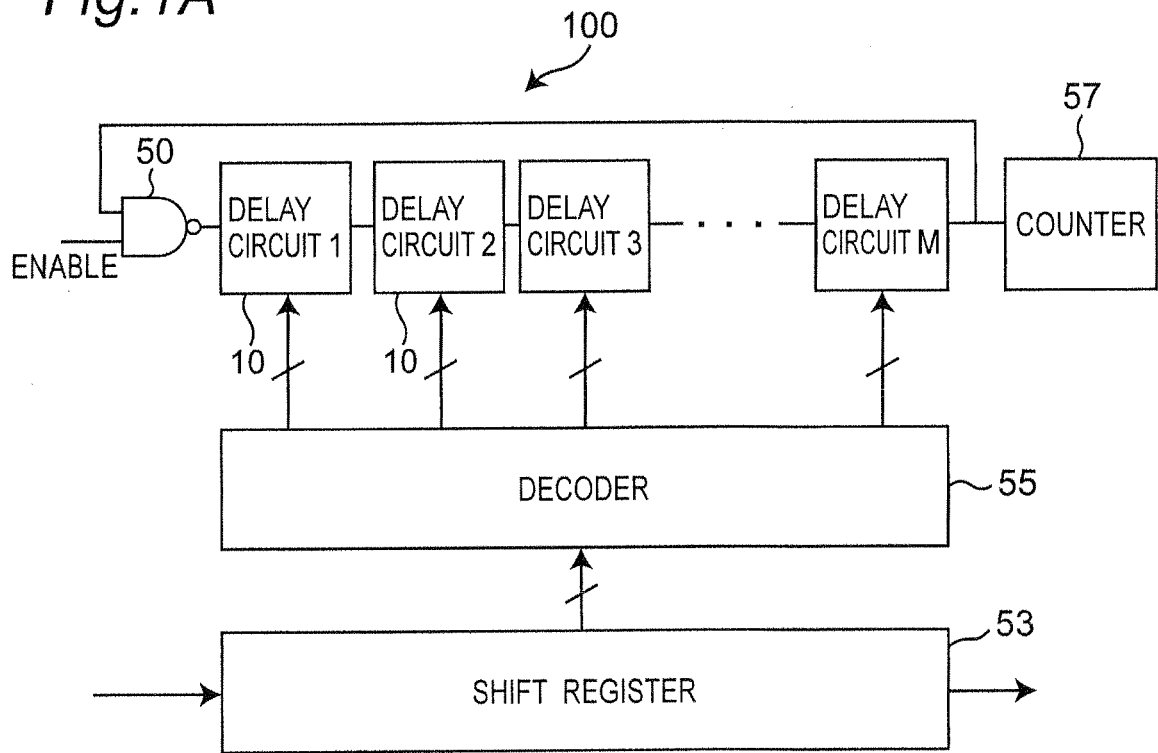


Fig. 1B

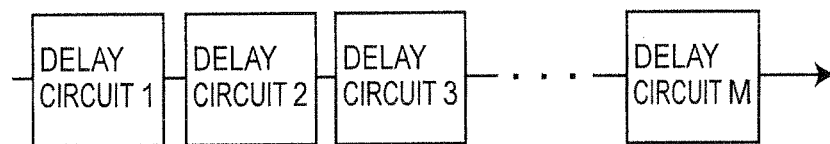


Fig.2

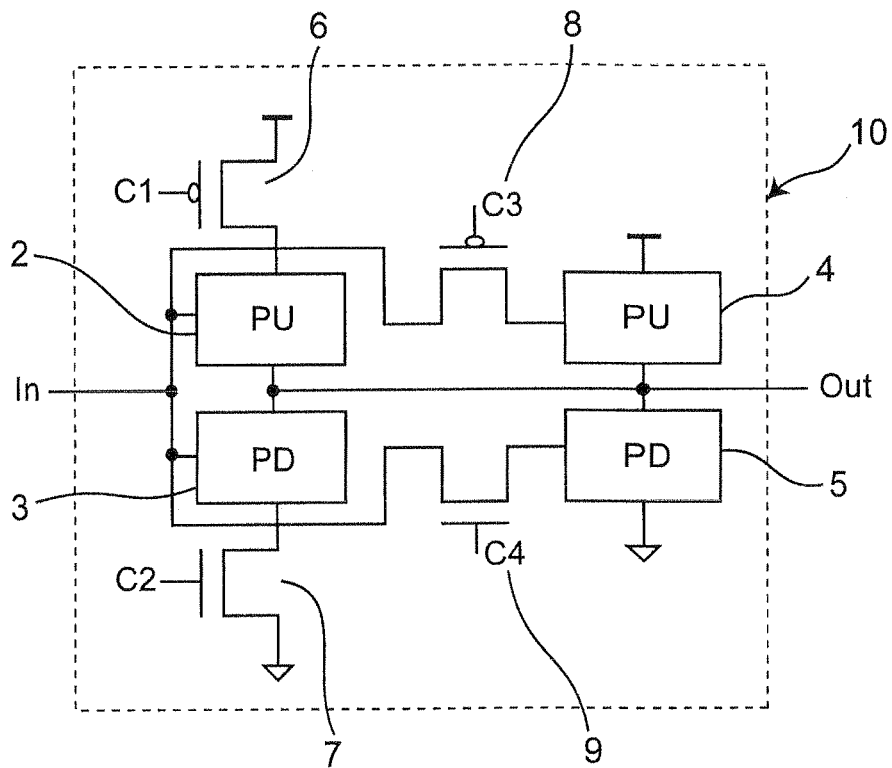


Fig.3

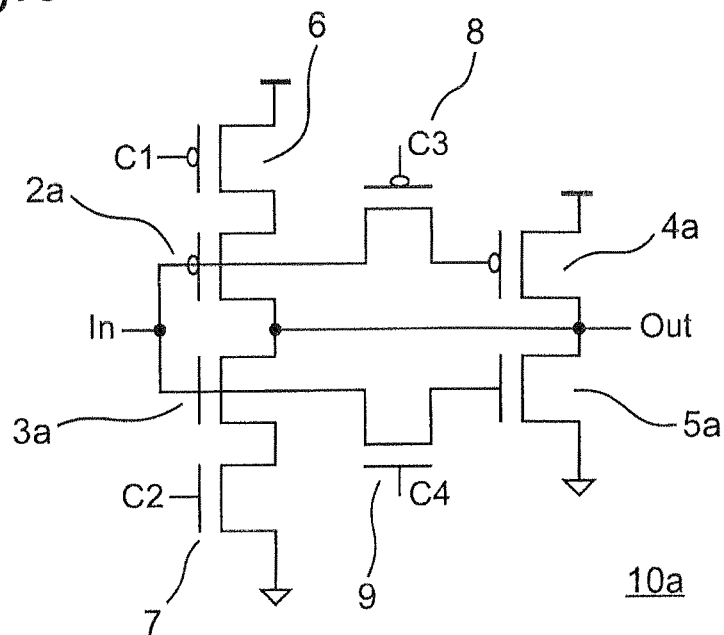


Fig.4A

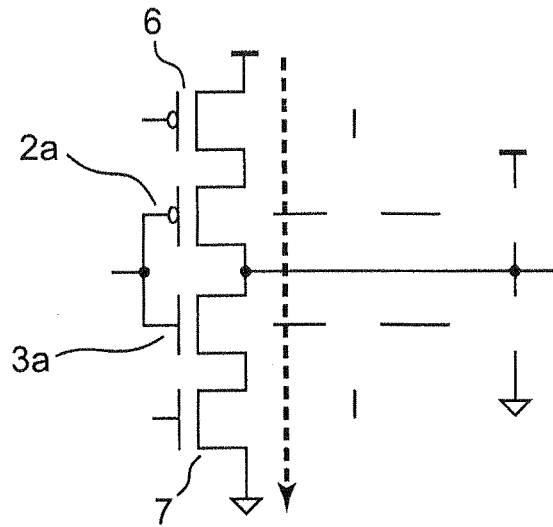


Fig.4B

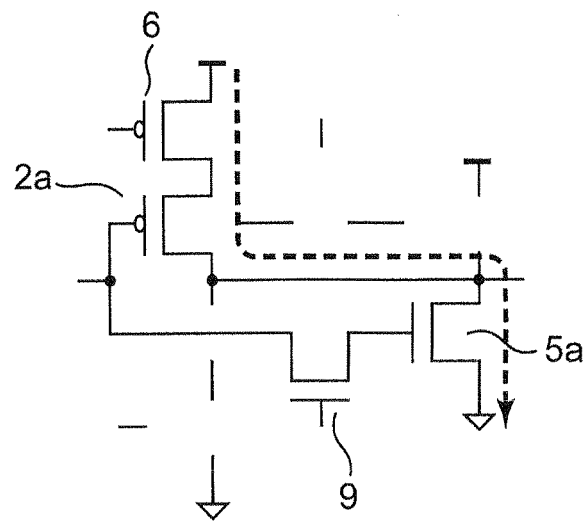


Fig.4C

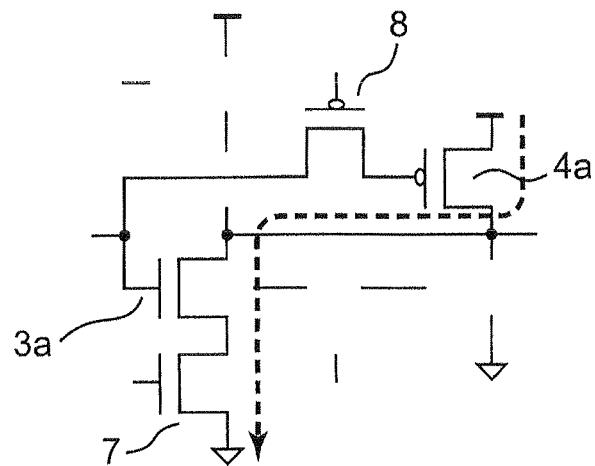


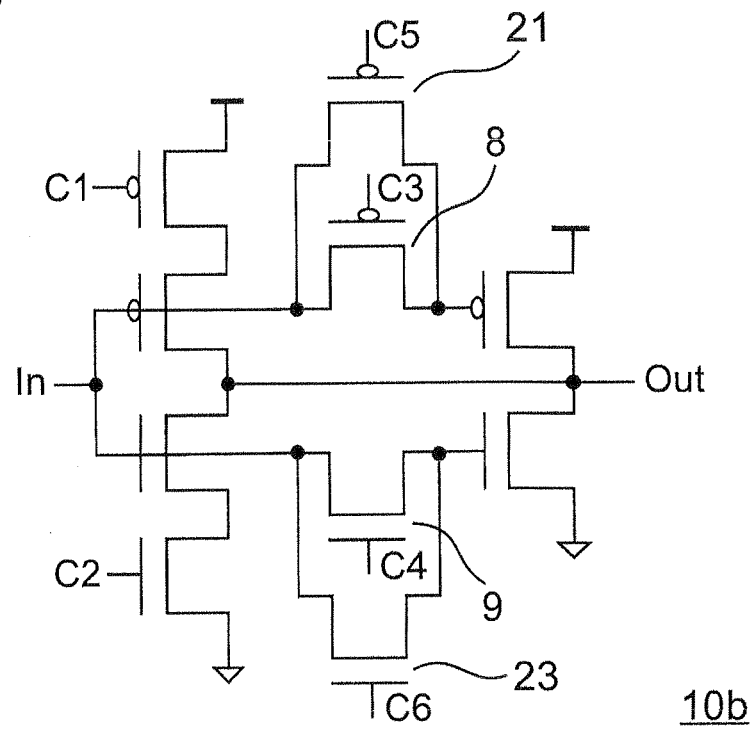
Fig.5

Fig. 6

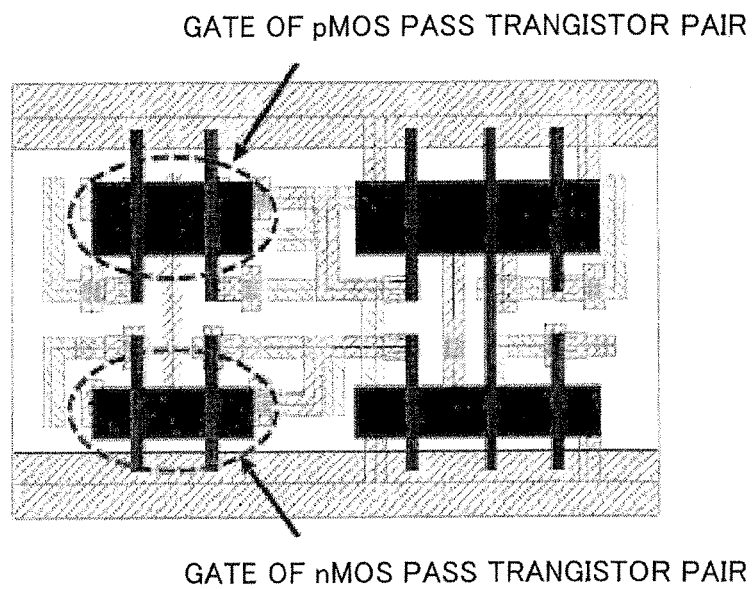


Fig. 7A

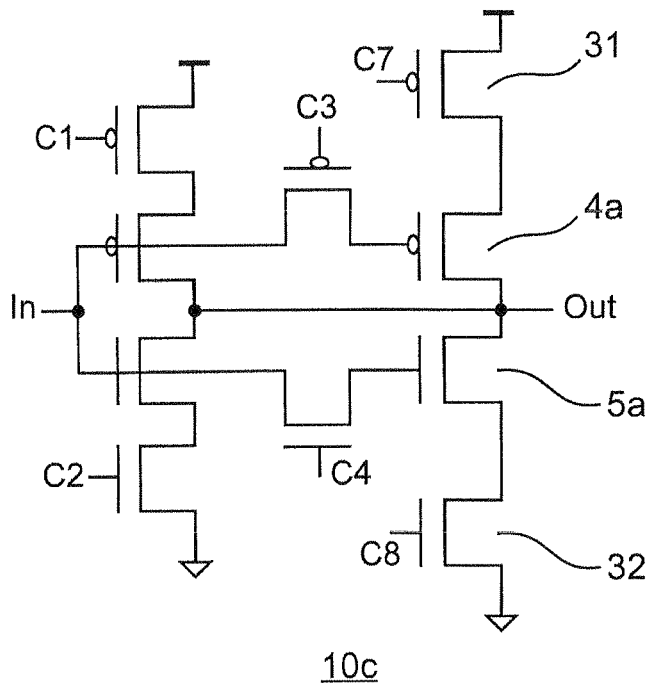


Fig. 7B

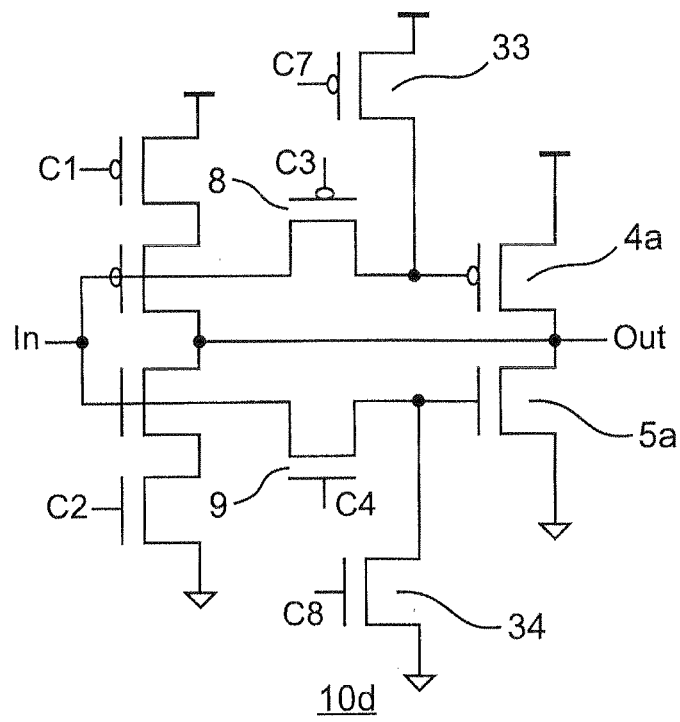


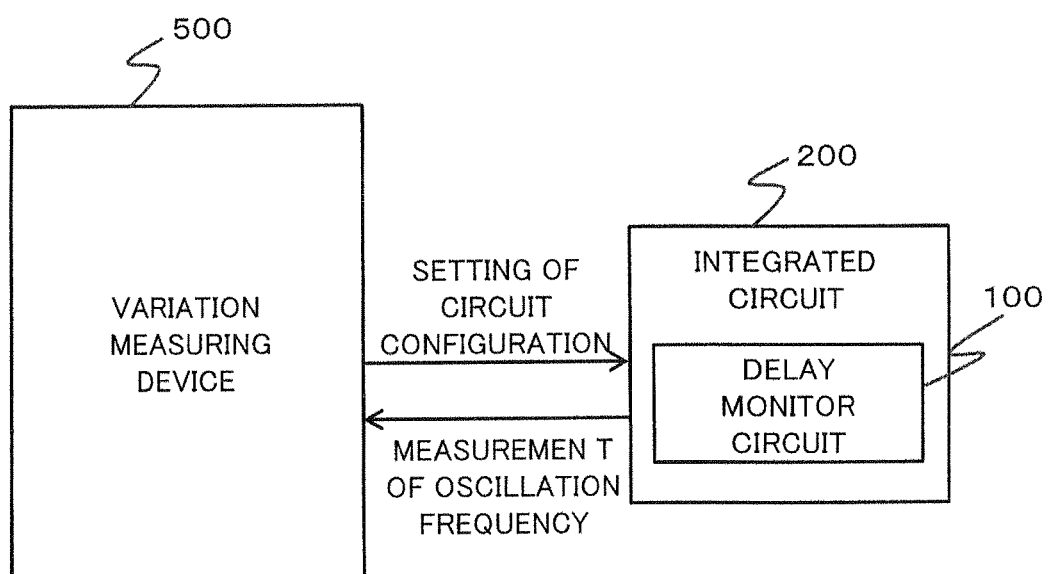
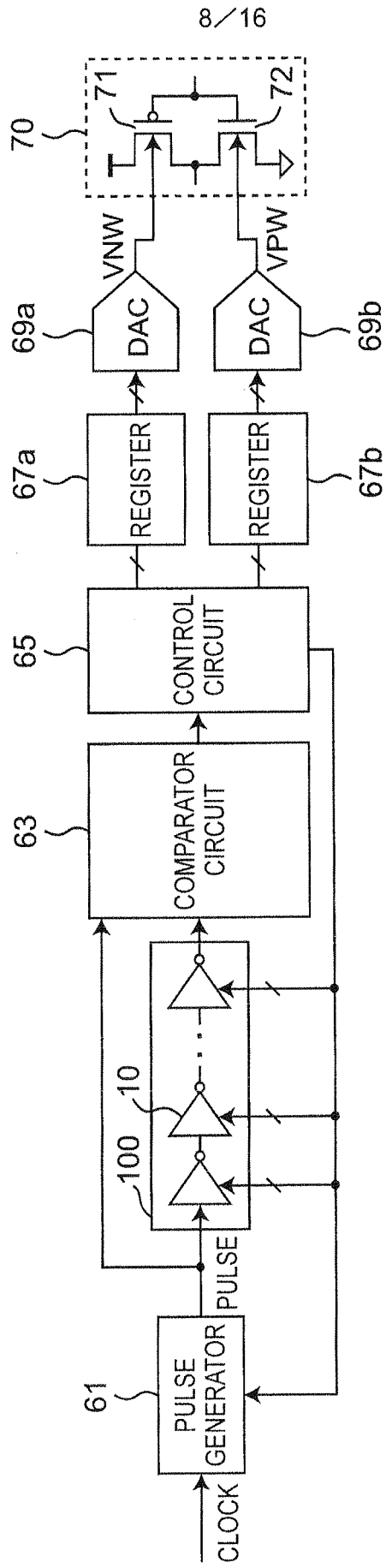
Fig. 8

Fig. 9



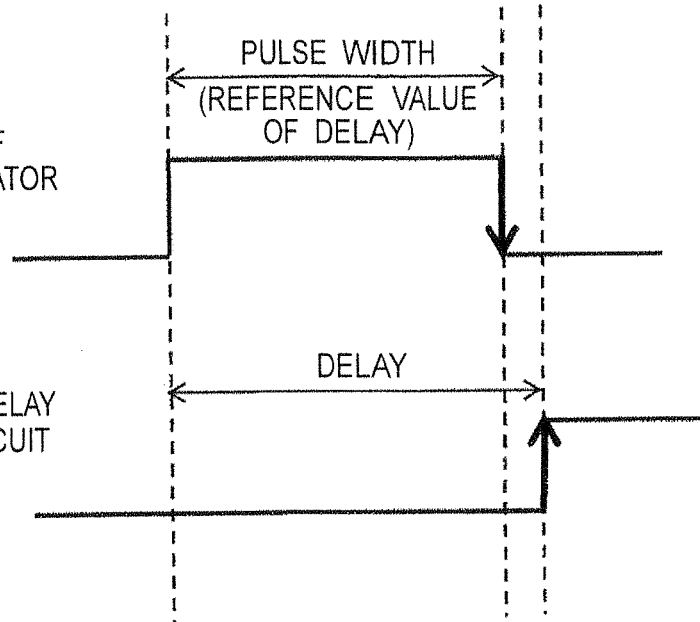
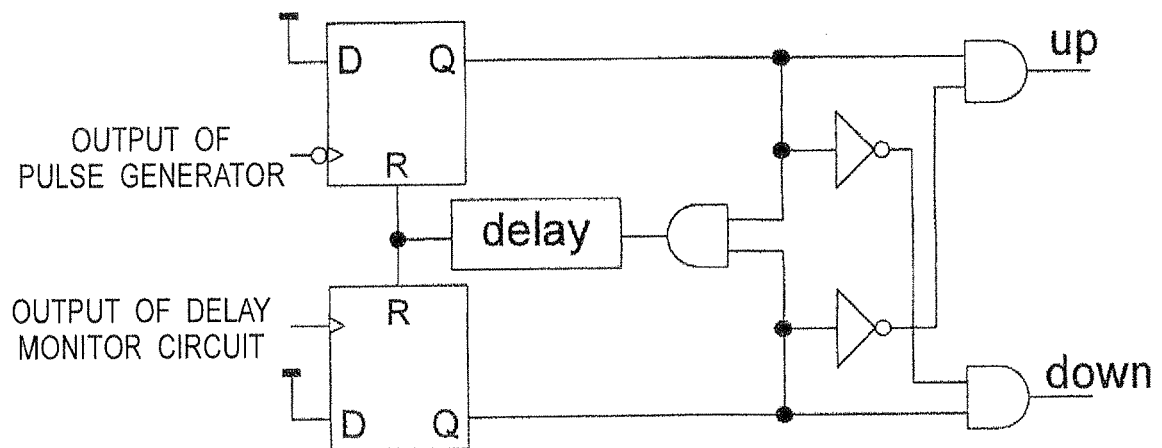
*Fig.10A*OUTPUT OF
PULSE GENERATOR*Fig.10B*OUTPUT OF DELAY
MONITOR CIRCUIT*Fig.11*

Fig.12

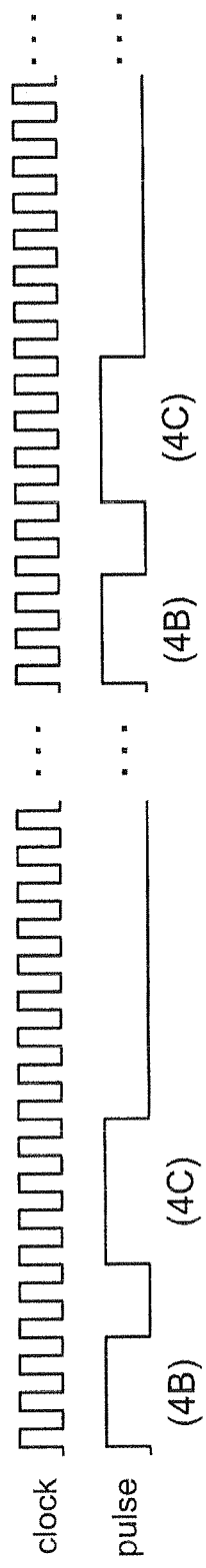


Fig. 13A

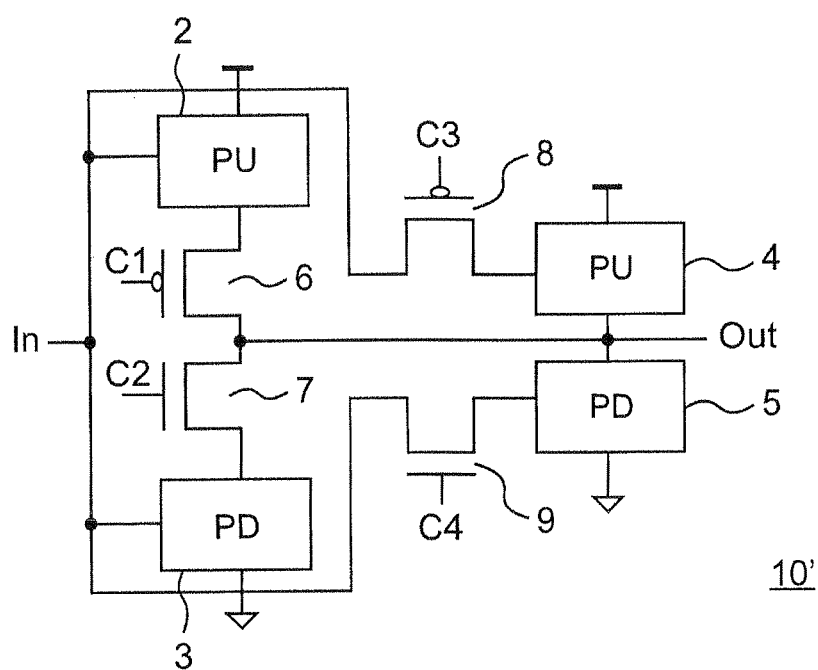


Fig. 13B

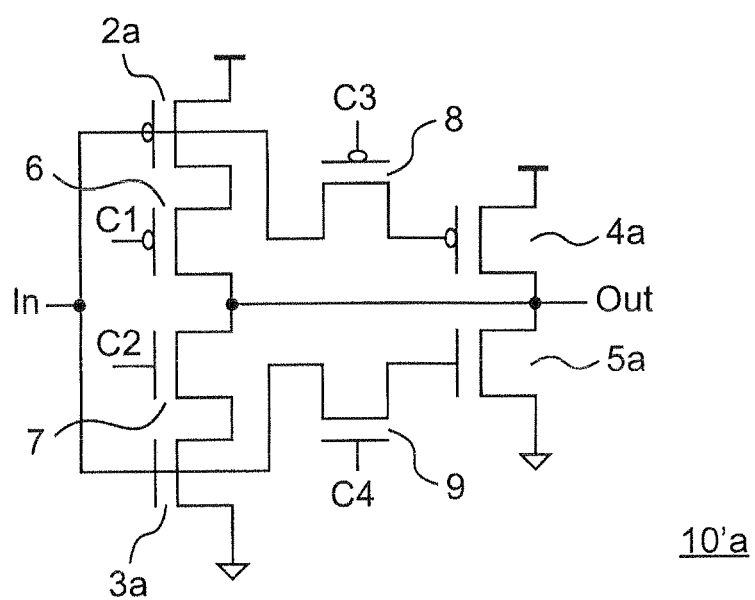


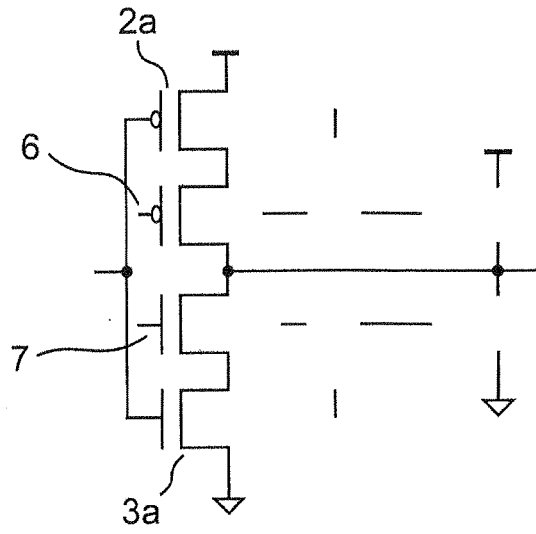
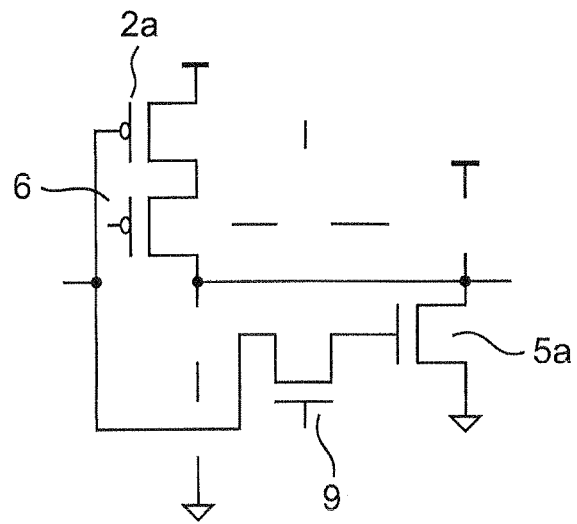
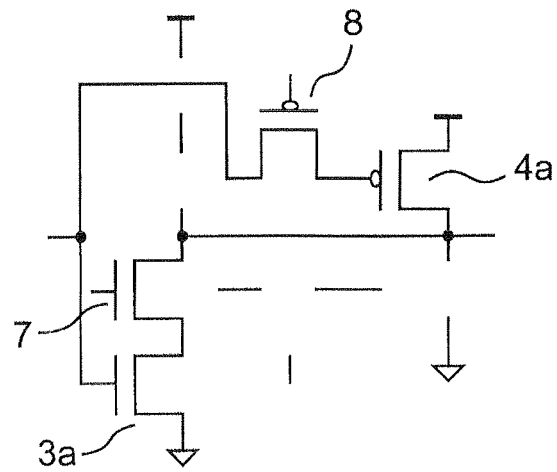
Fig. 14A*Fig. 14B**Fig. 14C*

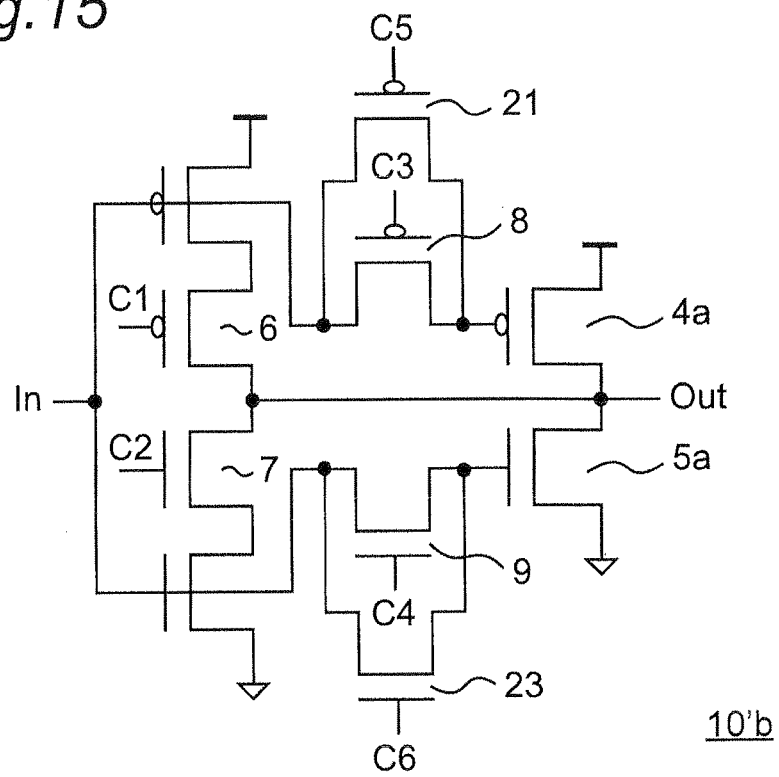
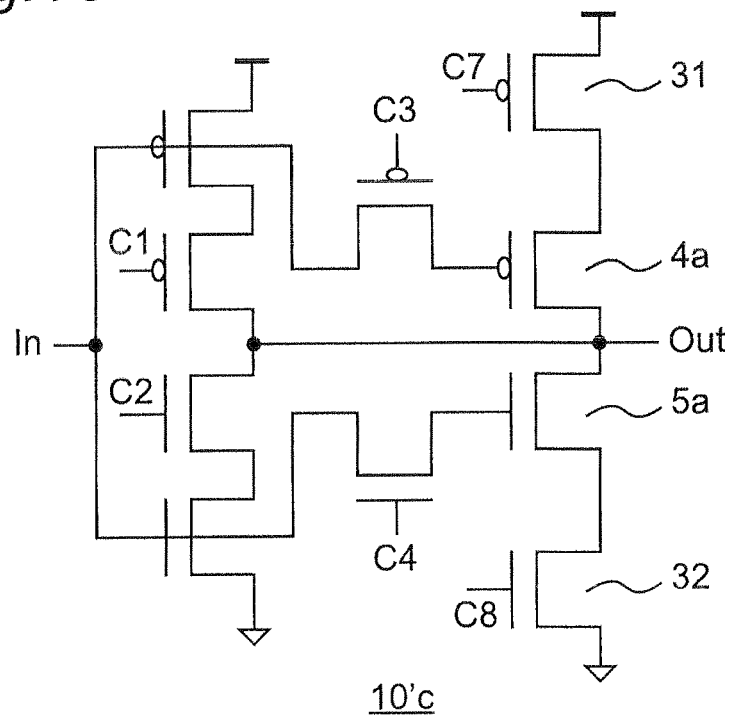
Fig. 15*Fig. 16*

Fig.17

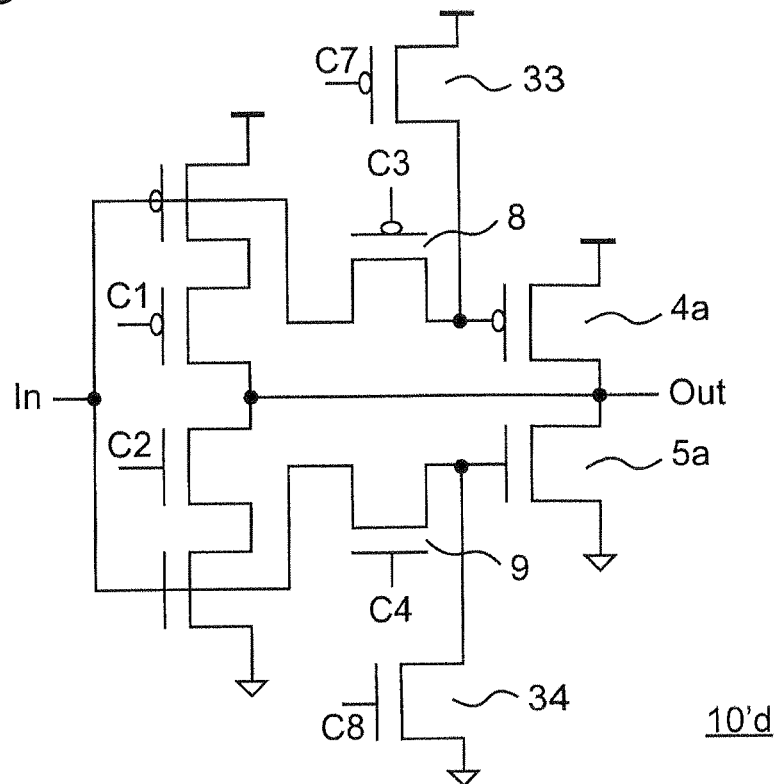


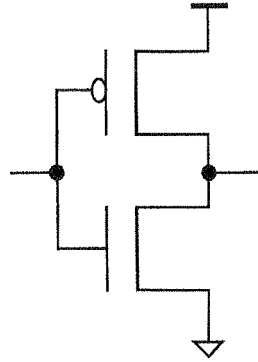
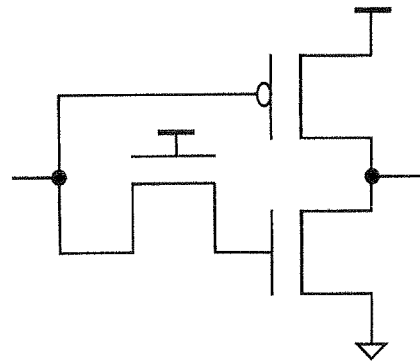
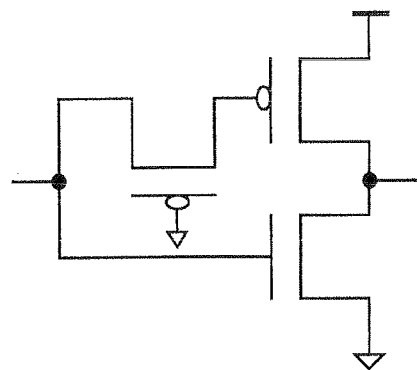
Fig.18A*Fig.18B**Fig.18C*

Fig.19