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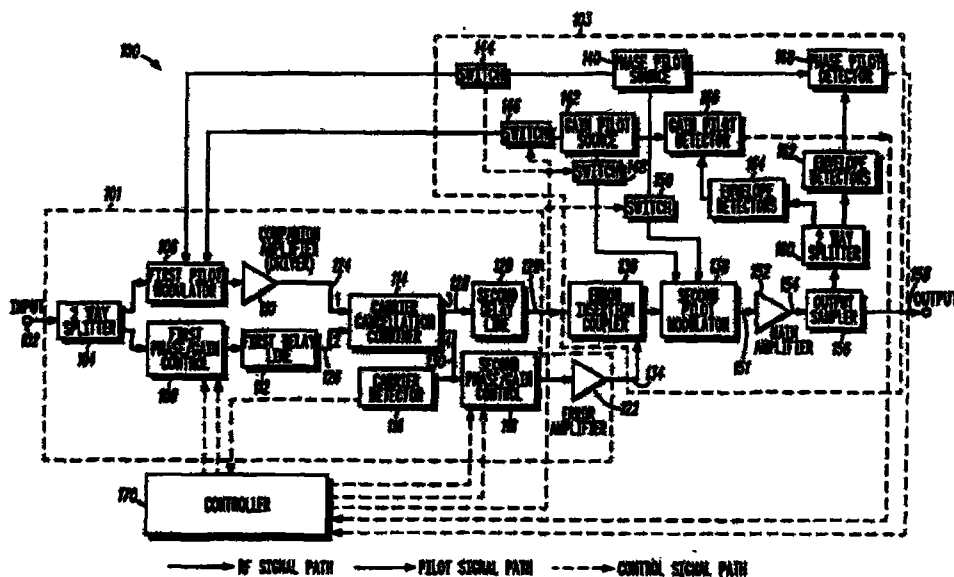
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(54) Title: APPARATUS AND METHOD FOR PERFORMING ERROR CORRECTED AMPLIFICATION IN A RADIO FREQUENCY SYSTEM



(57) Abstract

An error corrected amplifier circuit (100) for use in a radio frequency system is provided. The error corrected amplifier circuit (100) includes a first pilot modulation stage (101) and a second pilot modulation stage (103). The first pilot modulation stage (101) creates a first amplified signal (129) having an error component and a first error signal (134) indicative of the error component. The second pilot modulation stage (103) receives the first amplified signal (129) and the error signal (134) and generates an error reduced amplified signal (158).