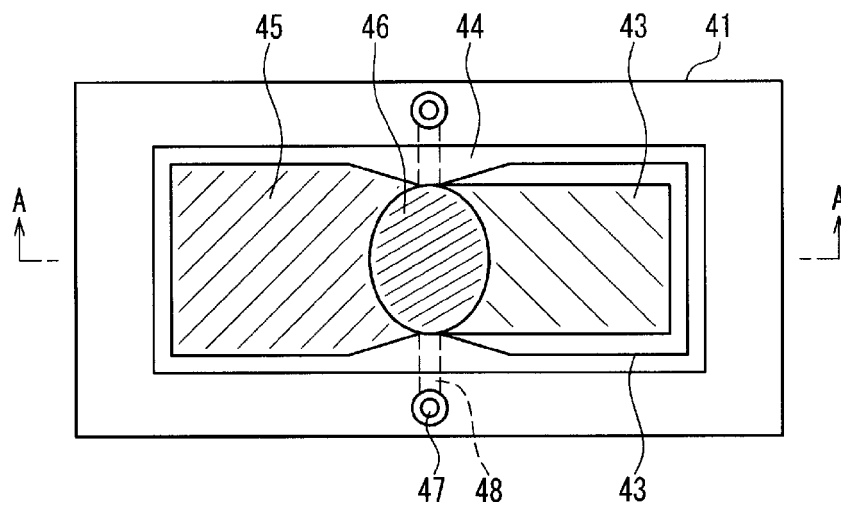
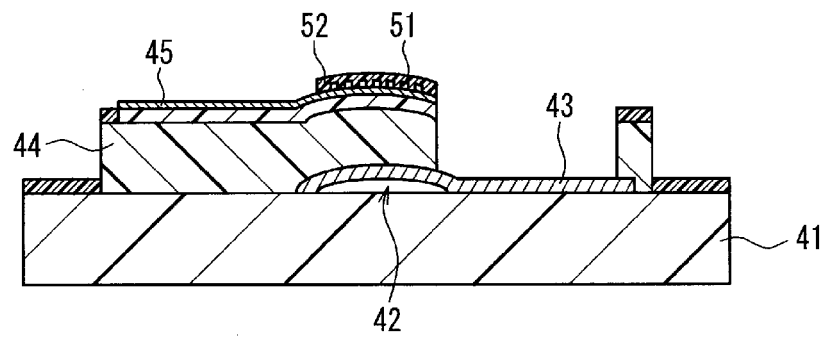


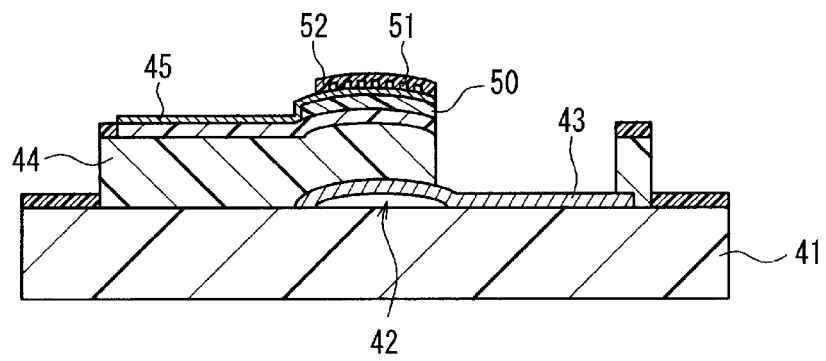
[図1A]



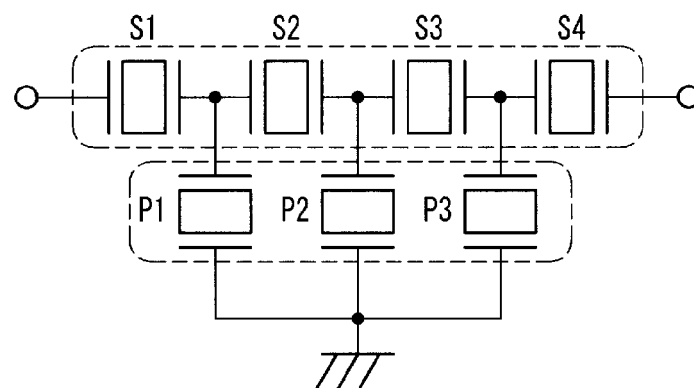
[図1B]



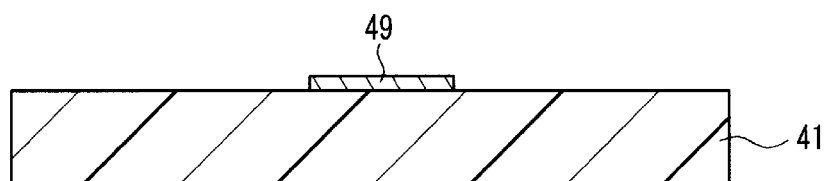
[図1C]



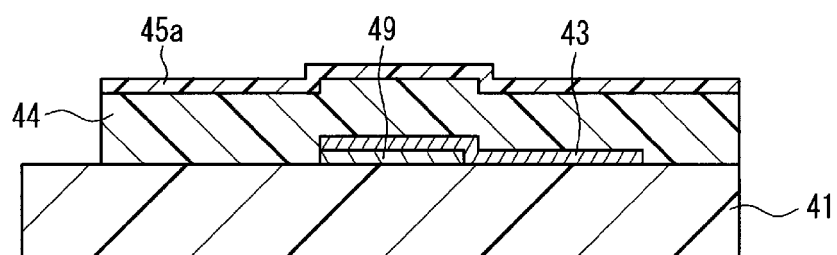
[図2]



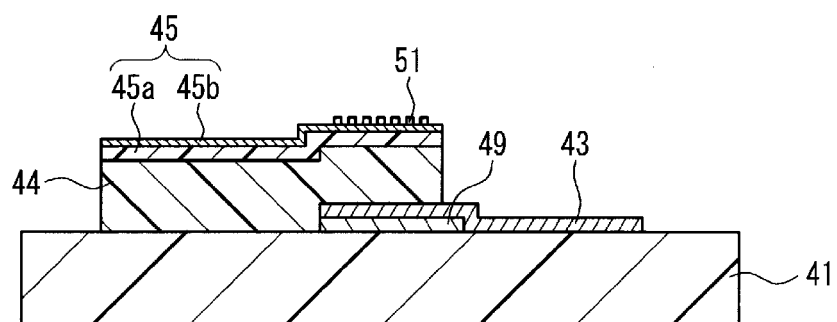
[図3A]



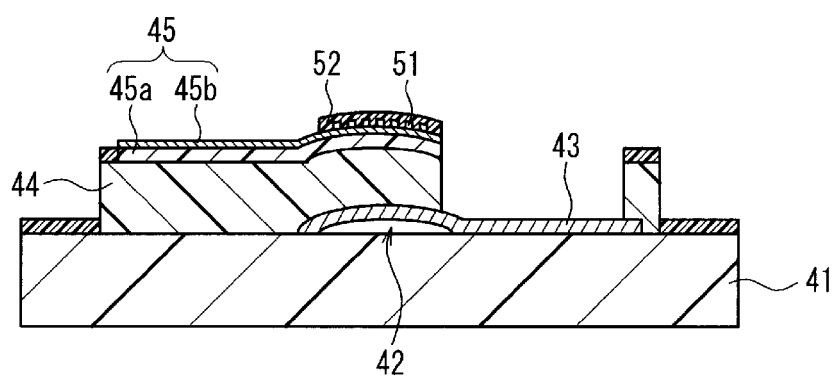
[図3B]



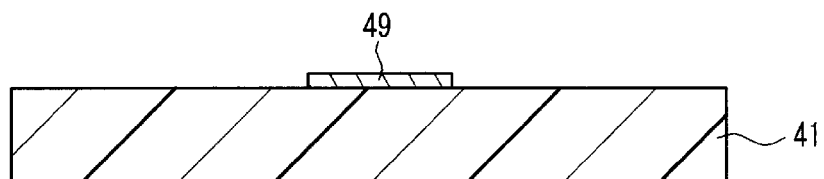
[図3C]



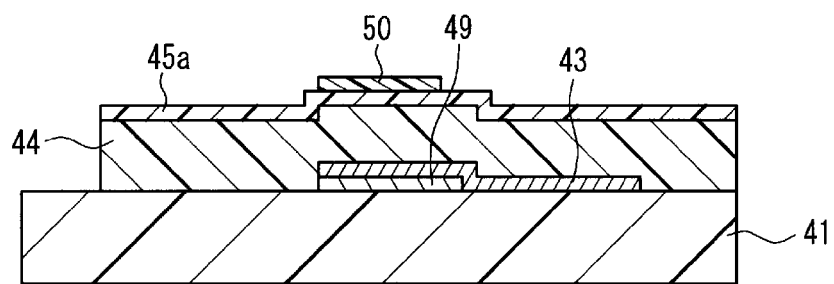
[図3D]



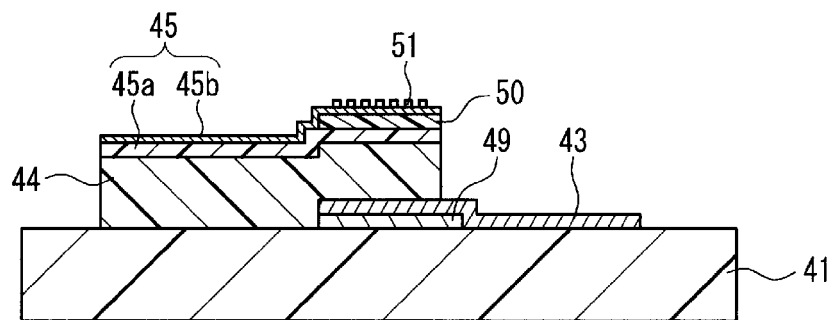
[図4A]



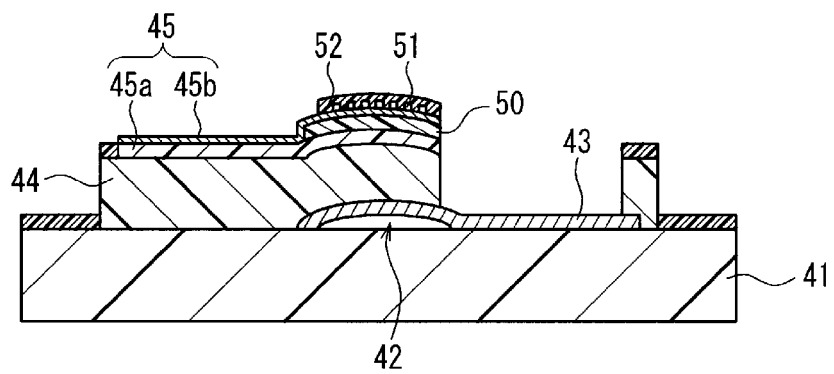
[図4B]



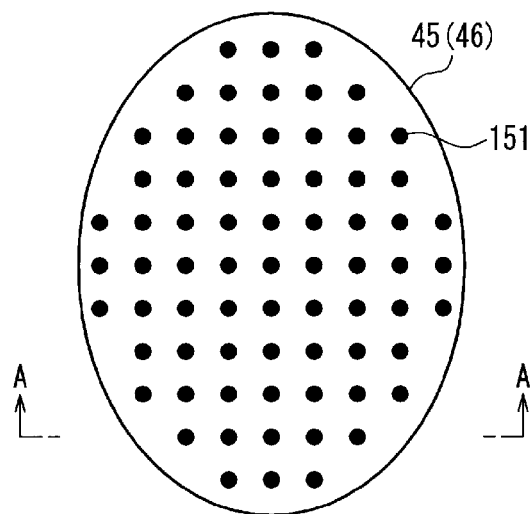
[図4C]



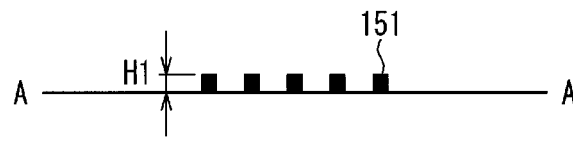
[図4D]



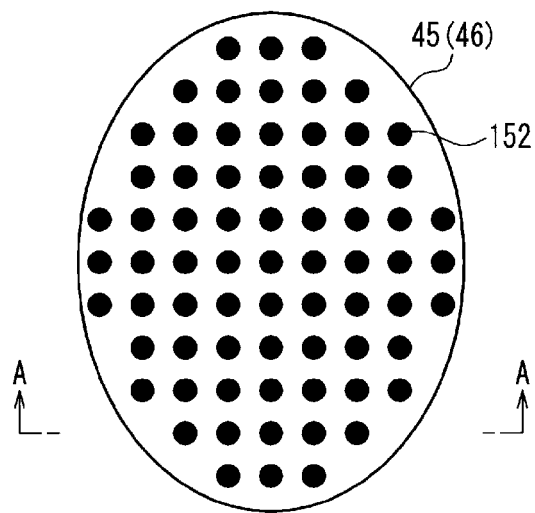
[図5A]



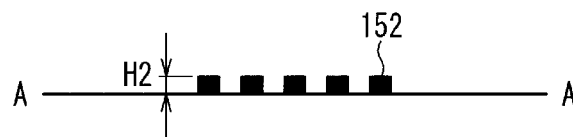
[図5B]



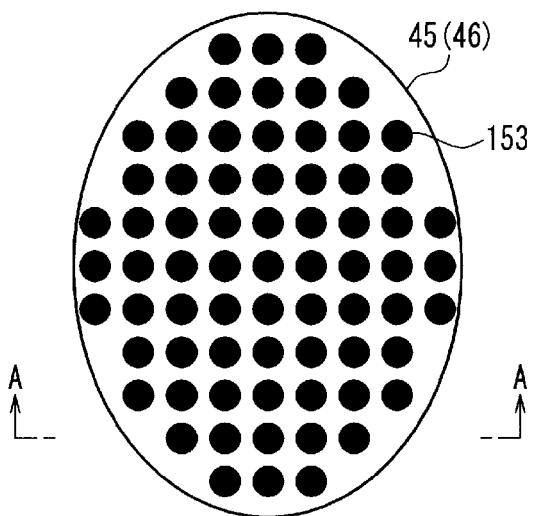
[図5C]



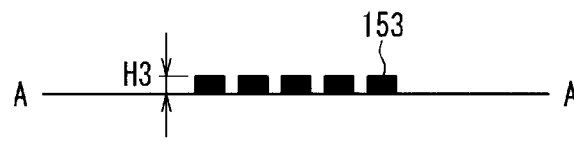
[図5D]



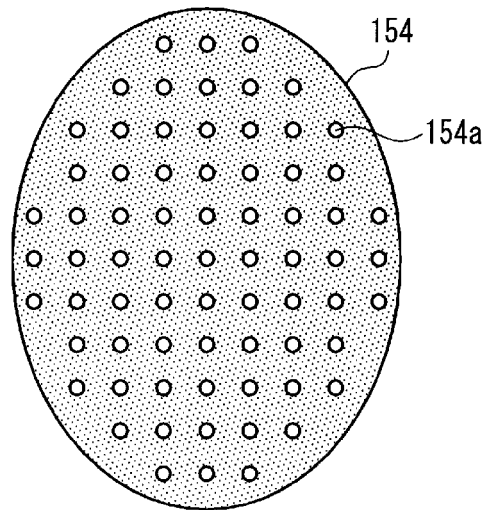
[図5E]



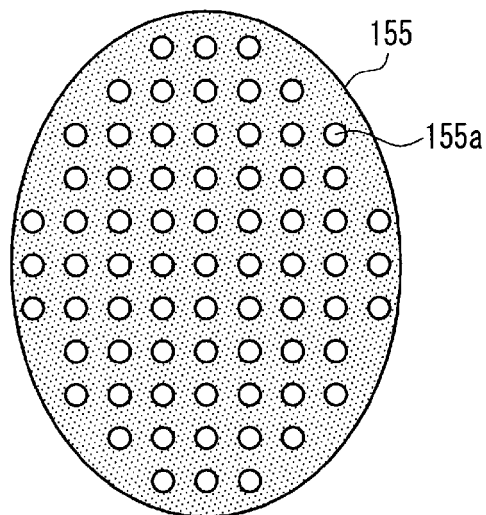
[図5F]



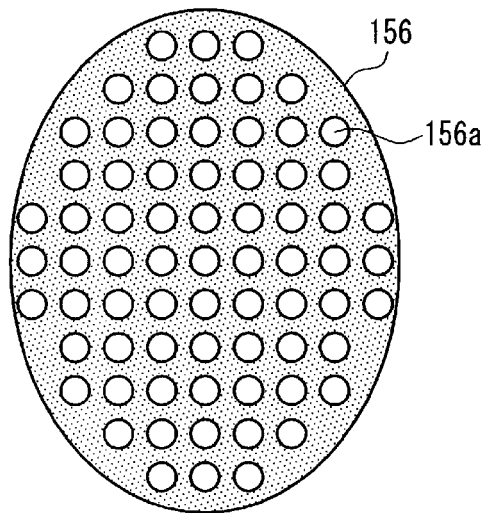
[図6A]



[図6B]

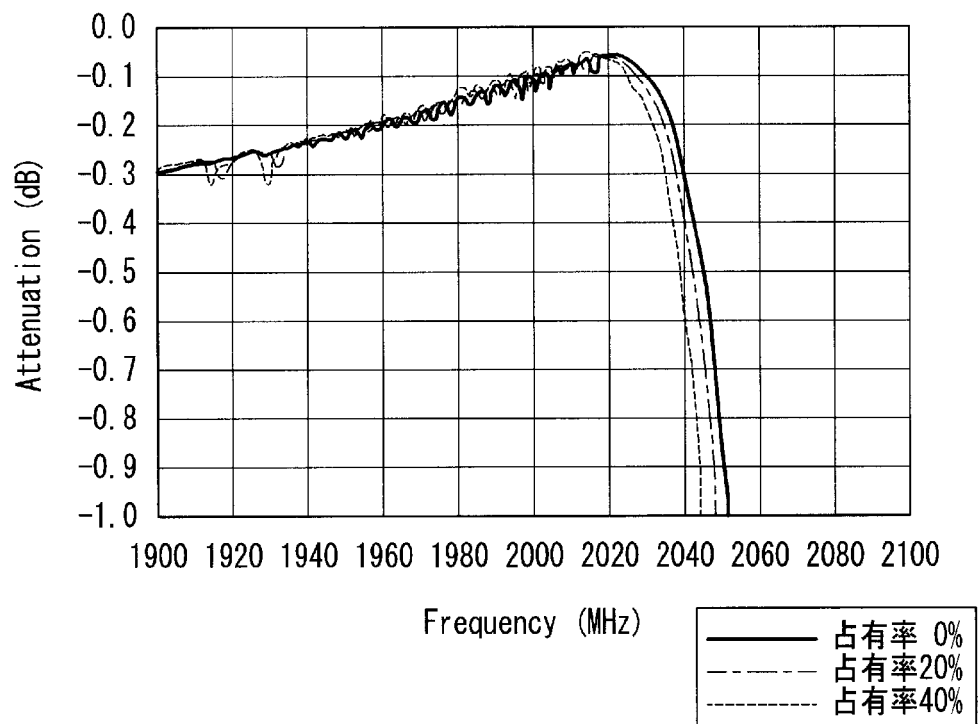


[図6C]

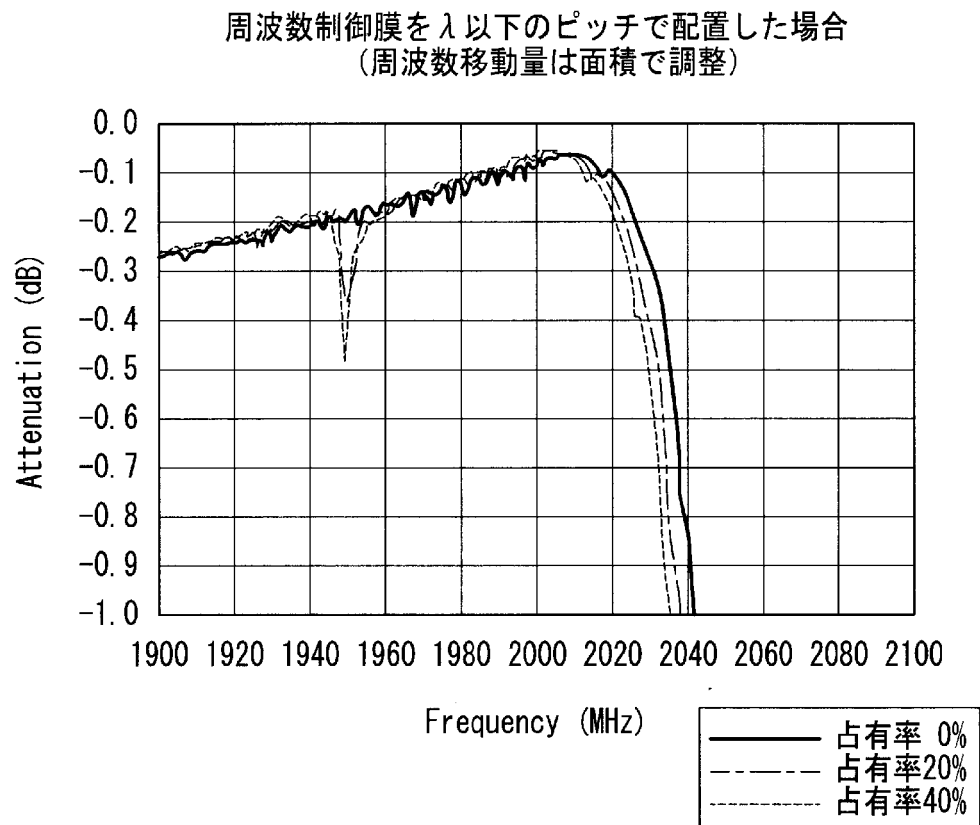


[図7A]

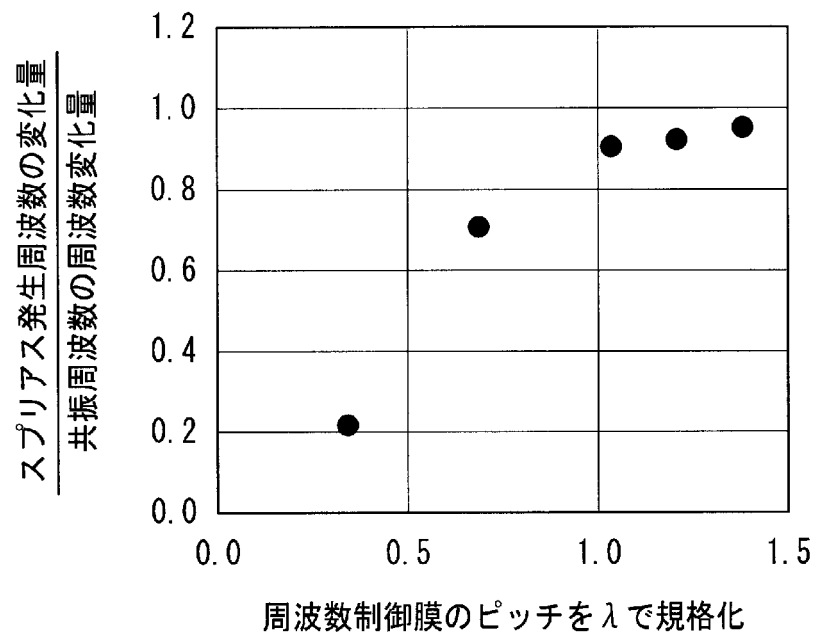
周波数制御膜を λ 以上のピッチで配置した場合
(周波数移動量は面積で調整)



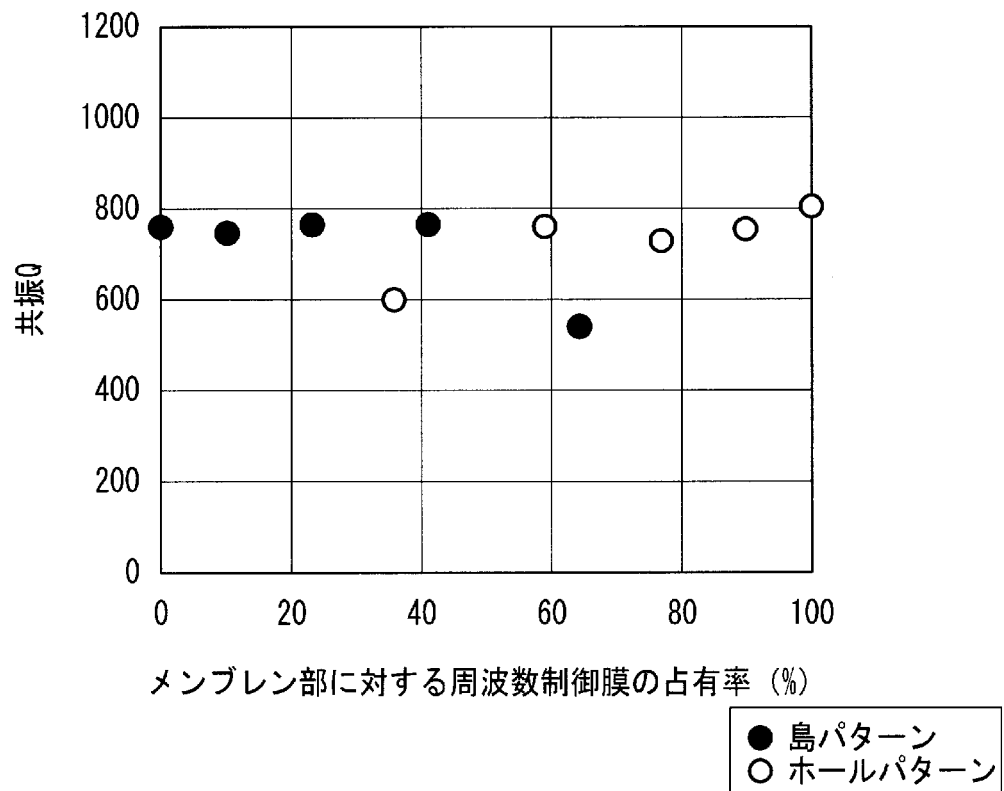
[図7B]



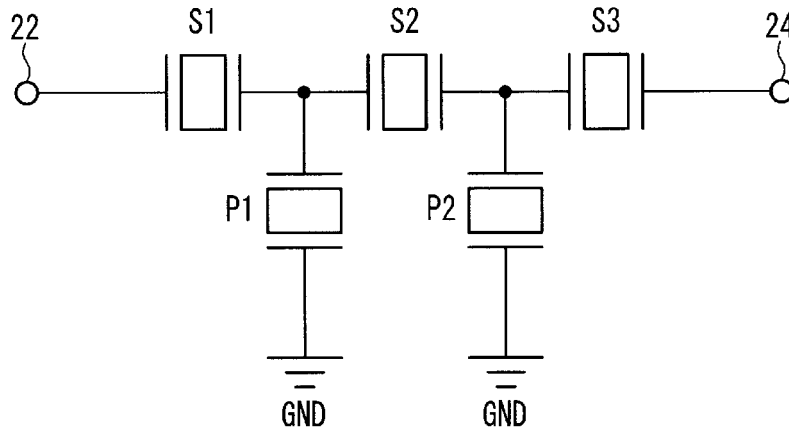
[図8]



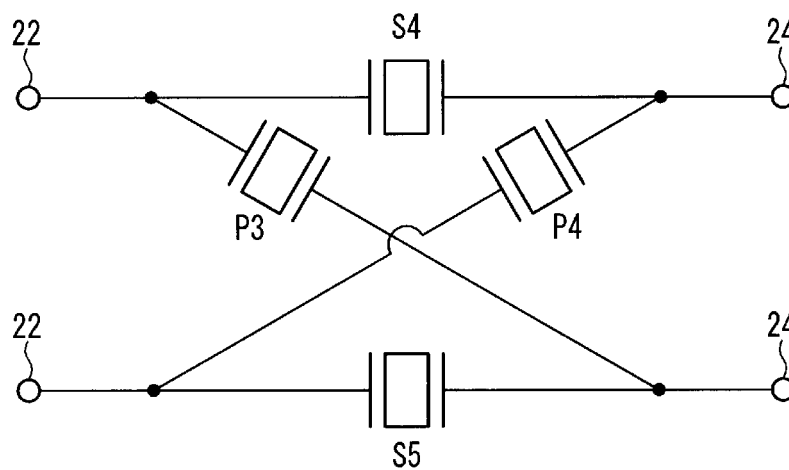
[図9]



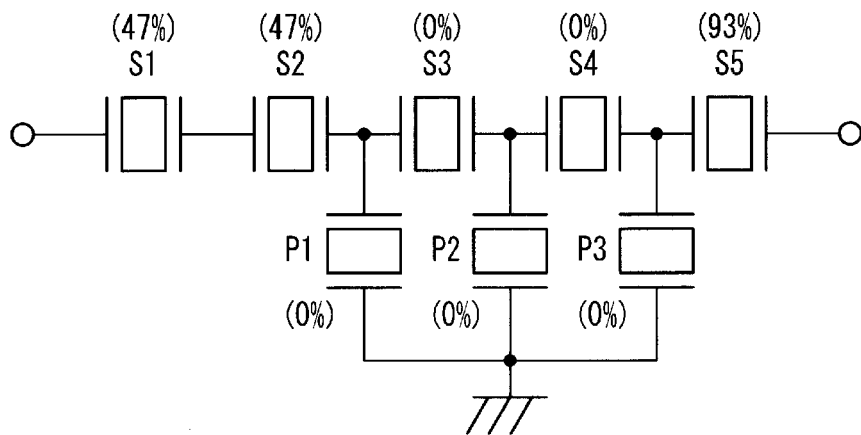
[図10]



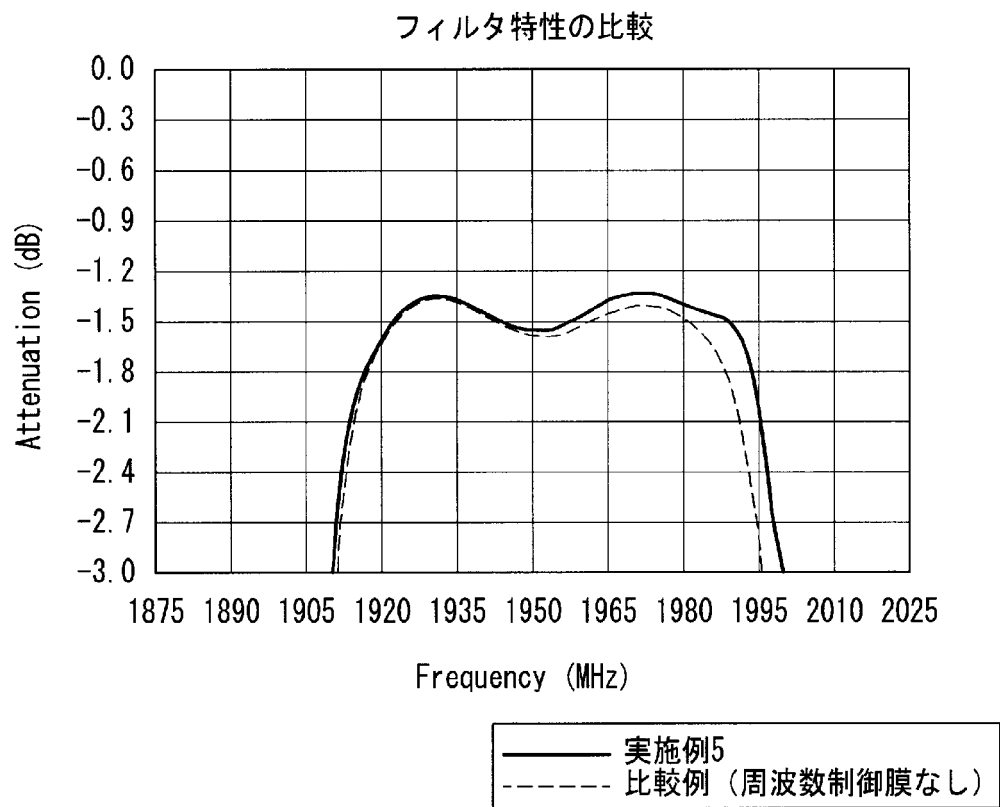
[図11]



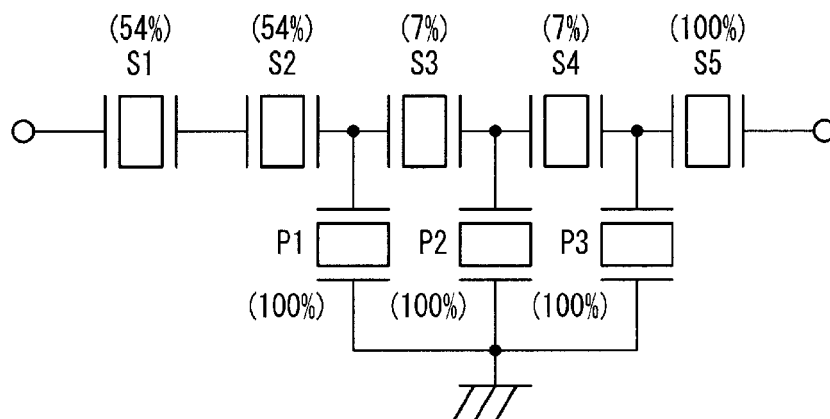
[図12]



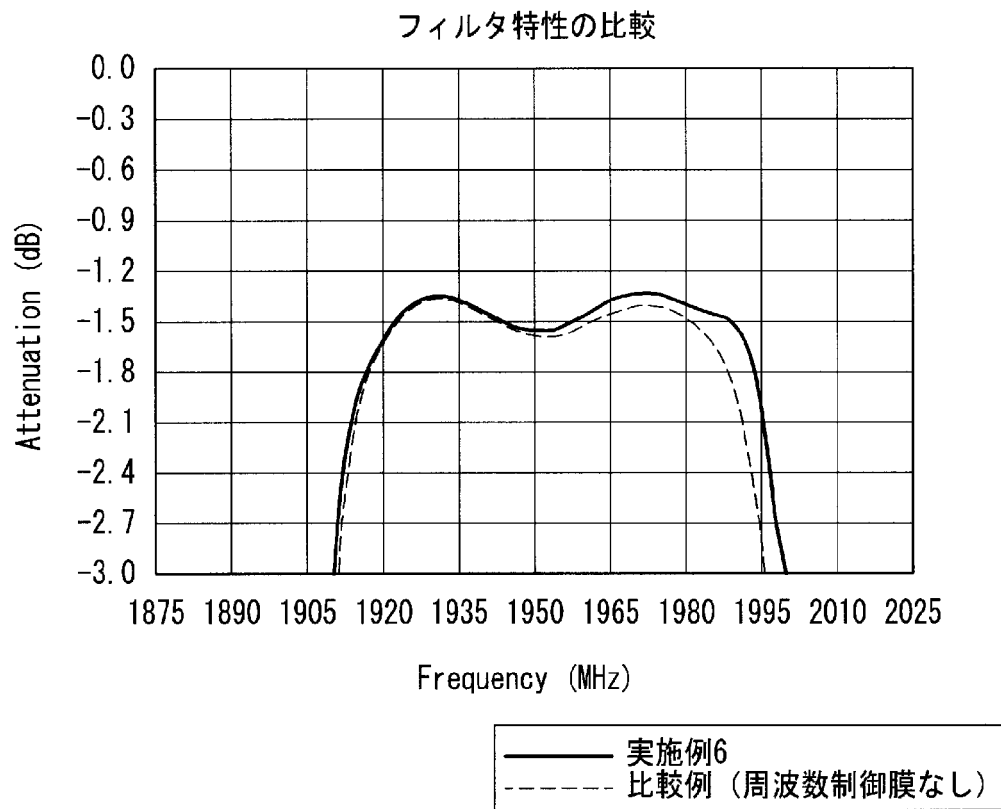
[図13]



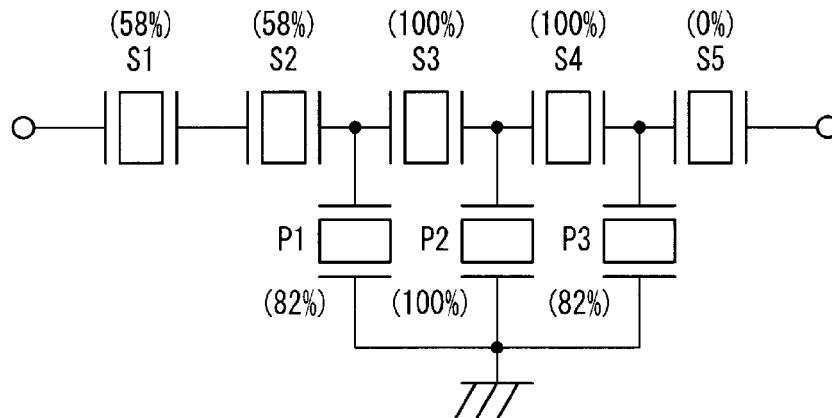
[図14]



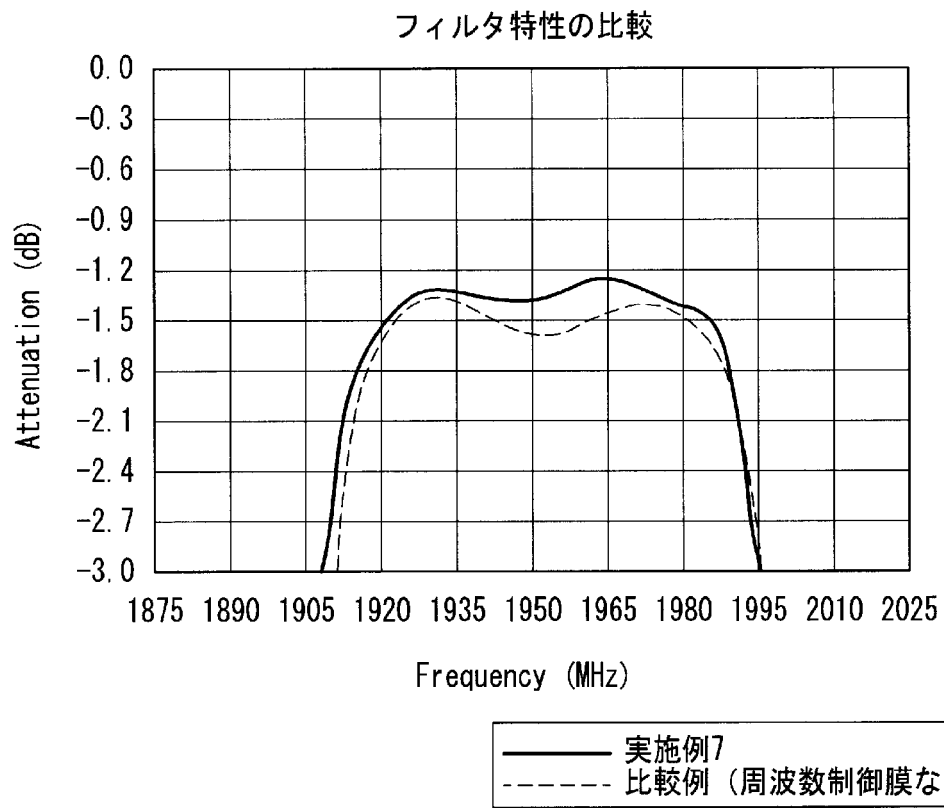
[図15]



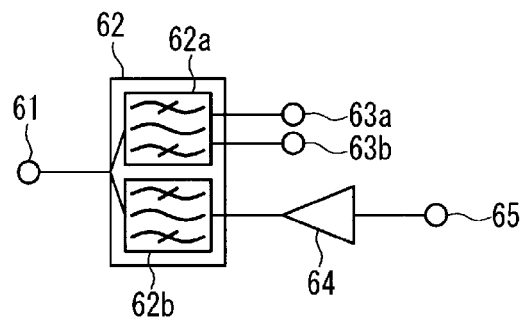
[図16]



[図17]



[図18]



[図19]

