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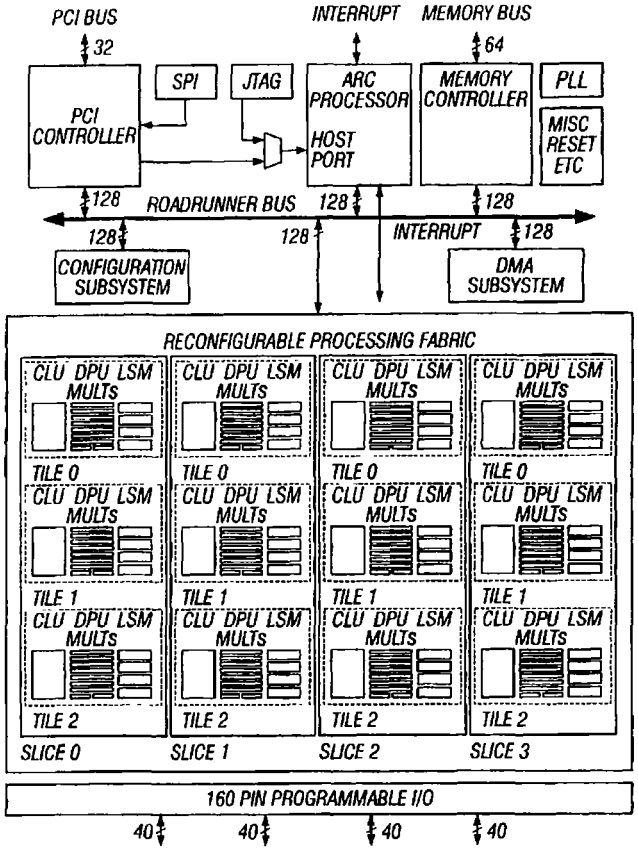
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(54) Title: METHOD OF SCHEDULING IN A RECONFIGURABLE HARDWARE ARCHITECTURE WITH MULTIPLE HARDWARE CONFIGURATIONS



(57) Abstract: A scheduler for a reconfigurable chip is described in which multiple configurations for single function are stored. The scheduler has the option of selecting any one of the configurations The system increase the efficiency of the reconfiguration chips operation

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