

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
22 March 2001 (22.03.2001)

PCT

(10) International Publication Number
WO 01/19478 A1

(51) International Patent Classification: A63F 13/06

(21) International Application Number: PCT/JP00/06119

(22) International Filing Date:
8 September 2000 (08.09.2000)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
11/297205 11 September 1999 (11.09.1999) JP

(71) Applicant: SONY COMPUTER ENTERTAINMENT
INC. [JP/JP]; 1-1, Akasaka 7-chome, Minato-ku, Tokyo
107-0052 (JP).

Computer Entertainment Inc., 1-1, Akasaka 7-chome, Mi-
nato-ku, Tokyo 107-0052 (JP). SAKAKURA, Youtaro;
Alps Electric Co., Ltd., 1-7, Yukigaya-otsuka-cho, Ota-ku,
Tokyo 145-8501 (JP). MITA, Koji; Alps Electric Co.,
Ltd., 1-7, Yukigaya-otsuka-cho, Ota-ku, Tokyo 145-8501
(JP).

(74) Agent: YAMAMOTO, Toshitake; 301, Ogikubo Sunny
Garden, 28-9, Ogikubo 4-chome, Suginami-ku, Tokyo 167-
0051 (JP).

(81) Designated States (national): AU, BR, CA, CN, JP, KR,
MX, NZ, RU, SG.

(84) Designated States (regional): European patent (AT, BE,
CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC,
NL, PT, SE).

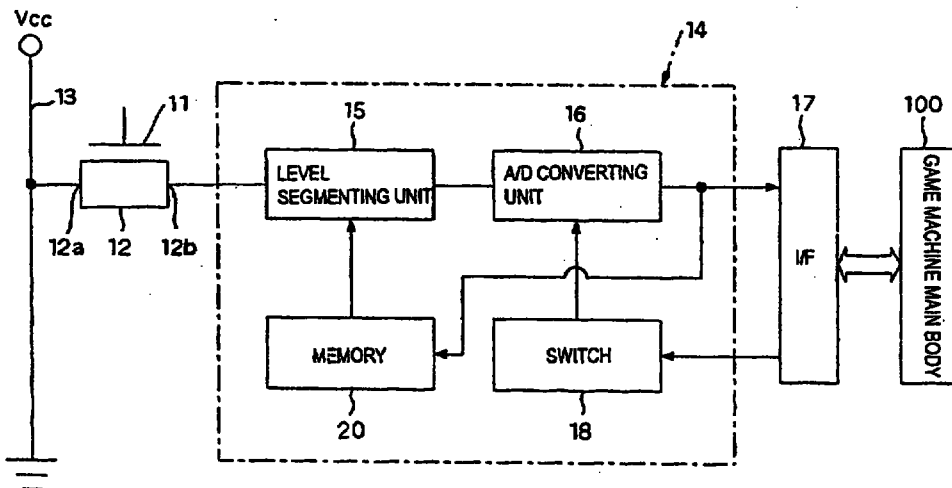
Published:

— With international search report.

(72) Inventors: OGATA, Hiroki; Sony Computer Enter-
tainment Inc., 1-1, Akasaka 7-chome, Minato-ku, Tokyo
107-0052 (JP). TAGAWA, Kazusato; Sony Computer
Entertainment Inc., 1-1, Akasaka 7-chome, Minato-ku,
Tokyo 107-0052 (JP). NAKAZAWA, Hiroyuki; Sony

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(54) Title: CONTROL APPARATUS AND OUTPUTTING SIGNAL ADJUSTING METHOD THEREFOR



(57) Abstract: An analog operation is possible by a pressing operation of a controller which is normally used for conventional digital operation. A detecting device (e.g., a pressure-sensitive device) for outputting an analog signal corresponding to the pressing operation by the controller is provided, the analog signal which is outputted by the detecting device is segmented into levels by a level segmenting unit, and the segmented analog signal is converted into a digital signal having a plurality of bits corresponding to the output level by an A/D converting unit. A segmenting-range setting unit (such as a memory) for setting the range of output levels of the analog signal which is segmented by the level segmenting unit is also provided. The level segmenting unit segments the output level of the analog signal which is outputted by the detecting device within the range which is set by the segmenting-range setting unit.