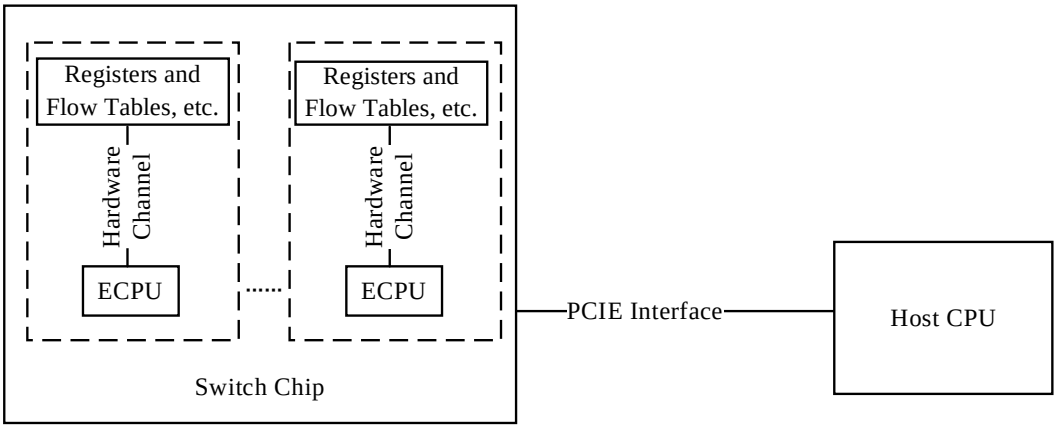

Registers and Flow Tables, etc.
Hardware Channel
ECPU
Registers and Flow Tables, etc.
Hardware Channel
ECPU
PCIE Interface
Host CPU
Switch Chip

FIG. 1

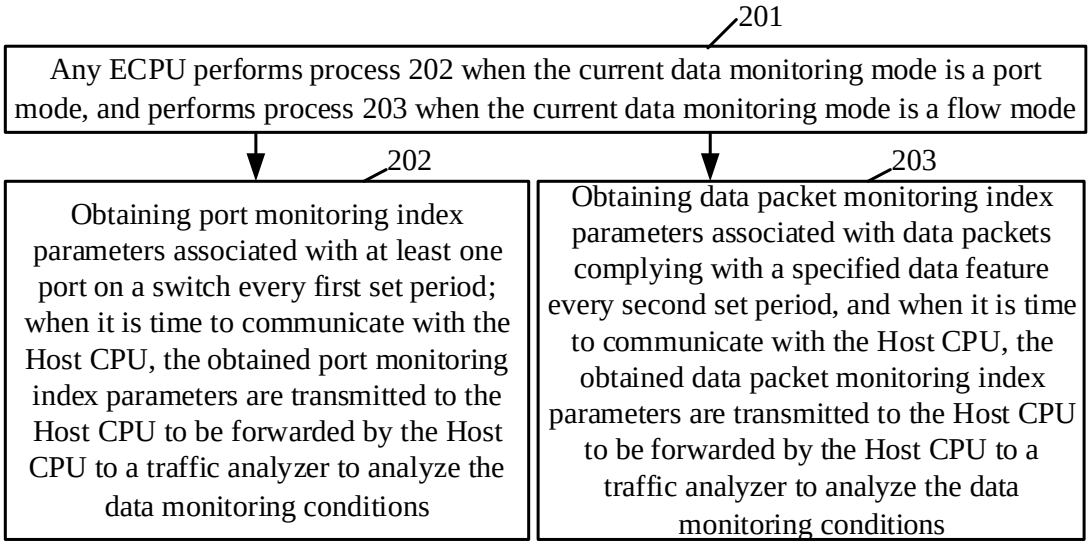

201
Any ECPU performs process 202 when the current data monitoring mode is a port mode, and performs process 203 when the current data monitoring mode is a flow mode
202
Obtaining port monitoring index parameters associated with at least one port on a switch every first set period; when it is time to communicate with the Host CPU, the obtained port monitoring index parameters are transmitted to the Host CPU to be forwarded by the Host CPU to a traffic analyzer to analyze the data monitoring conditions
203
Obtaining data packet monitoring index parameters associated with data packets complying with a specified data feature every second set period, and when it is time to communicate with the Host CPU, the obtained data packet monitoring index parameters are transmitted to the Host CPU to be forwarded by the Host CPU to a traffic analyzer to analyze the data monitoring conditions

FIG. 2

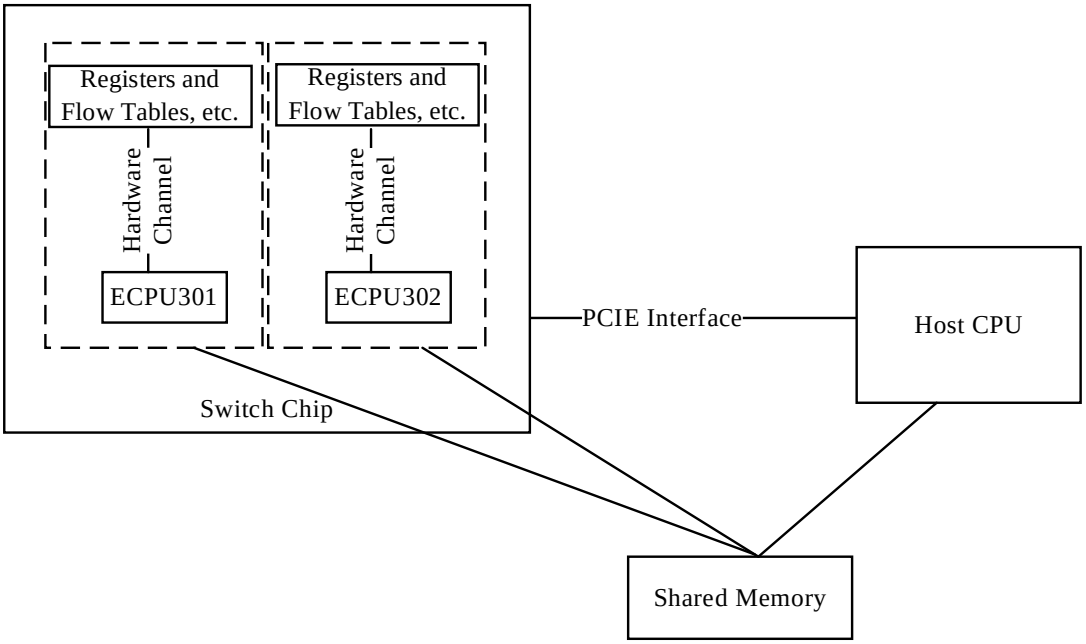

Registers and Flow Tables, etc.
Hardware Channel
ECPU301
Registers and Flow Tables, etc.
Hardware Channel
ECPU302
PCIE Interface
Host CPU
Switch Chip
Shared Memory

FIG. 3

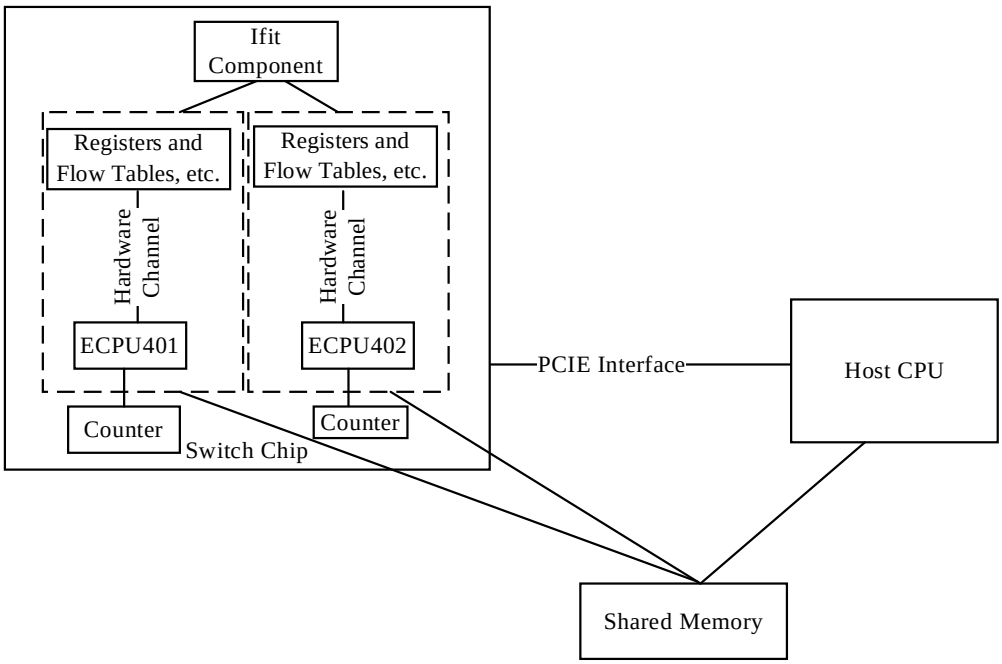

Ifit Component
Registers and Flow Tables, etc.
Hardware Channel
ECPU401
Counter
Registers and Flow Tables, etc.
Hardware Channel
ECPU402
Counter
PCIE Interface
Host CPU
Switch Chip
Shared Memory

FIG. 4

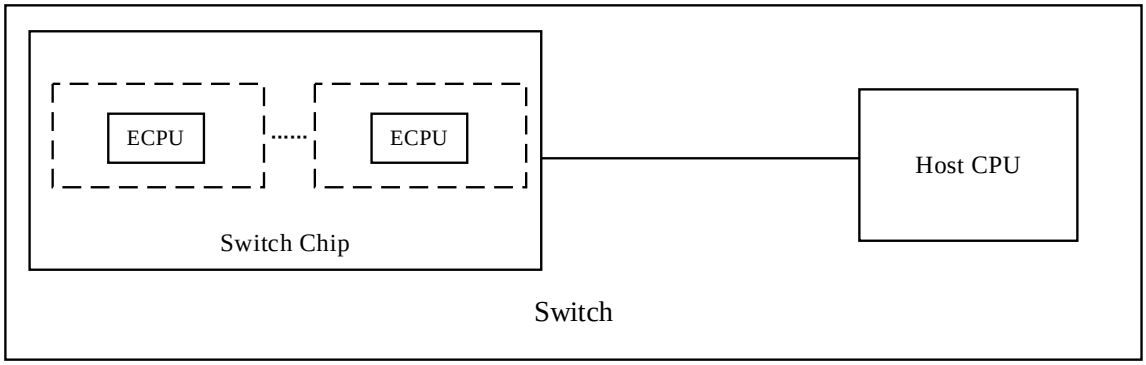

ECPU
.....
ECPU
Switch Chip
Host CPU
Switch

FIG. 5

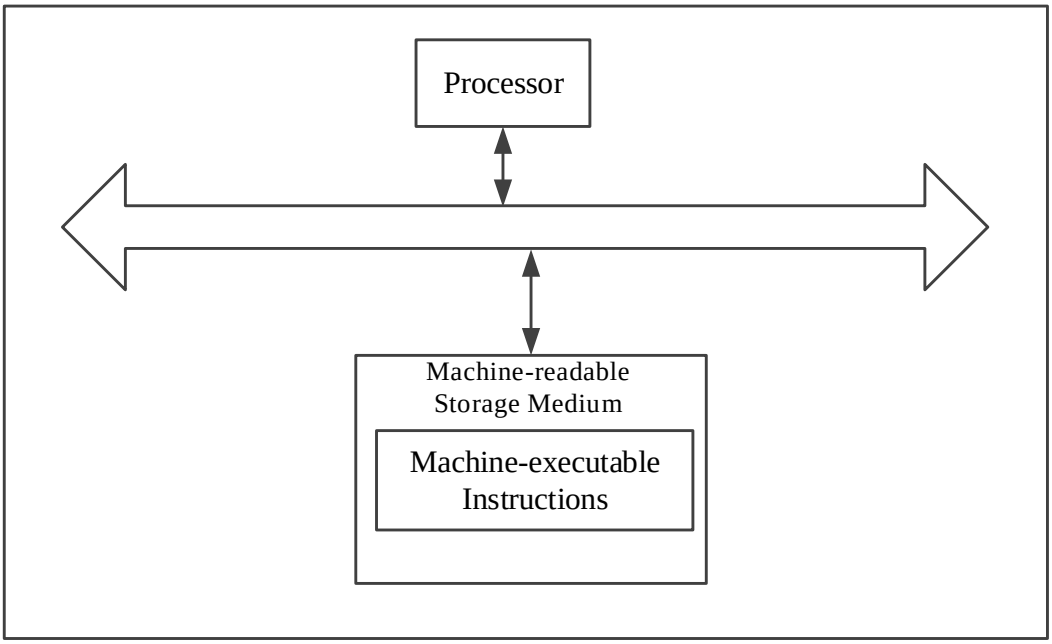

Processor
Machine-readable
Storage Medium
Machine-executable
Instructions

FIG. 6