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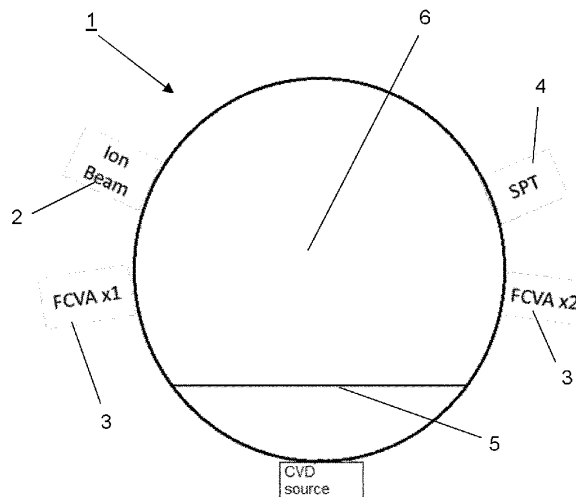


Fig. 1

(57) Abstract: A substrate coated with a multi-layer coating, comprising in order: i. a substrate, ii. a seed layer, iii. a ta-C containing layer, and iv. a DLC containing layer, wherein the coating has a surface electrical resistance of from $10^5 \Omega/\text{sq}$ to $10^9 \Omega/\text{sq}$, and methods of making the same.

[Continued on next page]

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