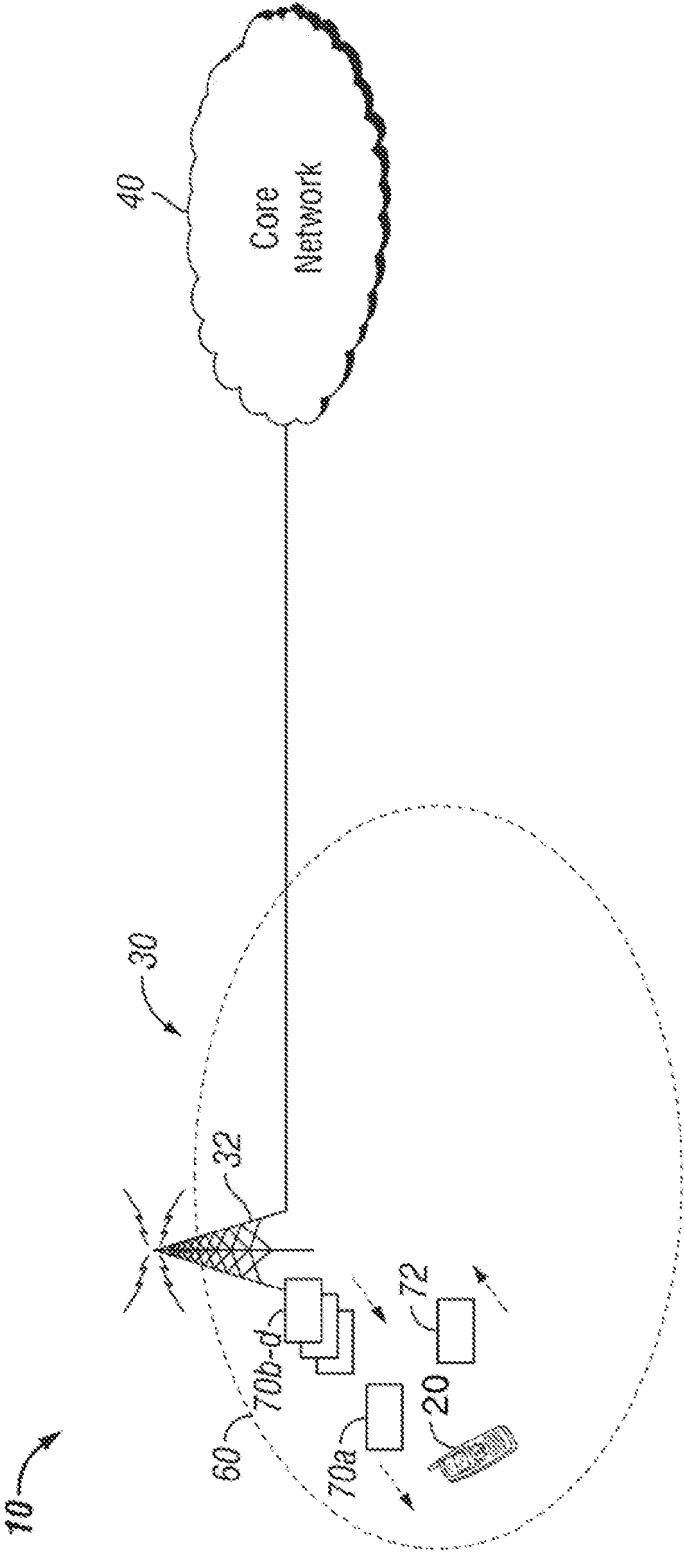
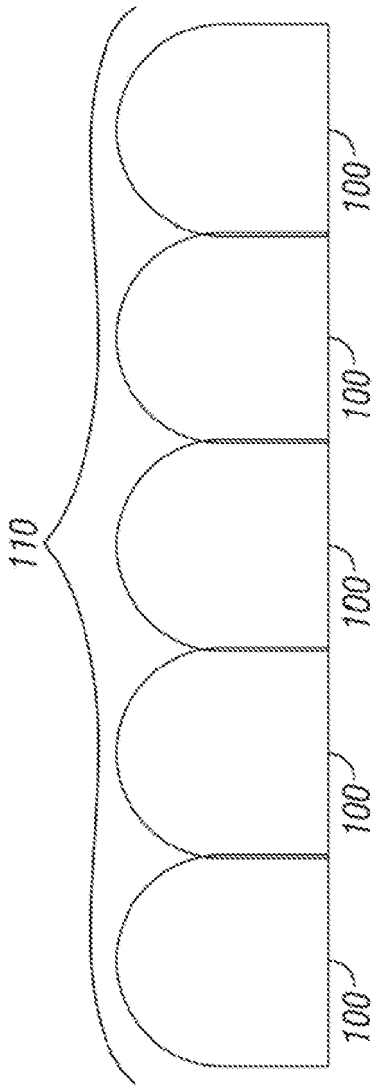




Example Power Control Parameters
(2 configured CCs, no spatial feedback bundling)

SCHEDULED CC INDICES	INCREMENTAL POWER SETTING FOR SCHEDULING SCC
0,1	+0 dB
1	+0 dB

FIG. 3A

Example Power Control Parameters
(3 configured CCs, no spatial feedback bundling)

SCHEDULED CC INDICES	INCREMENTAL POWER SETTING FOR SCHEDULING SCC
0,1,2	+0 dB
0,1	+0 dB
0,2	+0 dB
1,2	+0 dB
1	+0 dB
2	+0 dB

FIG. 3B

Example Power Control Parameters
(3 Configured CCs, No Spatial Feedback Bundling)

SCHEDULED CC INDICES	INCREMENTAL TPC SETTING IN DCI FOR SCHEDULING SCC
0,1,2,3	+0 dB
0,1,2	+0 dB
0,1,3	+1 dB
0,2,3	+0 dB
1,2,3	+1 dB
0,1	+0 dB
0,2	+0 dB
1,2	+0 dB
0,3	+0 dB
1,3	+0 dB
2,3	+1 dB
1	+0 dB
2	+0 dB
3	+0 dB

FIG. 3C

Example Power Control Parameters
(3 configured CCs, no spatial feedback bundling)

SCHEDULED CC INDICES	INCREMENTAL TPC SETTING IN DCI FOR SCHEDULING SCC
0,1,2,3,4	+0 DB
0,1,2,3	+0 DB
0,1,2,4	+0 DB
0,1,3,4	+0 DB
0,2,3,4	+0 DB
1,2,3,4	+0 DB
0,1,2	+0 DB
0,1,3	+1 DB
0,2,3	+0 DB
1,2,3	+1 DB
0,1,4	+1 DB
0,2,4	+0 DB
1,2,4	+1 DB
0,3,4	+0 DB
1,3,4	+1 DB
2,3,4	+1 DB
0,1	+0 DB
0,2	+0 DB
1,2	+0 DB
0,3	+0 DB
1,3	+0 DB
2,3	+1 DB
0,4	+0 DB
1,4	+1 DB
2,4	+0 DB
3,4	+0 DB
1	+0 DB
2	+0 DB
3	+0 DB
4	+0 DB

FIG. 3D

**Example Hypothesis/Covering Vector Combinations
(for (32,10) Reed-Muller code)**

HYPOTHESIS	O_6	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	O_7	0	0	0	0	1	1	1	1	0	0	0	0	1	1	1	1
	O_8	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1
	O_9	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1
COVERING VECTOR	C_0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	C_1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	C_2	1	1	1	1	1	1	1	1	-1	-1	-1	-1	-1	-1	-1	-1
	C_3	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1
	C_4	1	-1	-1	1	1	-1	-1	1	-1	1	1	-1	-1	1	1	-1
	C_5	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1
	C_6	1	1	-1	-1	1	1	-1	-1	1	1	-1	-1	1	1	-1	-1
	C_7	1	1	1	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1	-1
	C_8	1	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1	-1	1	1
	C_9	1	1	-1	-1	-1	-1	1	1	-1	-1	1	1	1	1	-1	-1
	C_{10}	1	-1	-1	1	1	-1	-1	1	-1	1	1	-1	-1	1	1	-1
	C_{11}	1	-1	-1	1	1	-1	-1	1	1	-1	-1	1	1	-1	-1	1
	C_{12}	1	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1	-1	1	1
	C_{13}	1	-1	1	-1	-1	1	-1	1	1	-1	1	-1	-1	1	-1	1
	C_{14}	1	-1	1	-1	1	-1	1	-1	-1	1	-1	1	-1	1	-1	1
	C_{15}	1	-1	-1	1	-1	1	1	-1	-1	1	1	-1	1	-1	-1	1
	C_{16}	1	1	1	1	1	1	1	1	-1	-1	-1	-1	-1	-1	-1	-1
	C_{17}	1	1	1	1	-1	-1	-1	-1	-1	-1	-1	-1	-1	1	1	1
	C_{18}	1	-1	1	-1	-1	1	-1	1	-1	1	-1	1	1	-1	1	-1
	C_{19}	1	1	-1	-1	1	1	-1	-1	-1	-1	1	1	-1	-1	1	1
	C_{20}	1	-1	-1	1	-1	1	1	-1	1	-1	-1	1	-1	1	1	-1
	C_{21}	1	-1	1	-1	-1	1	-1	1	1	-1	1	-1	-1	1	-1	1
	C_{22}	1	-1	-1	1	1	-1	-1	1	1	-1	-1	1	1	-1	-1	1
	C_{23}	1	-1	-1	1	-1	1	1	-1	1	-1	-1	1	-1	1	1	-1
	C_{24}	1	1	1	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1	-1
	C_{25}	1	-1	1	-1	-1	1	-1	1	-1	1	-1	1	1	-1	1	-1
	C_{26}	1	1	-1	-1	1	1	-1	-1	-1	-1	1	1	-1	-1	1	1
	C_{27}	1	-1	1	-1	1	-1	1	-1	-1	1	-1	1	-1	1	-1	1
	C_{28}	1	1	-1	-1	1	1	-1	-1	1	1	-1	-1	1	1	-1	-1
	C_{29}	1	1	1	1	-1	-1	-1	-1	-1	-1	-1	-1	1	1	1	1
	C_{30}	1	1	-1	-1	-1	-1	1	1	-1	-1	1	1	1	1	-1	-1
	C_{31}	1	-1	-1	1	-1	1	1	-1	-1	1	1	-1	1	-1	-1	1

FIG. 4

**Comparison of Example Decoding Complexities
(for 5 configured CCs, without feedback bundling)**

SCHEDULED CC INDICES	BRUTE-FORCE ML DECODING COMPLEXITY	EFFICIENT ML DECODING COMPLEXITY
0,1,2,3,4	32768	3072
0,1,2,3	8192	768
0,1,2,4	8192	768
0,1,3,4	8192	3072
0,2,3,4	8192	3072
1,2,3,4	8192	3072
0,1,2	2048	160
0,1,3	2048	768
0,2,3	2048	768
1,2,3	2048	768
0,1,4	2048	768
1,2,4	2048	768
0,1	2048	768
0,2,4	512	160
0,2	512	160
1,2	512	160

FIG. 5

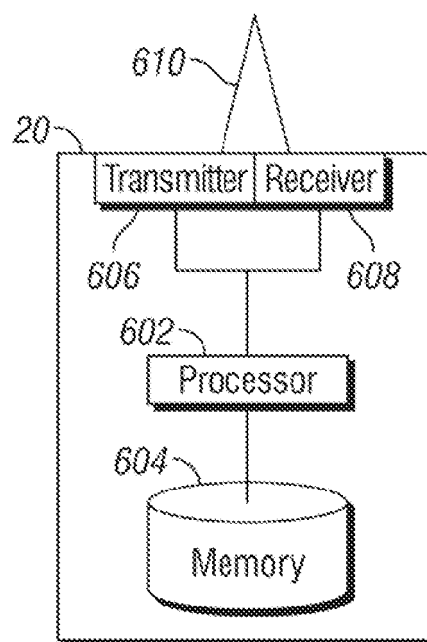
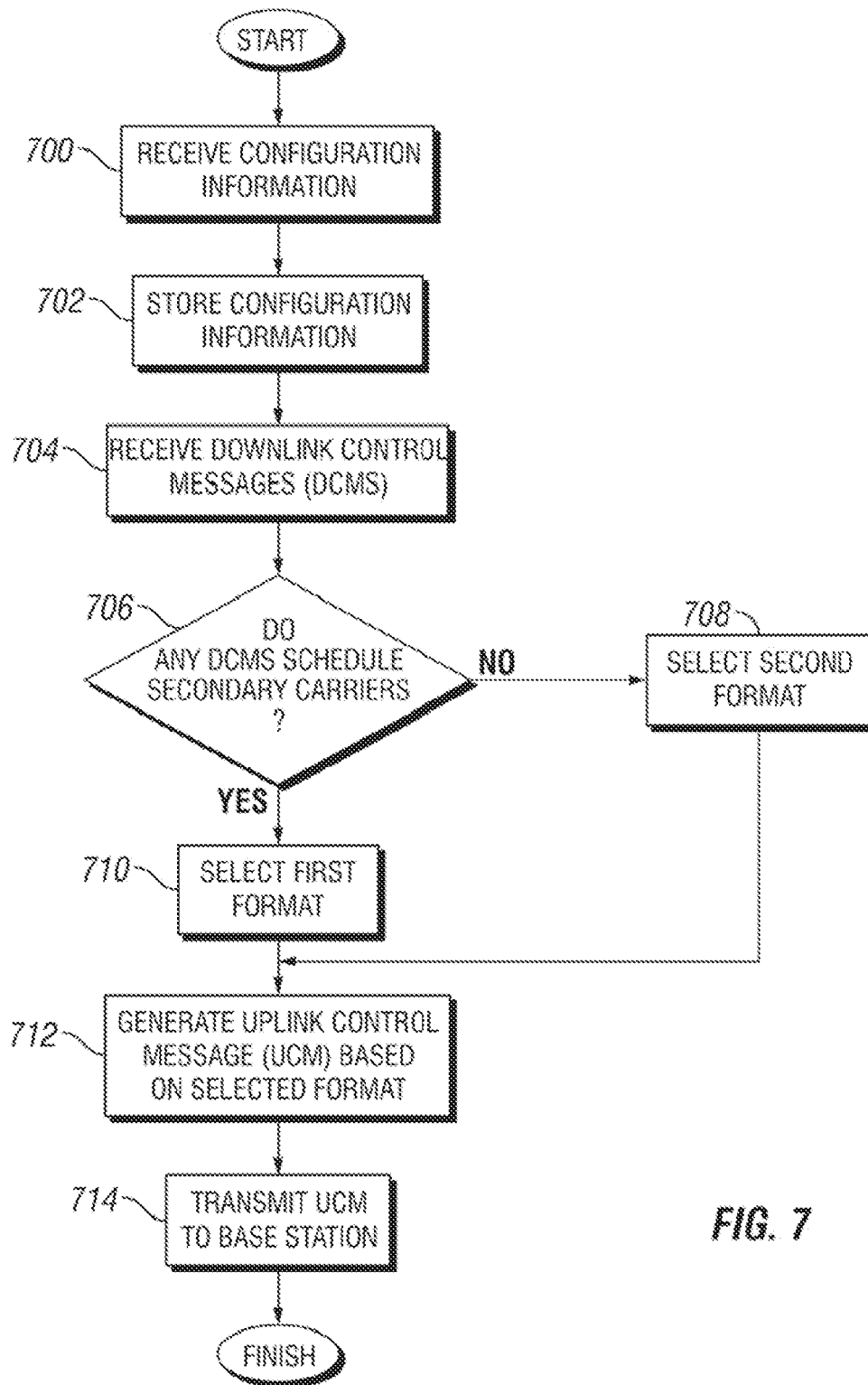
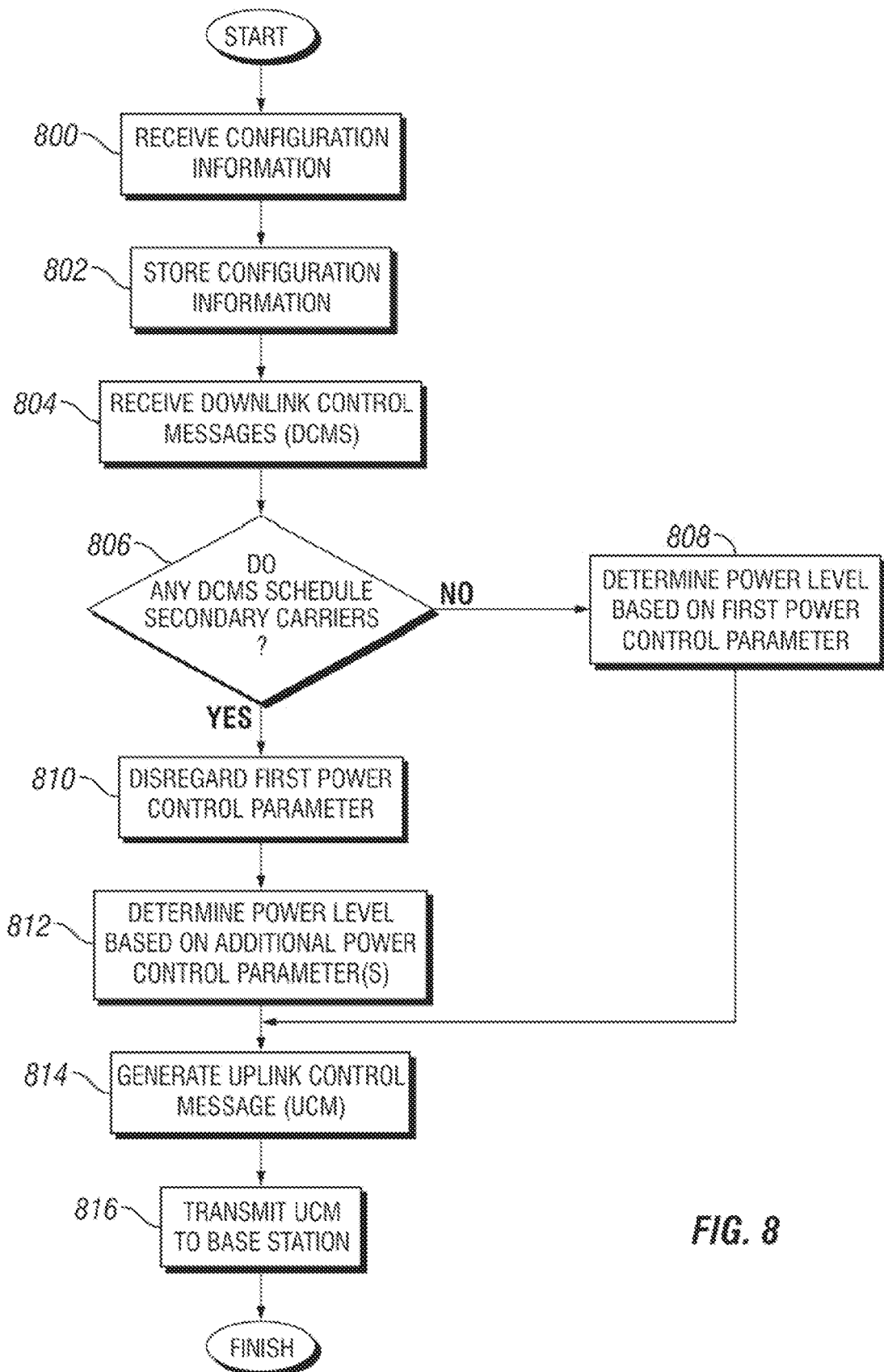
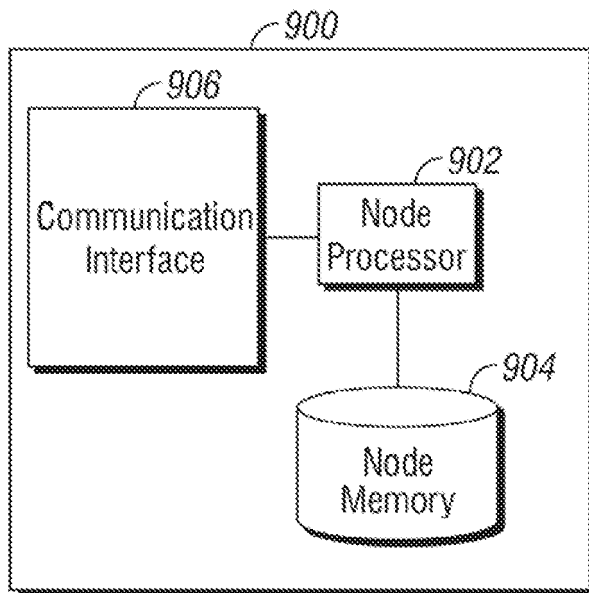
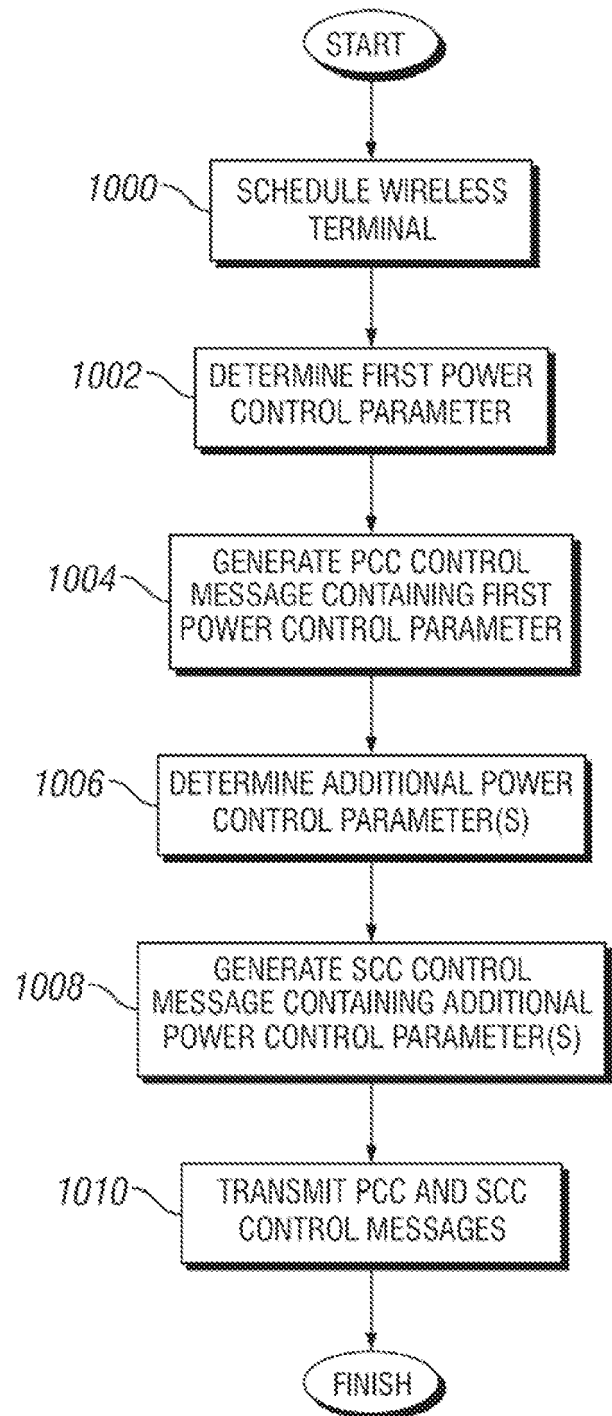


FIG. 6

**FIG. 7**

**FIG. 8**

**FIG. 9****FIG. 10**

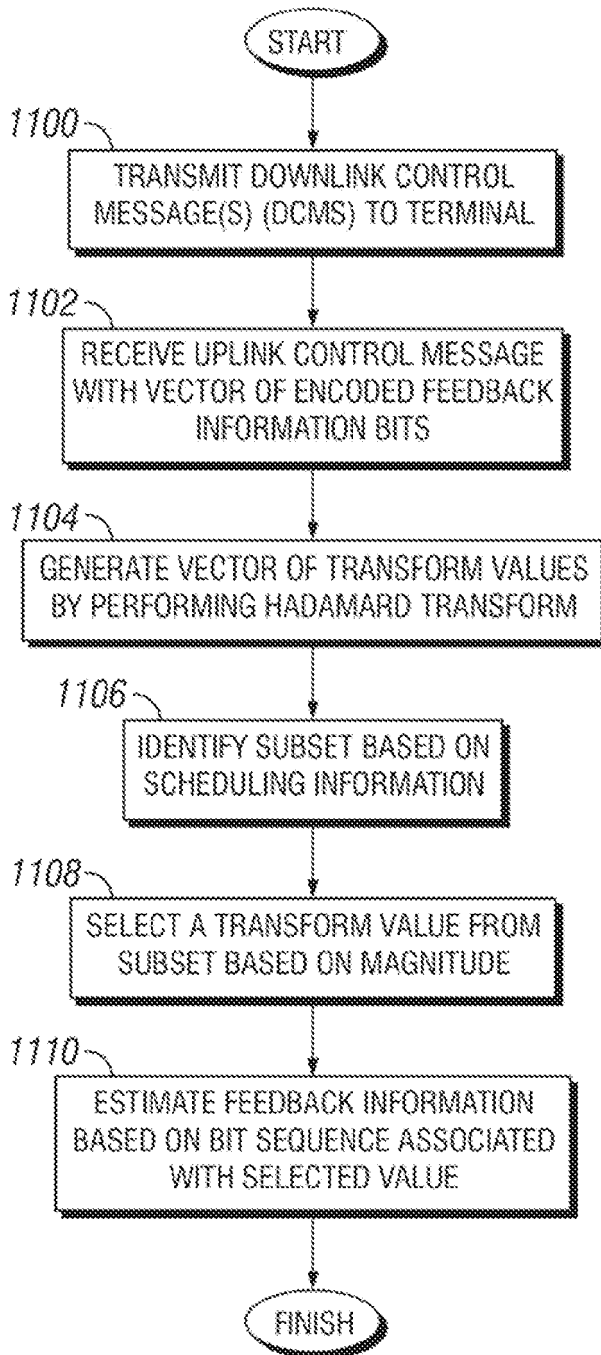


FIG. 11

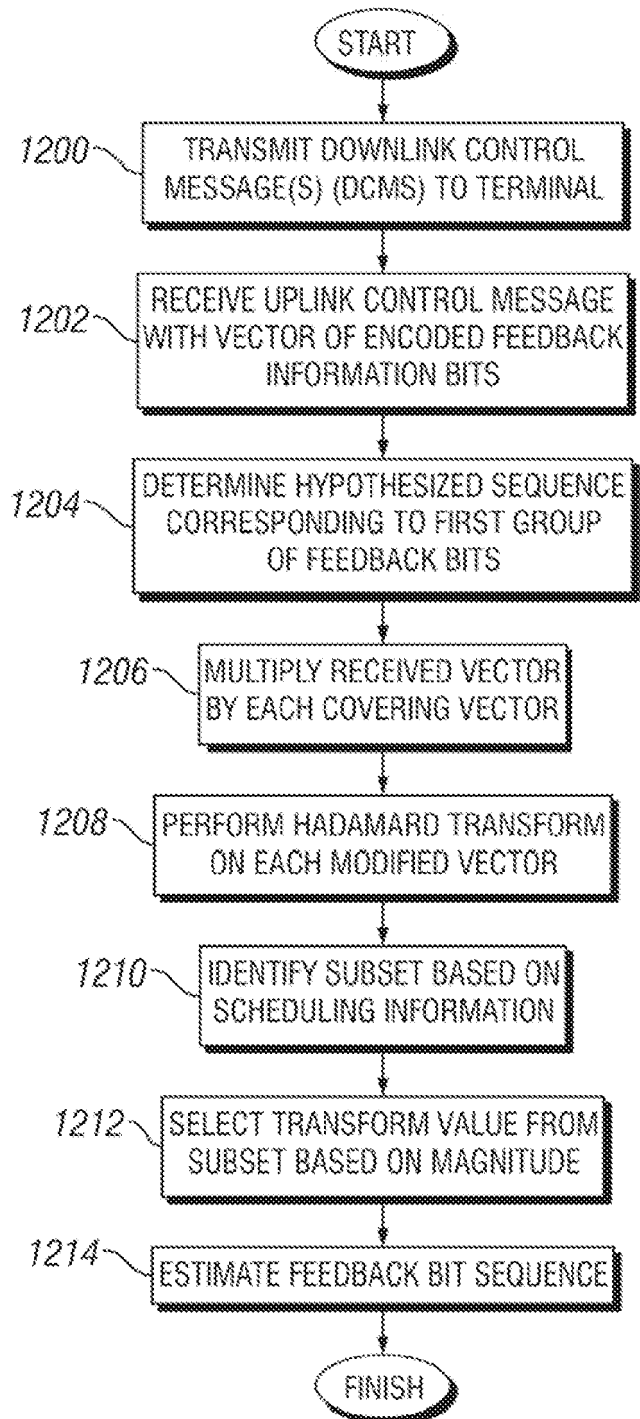


FIG. 12