

PHOTODETECTOR CIRCUIT AND METHODS

Abstract of the Disclosure

A photodetector circuit that operates a reduced power levels is provided. The photodetector circuit preferably includes a control circuit that alternates between an active mode and a standby mode. During an active mode, information regarding the relative strength of a light signal is acquired and reported. However, during a standby mode, portions of the photodetector are disabled or turned off so that no information regarding a light signal is acquired or reported and overall power consumption is reduced.

(Figure 3)