

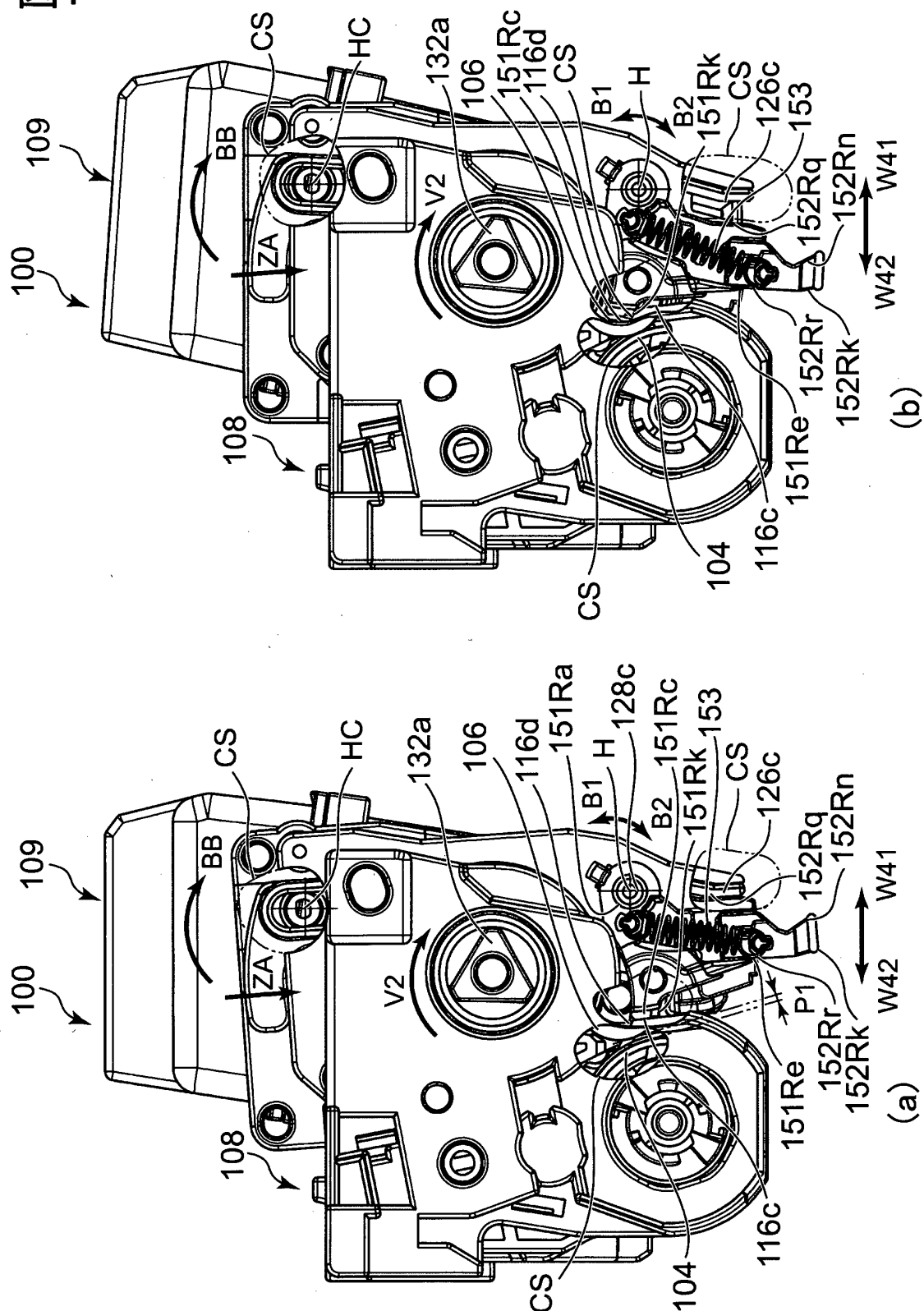


図2

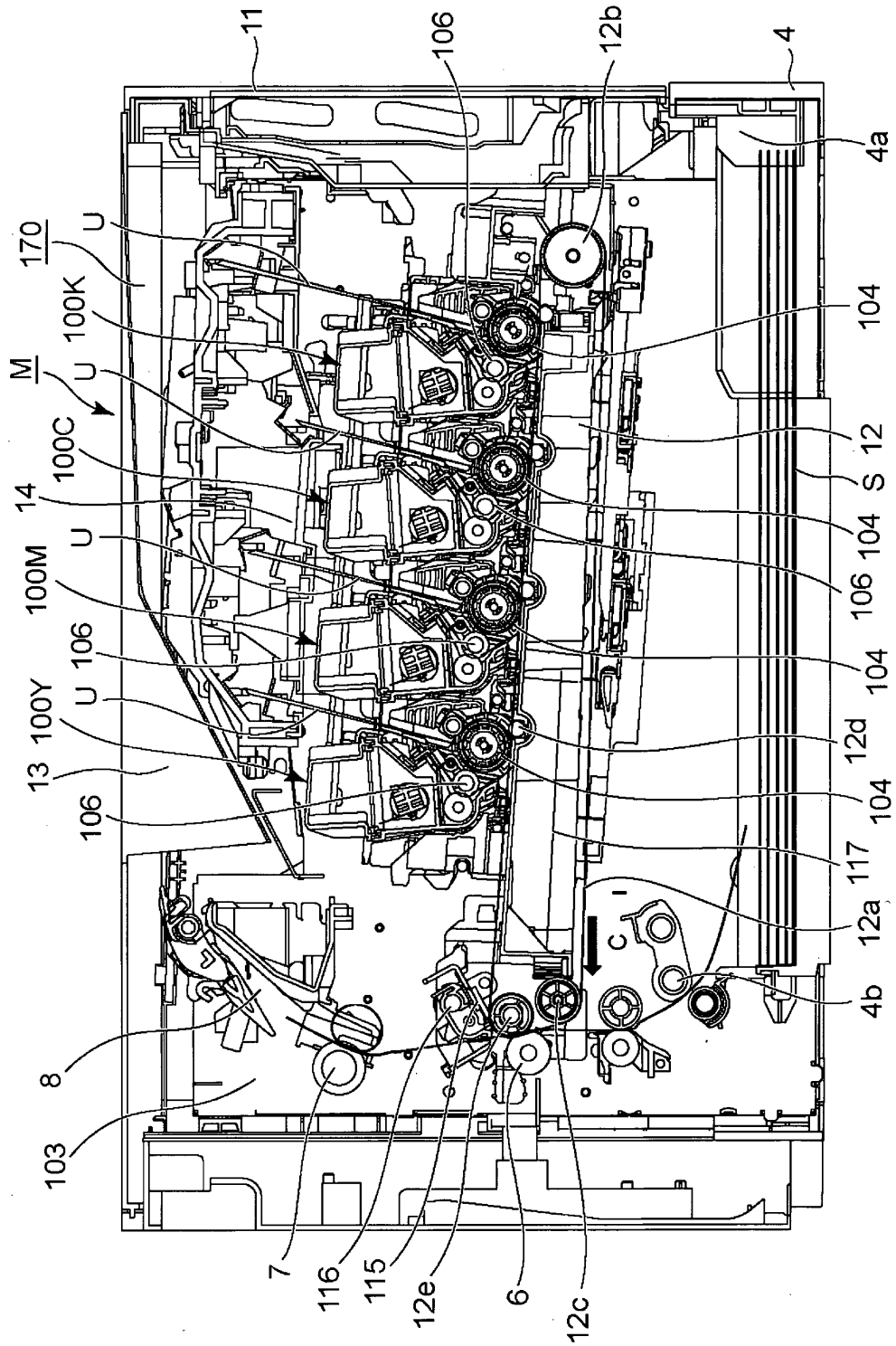


図4

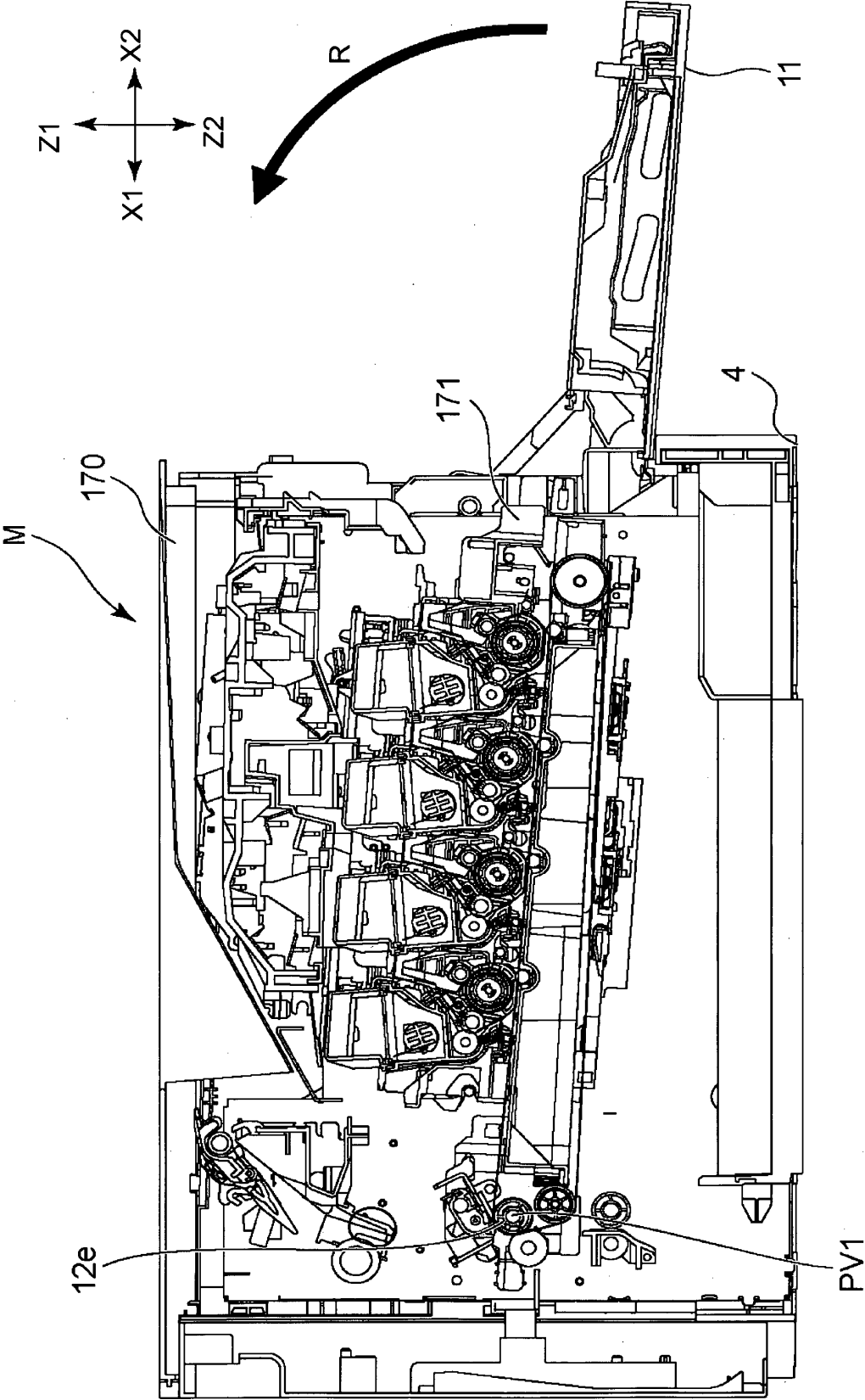


図5

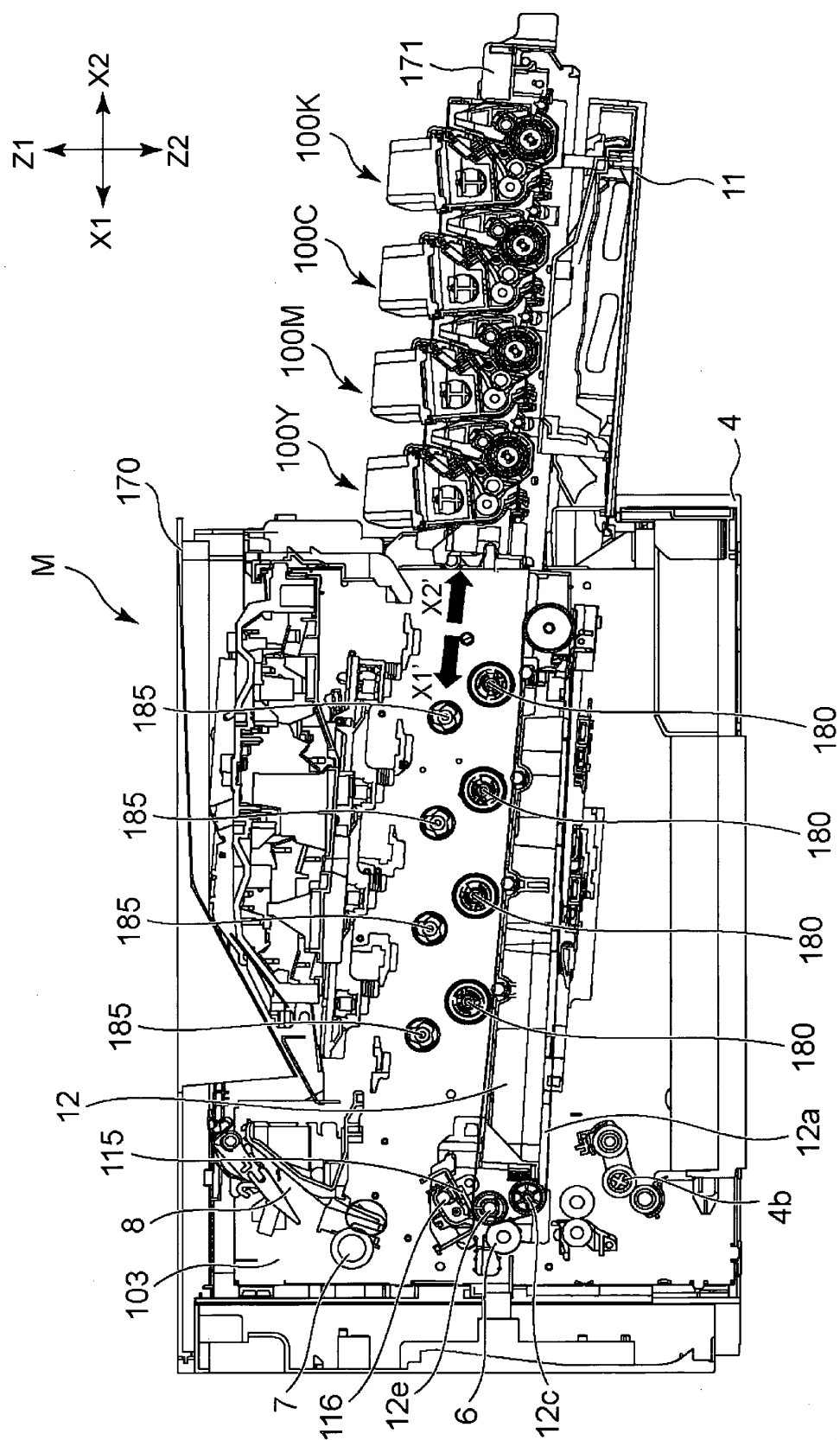


図6

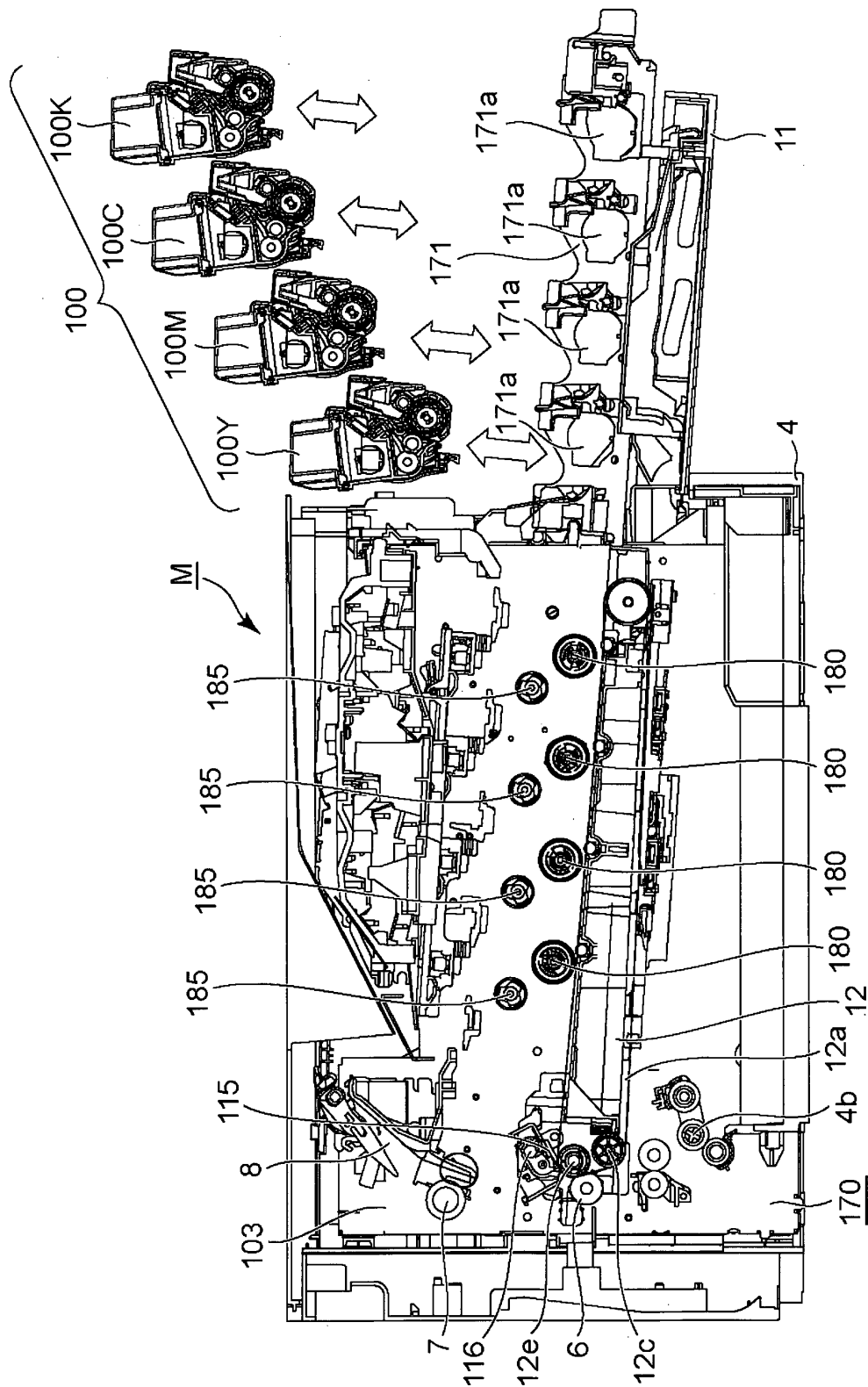


図7

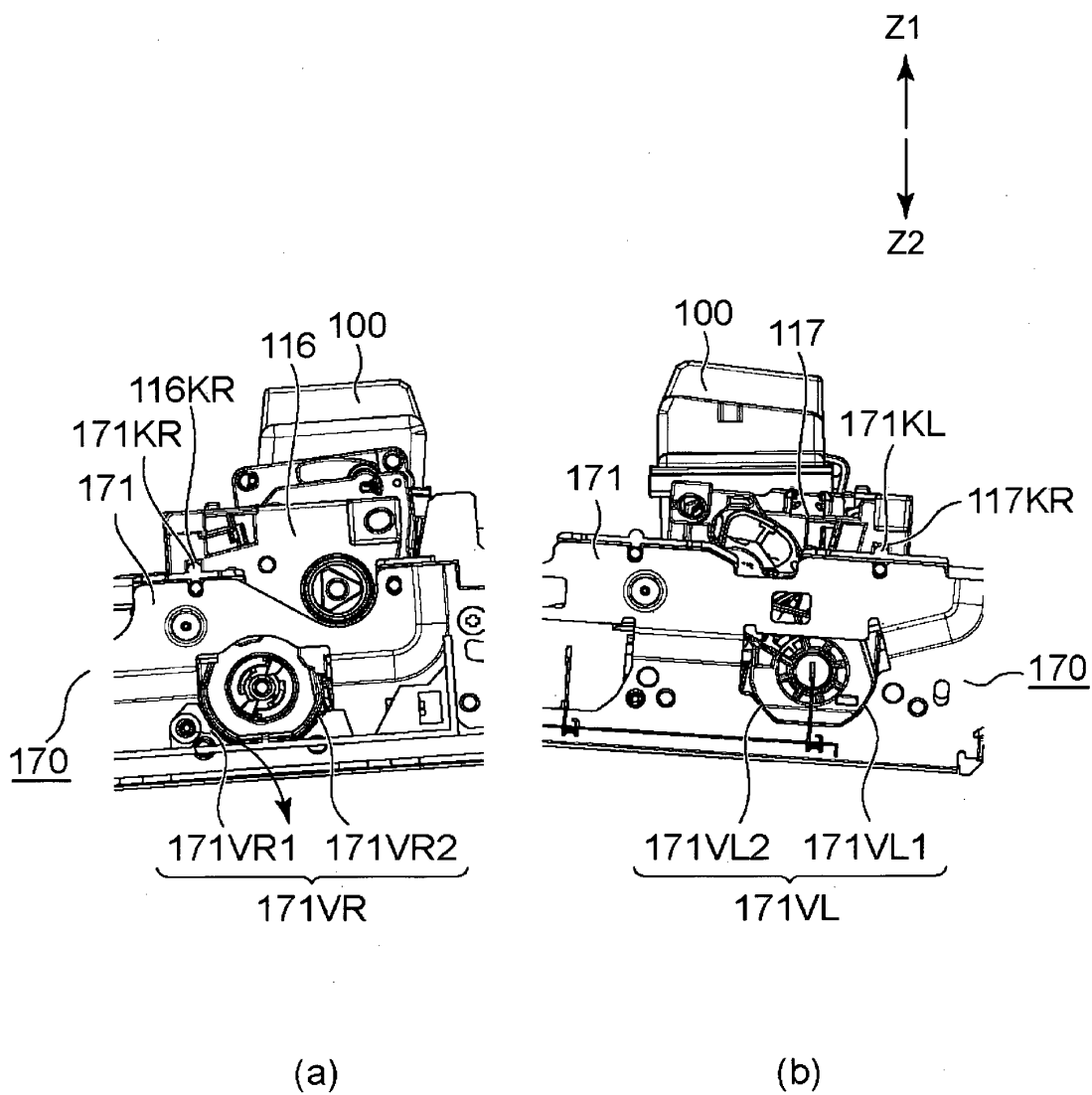
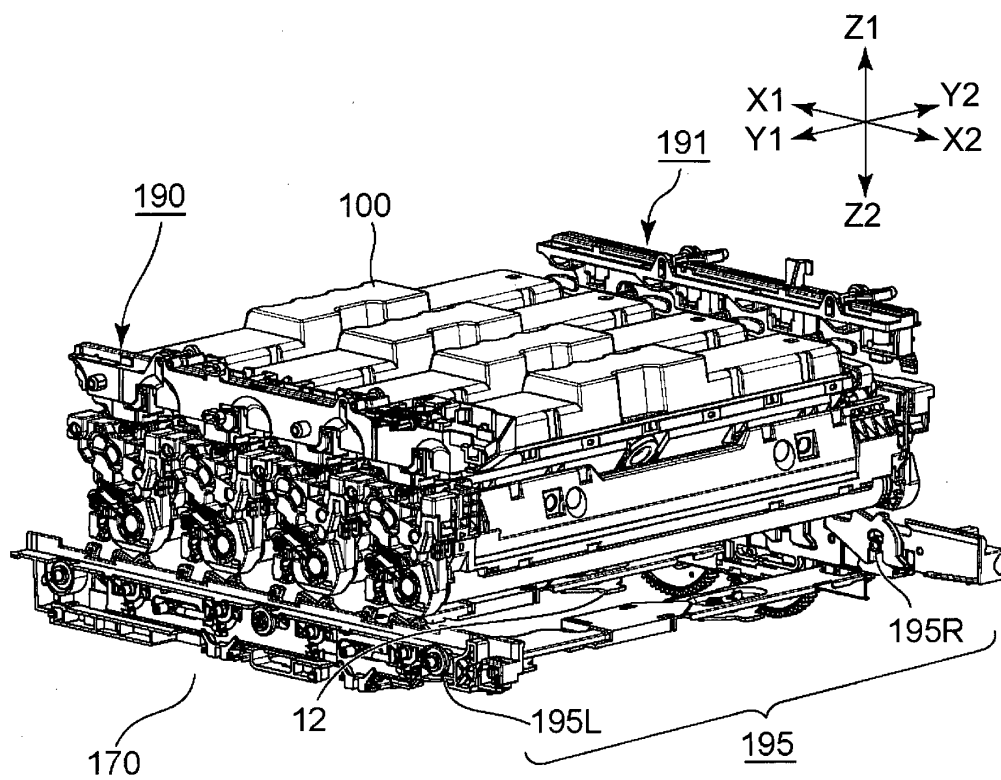
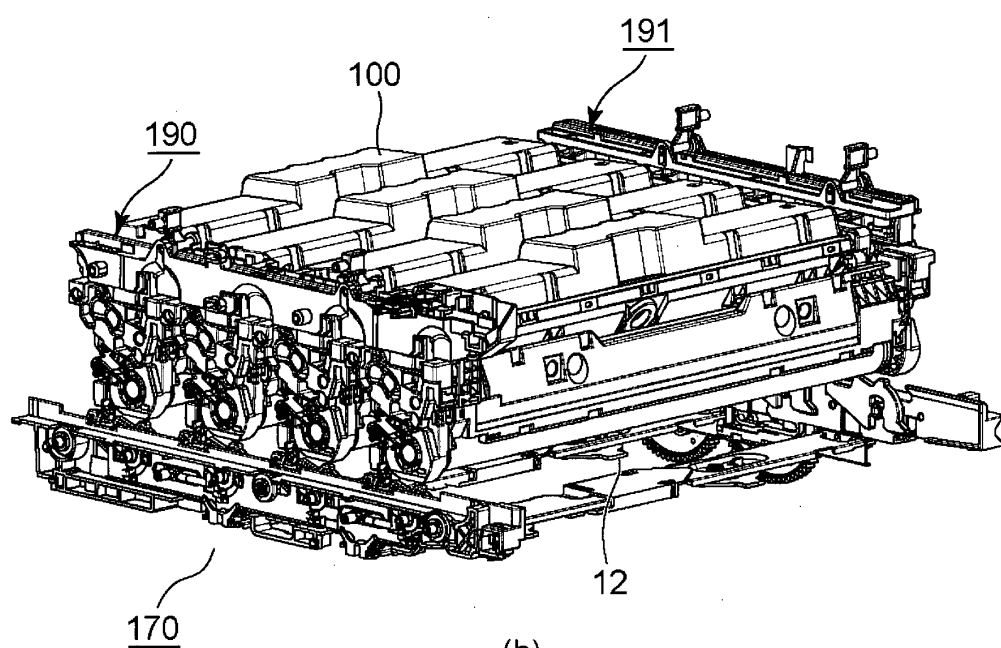


図8

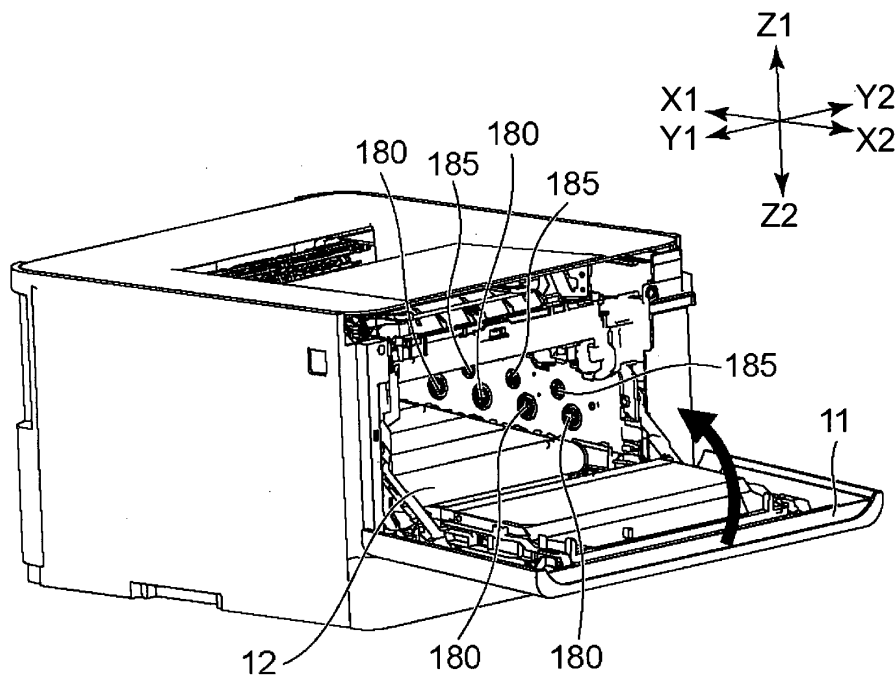


(a)

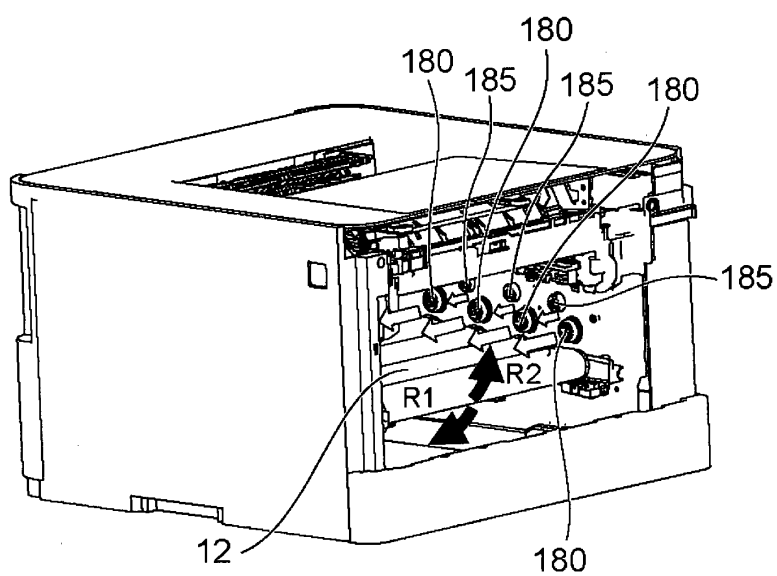


(b)

図9



(a)



(b)

図10

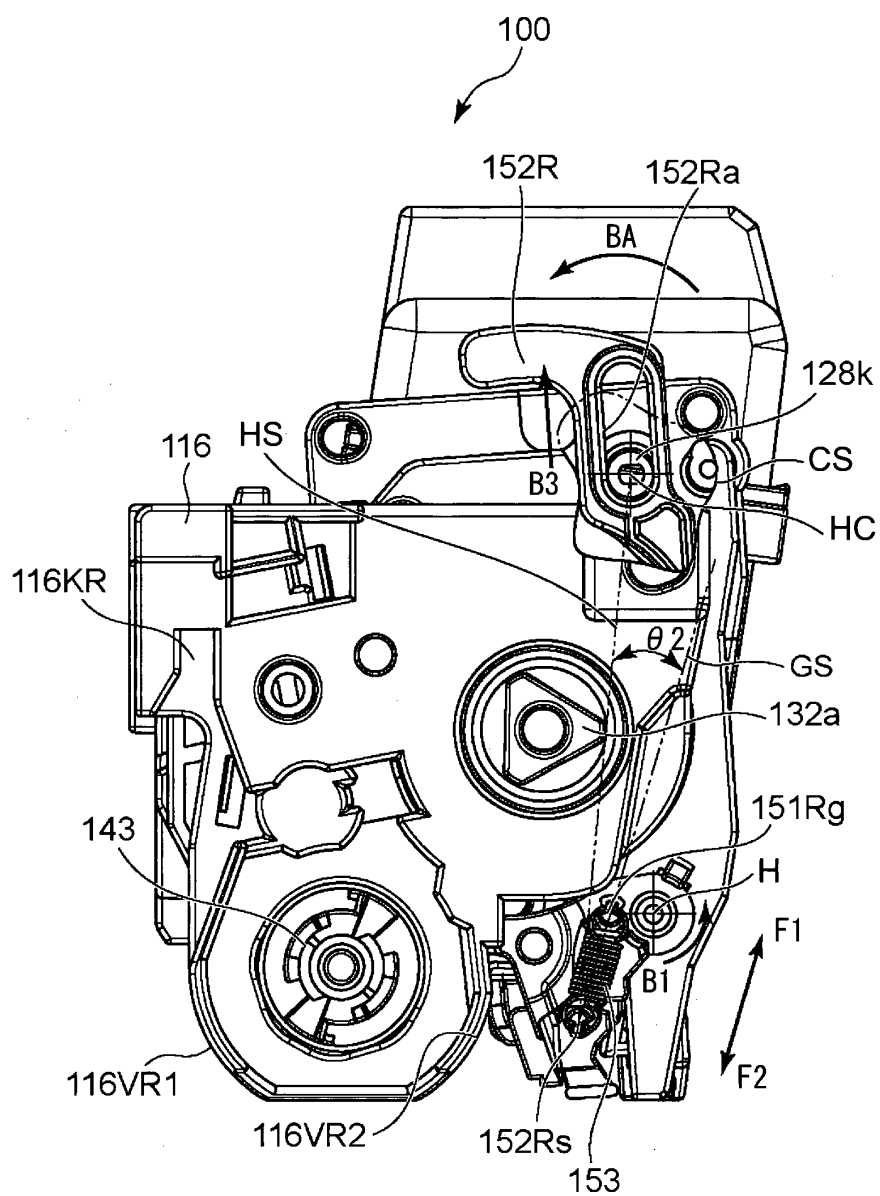


図 11

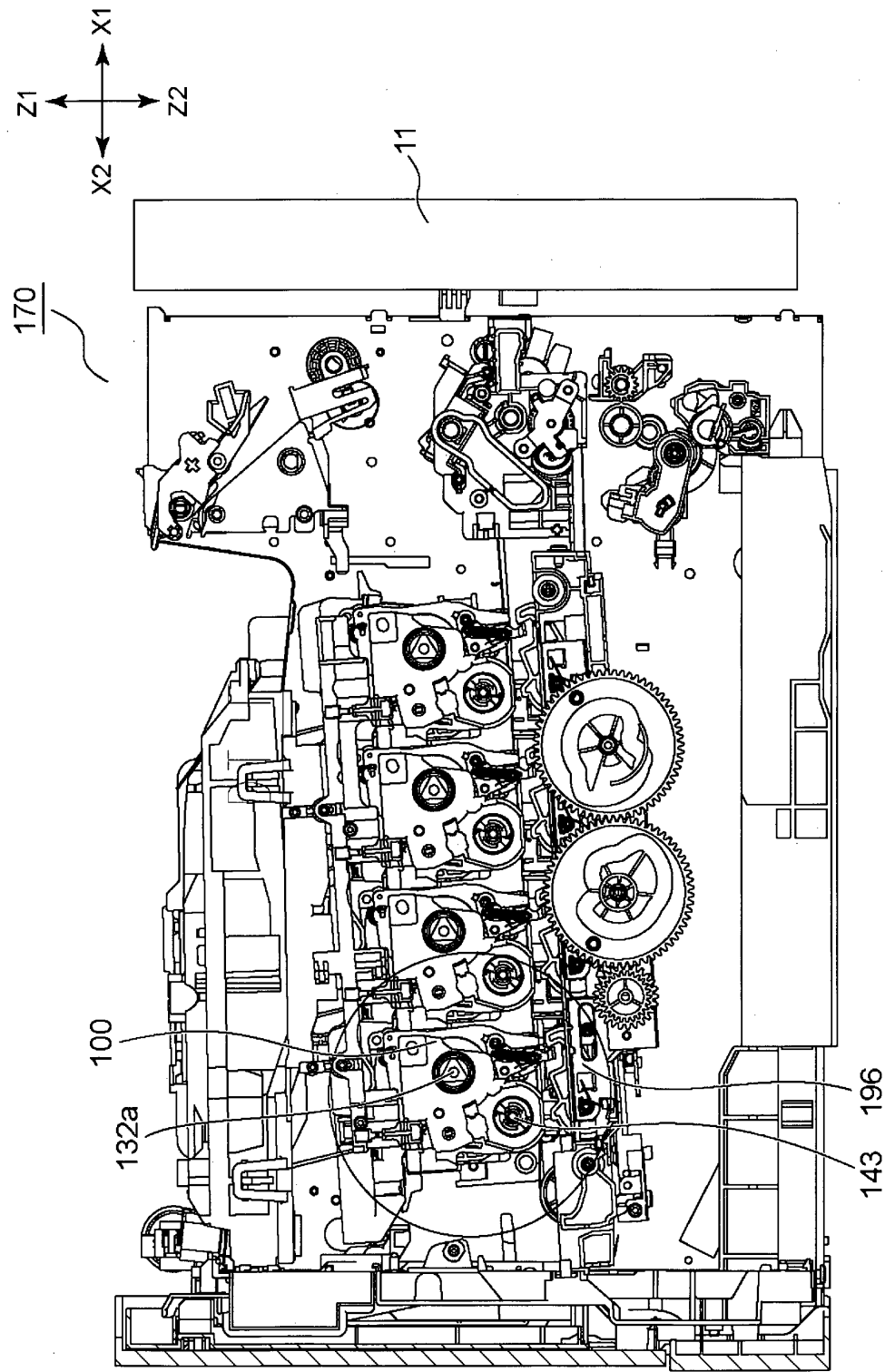


图 12

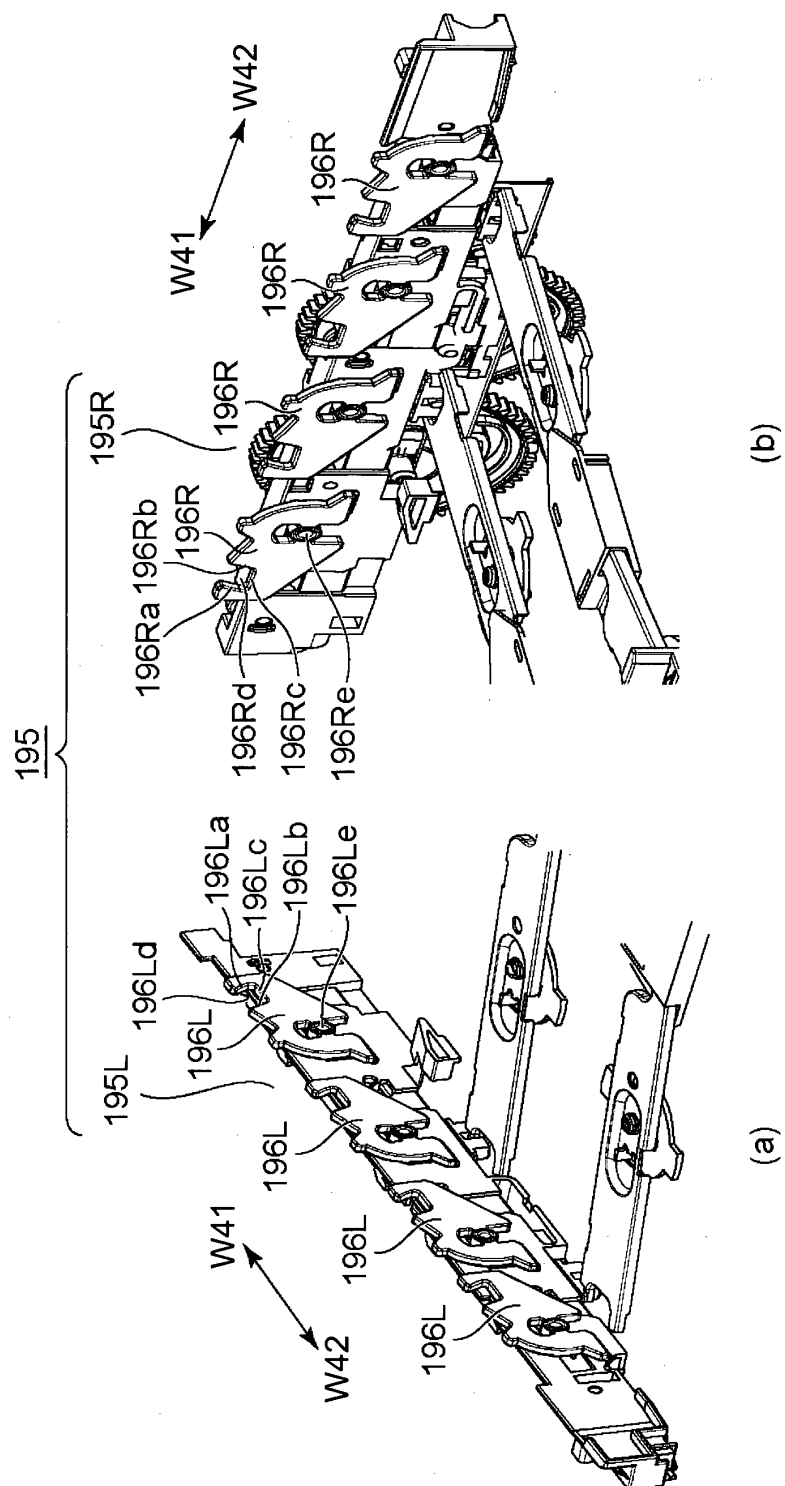


図13

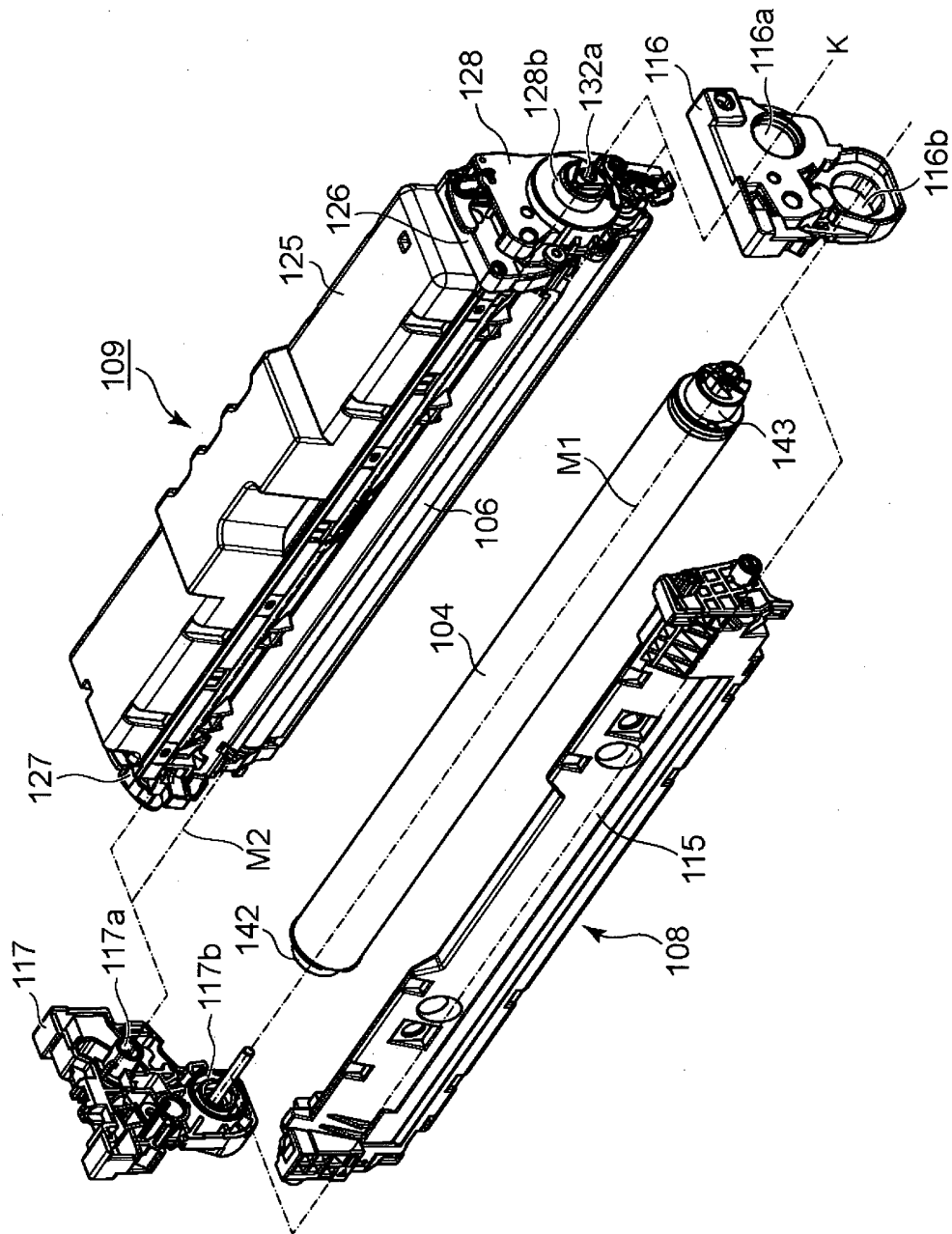


図 14

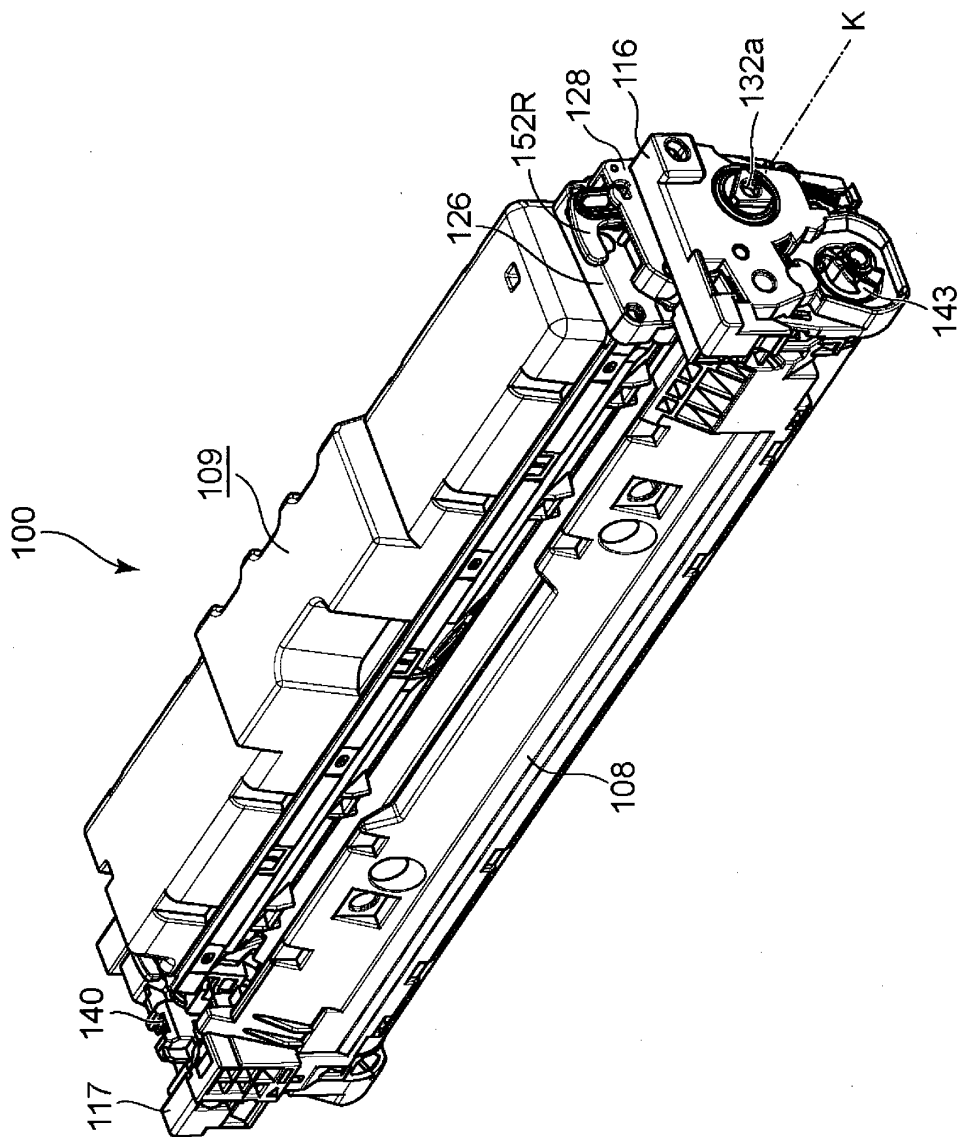


図15

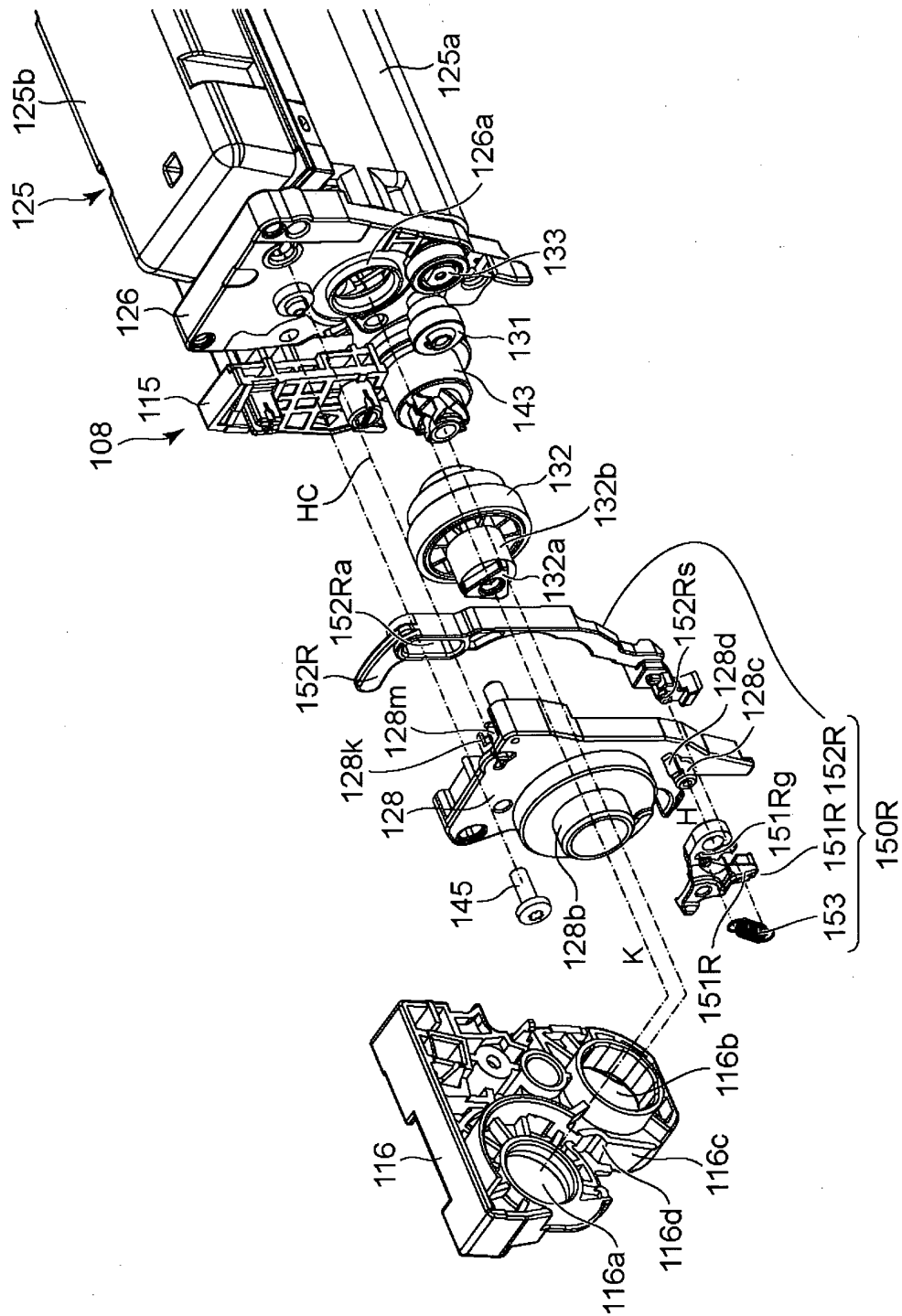


図 16

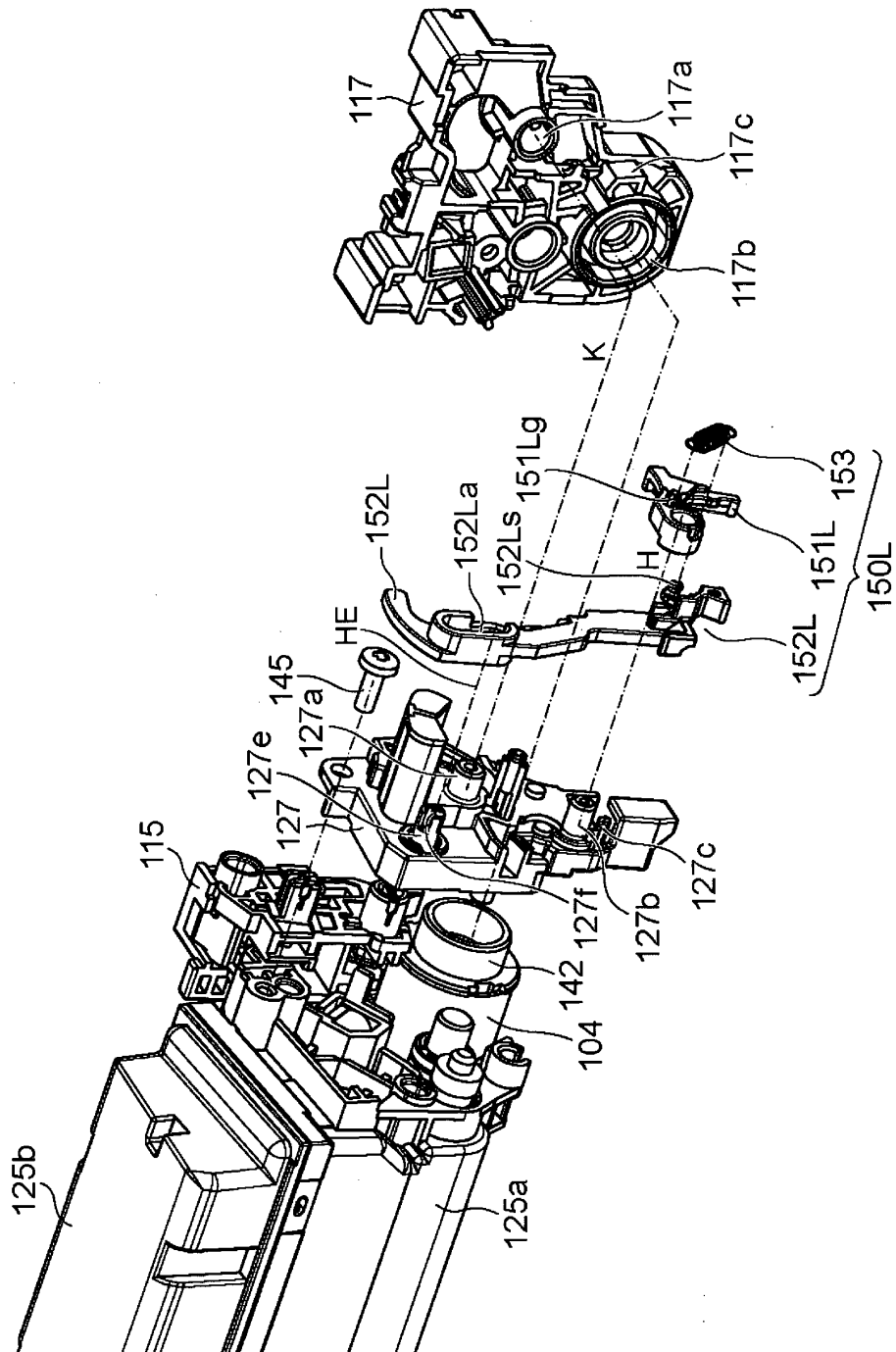
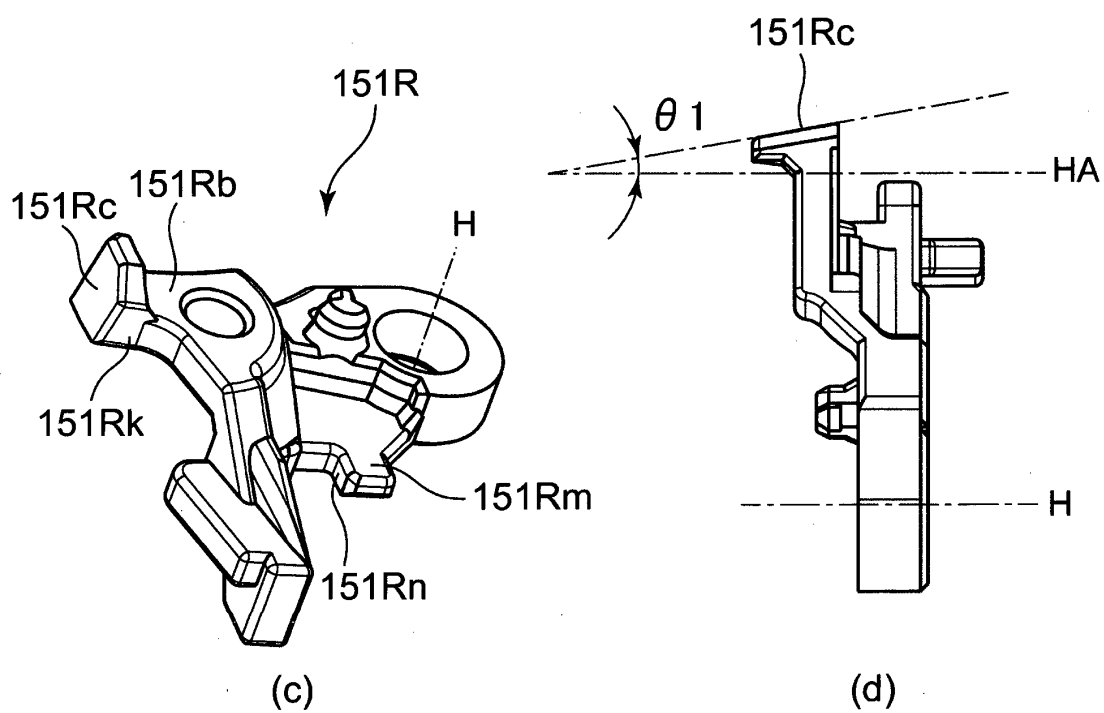
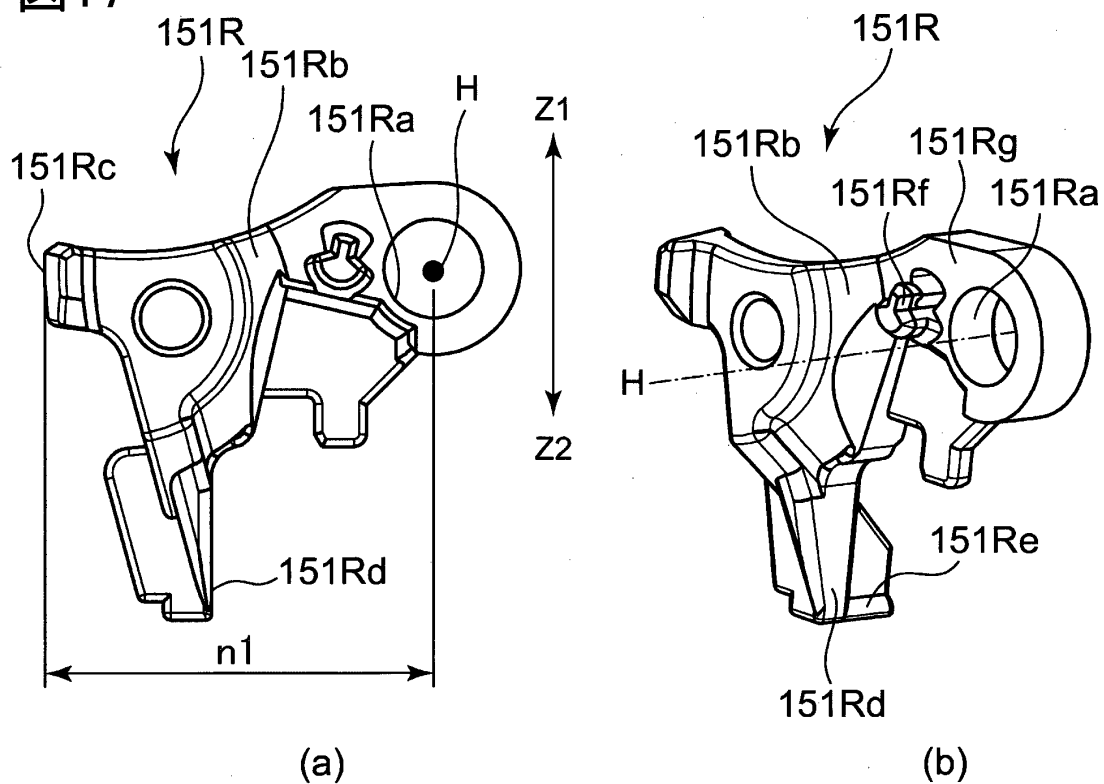


図17



 18

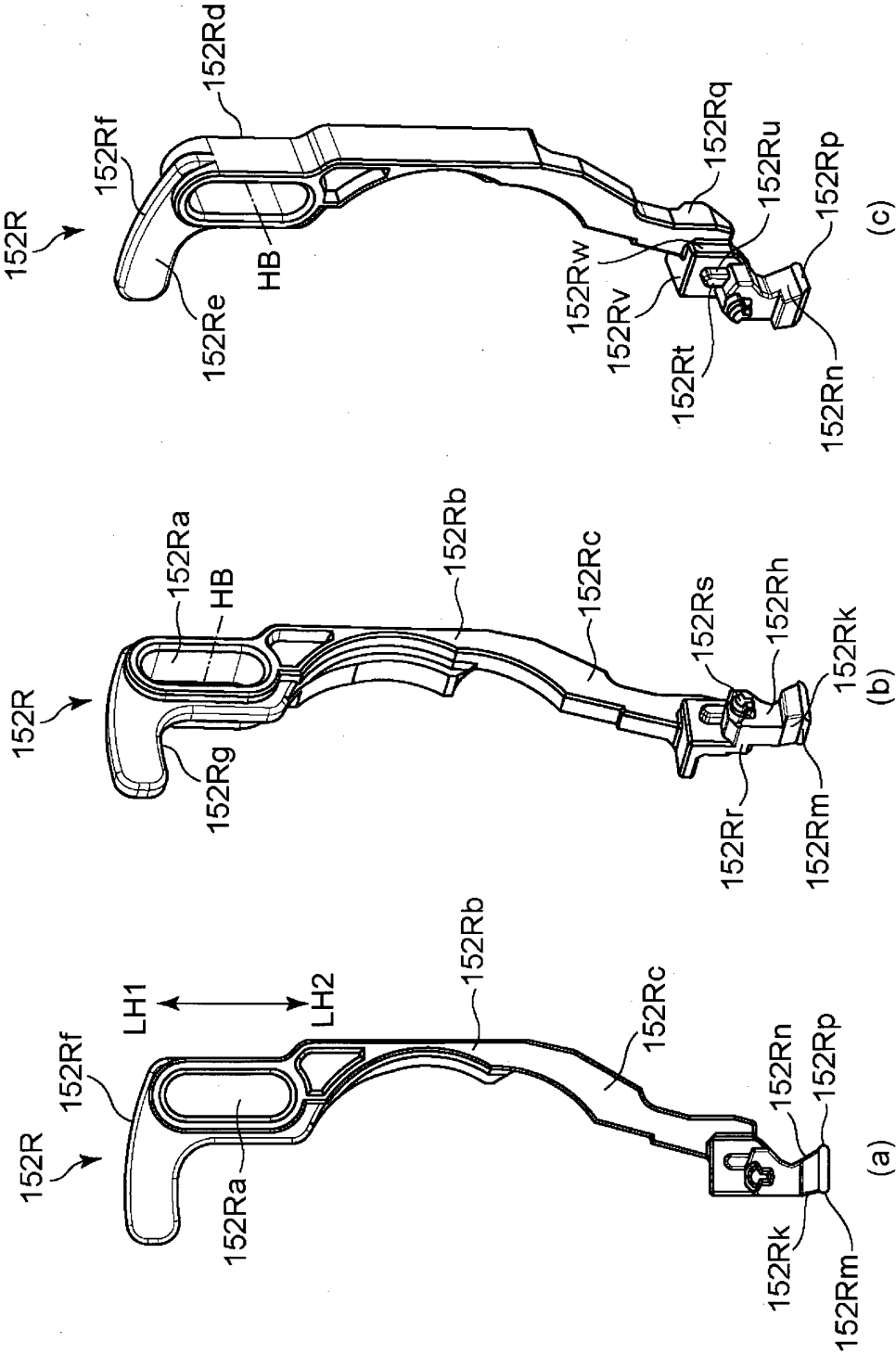


図 19

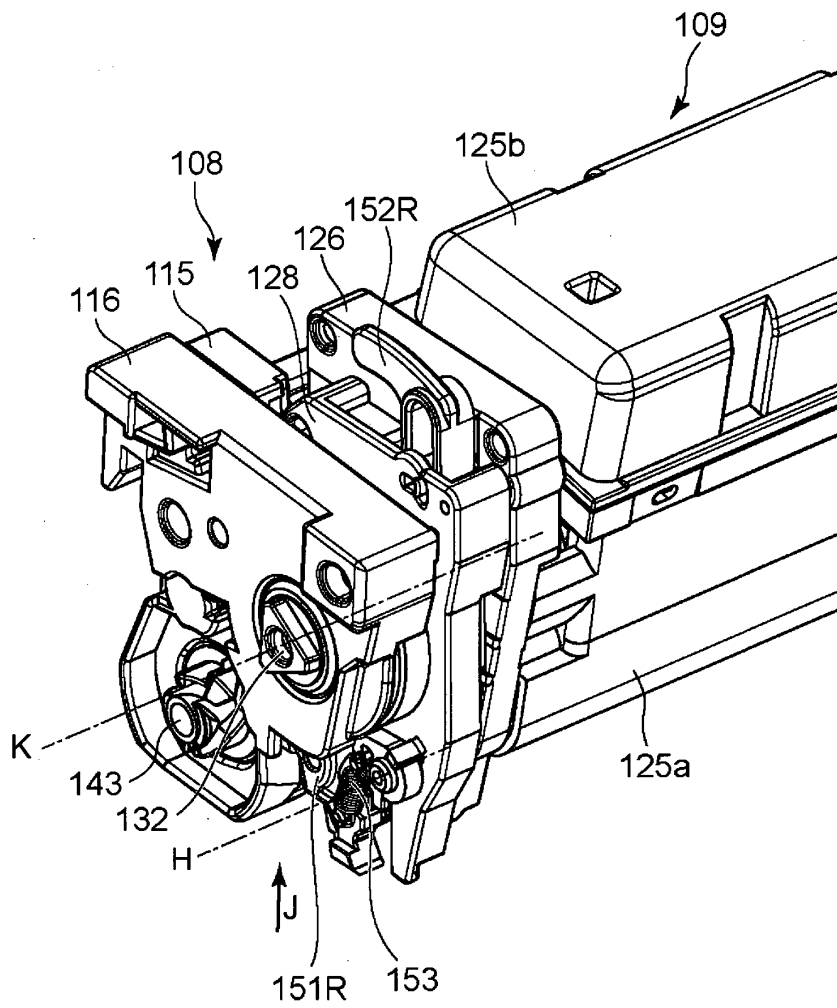


図20

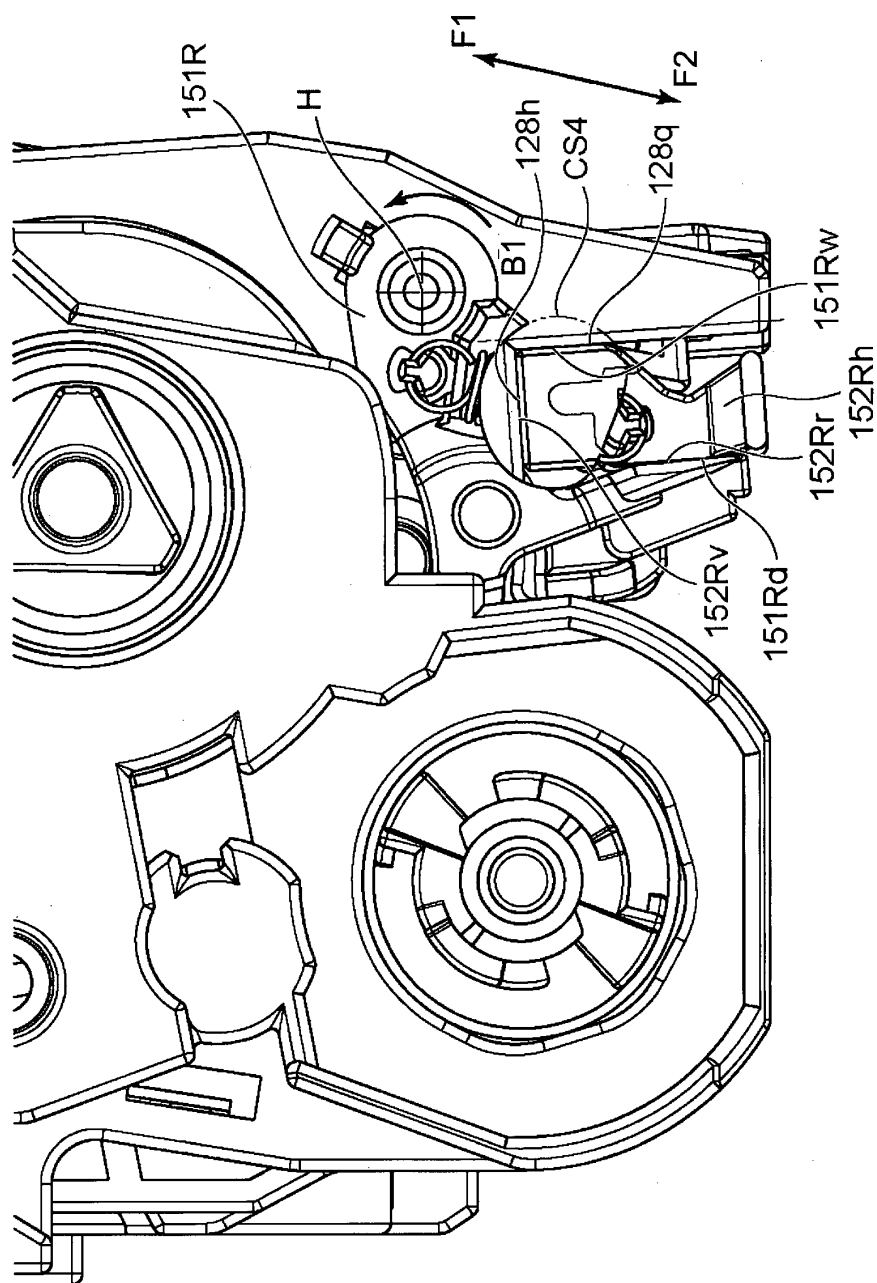


図21

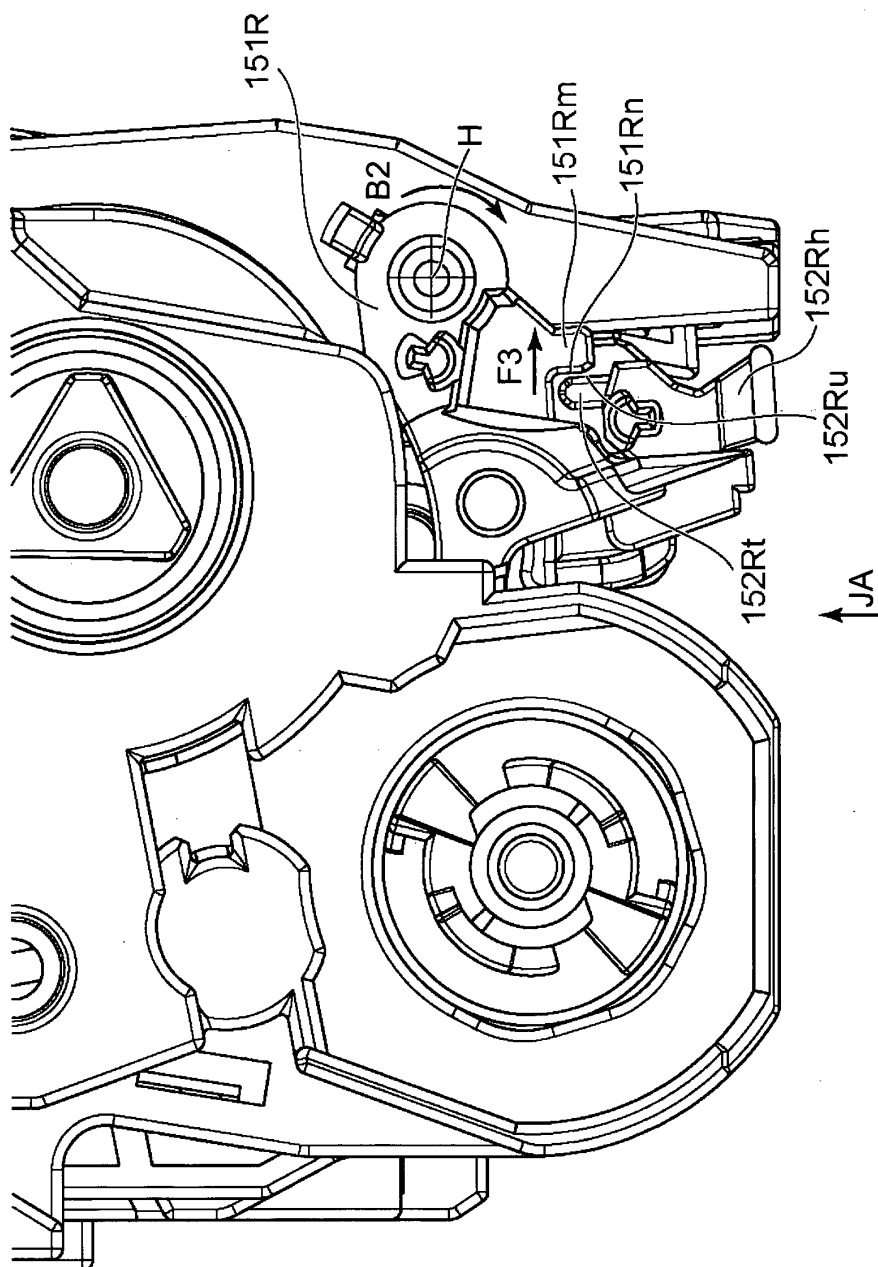


図22

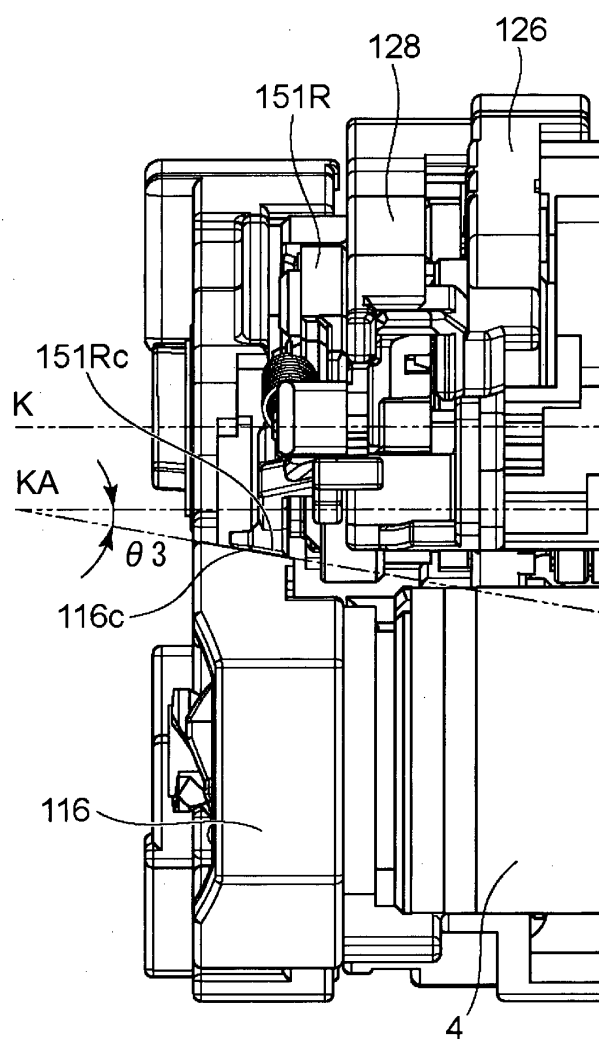


図23

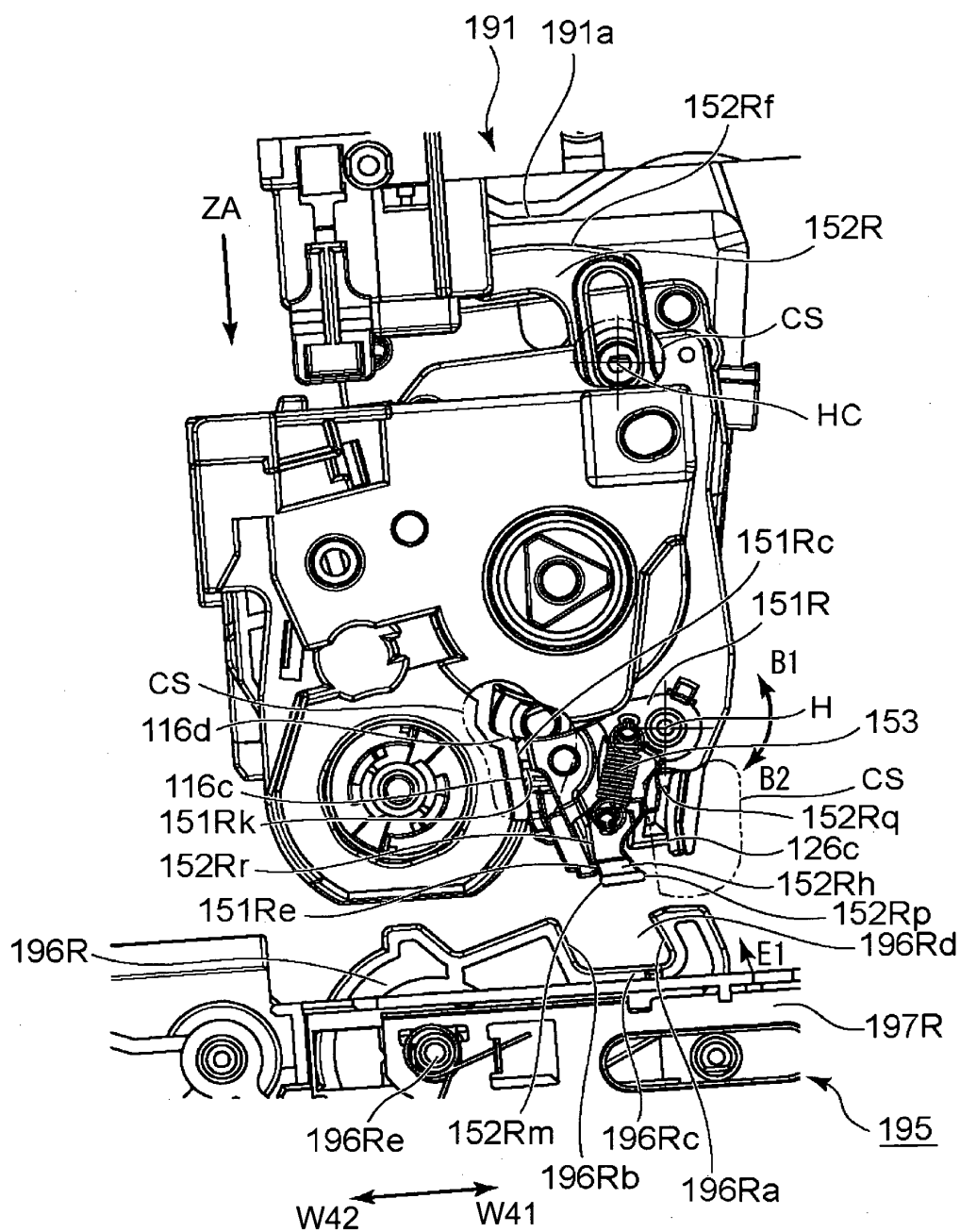


図24

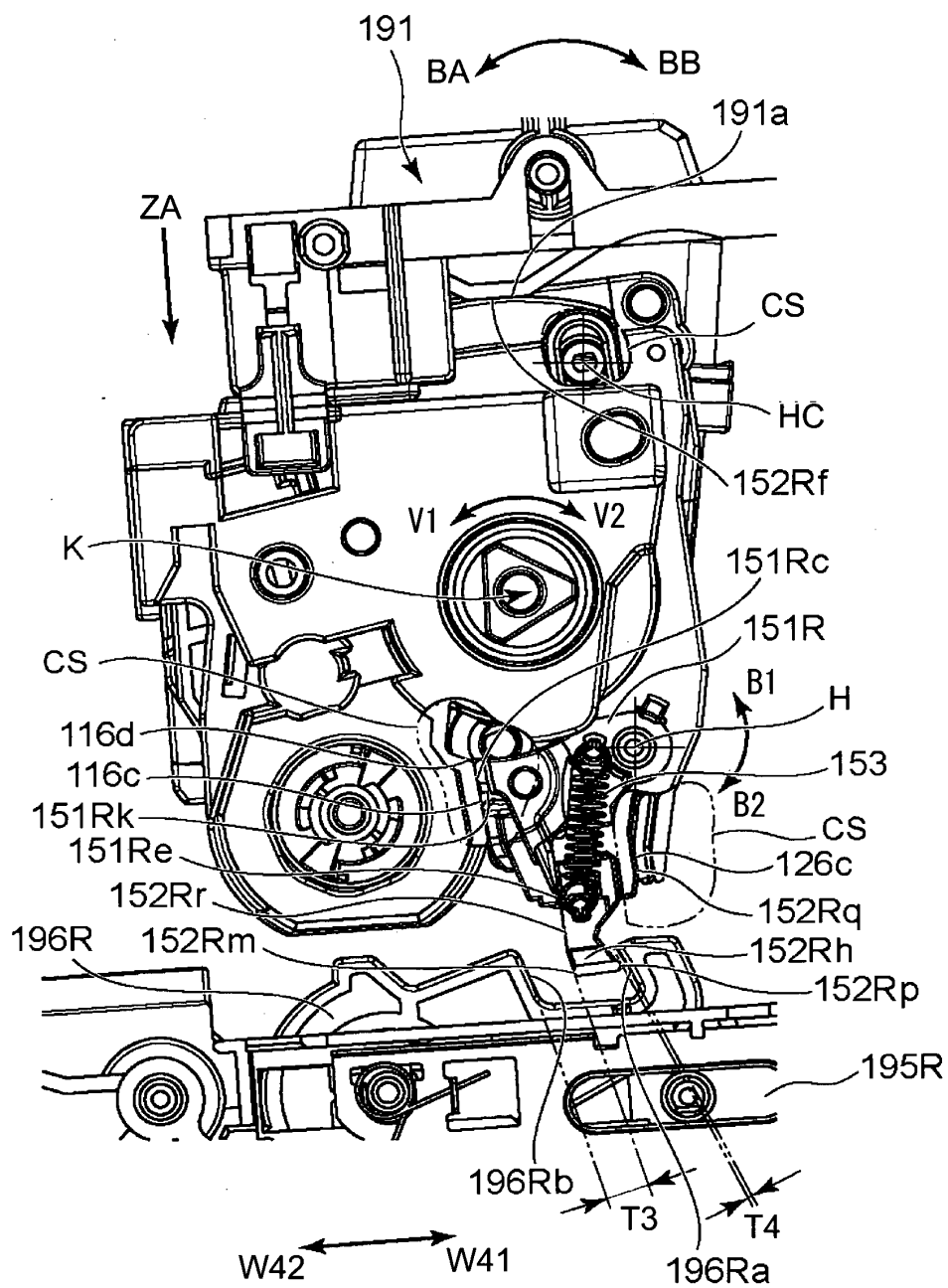


図25

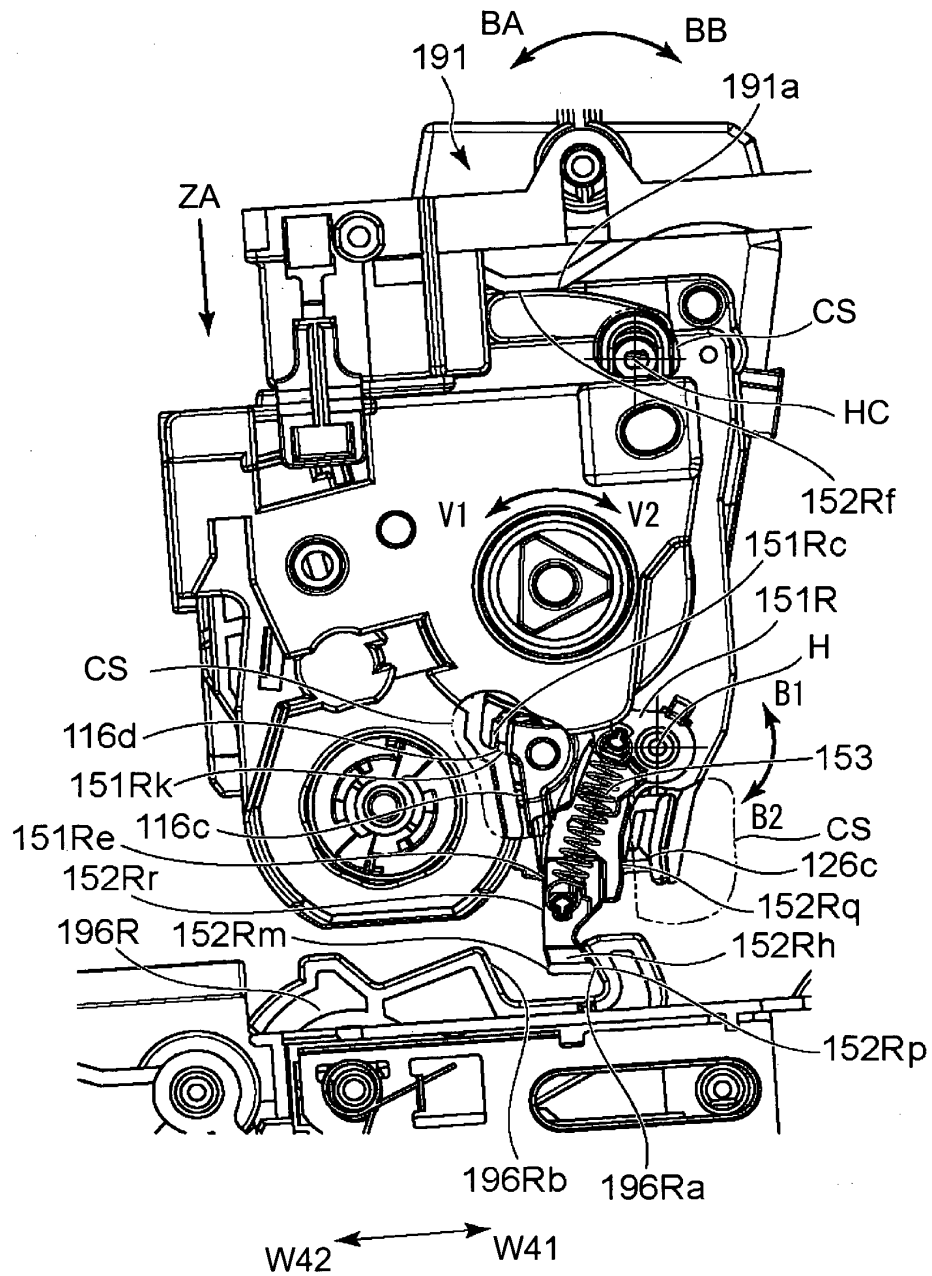


図26

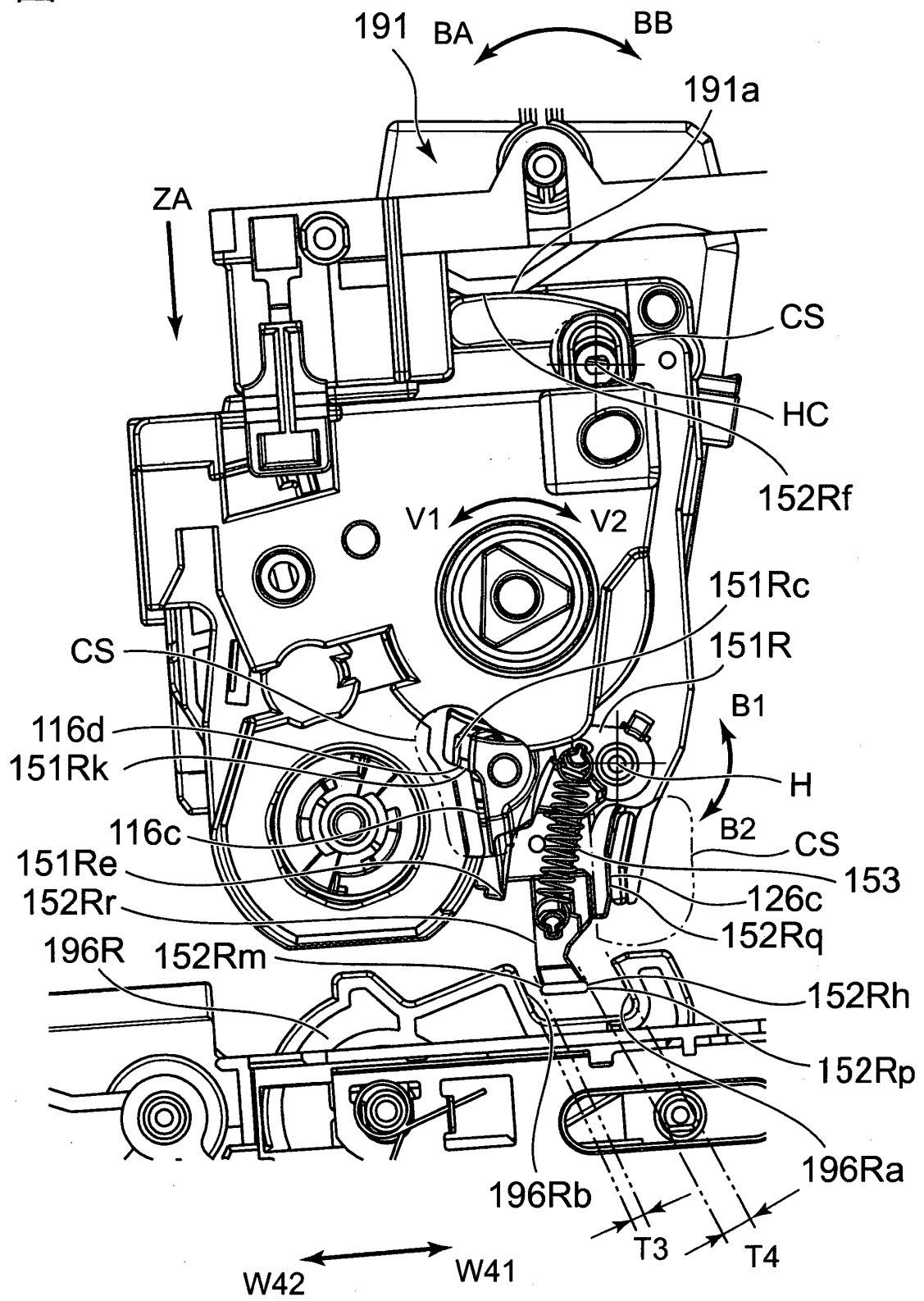


図27

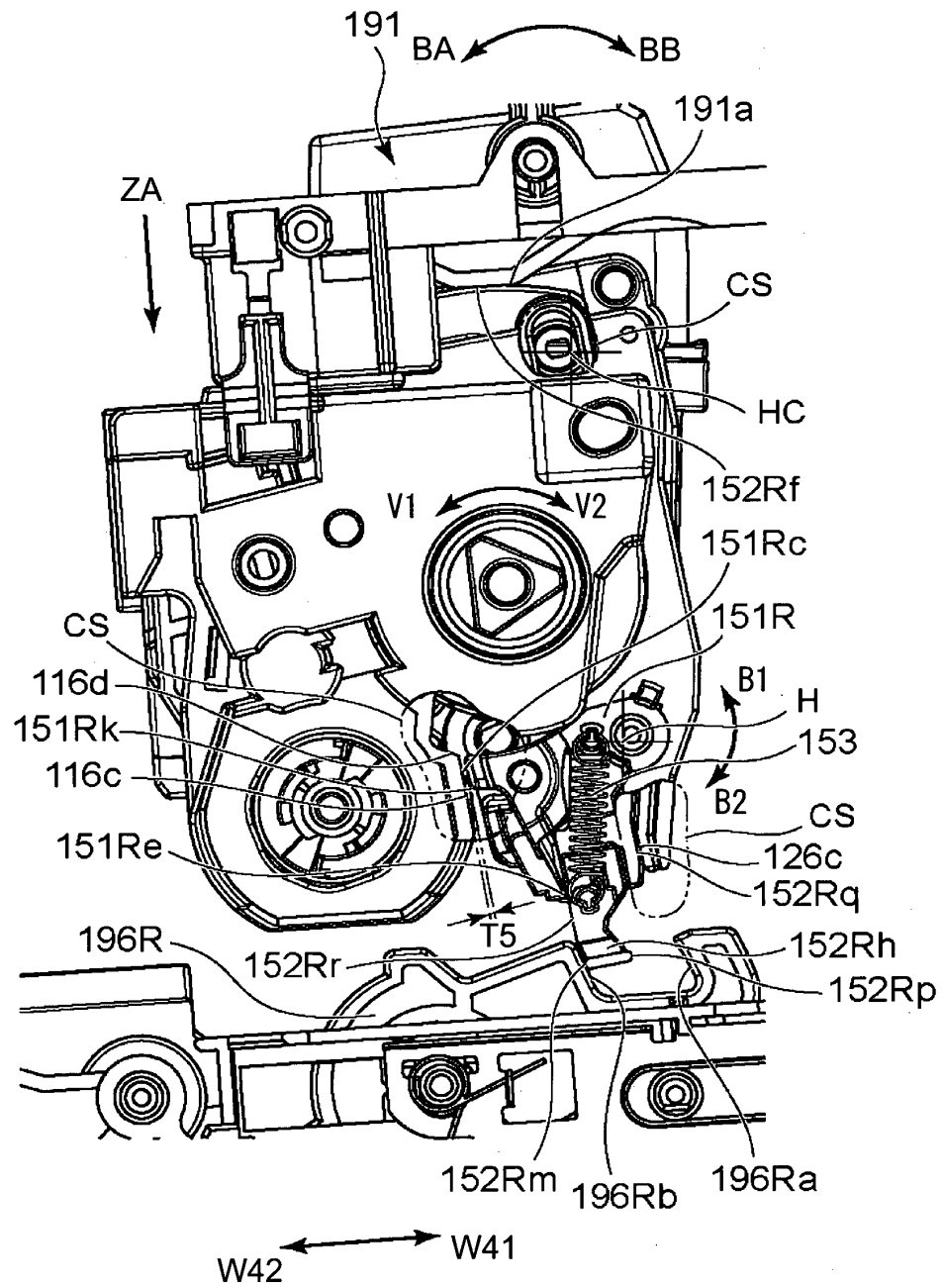
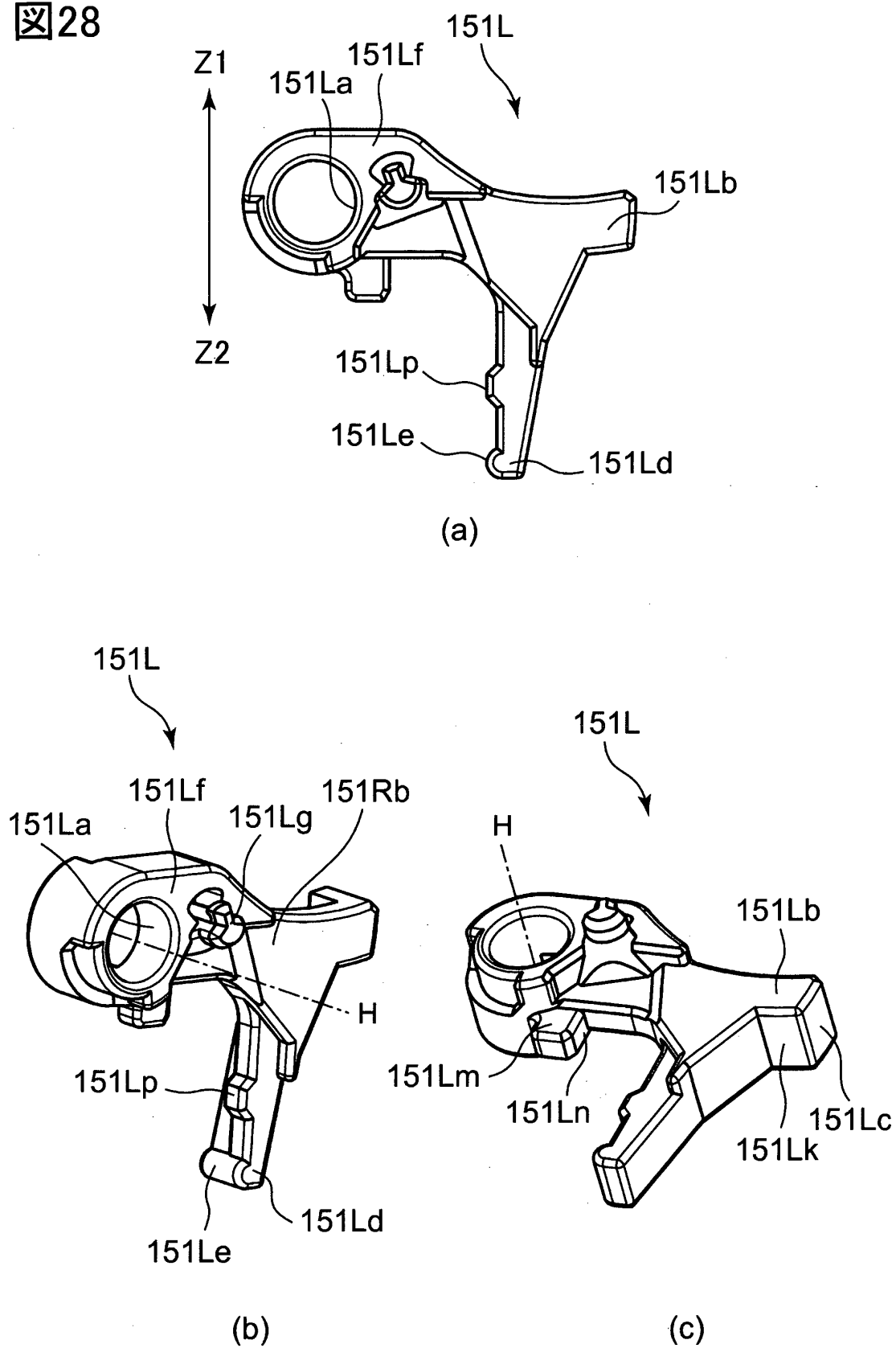


図28



29

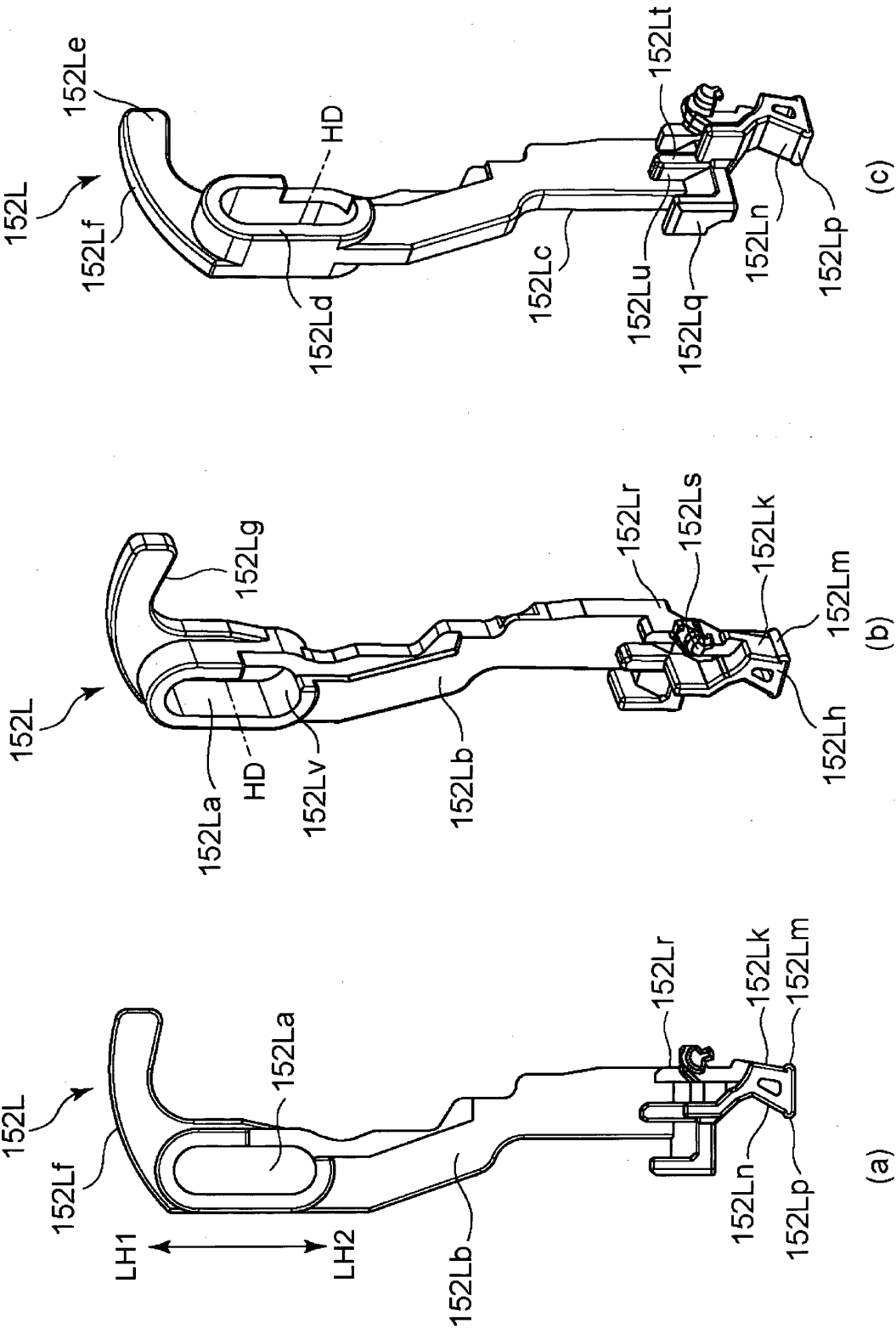


図30

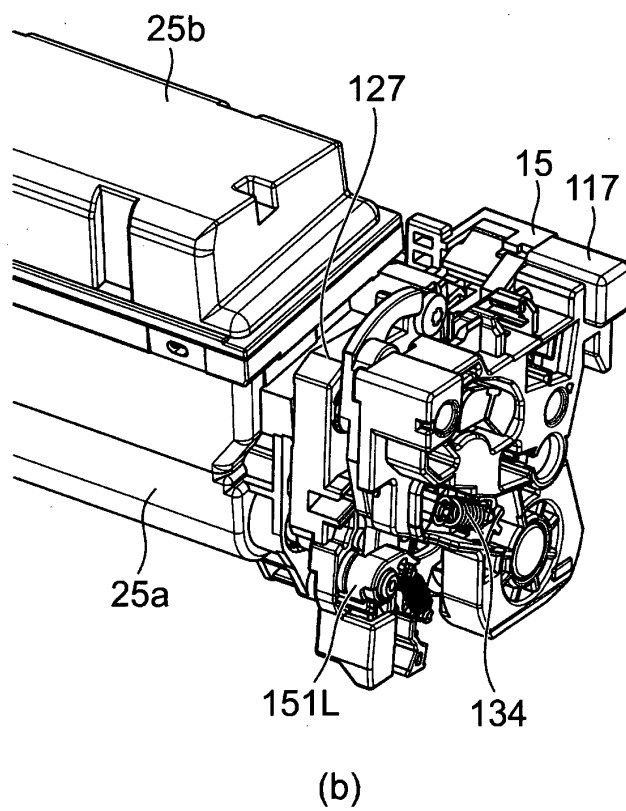
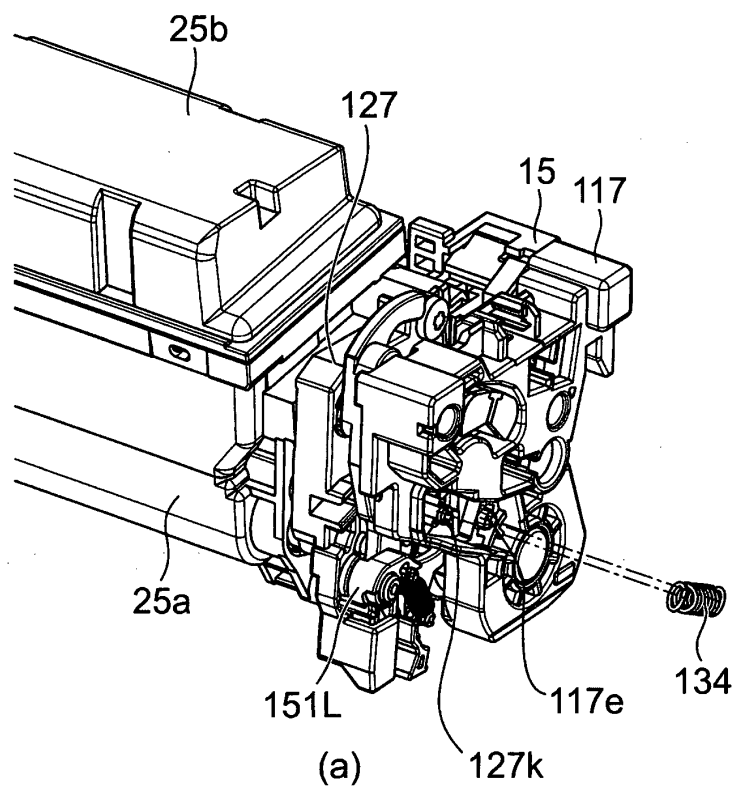


図32

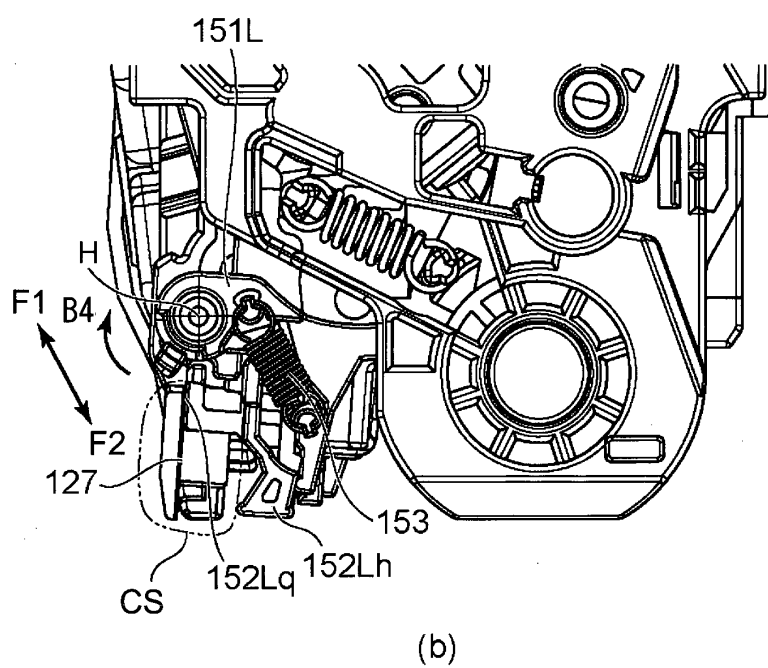
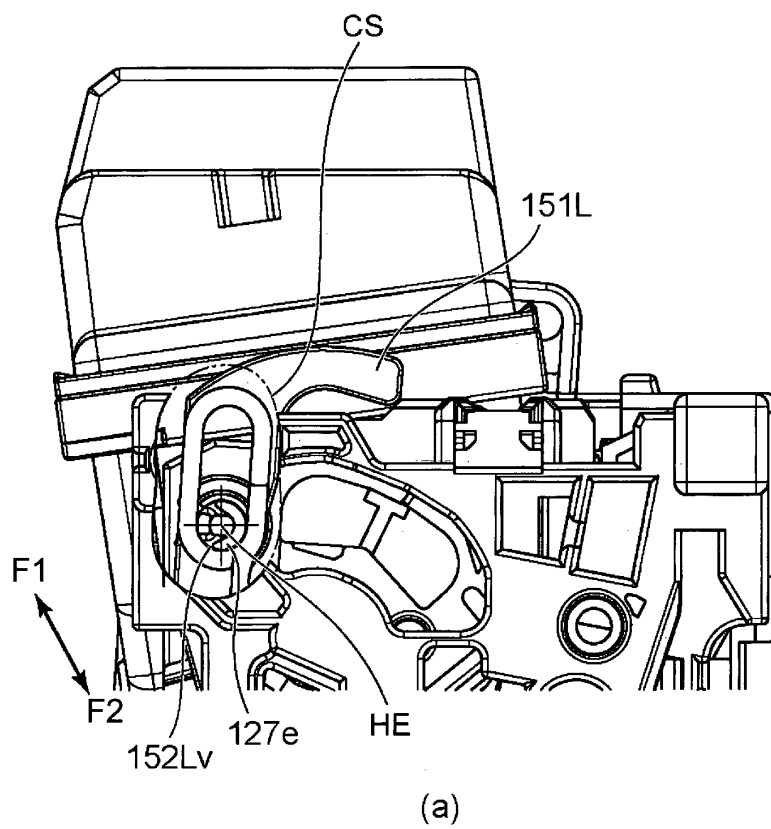


図33

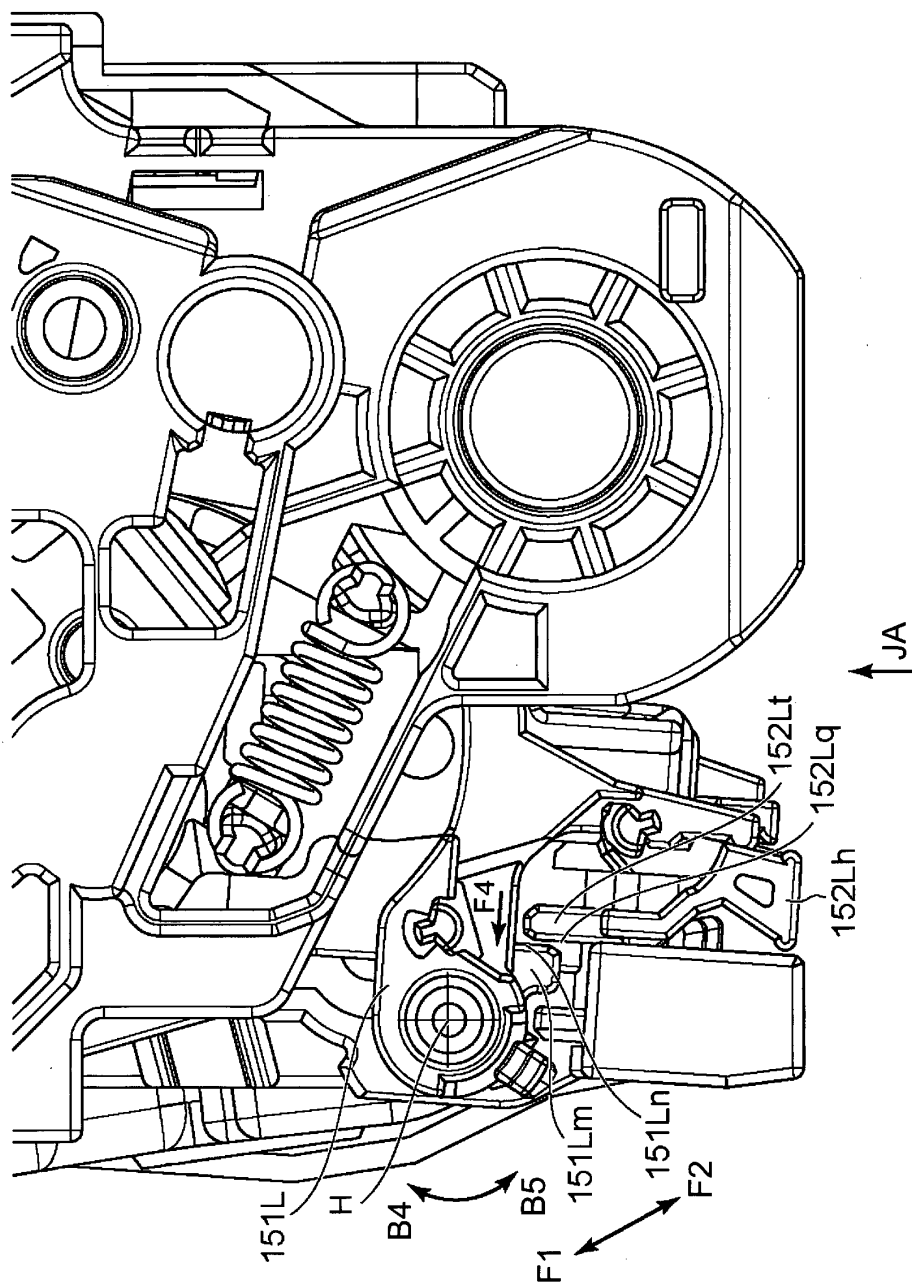


图 34

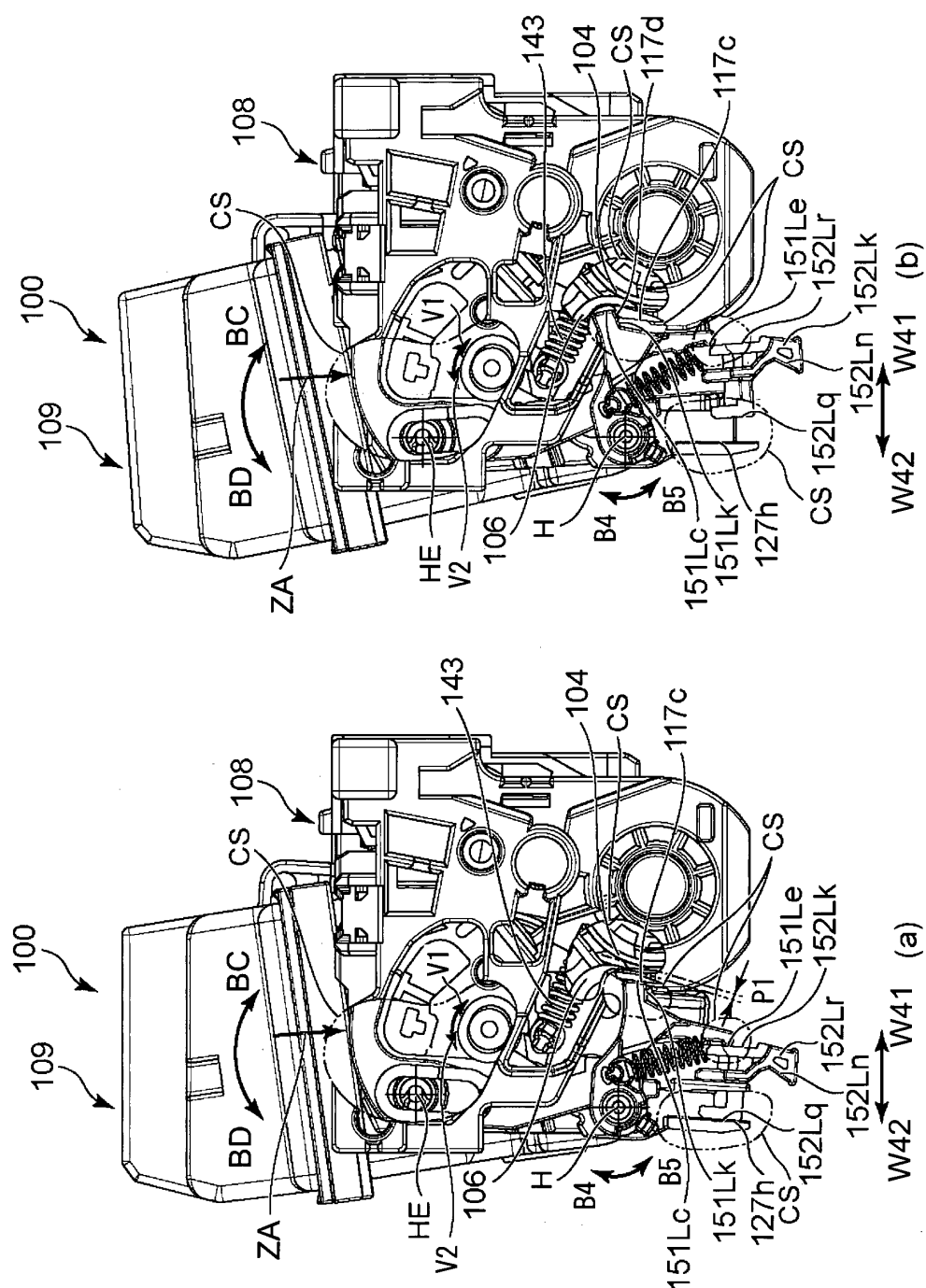


図35

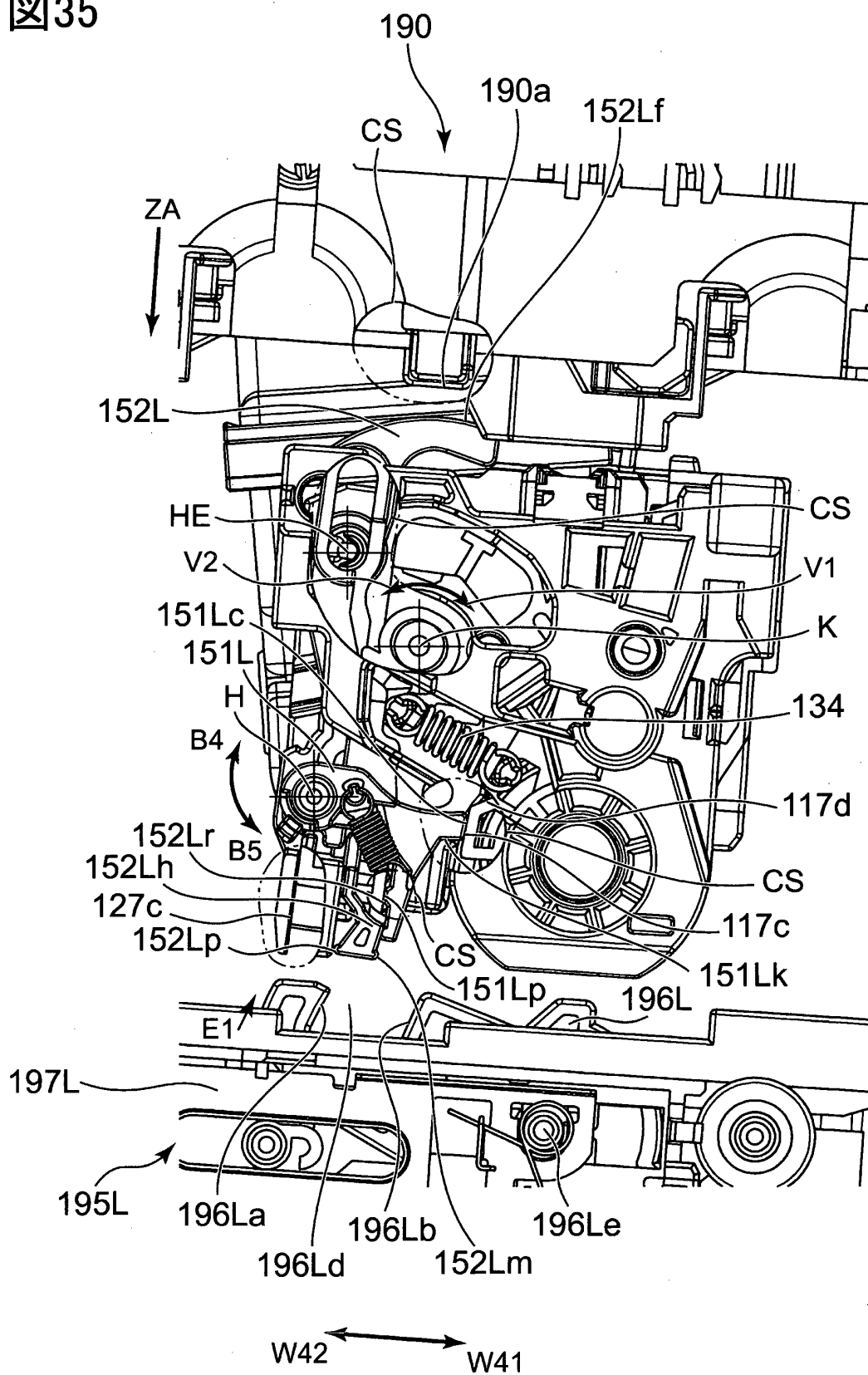


図36

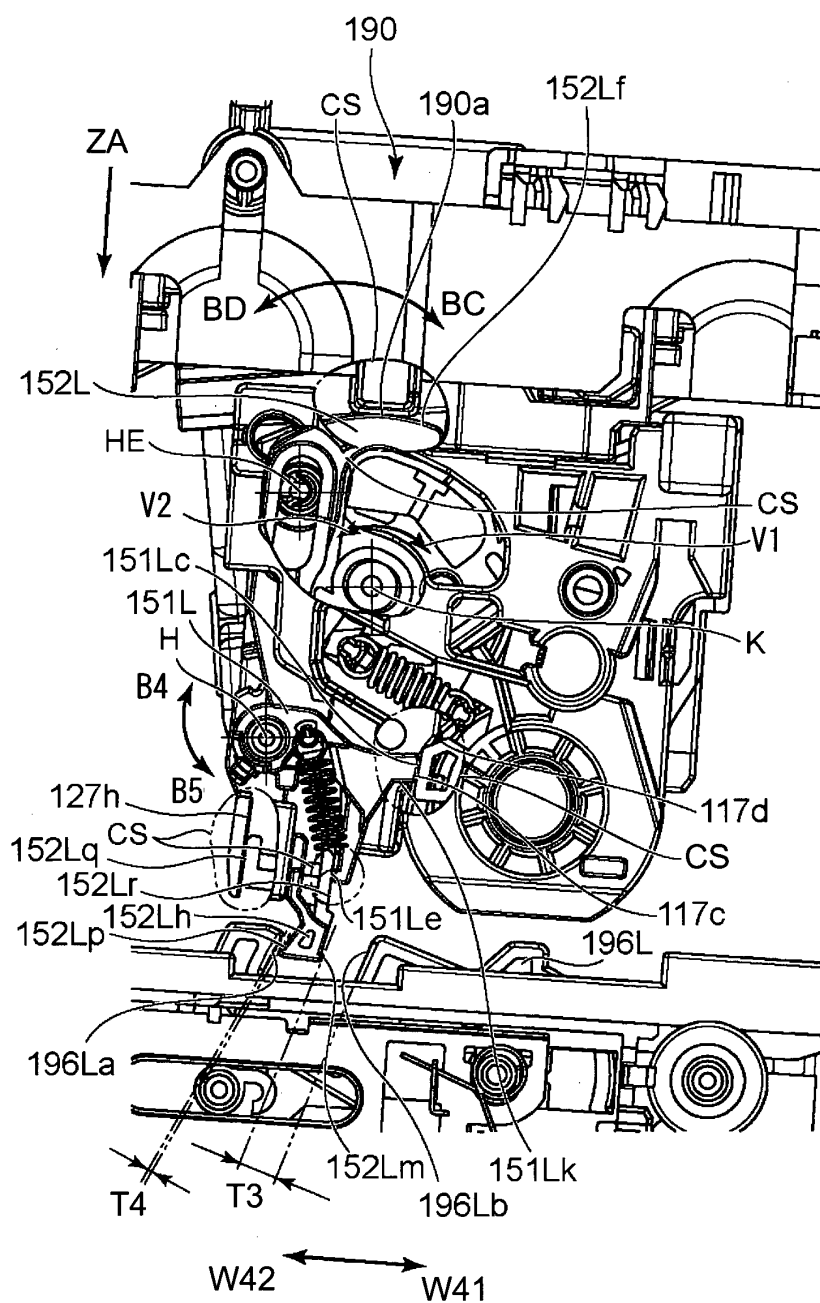


図37

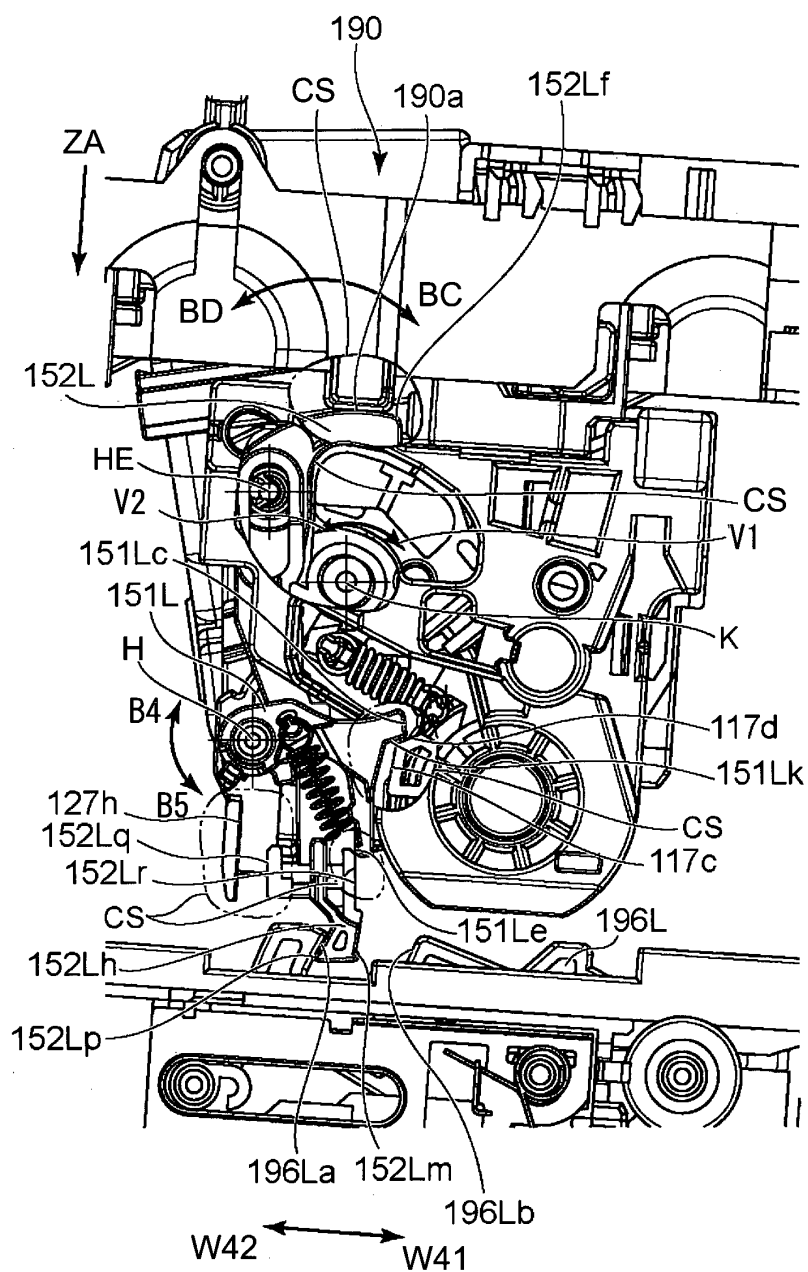


図38

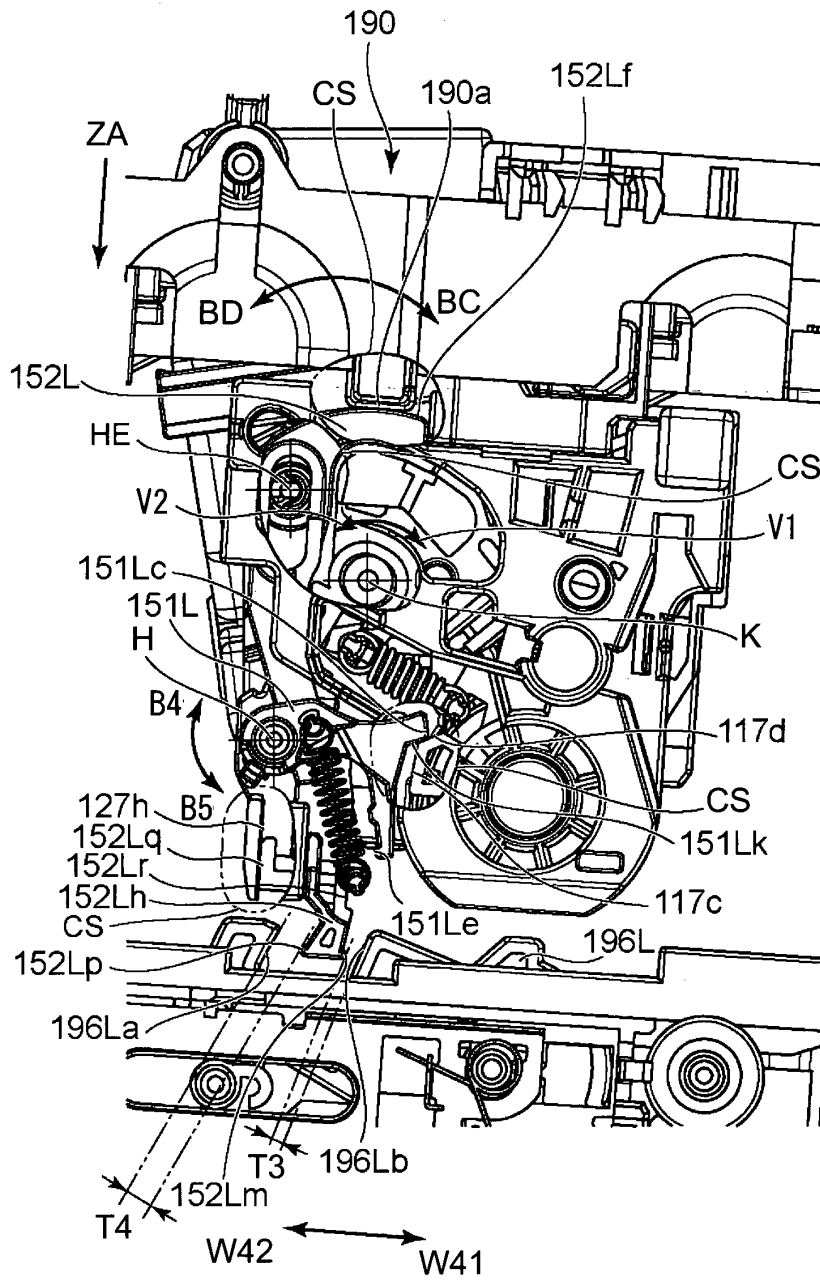


図 39

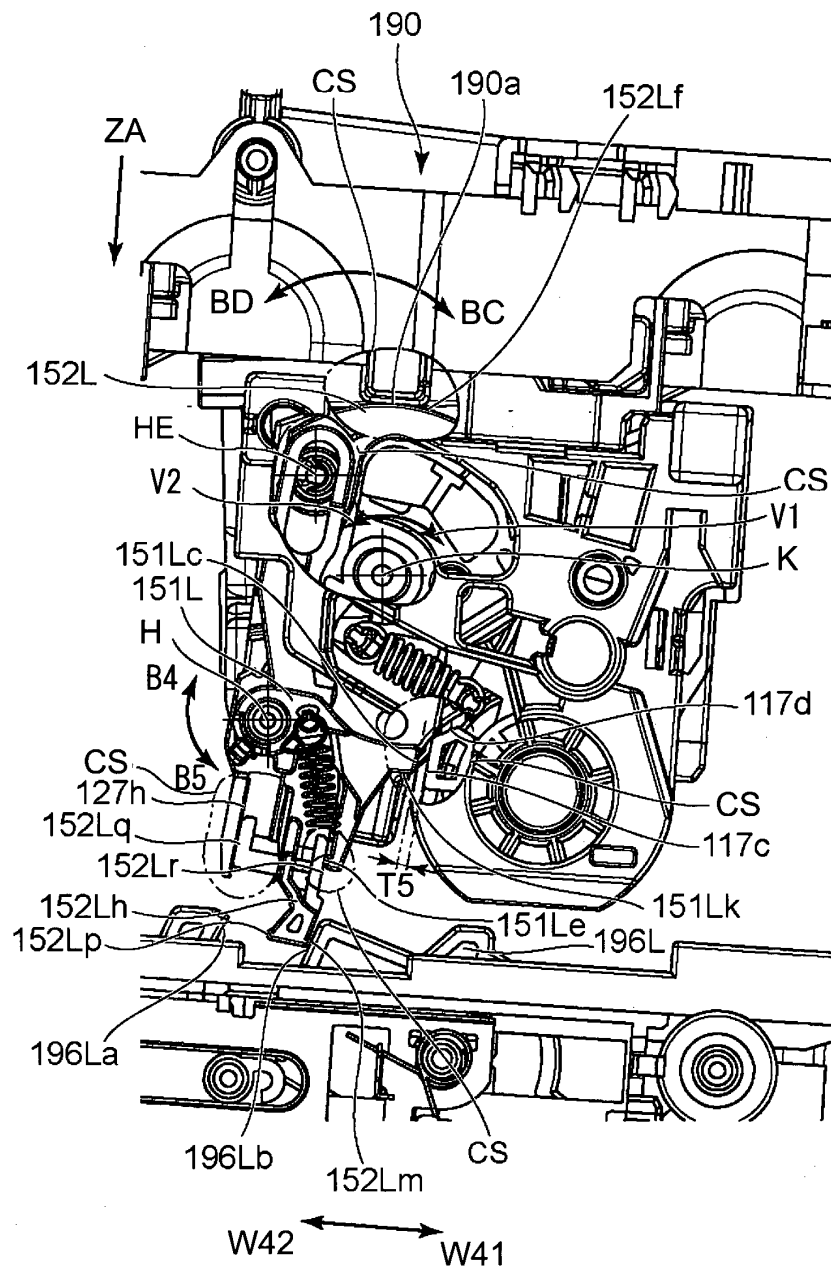


図40

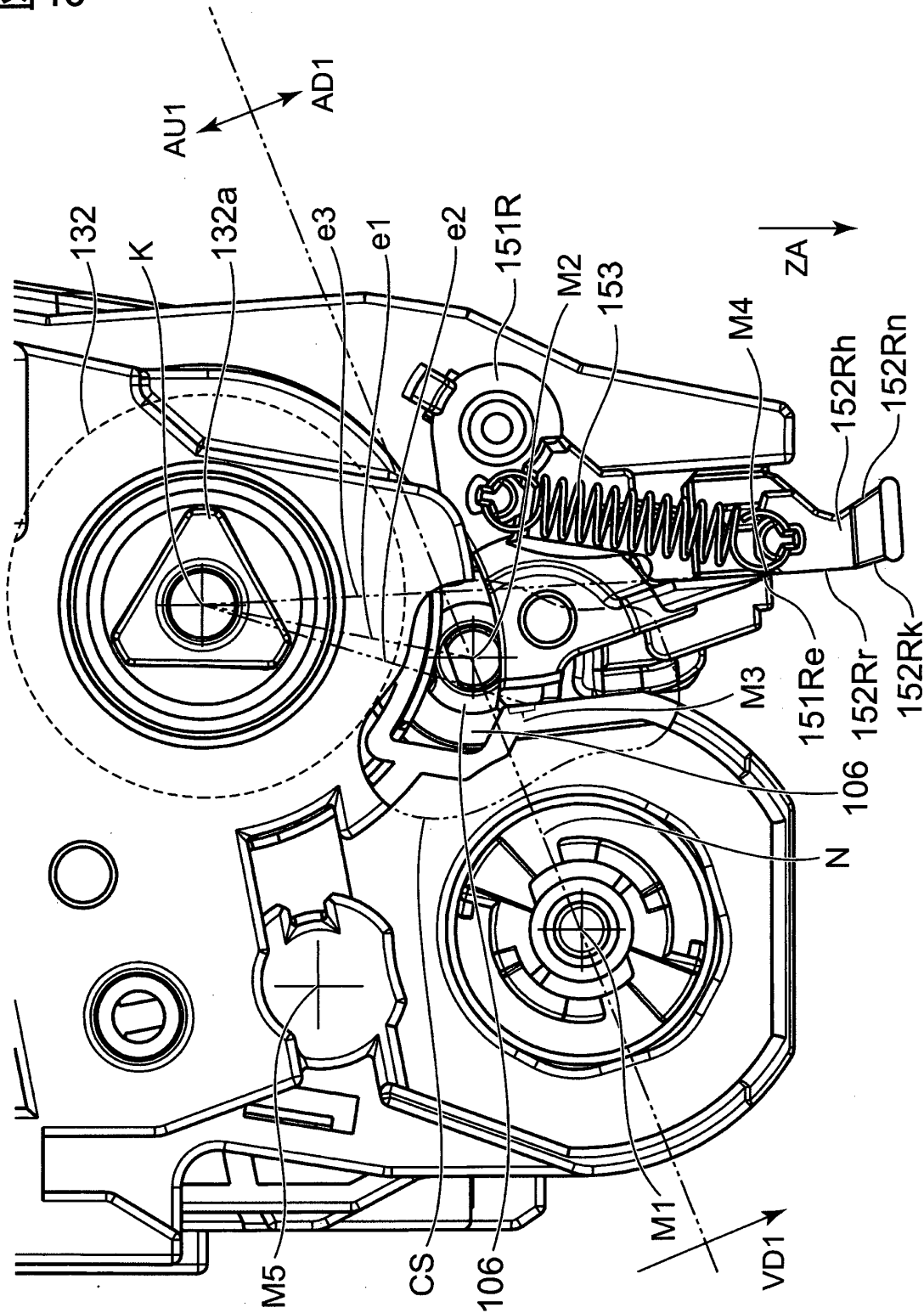


図43

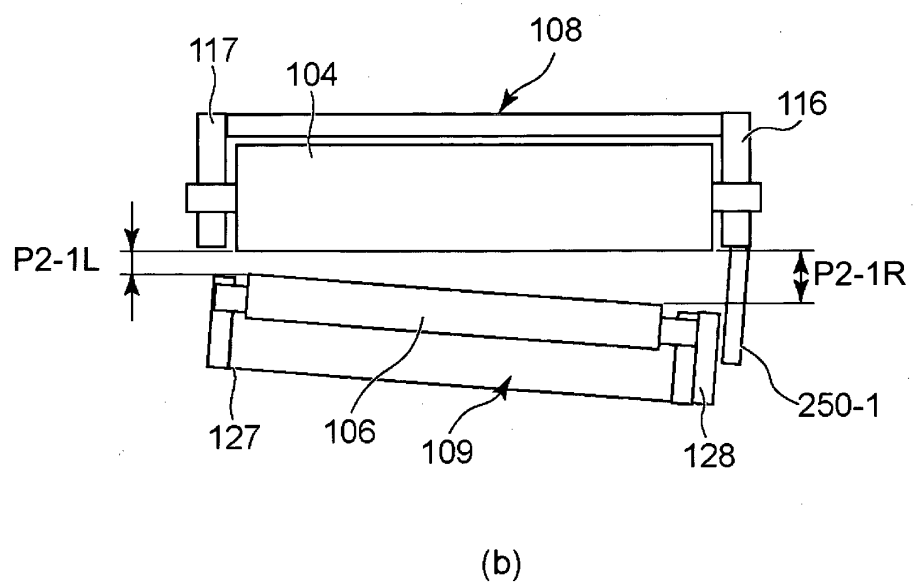
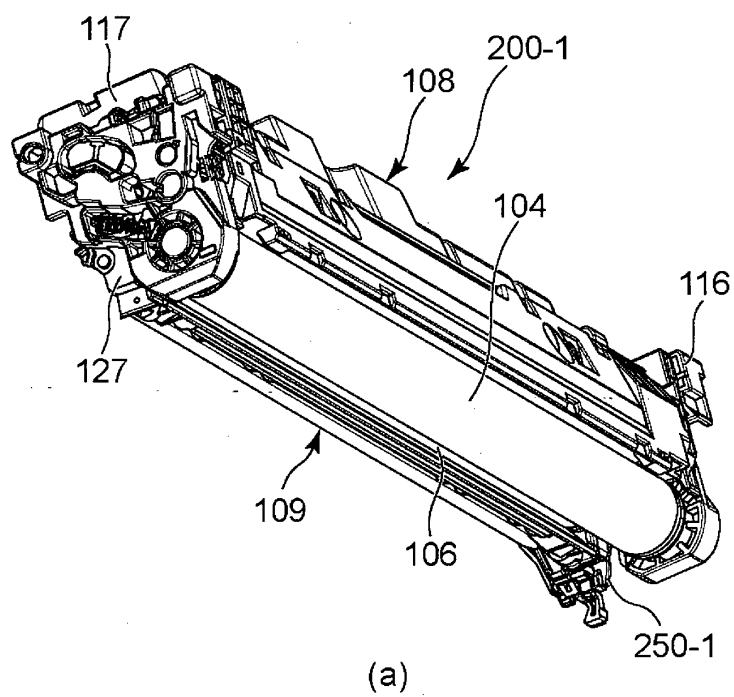


図44

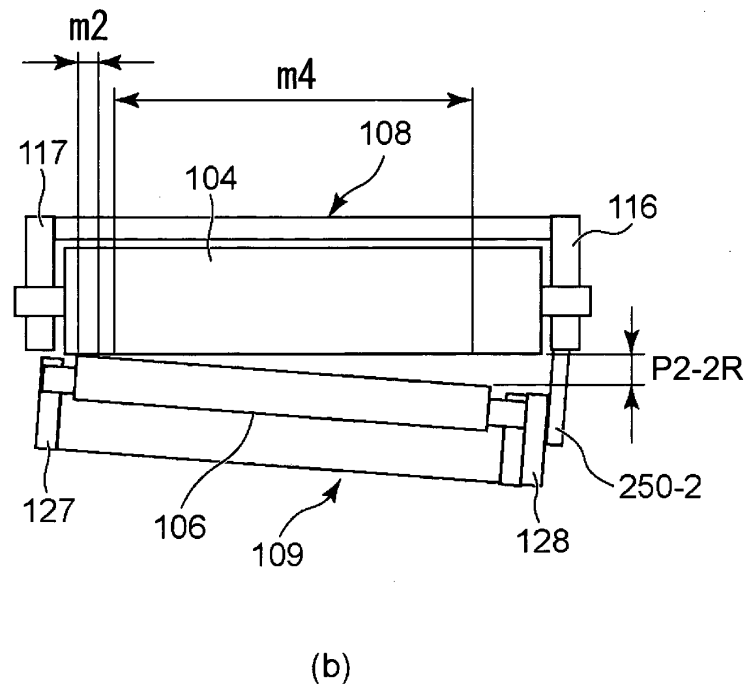
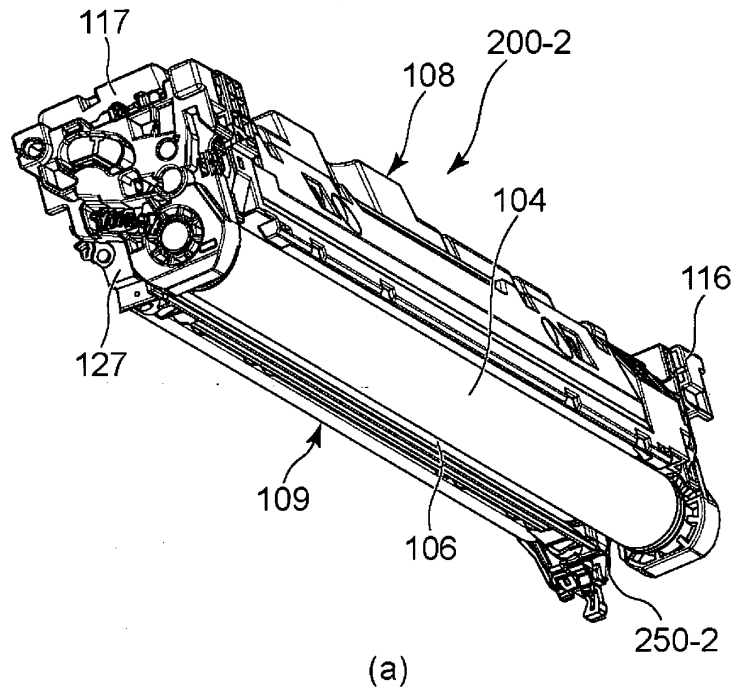
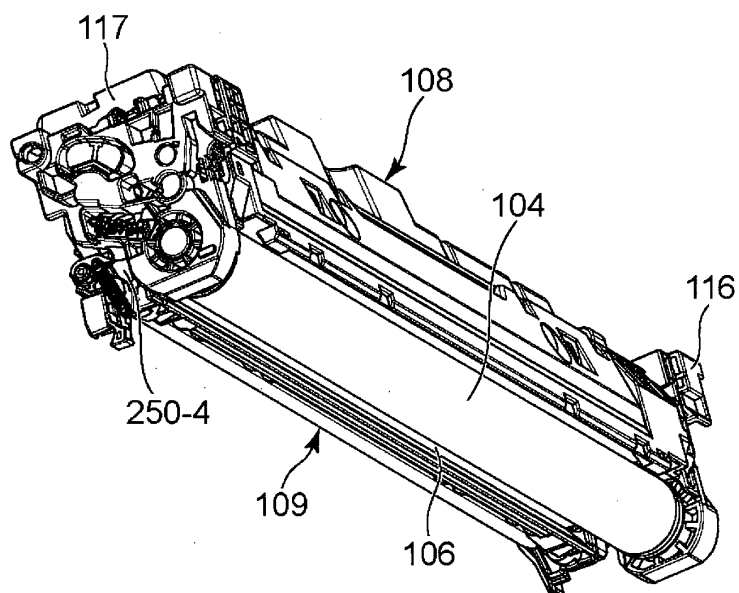
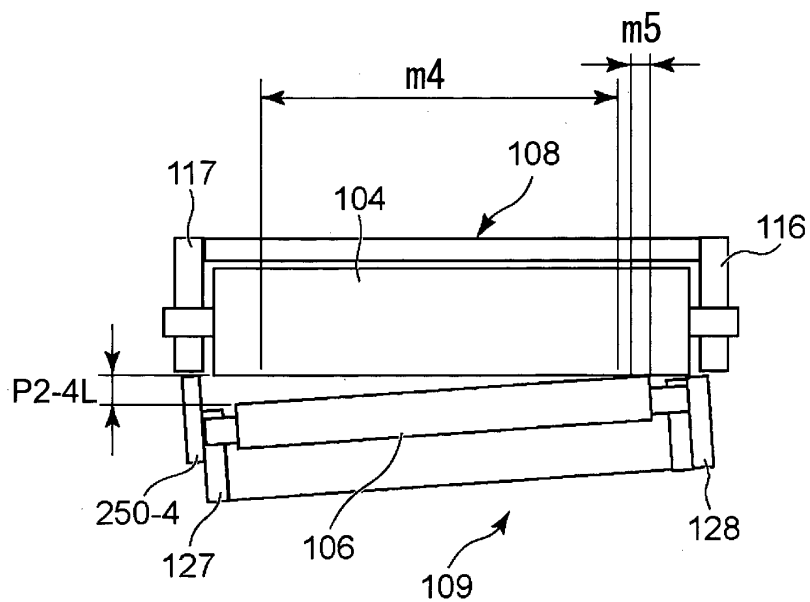


図46



(a)



(b)

図47

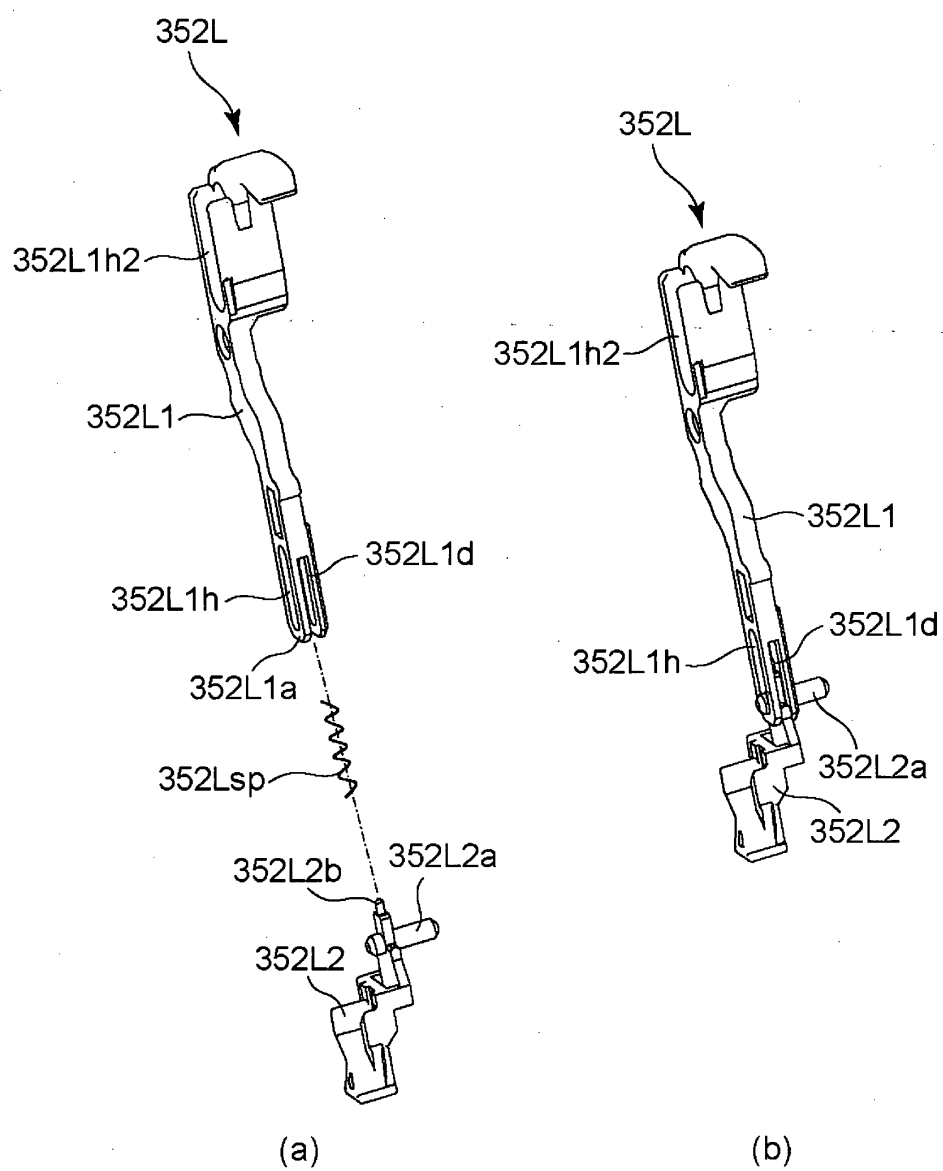


図48

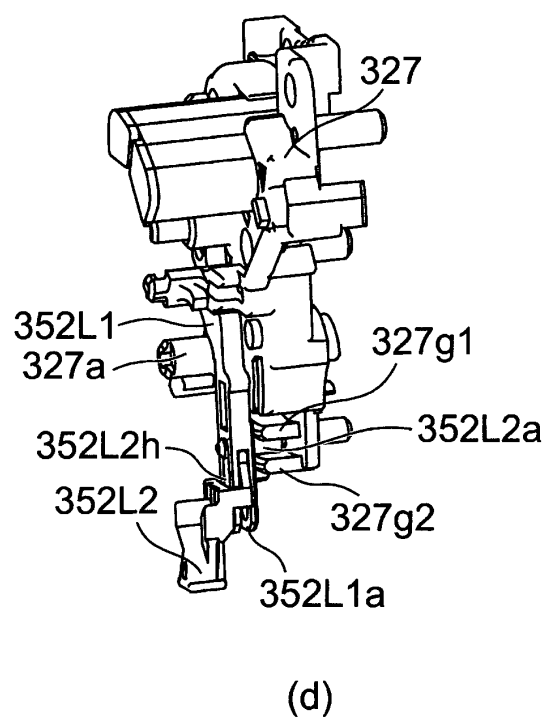
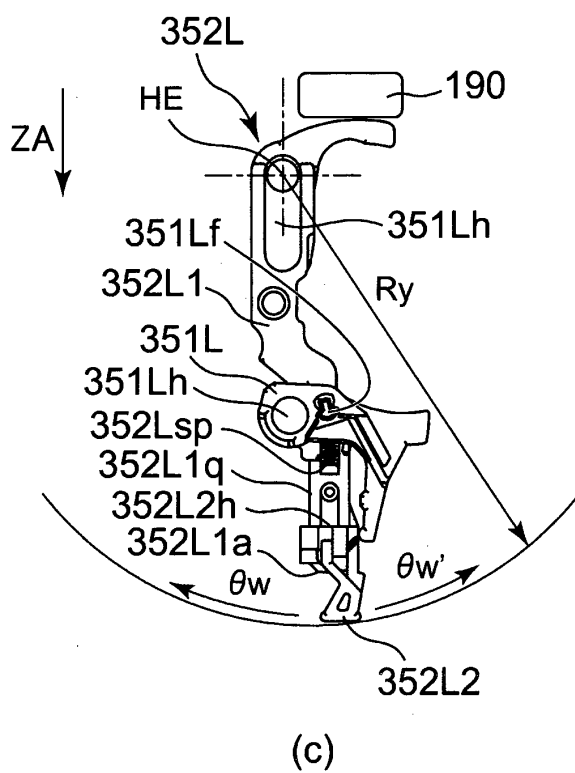
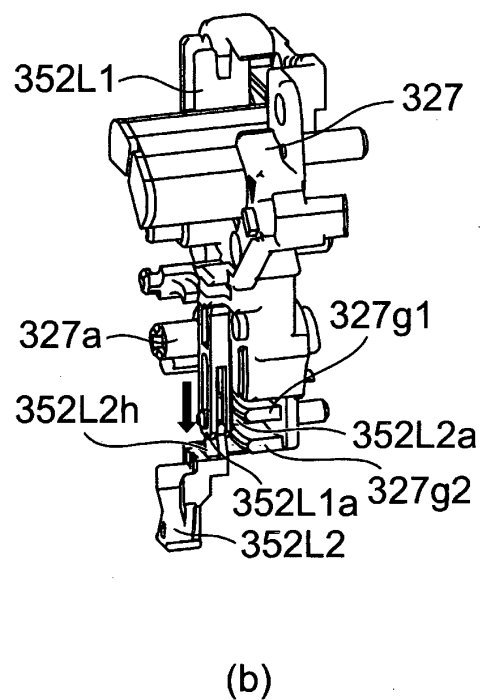
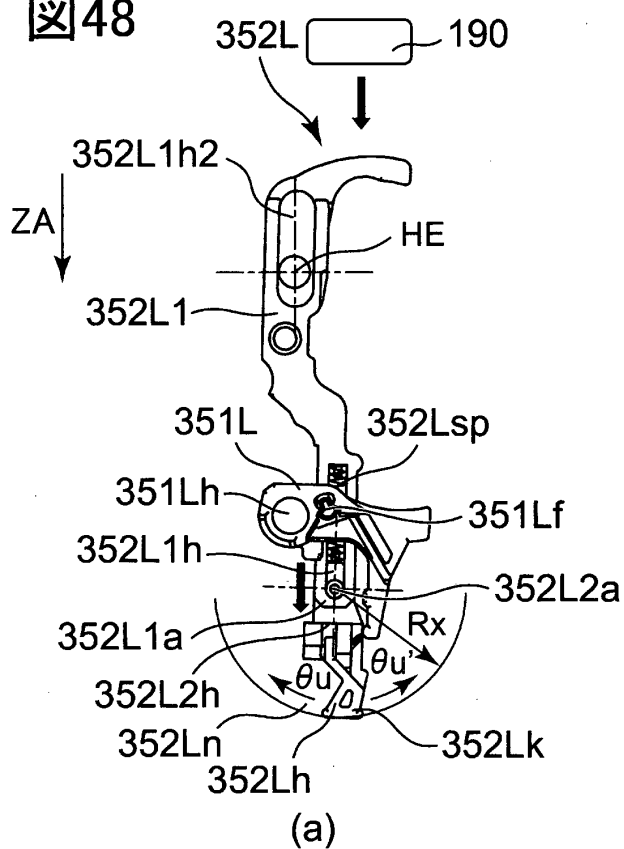


図50

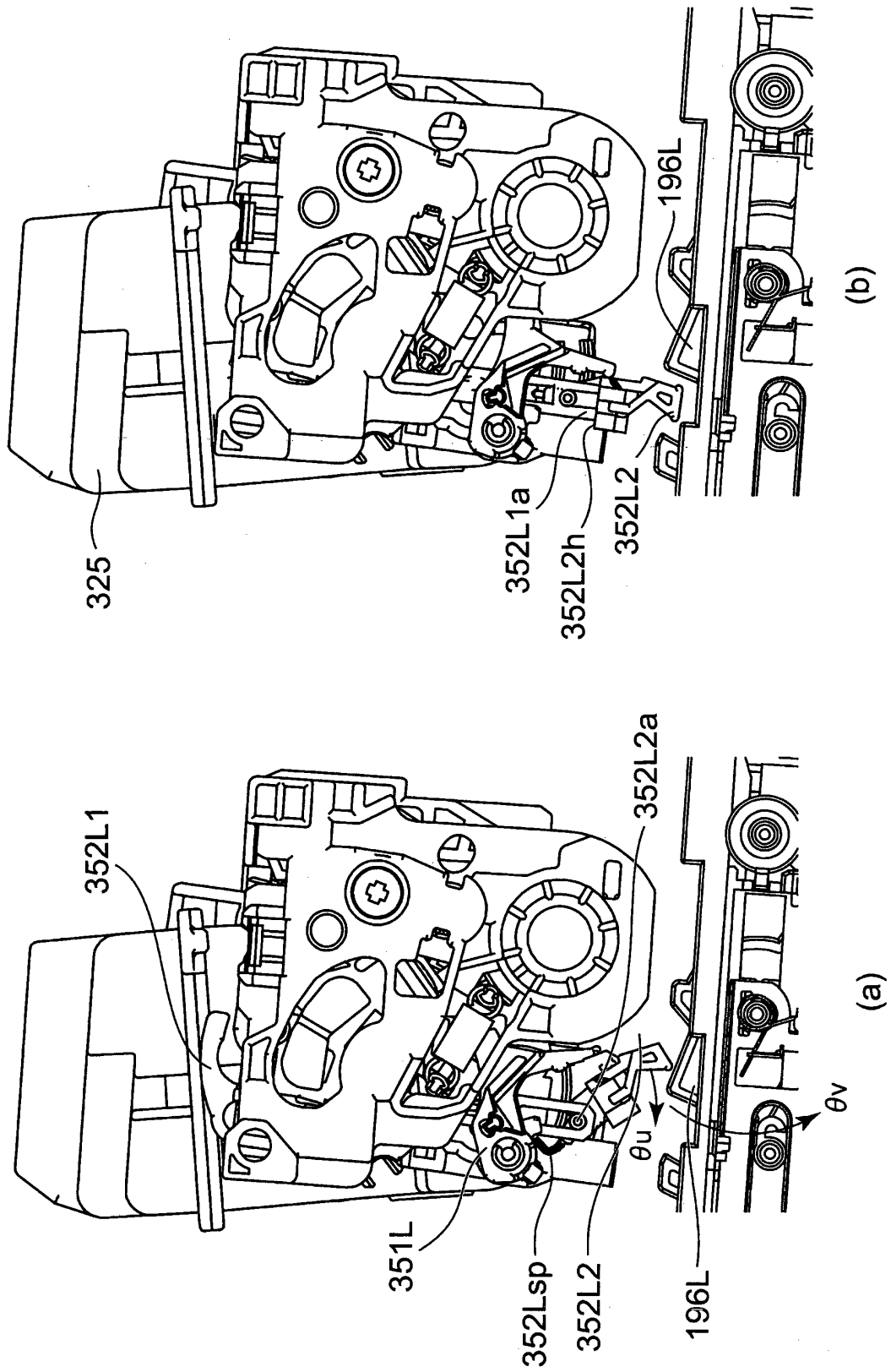


図51

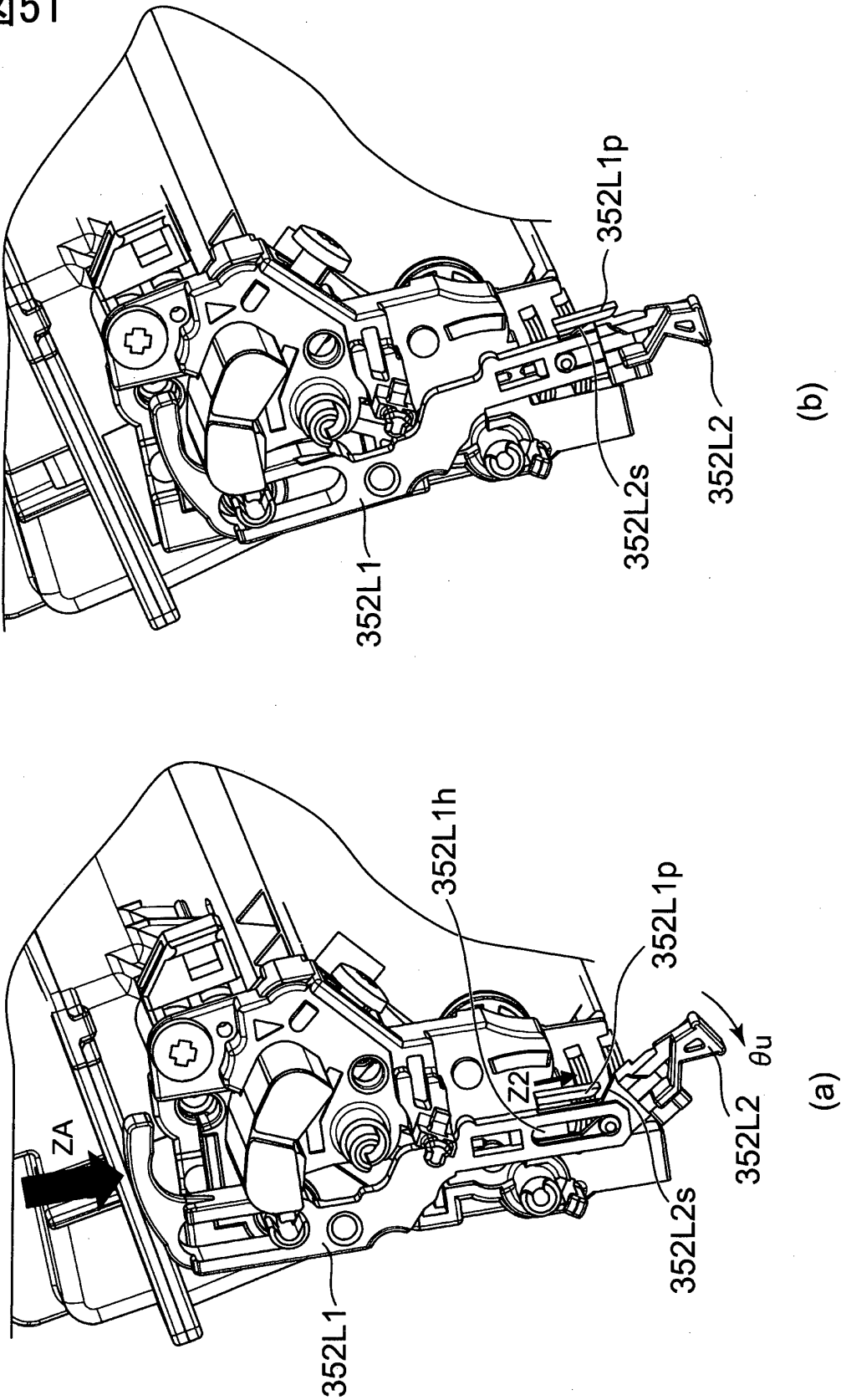


図52

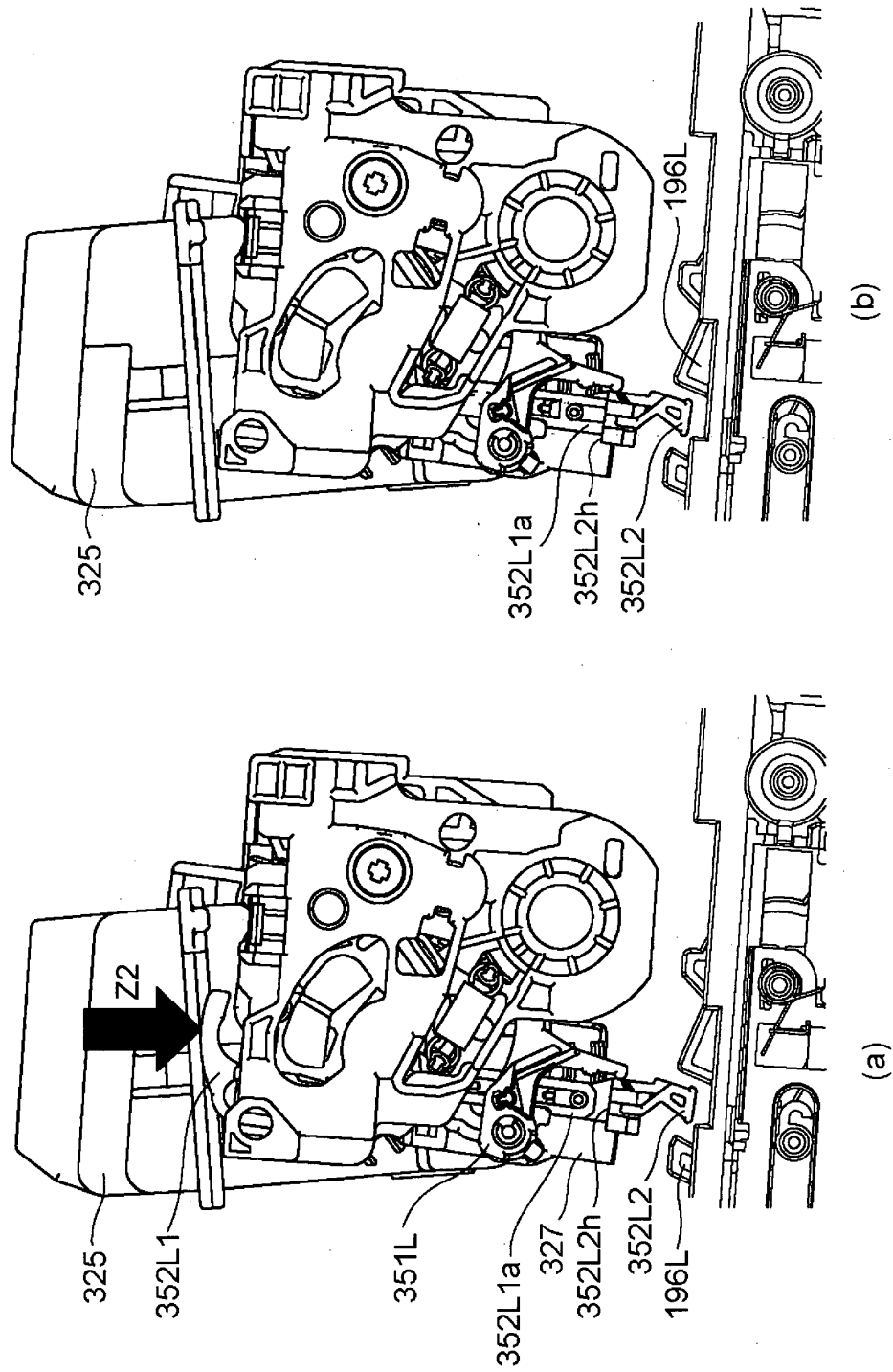


図53

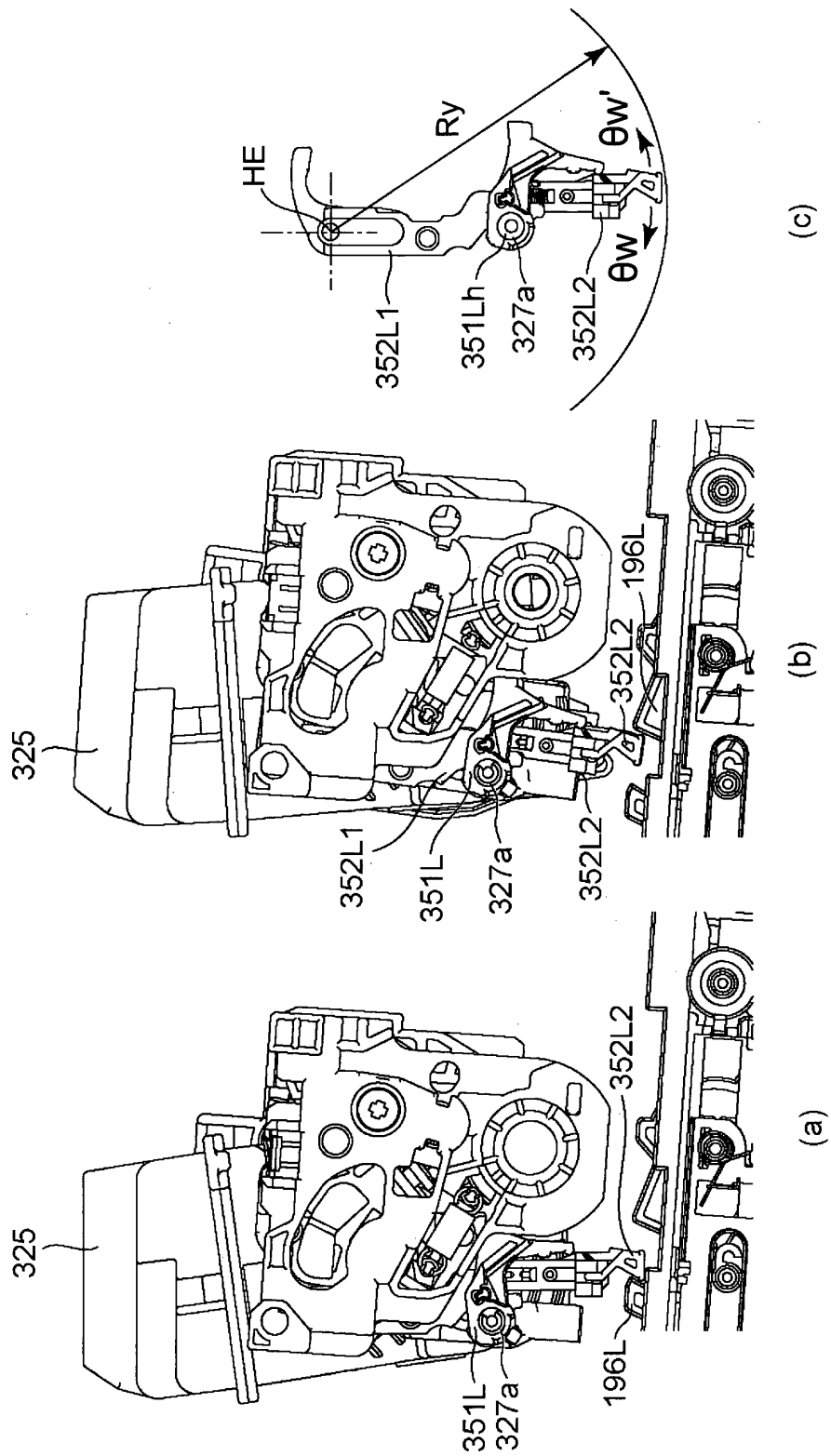


図54

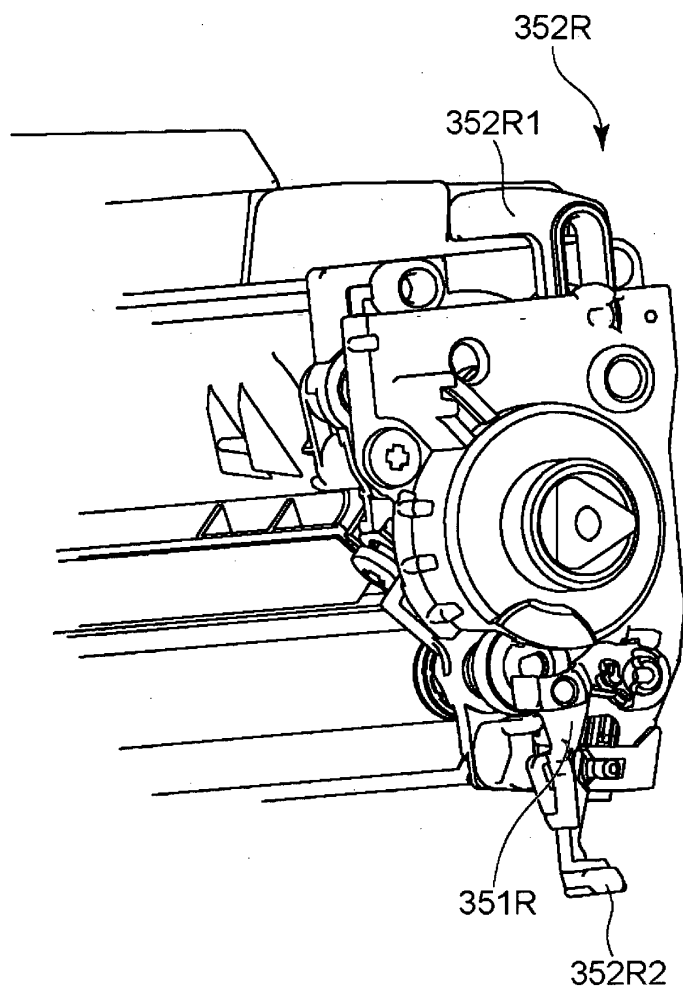


図55

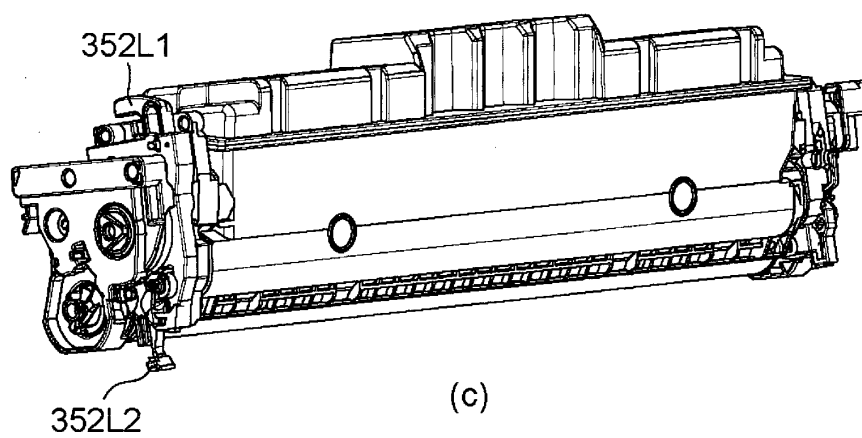
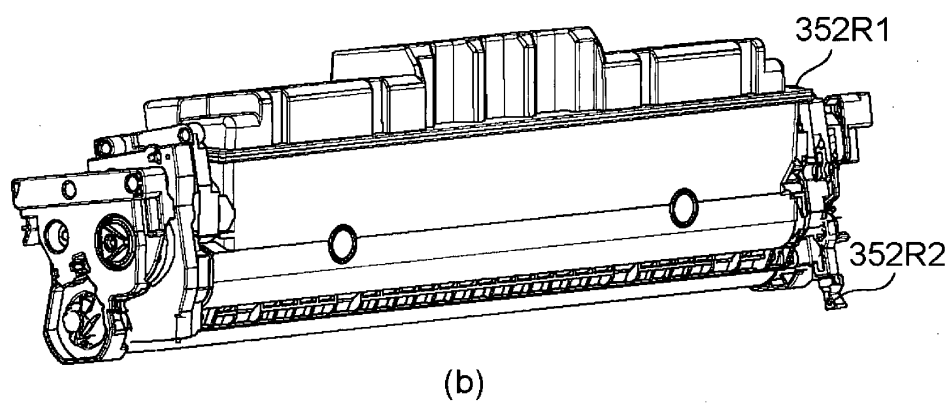
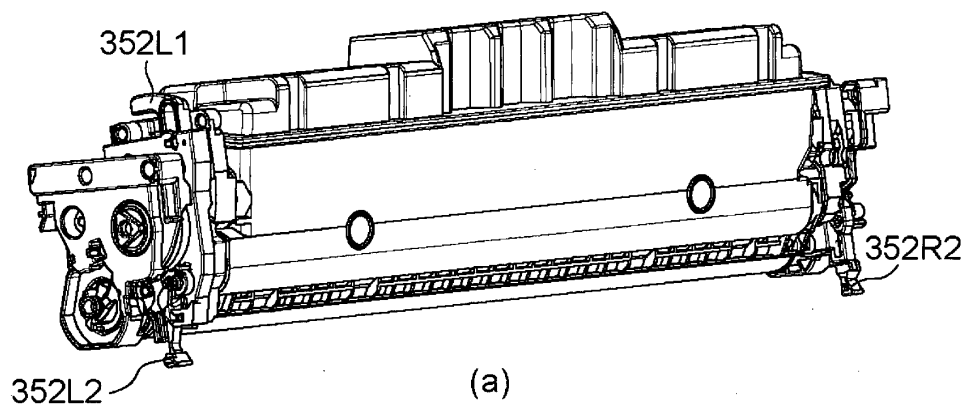


図56

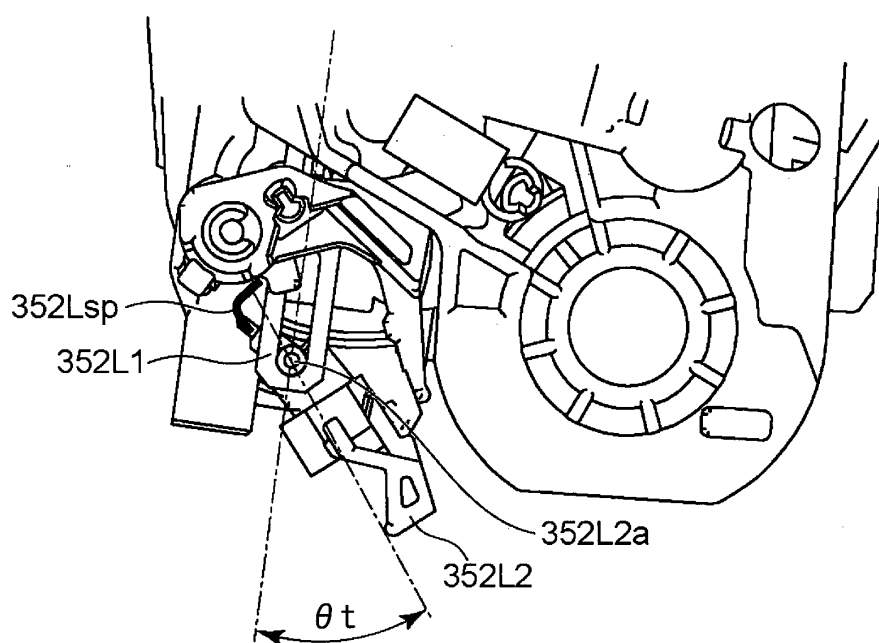


図57

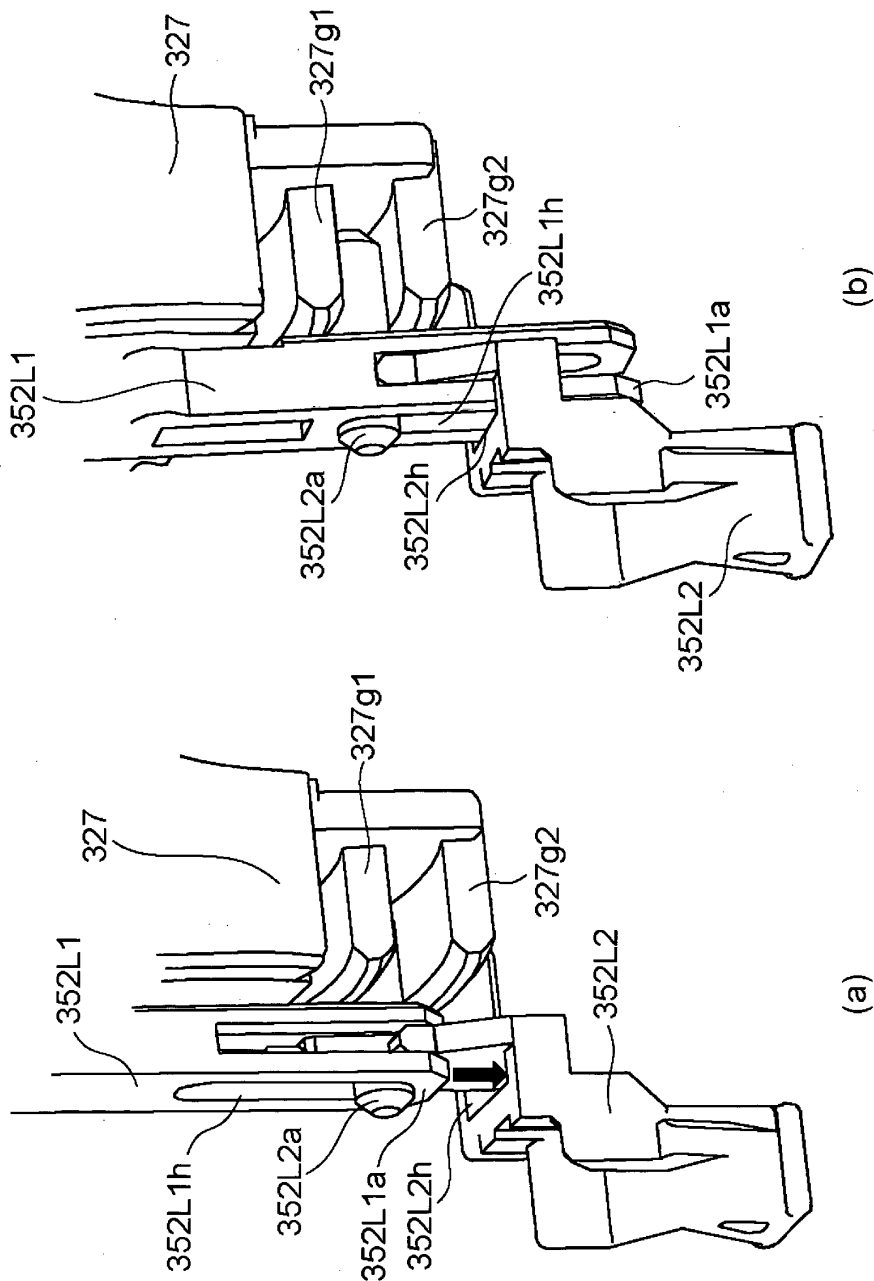


図58

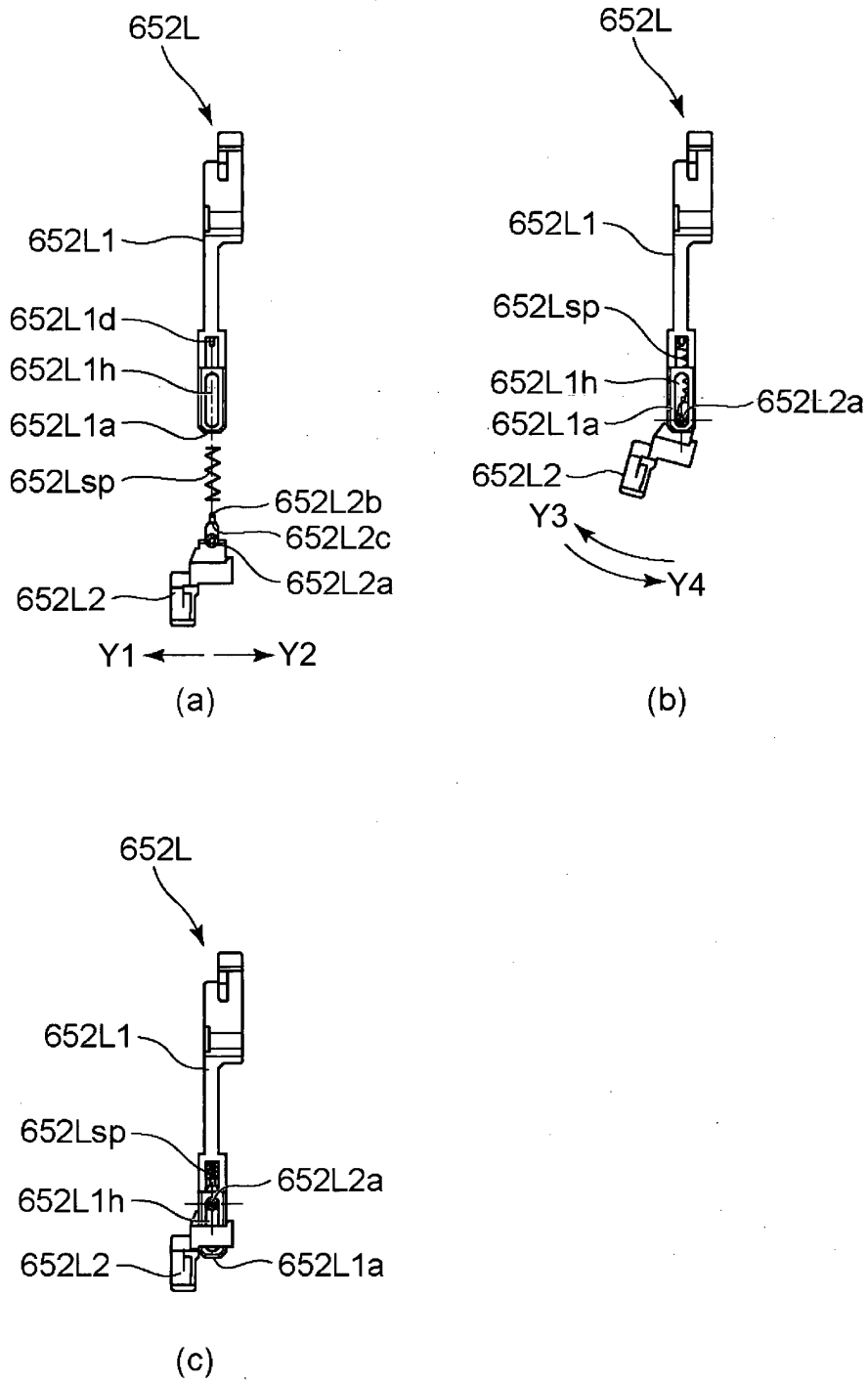
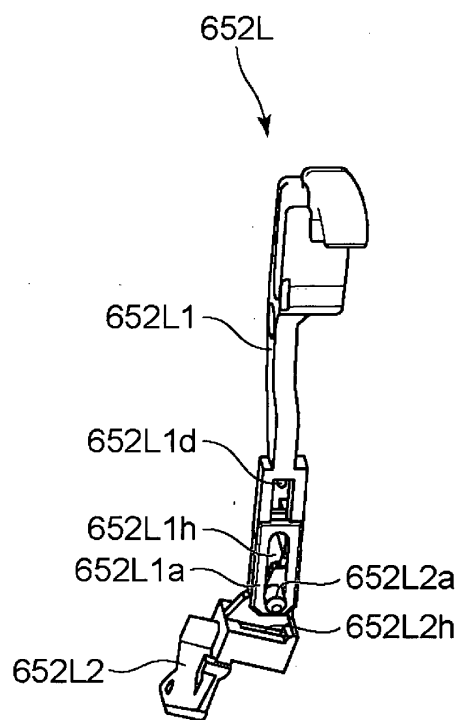
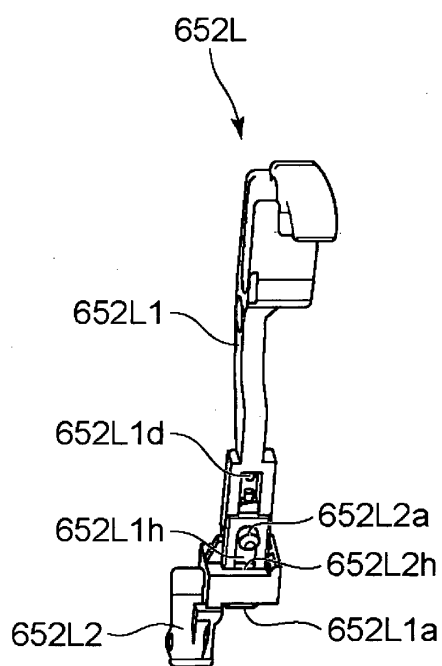


図59



(a)



(b)

図60

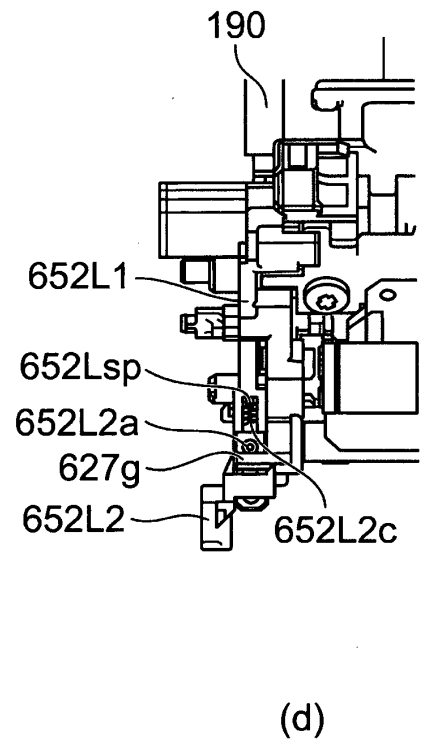
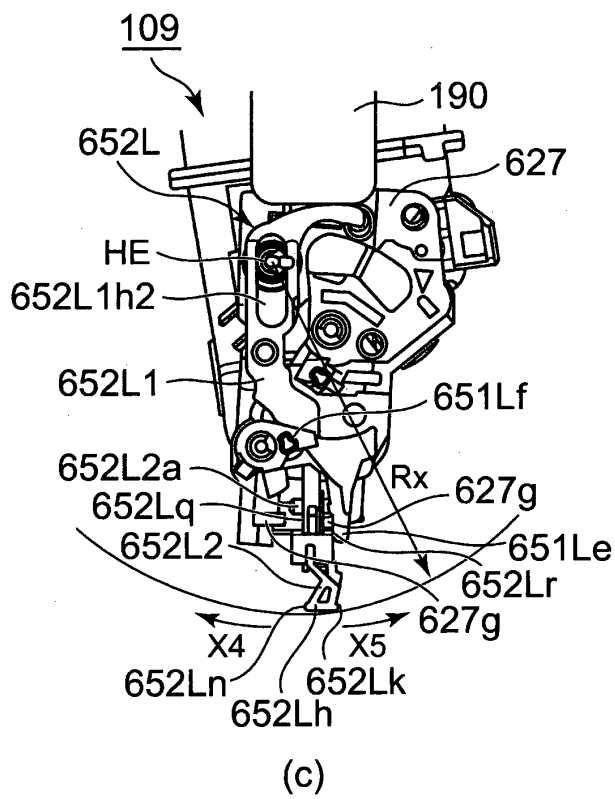
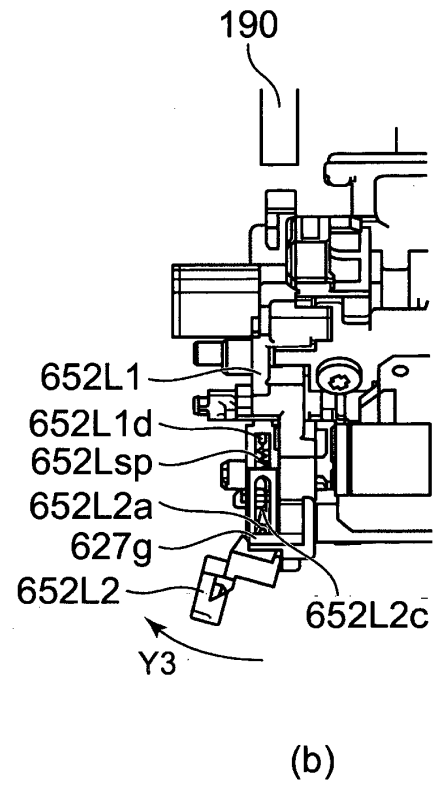
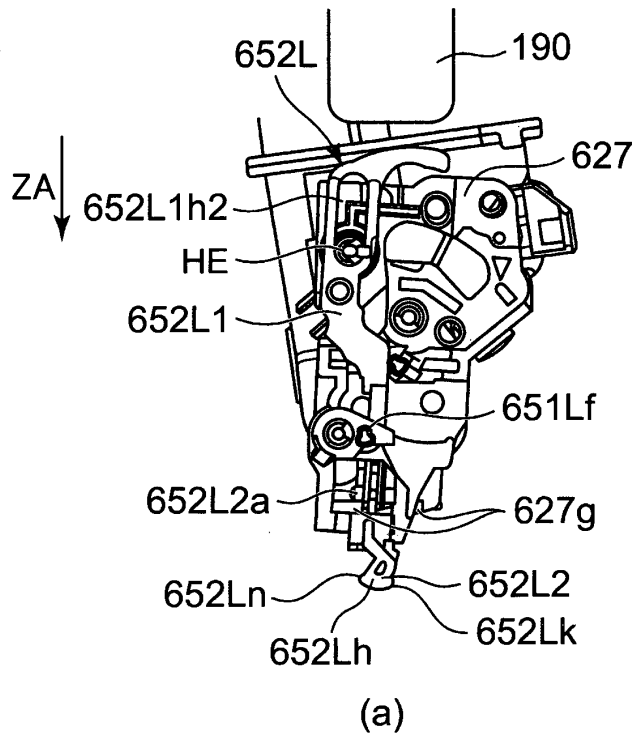


図61

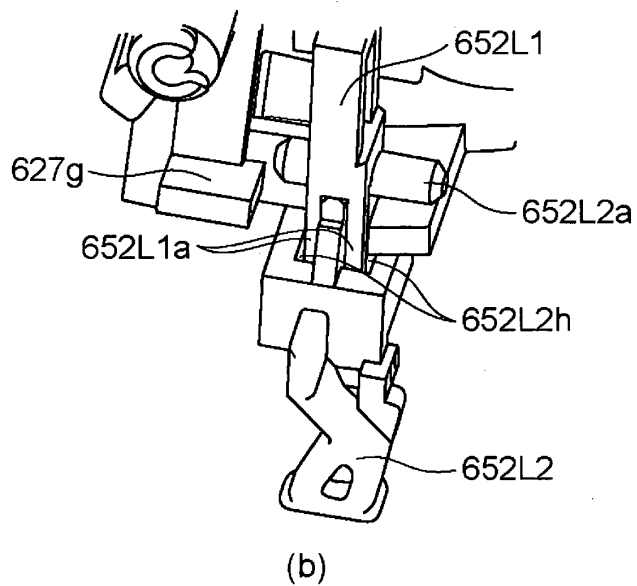
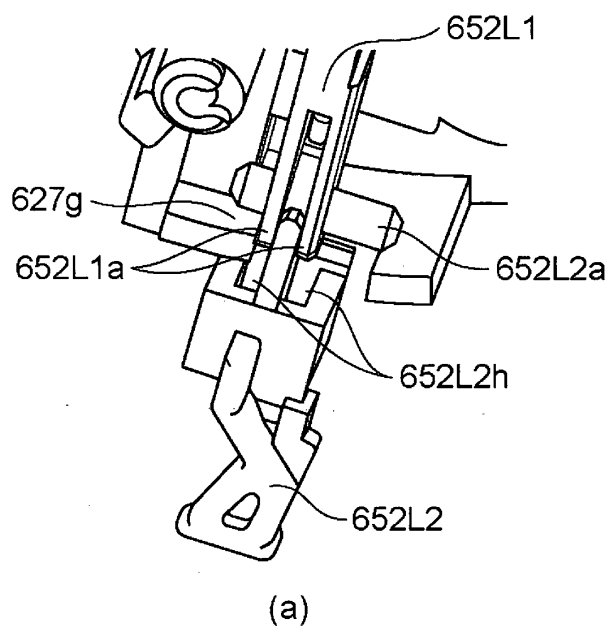


図62

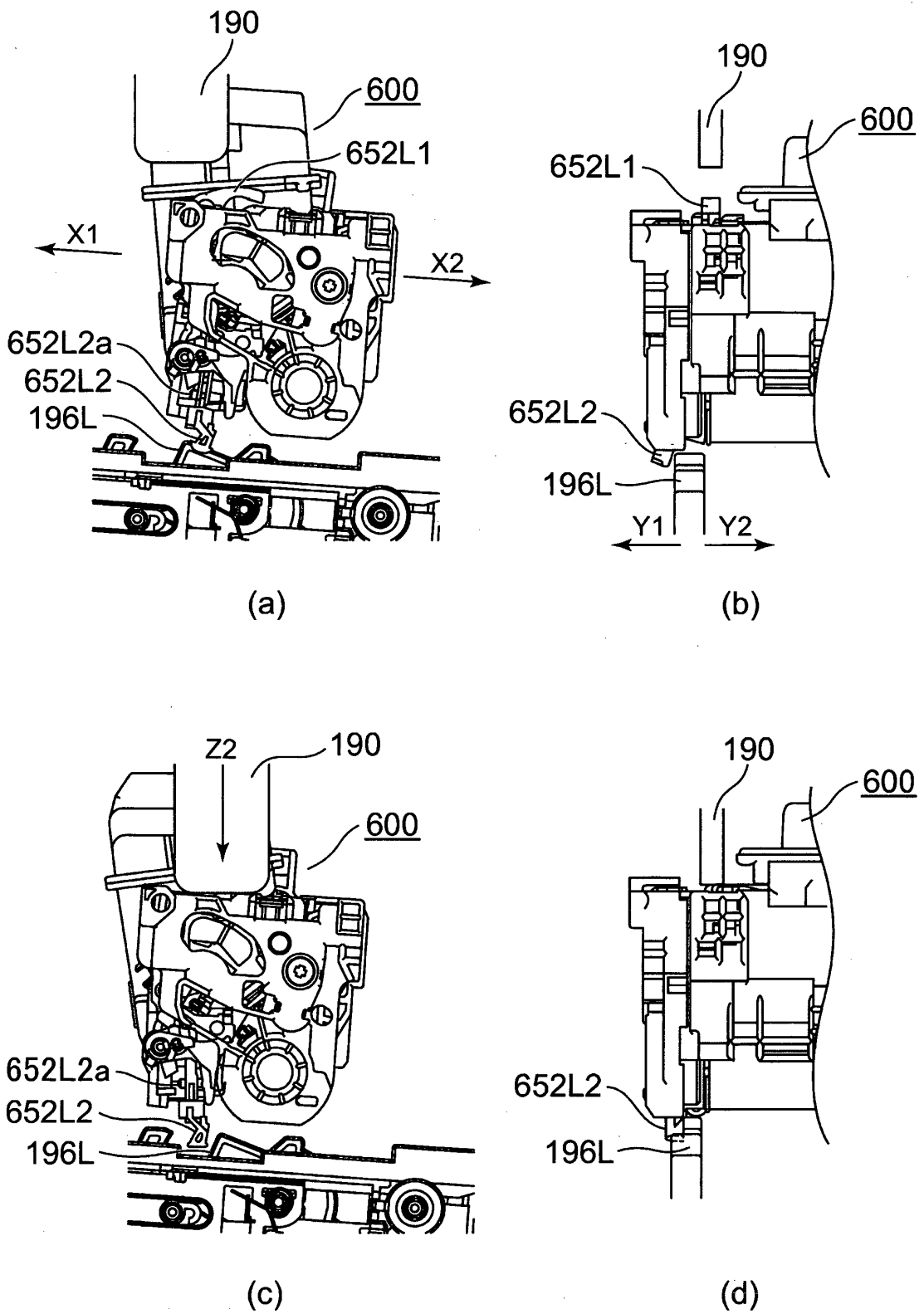


図63

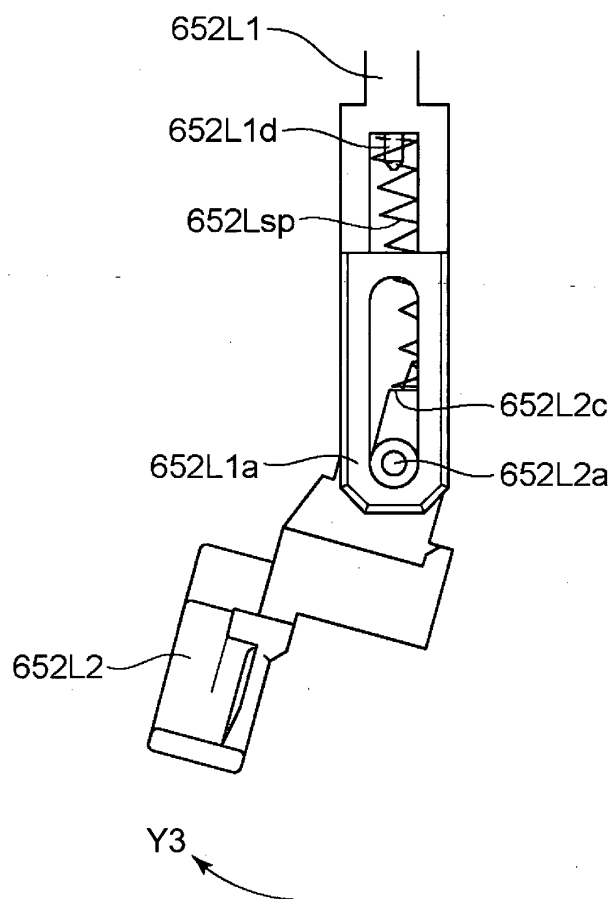
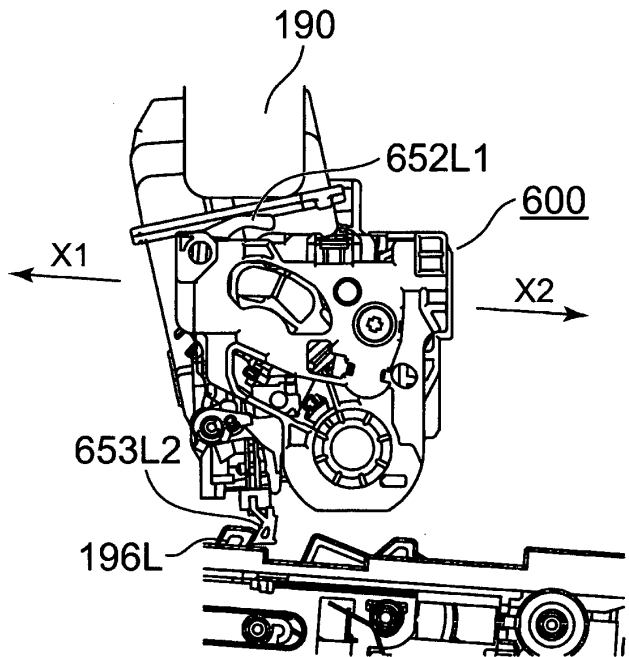
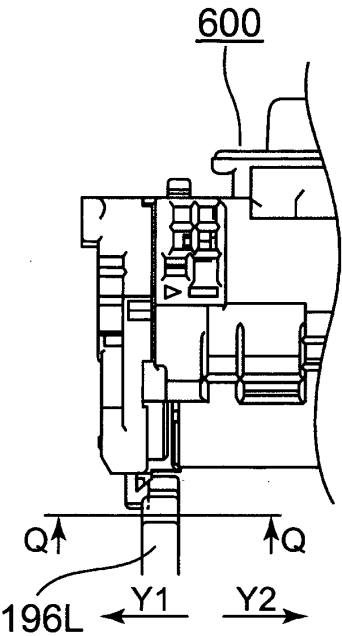


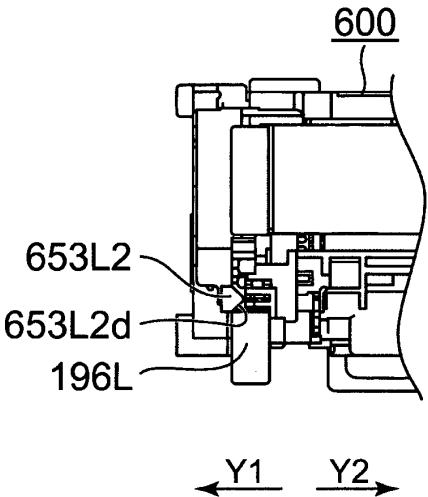
図64



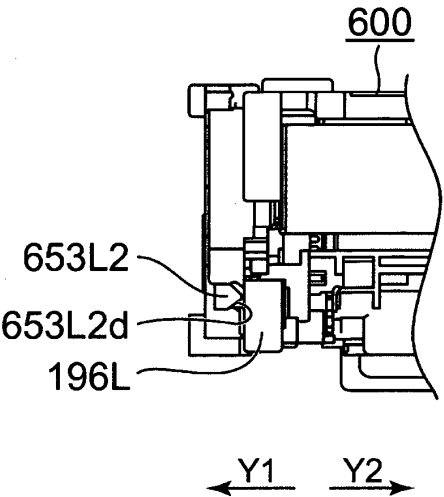
(a)



(b)



(c)



(d)

図65

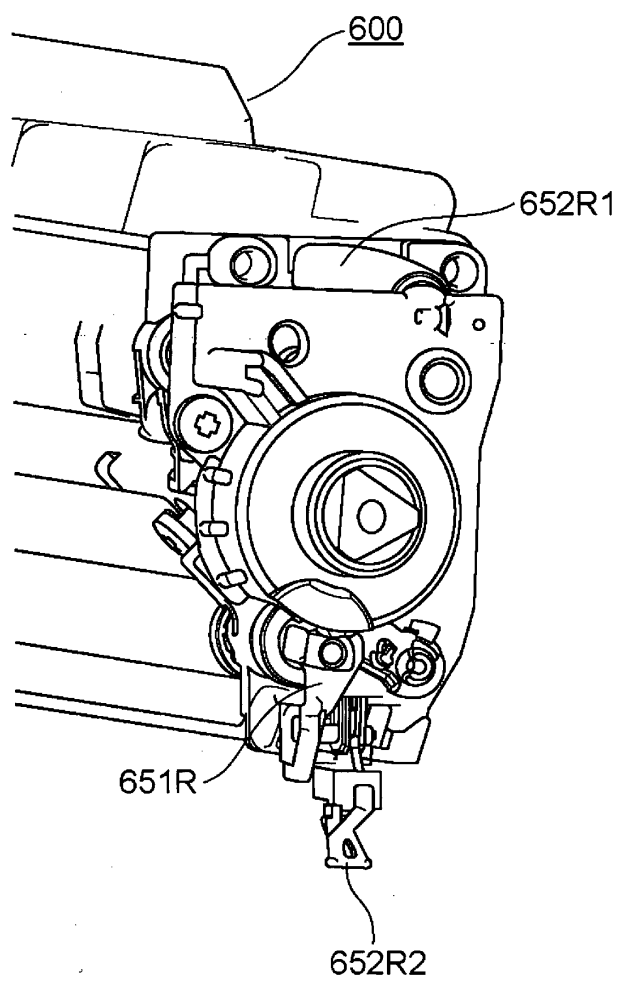


図 66

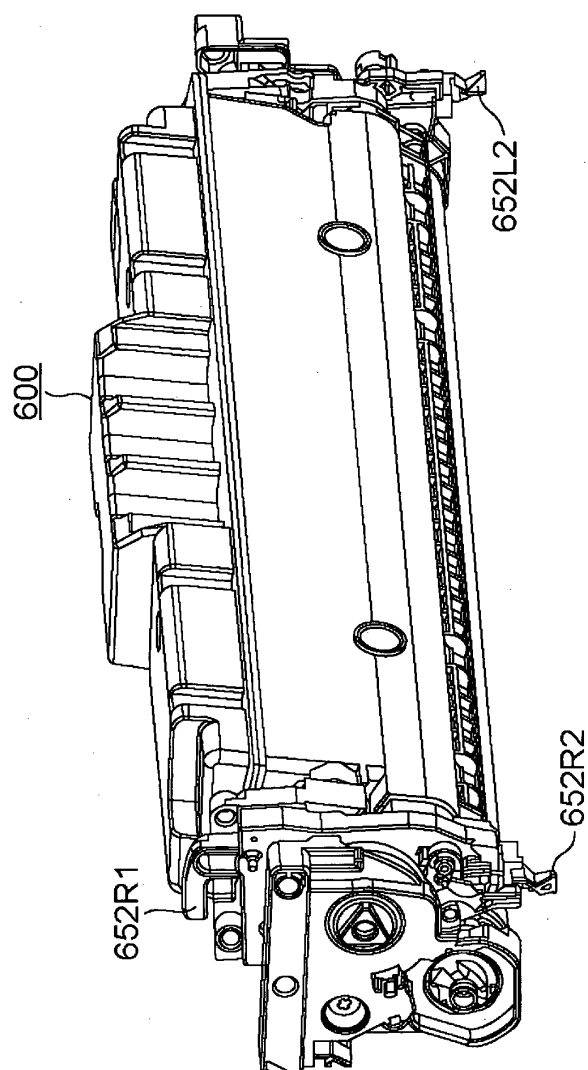


图67

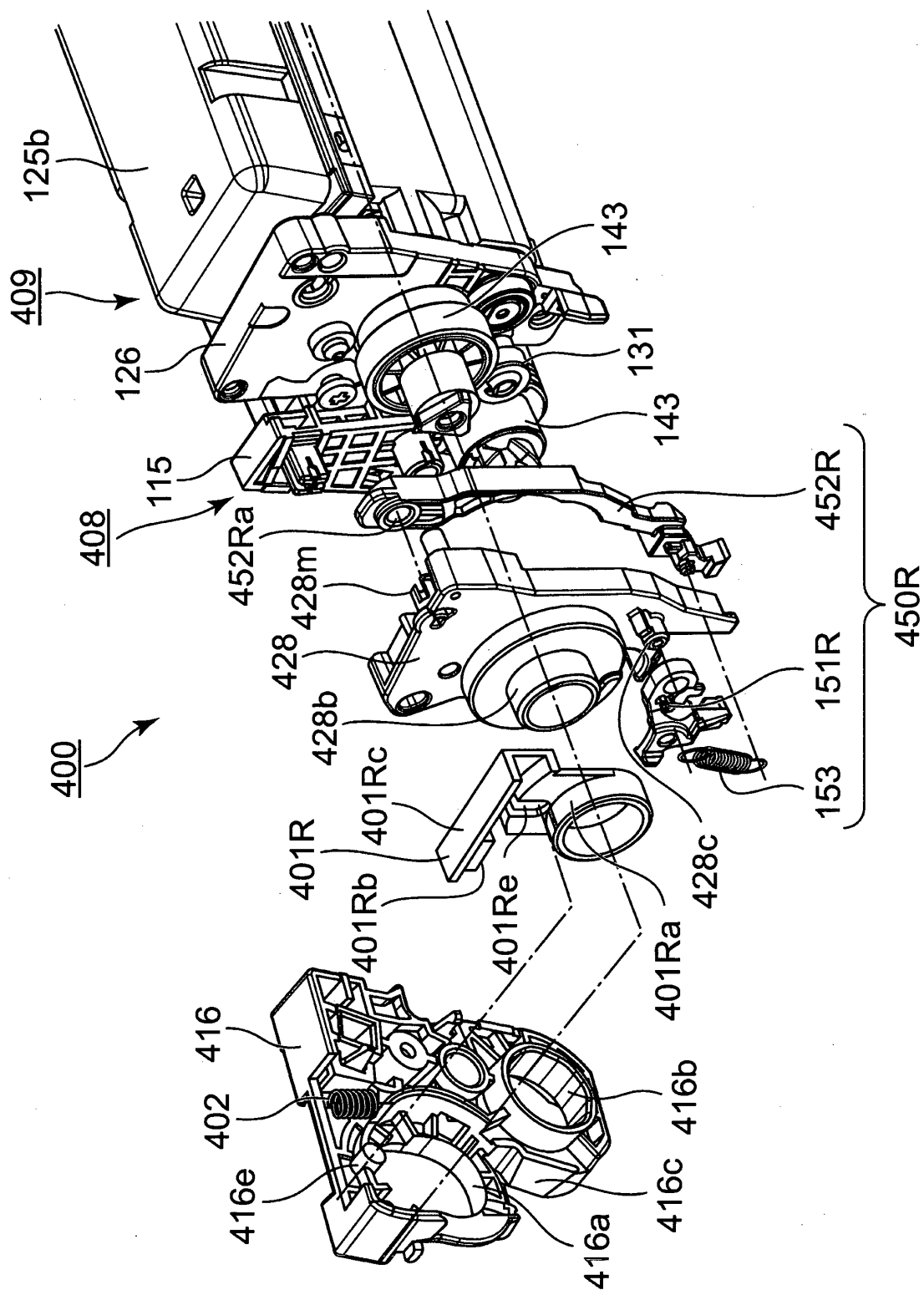


図70

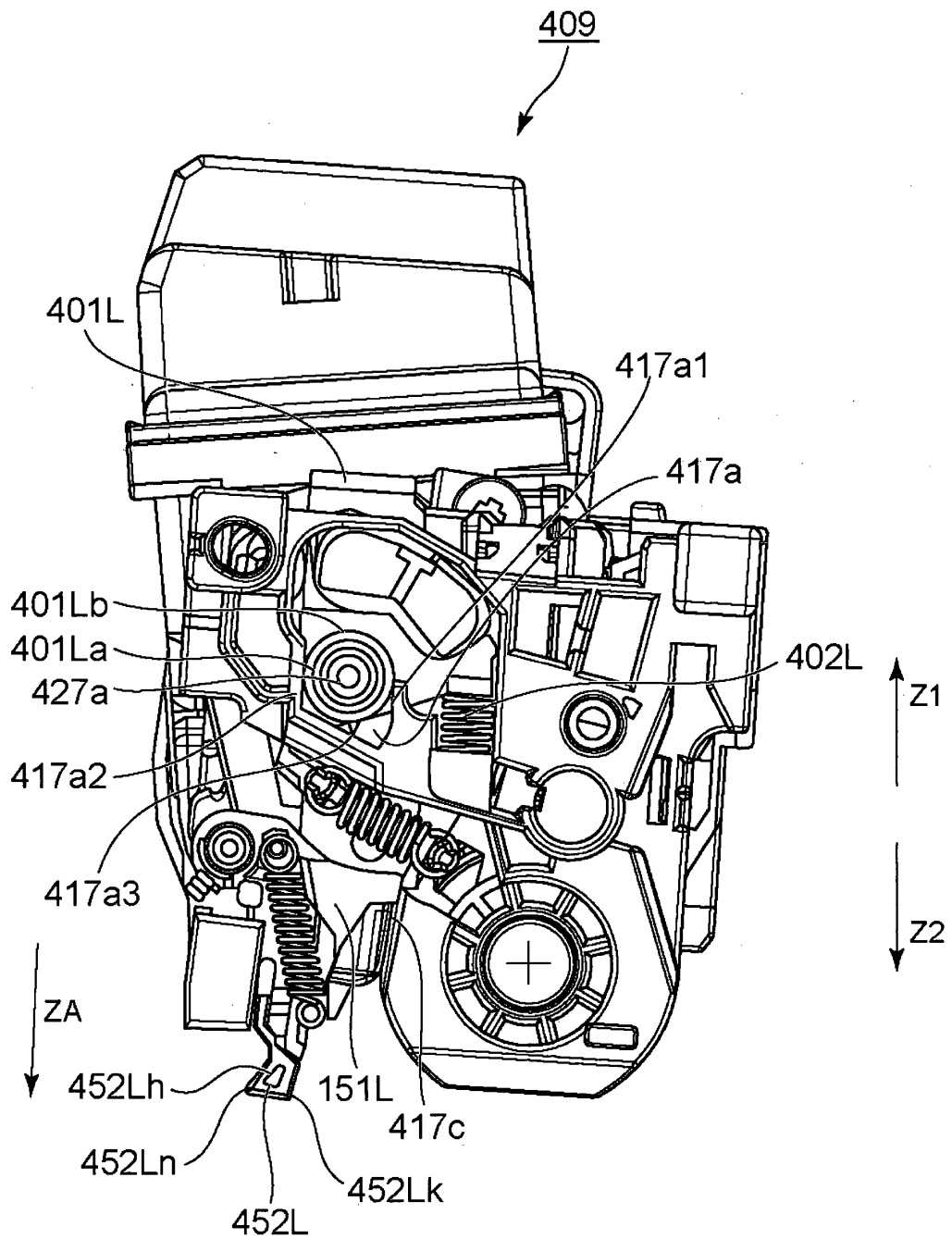


図71

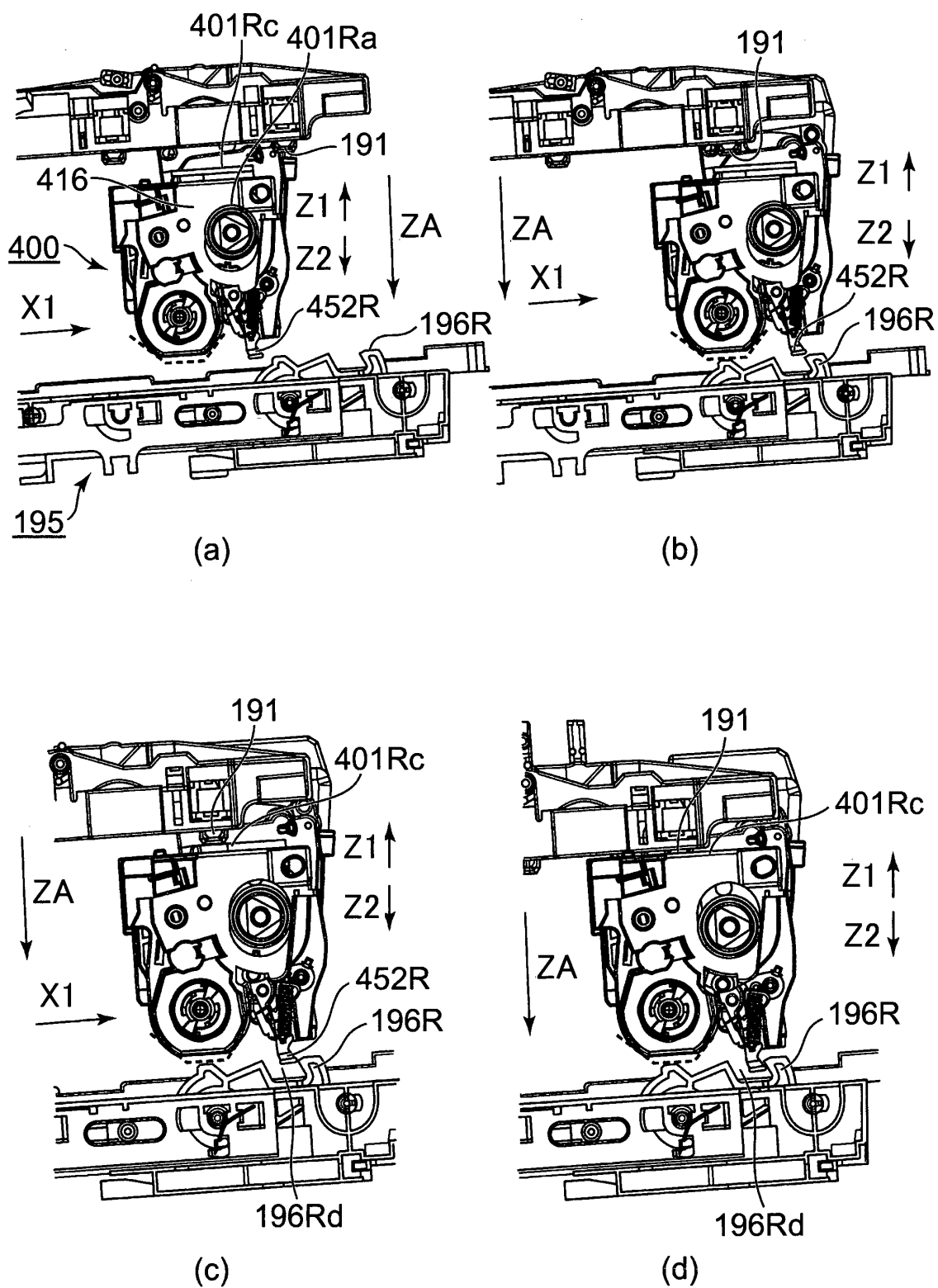


図72

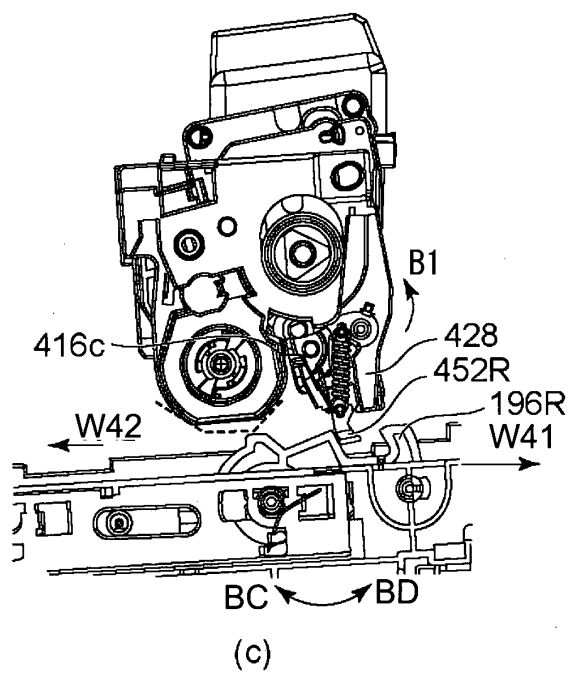
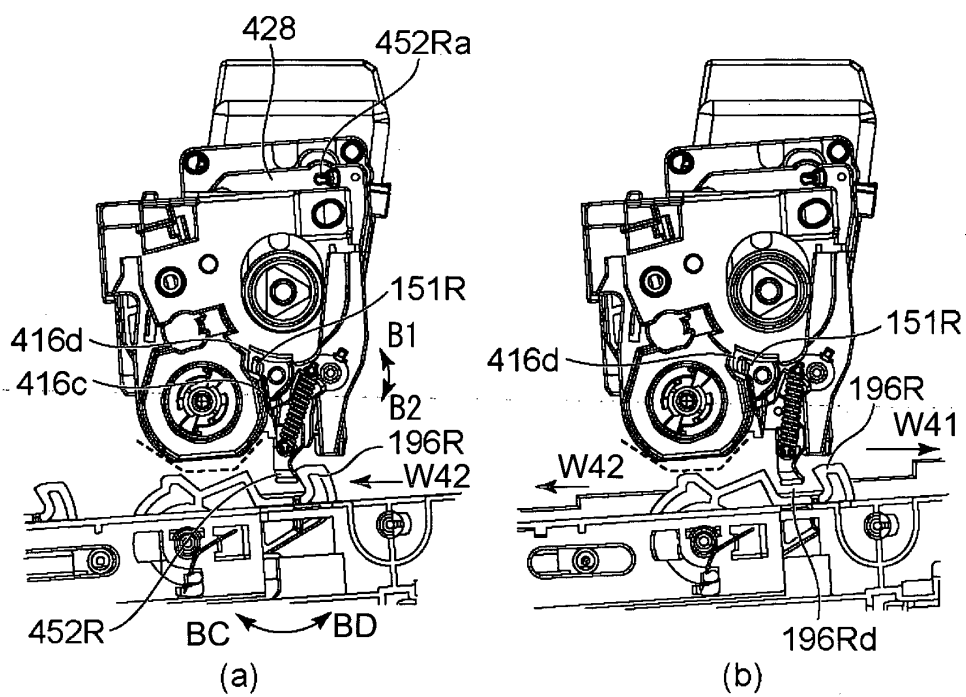


図73

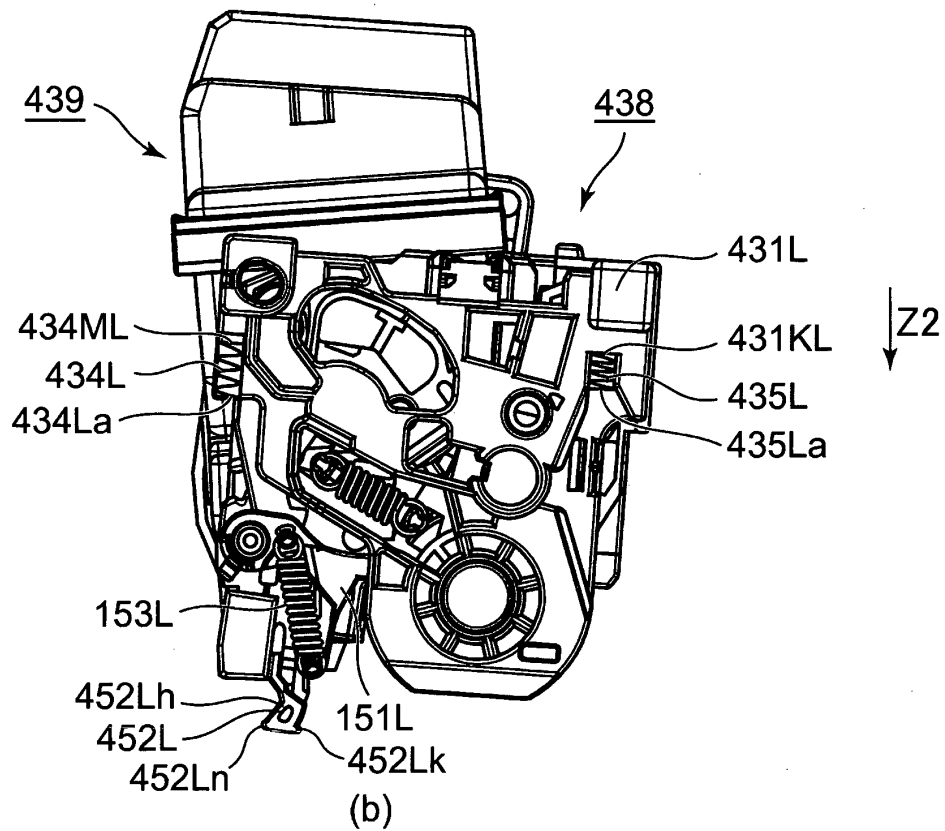
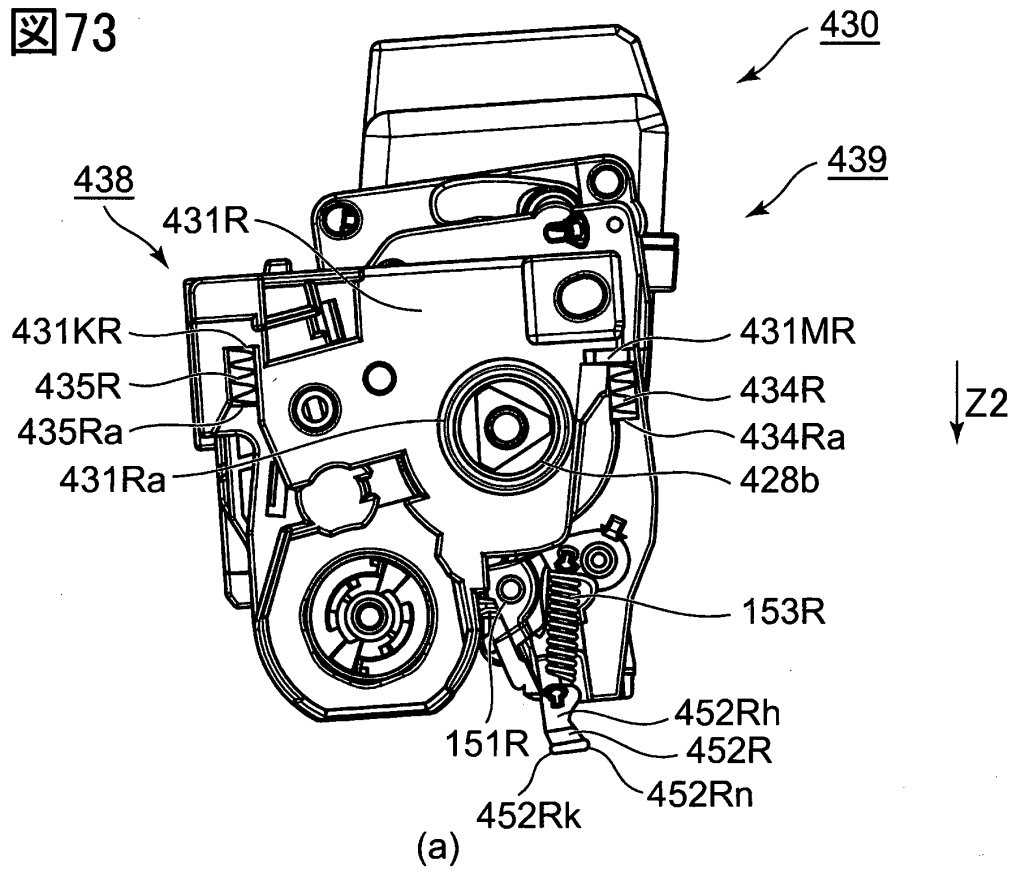


図74

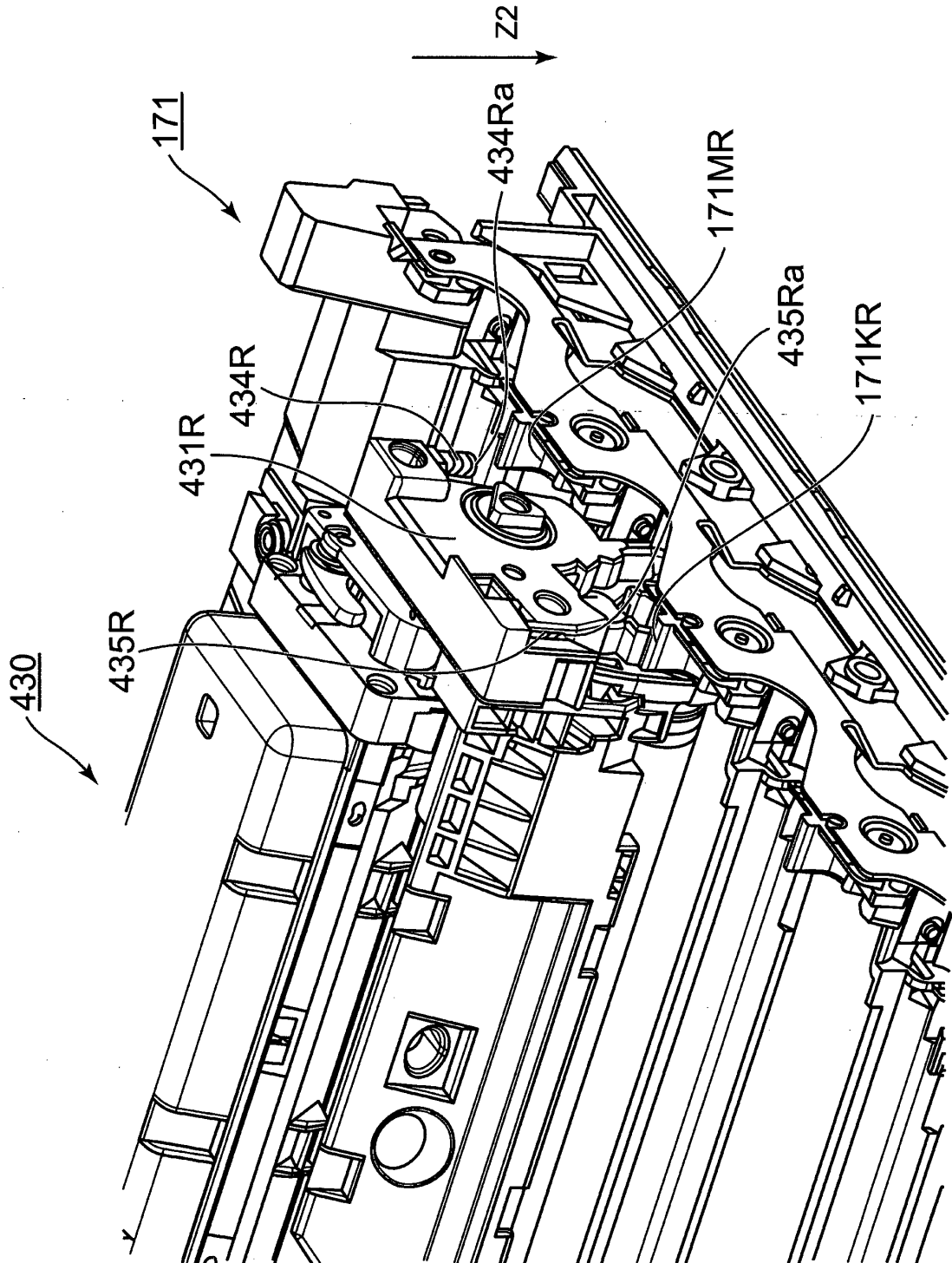


図75

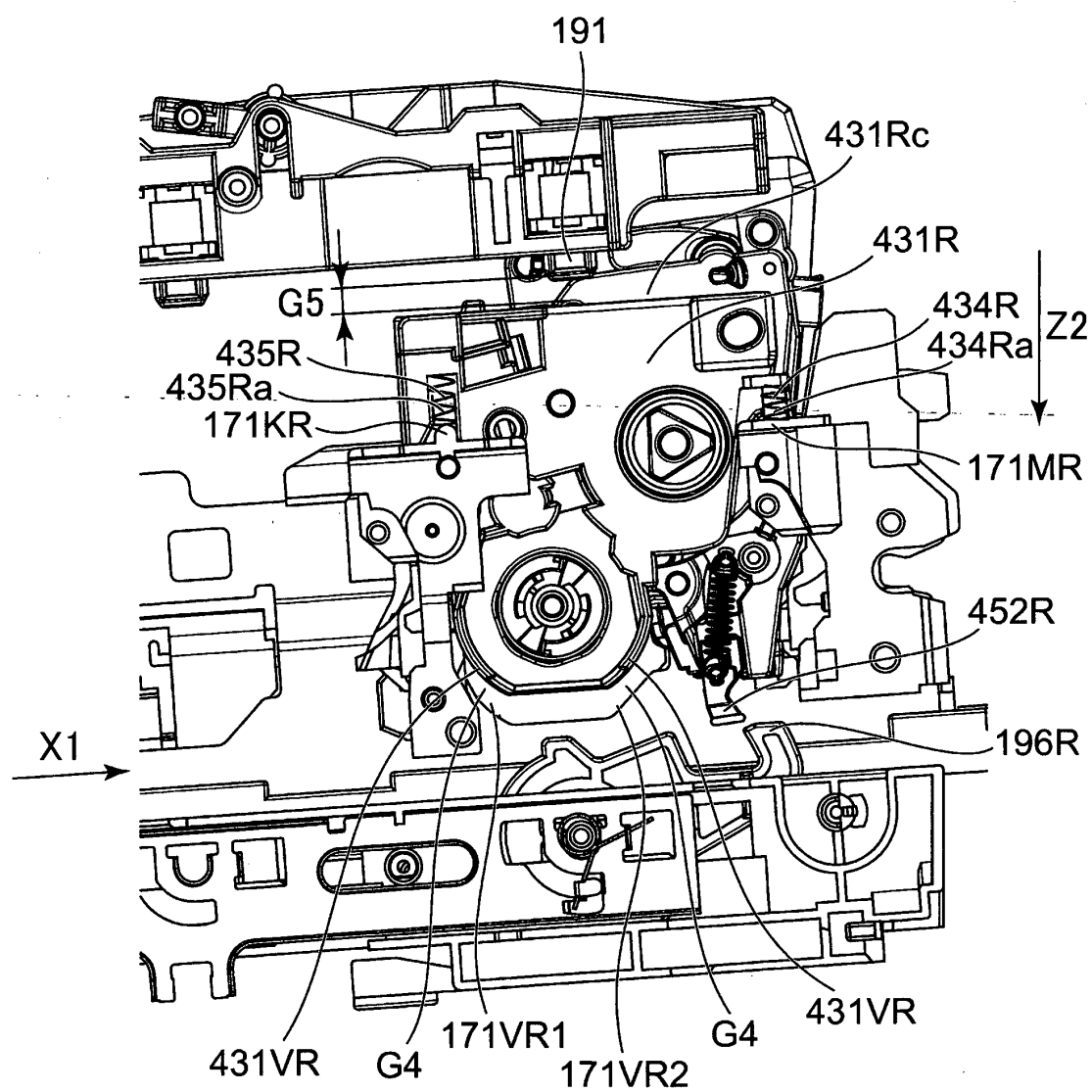


図76

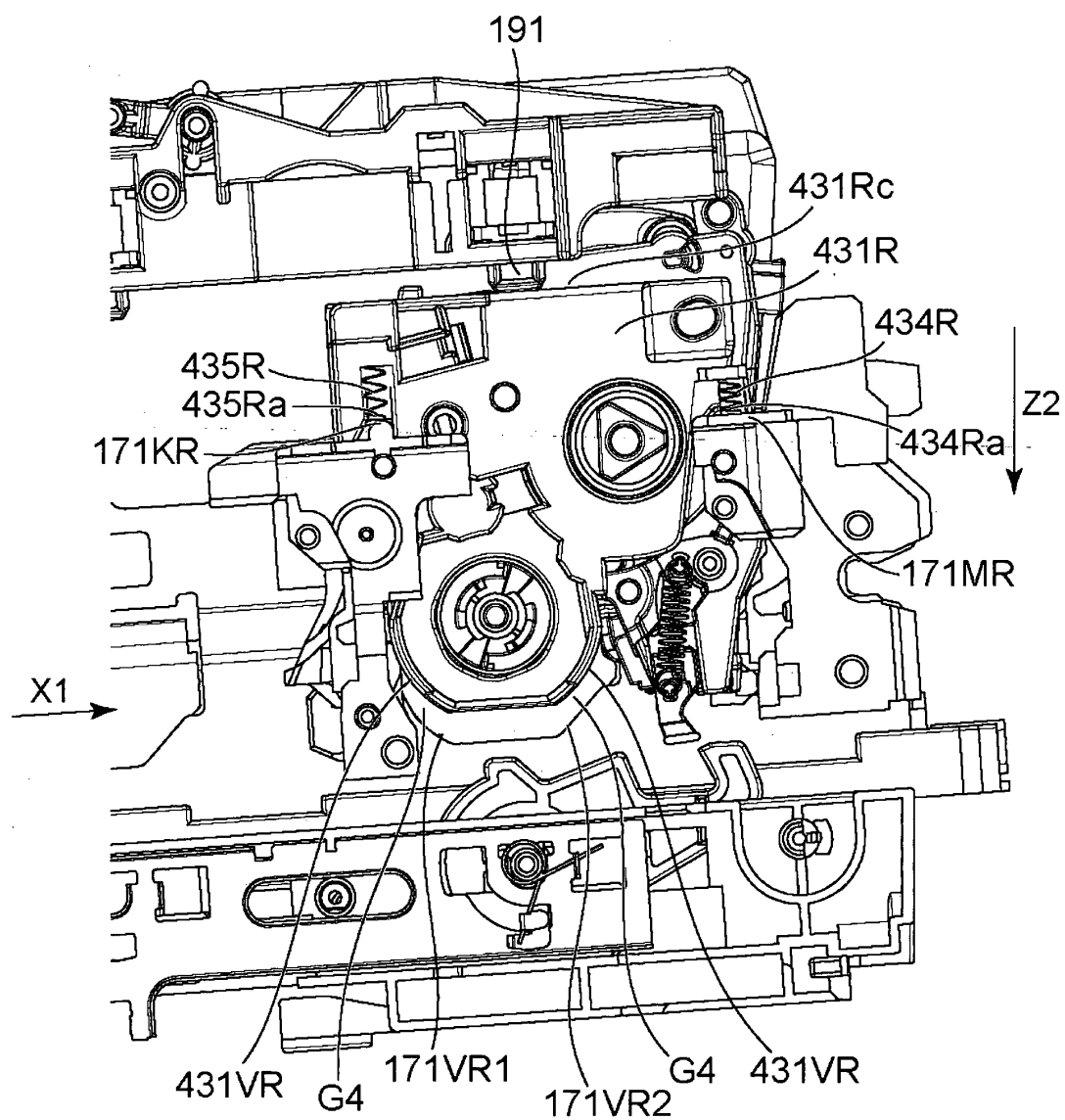


図77

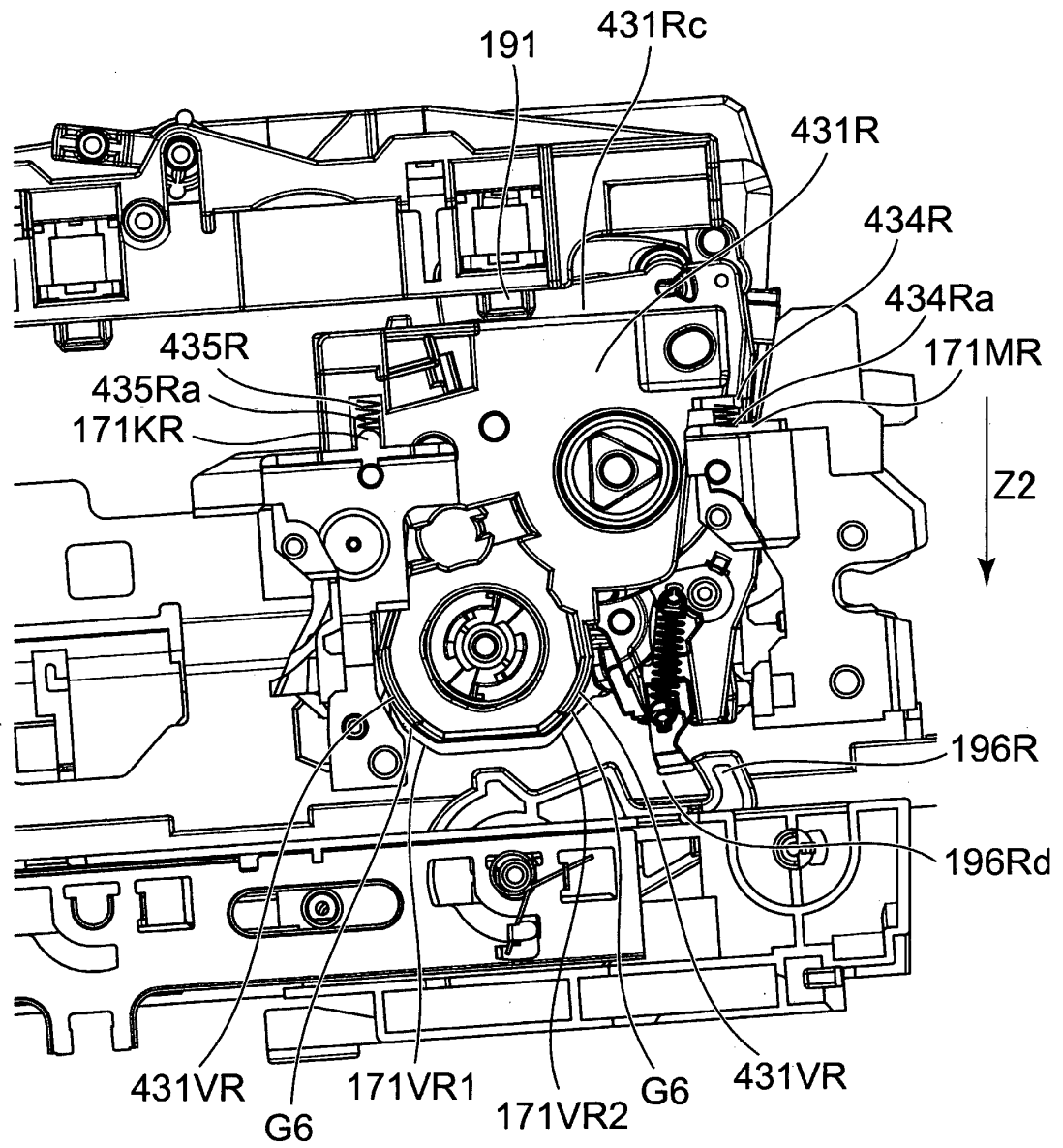


図78

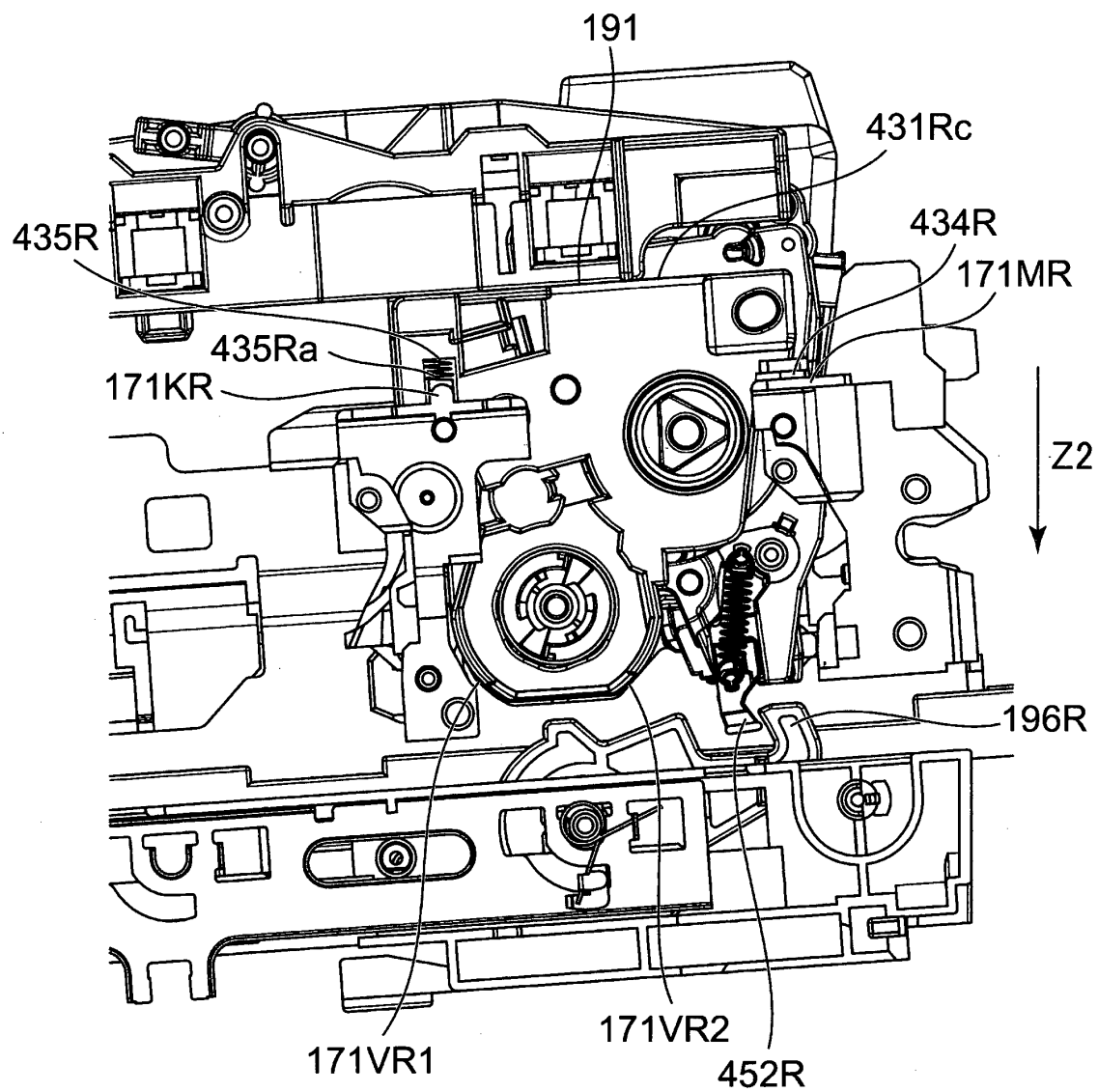


図 79

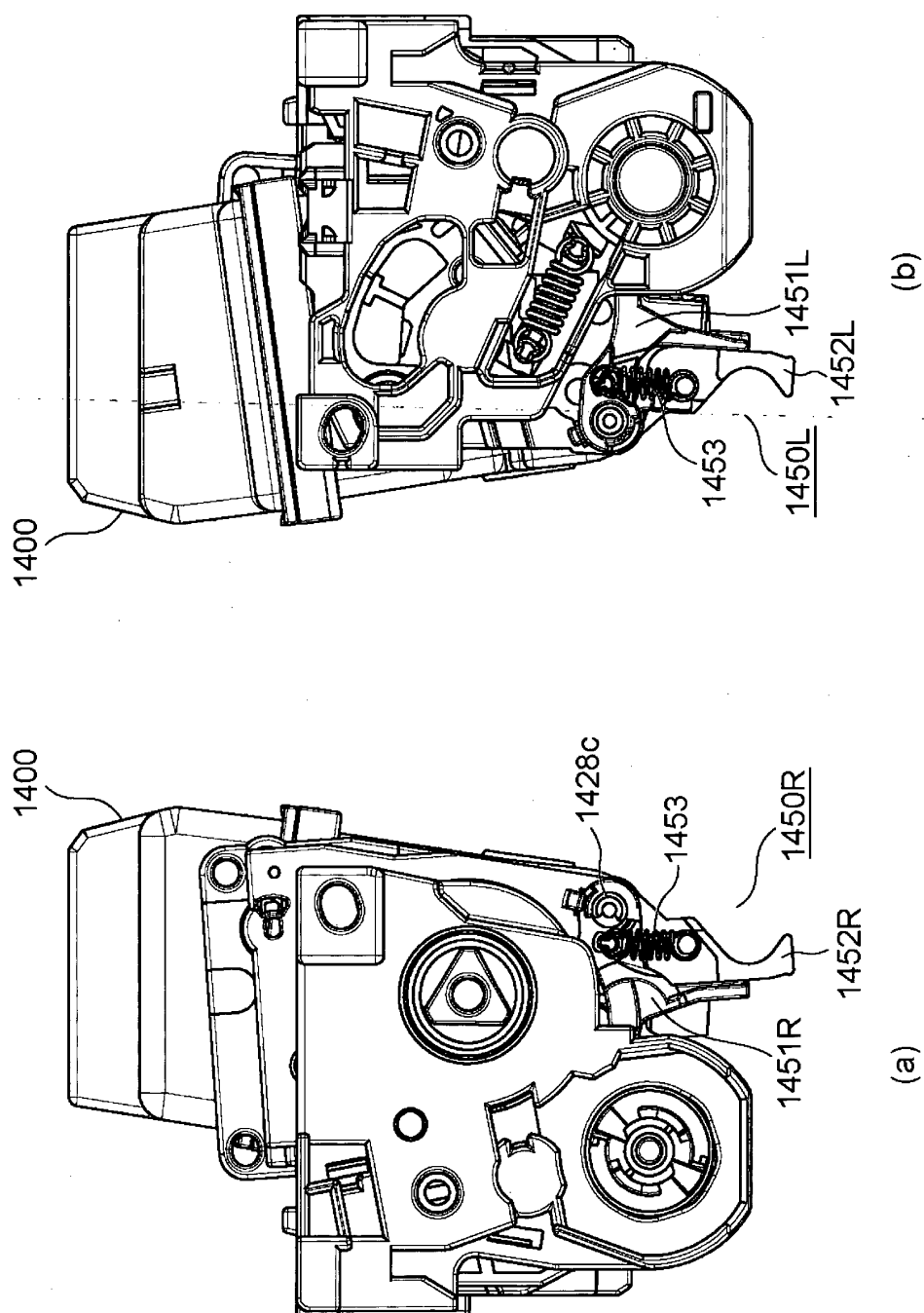


図80

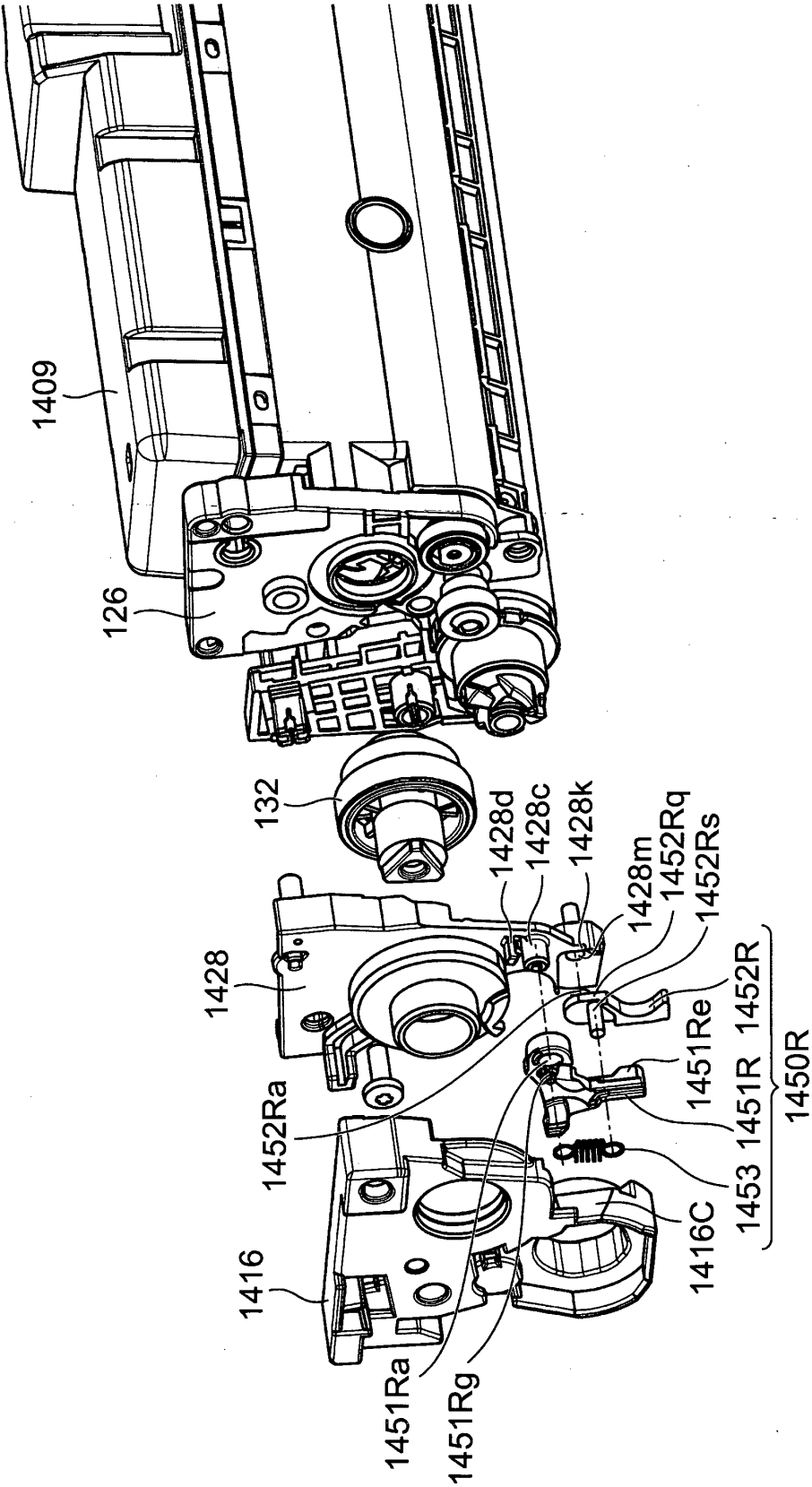


図81

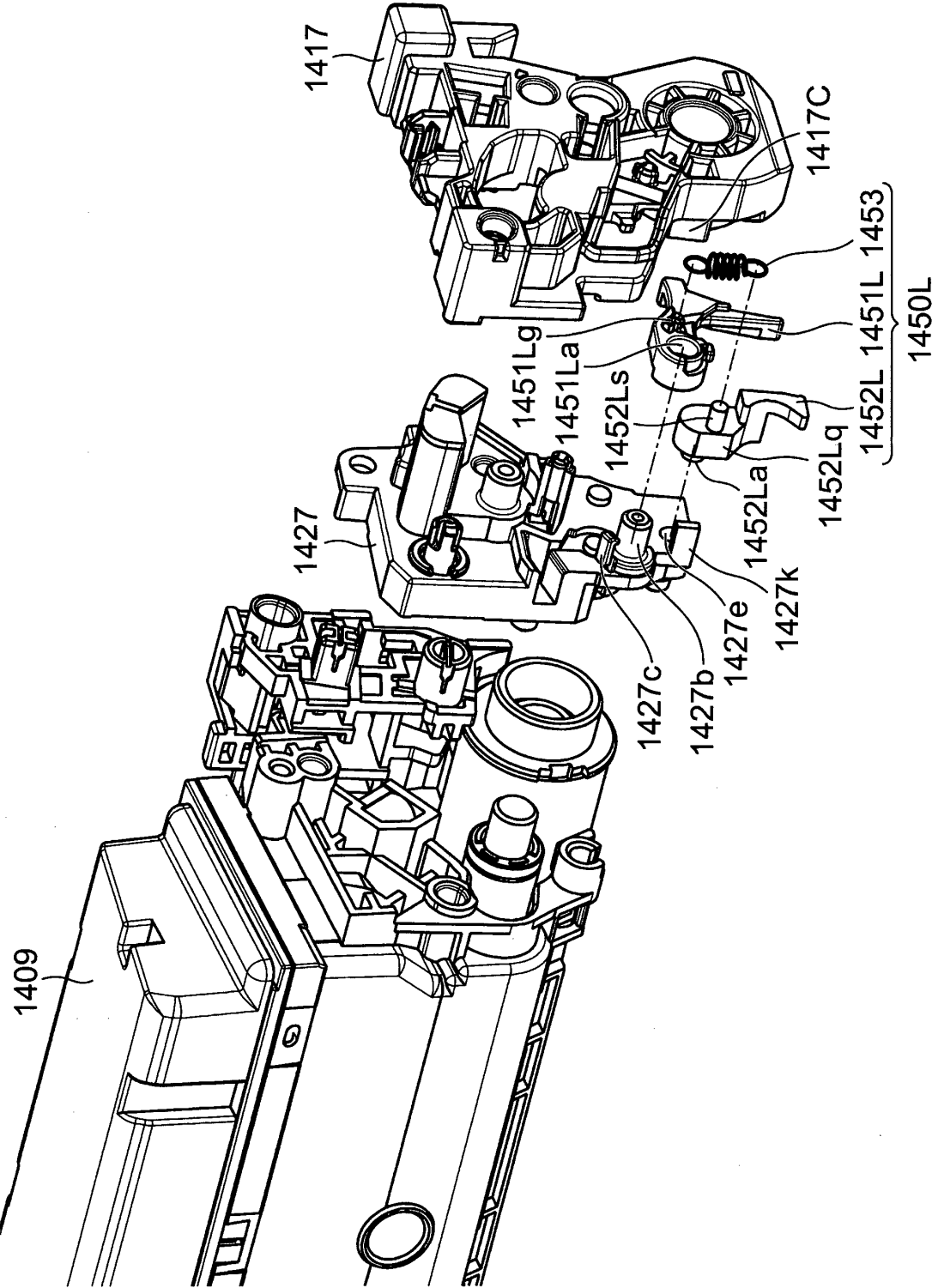


図82

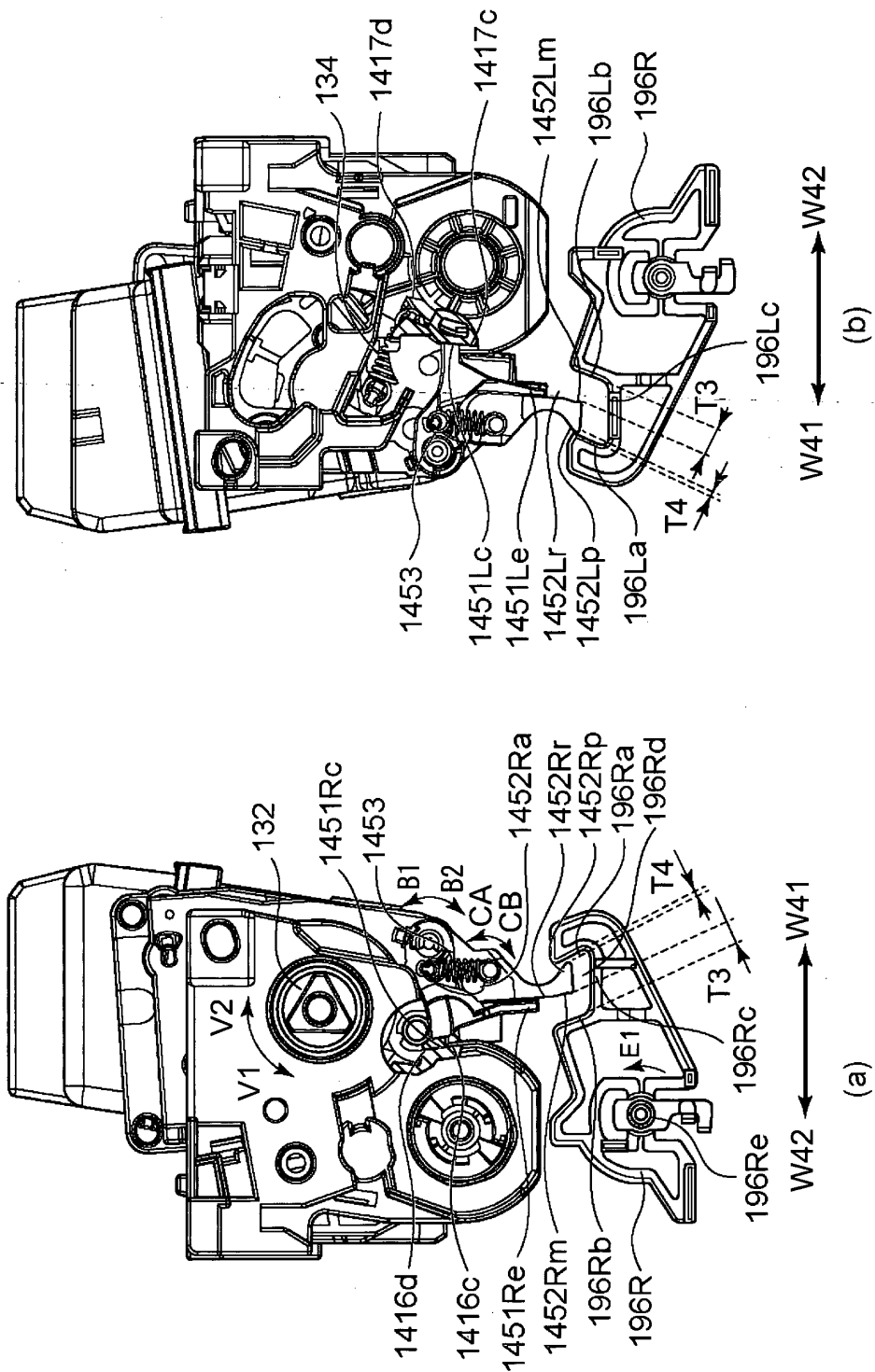


图83

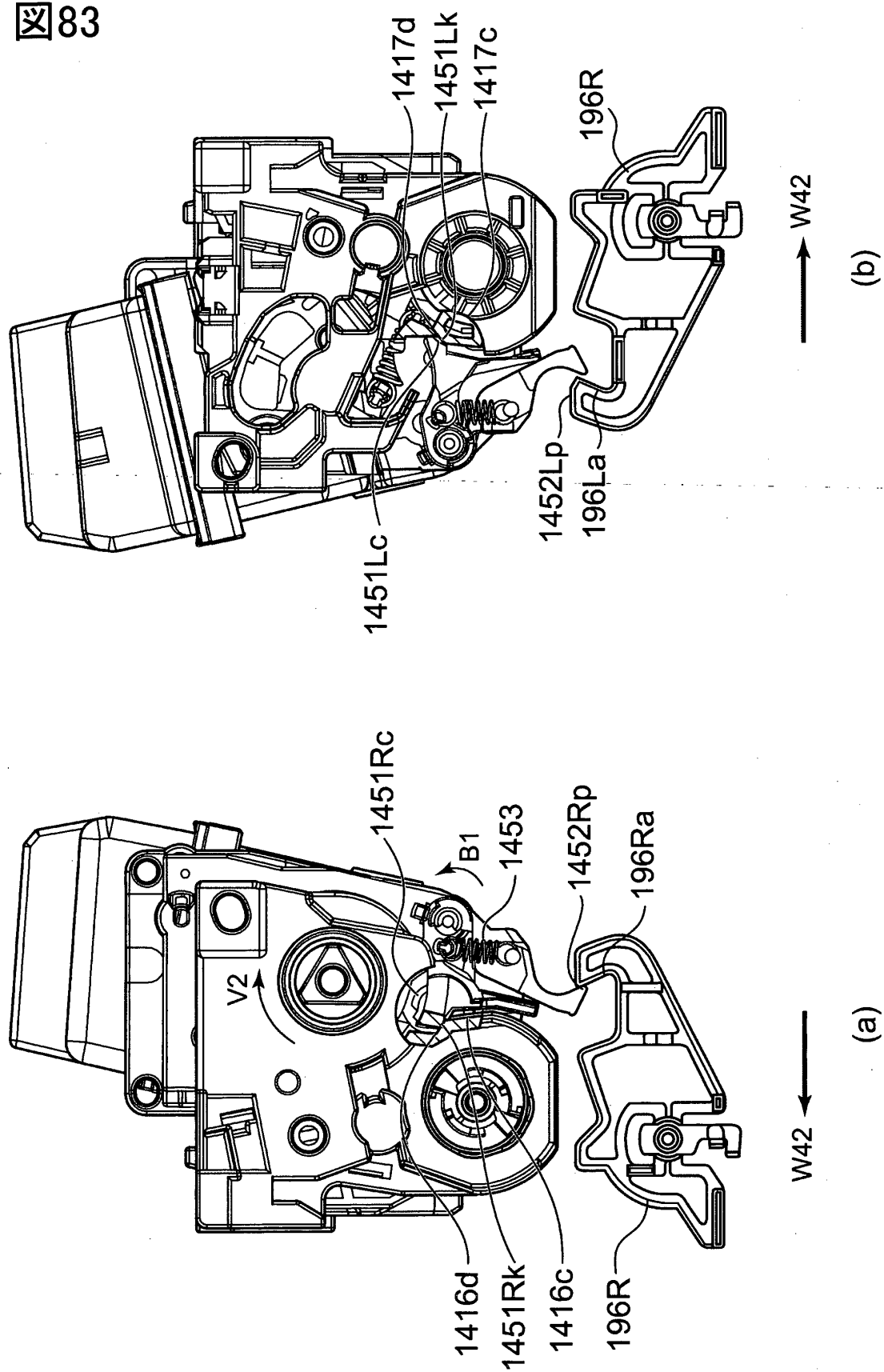


図84

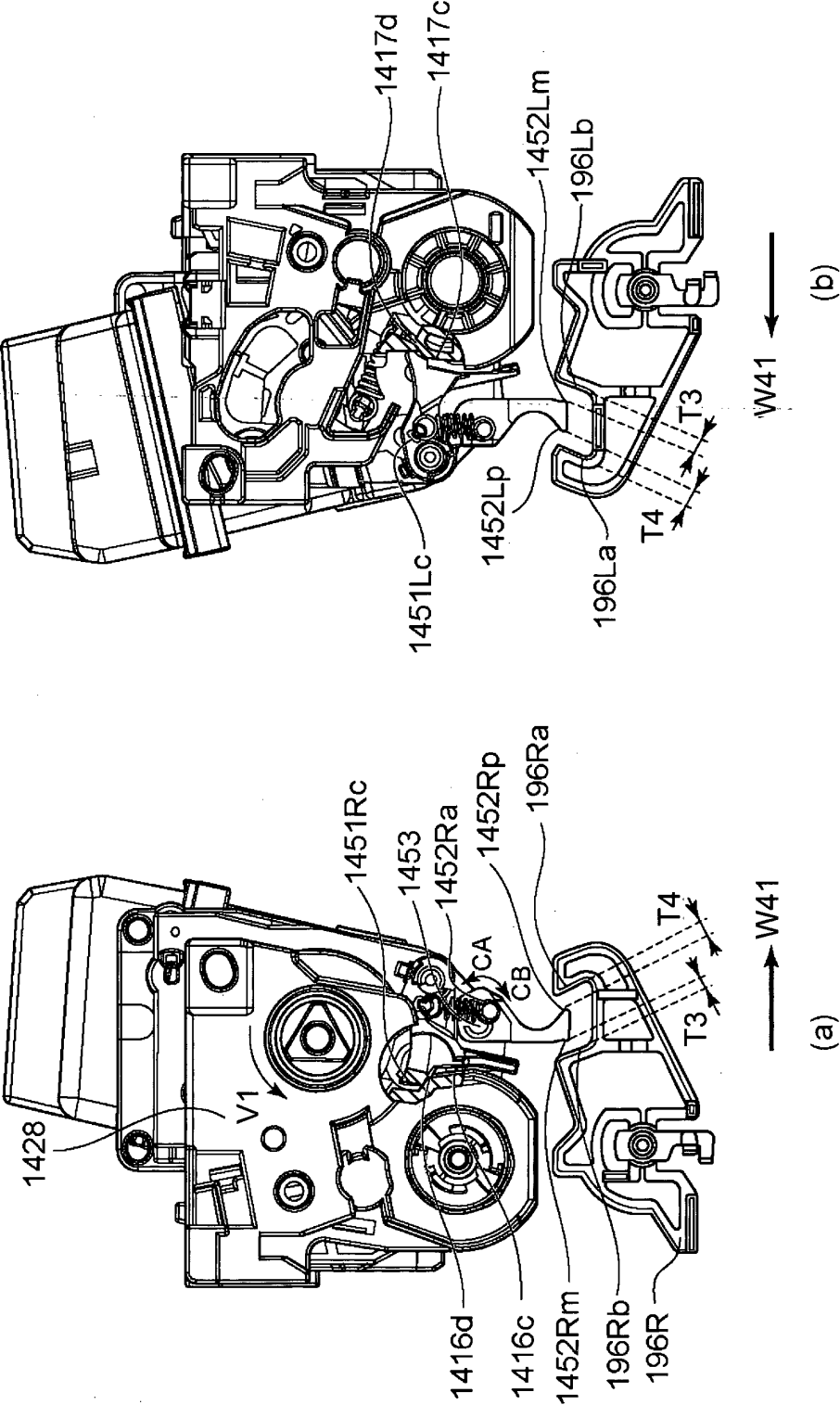


図85

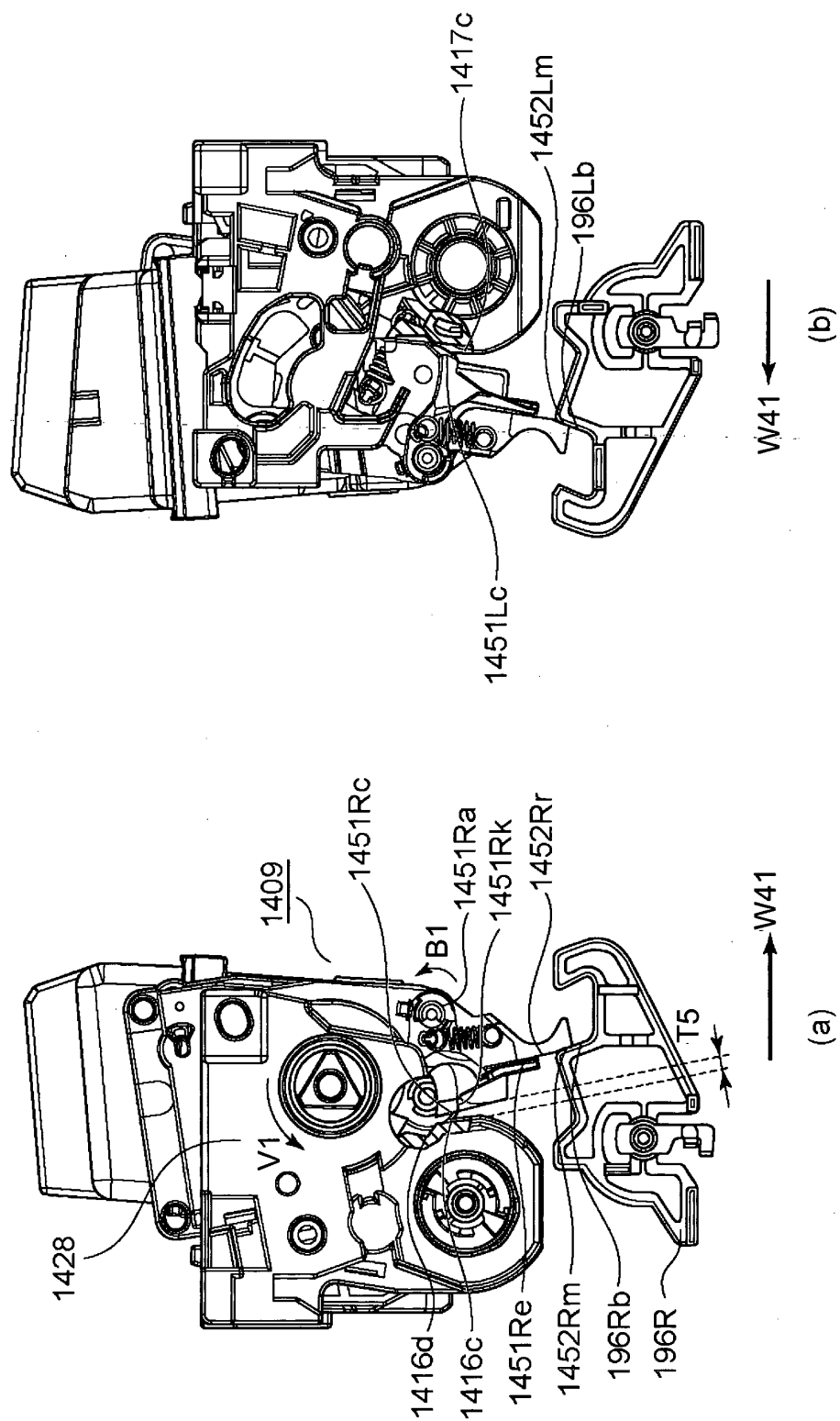


図86

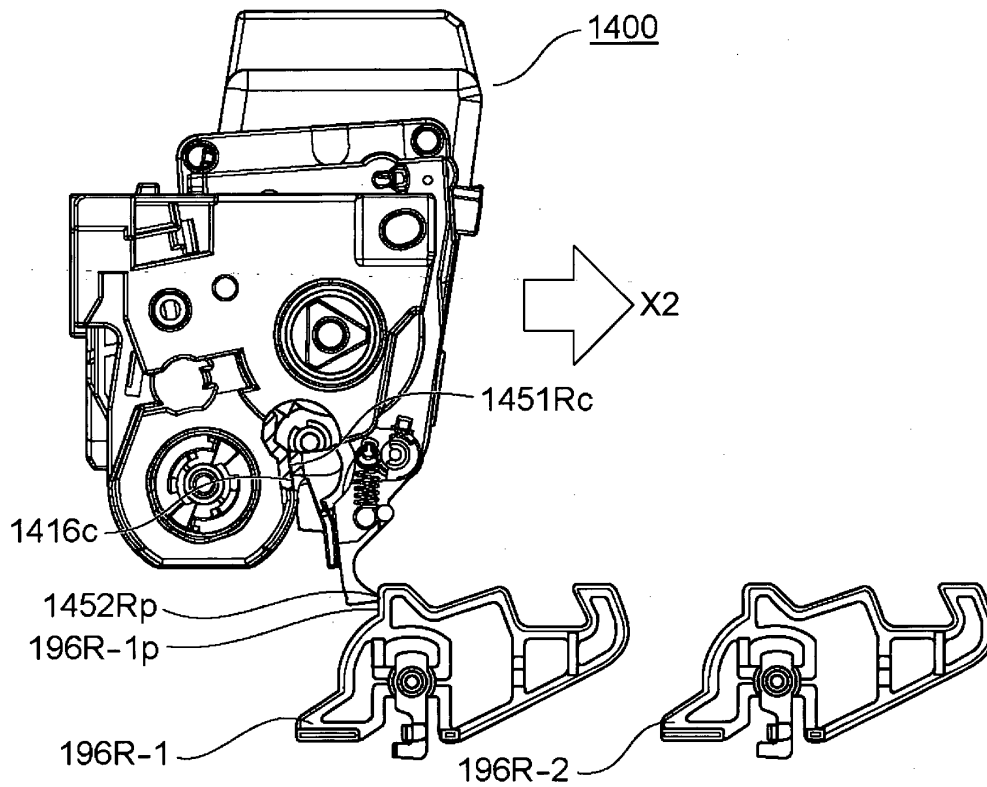


図87

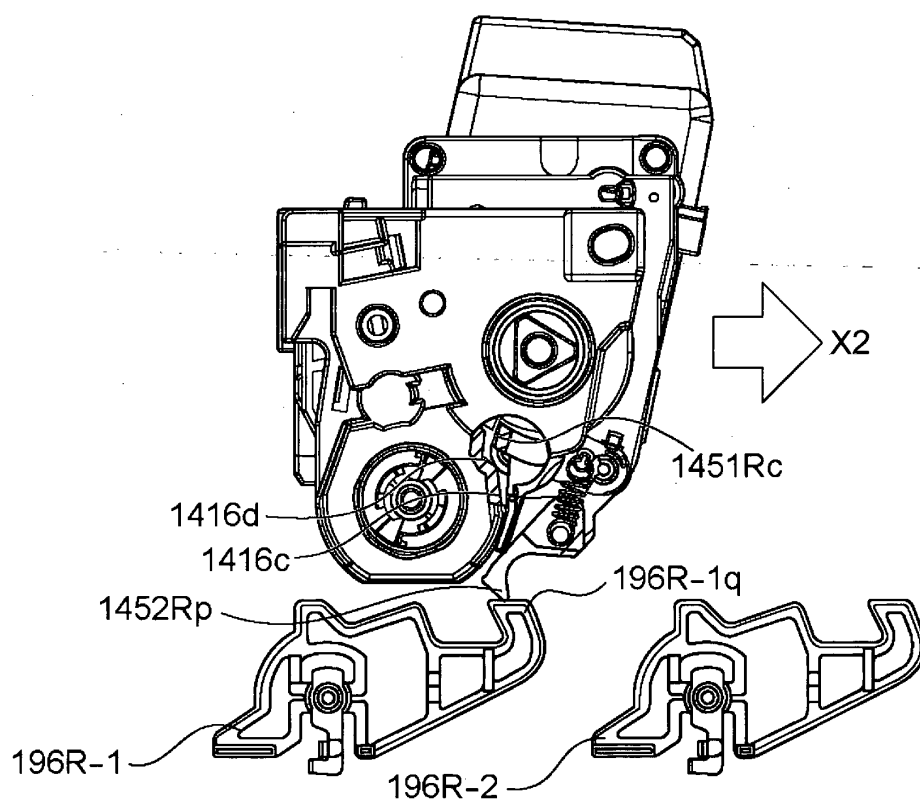


図88

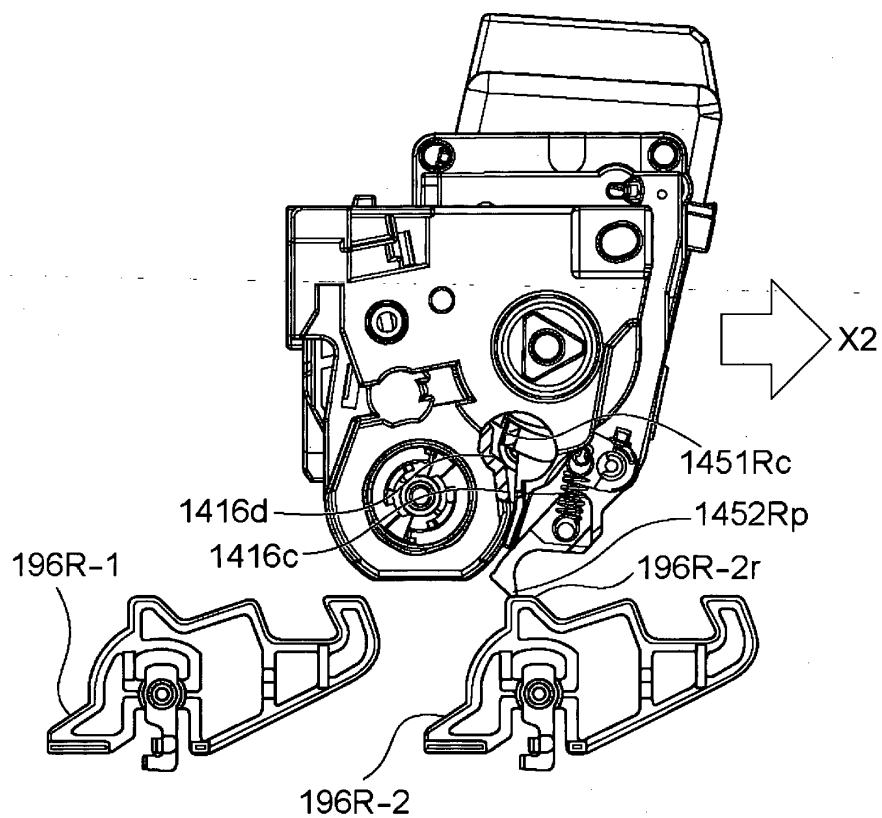


図89

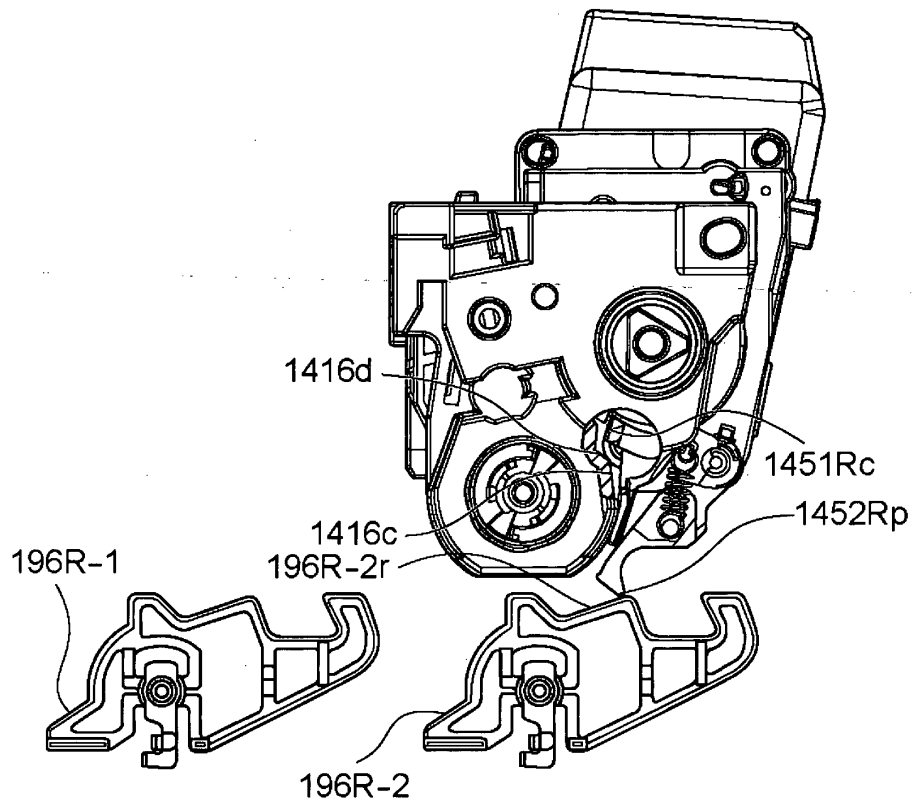


図90

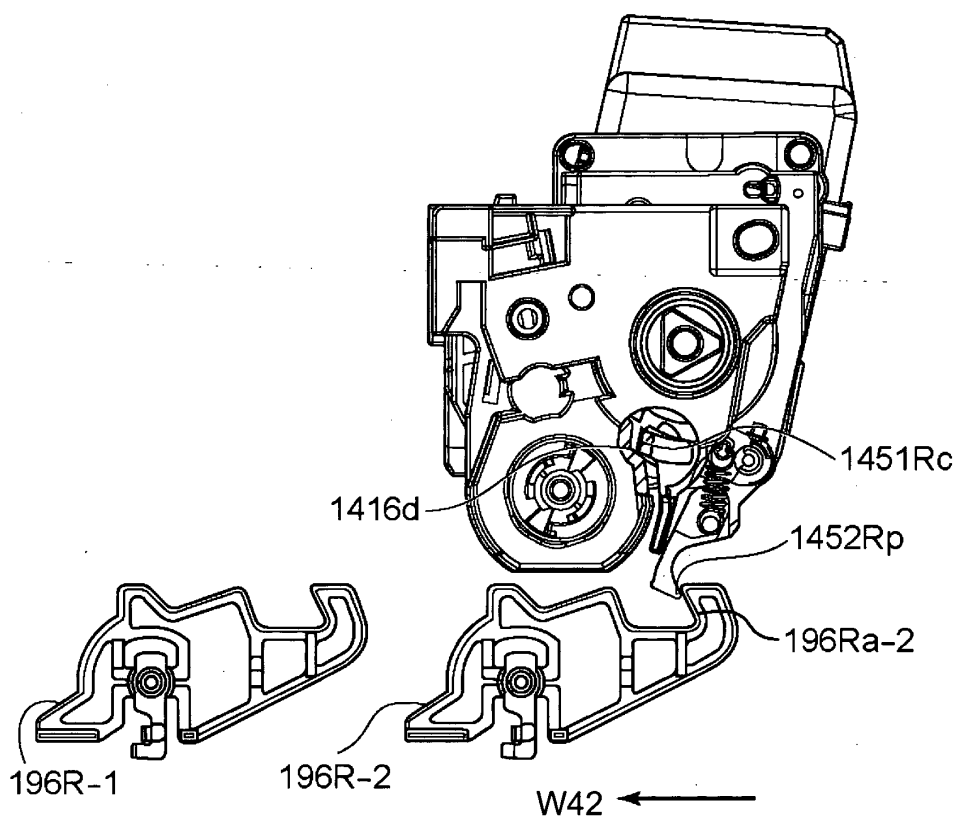


図91

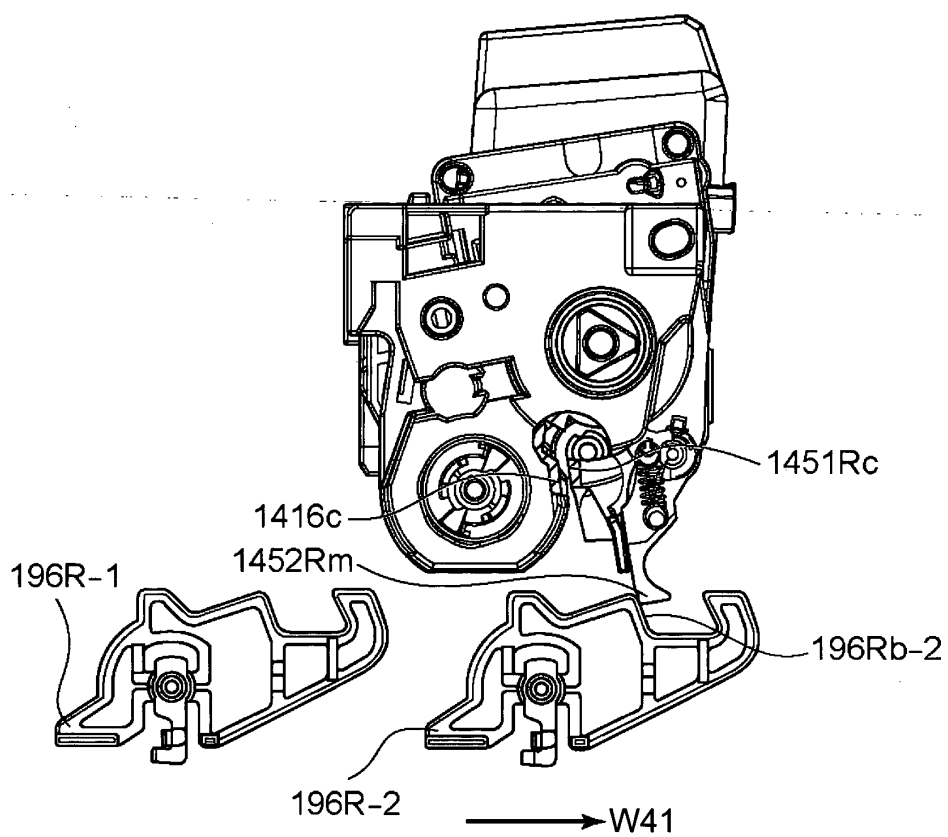


図92

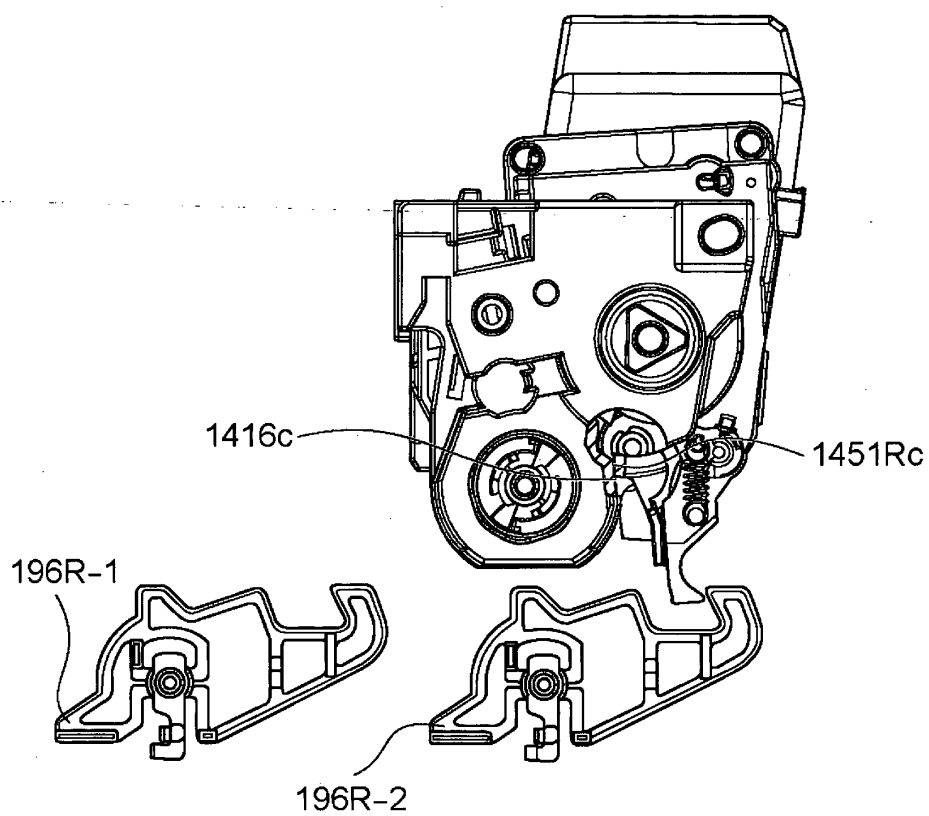


図93

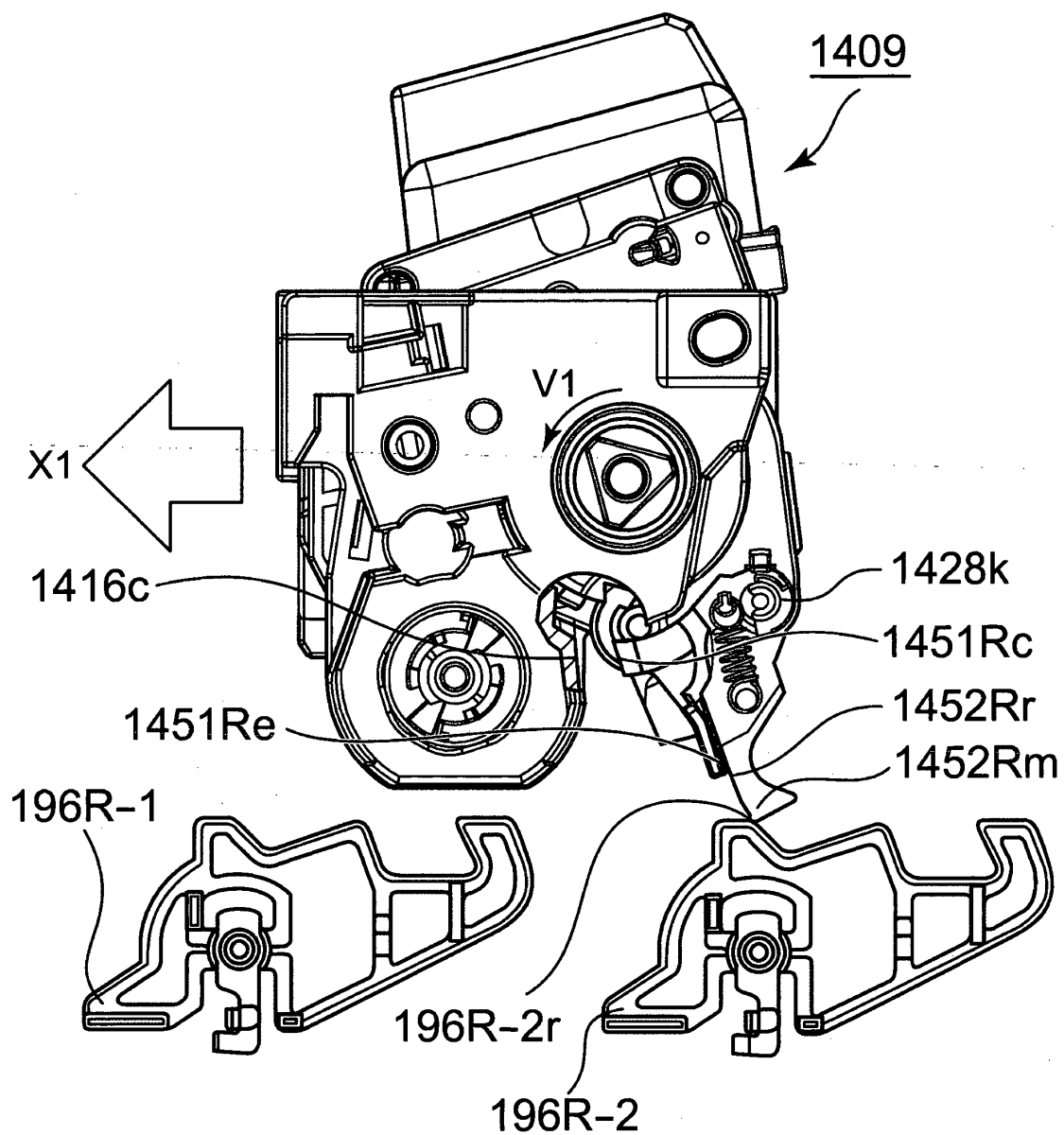


図94

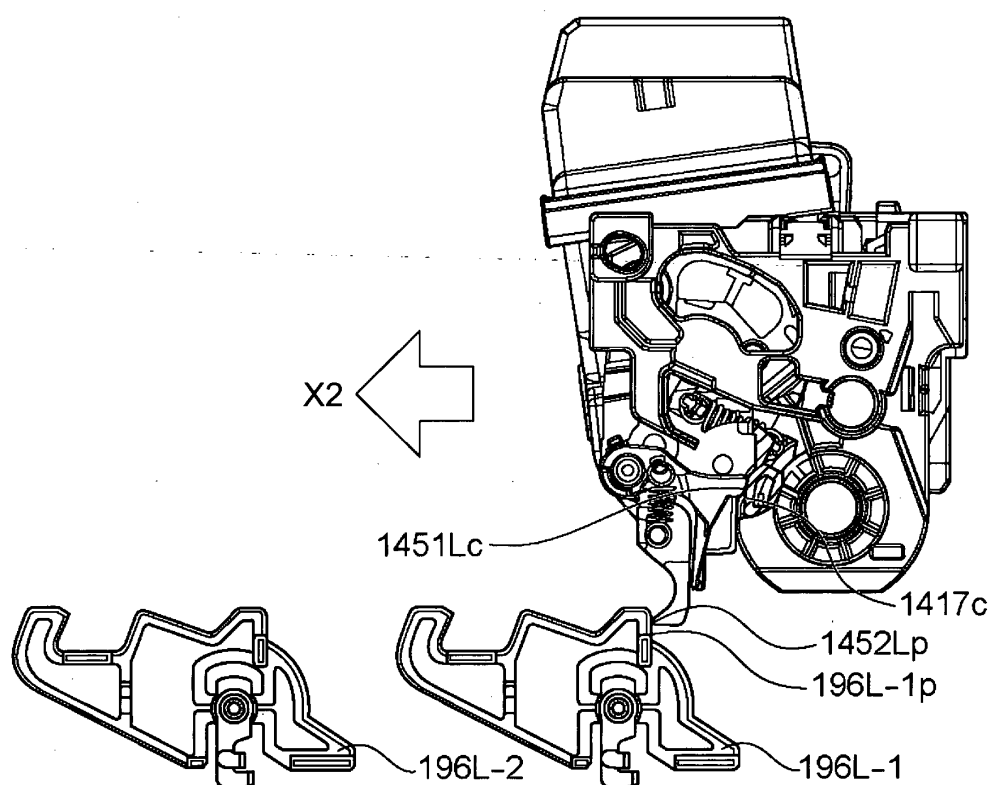


図95

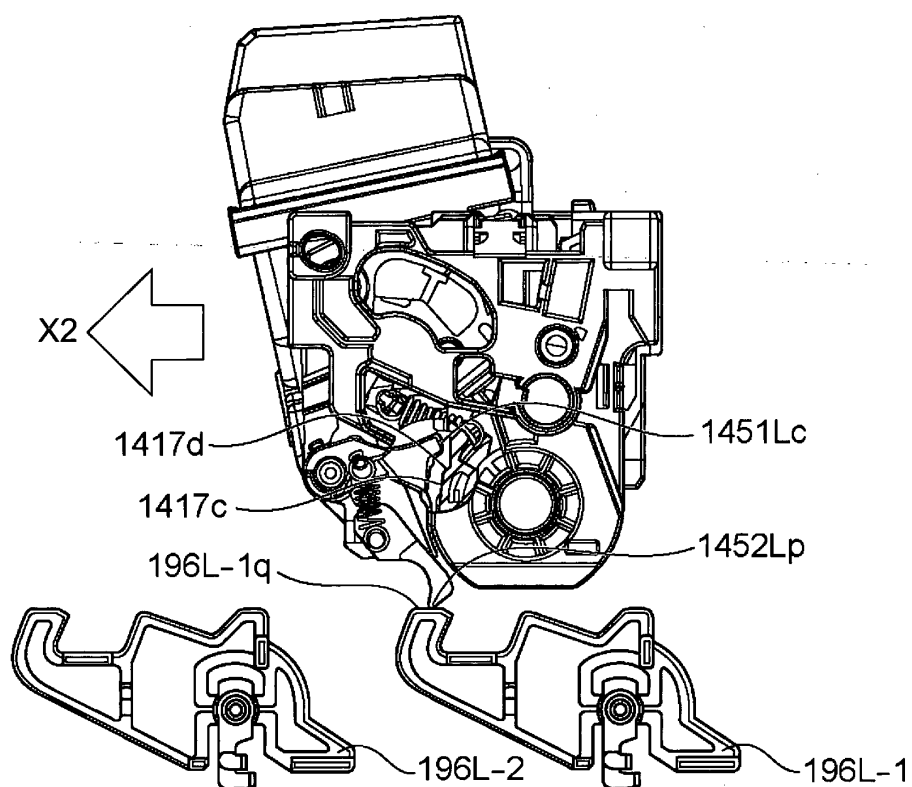


図96

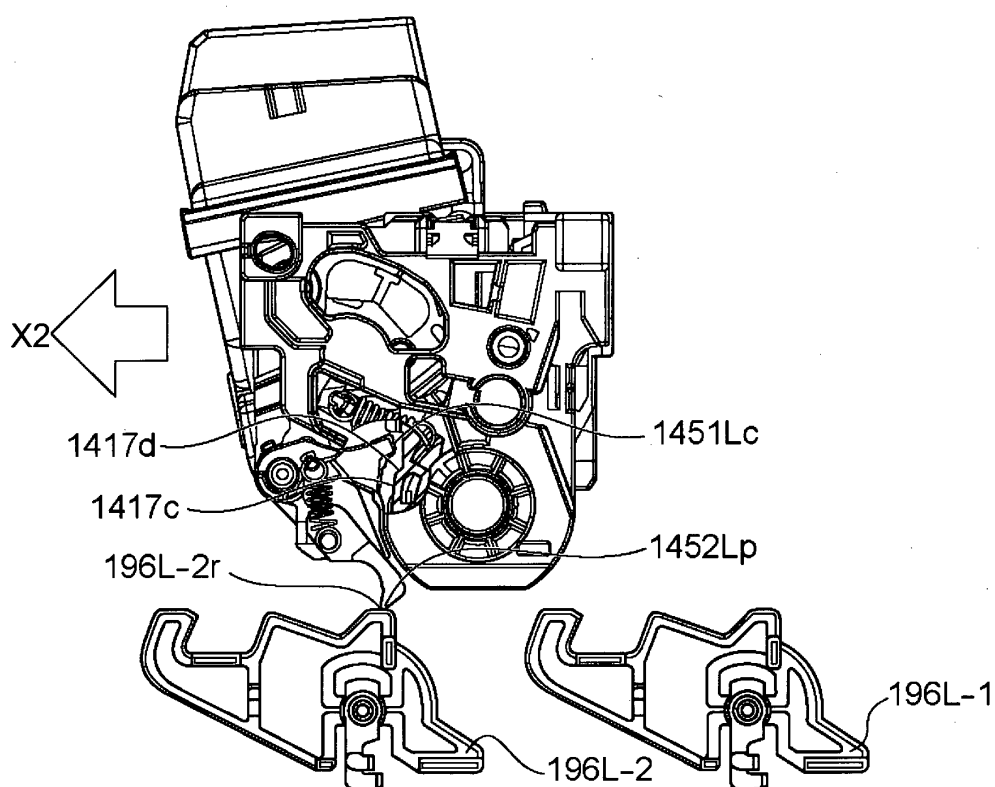


図97

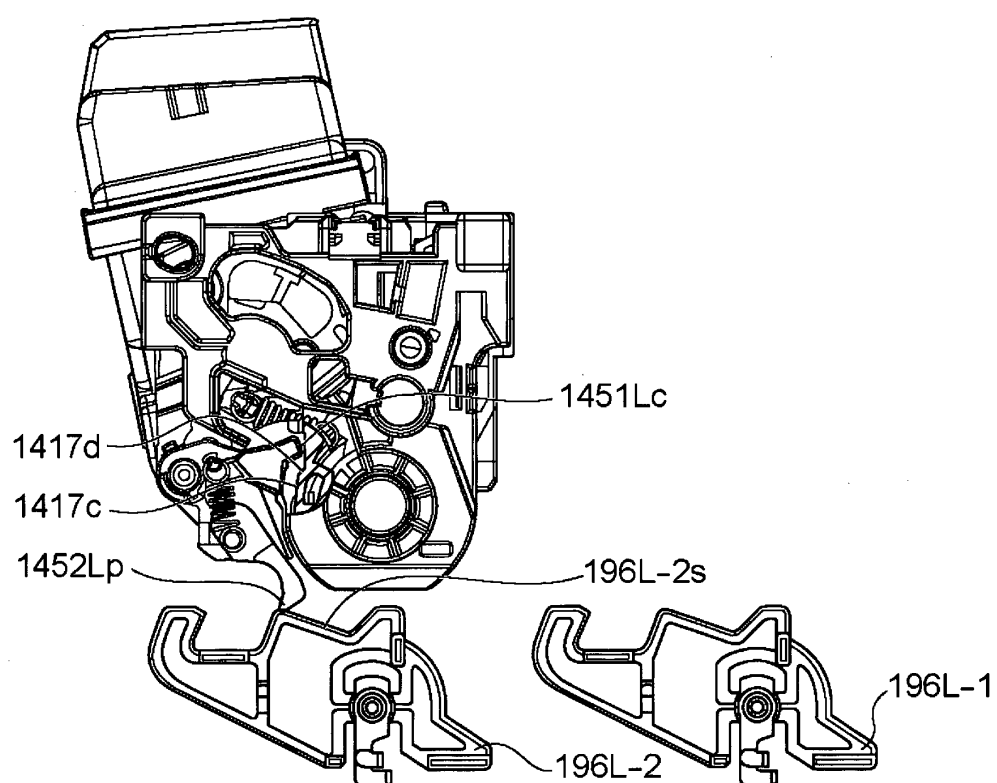


図99

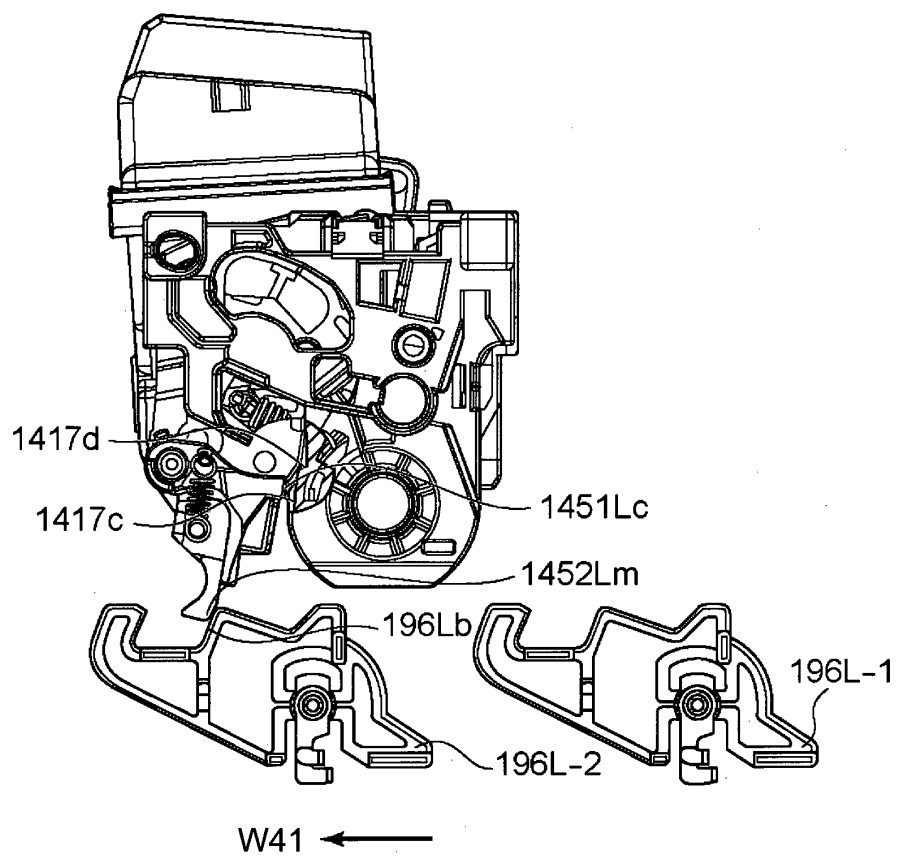


図100

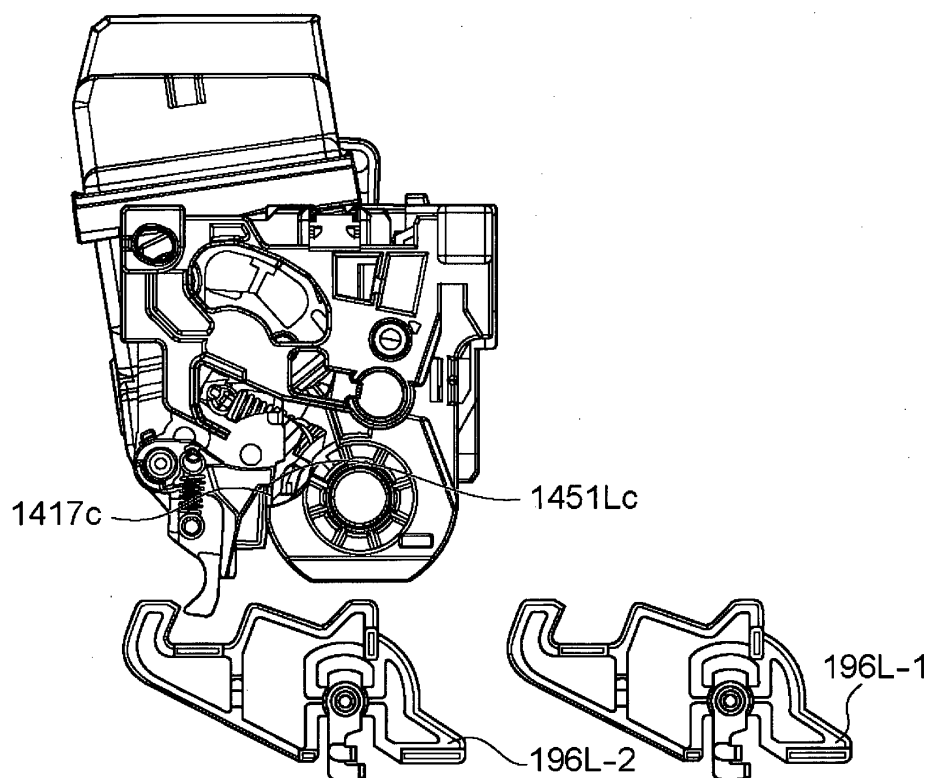


図101

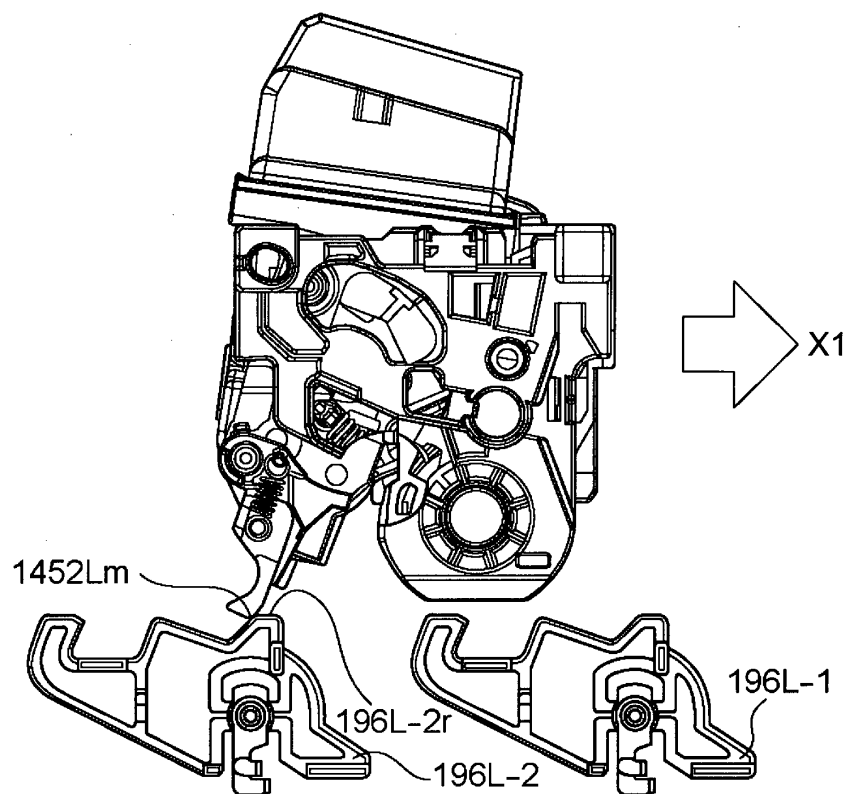


図102

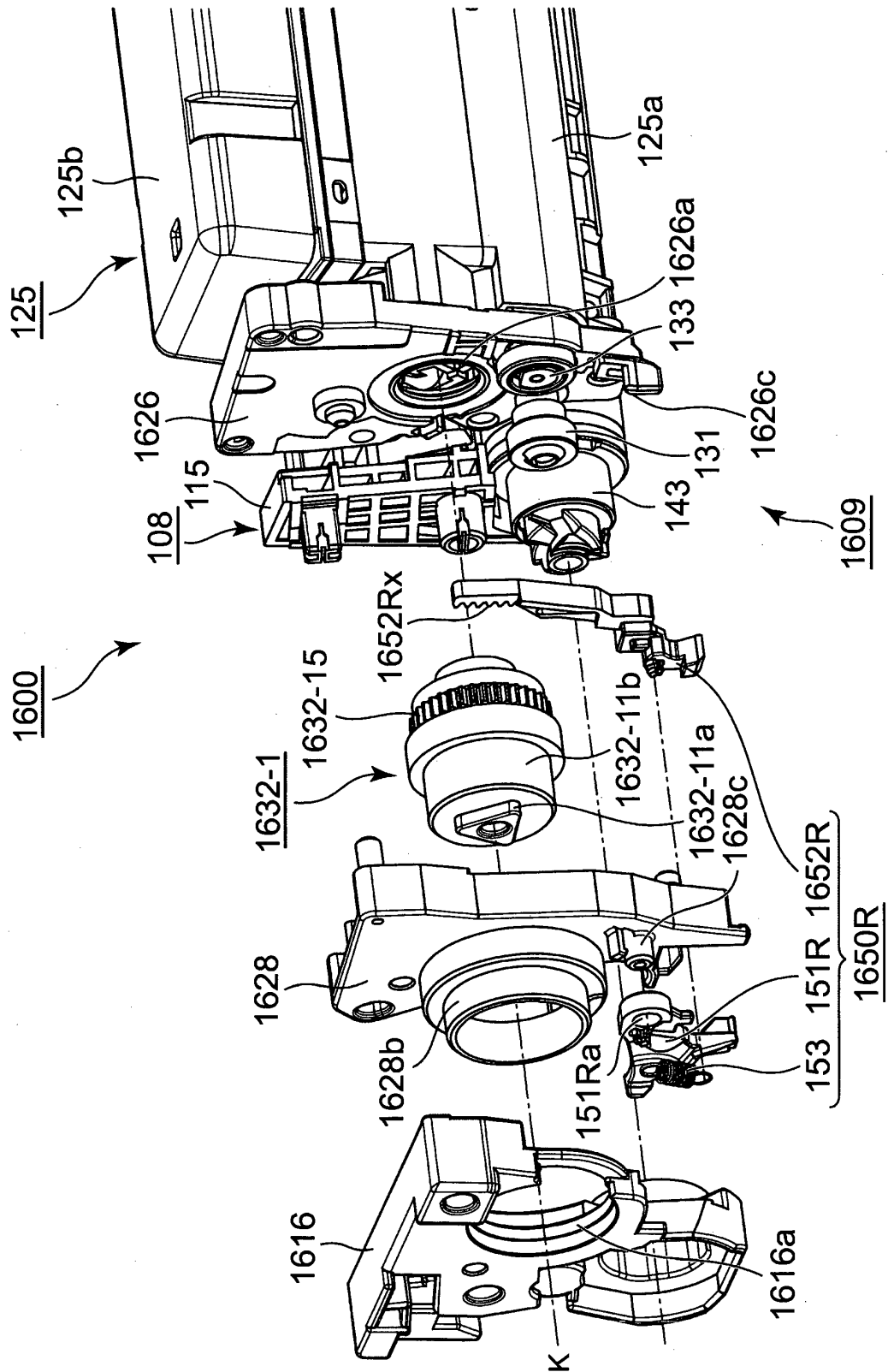


図103

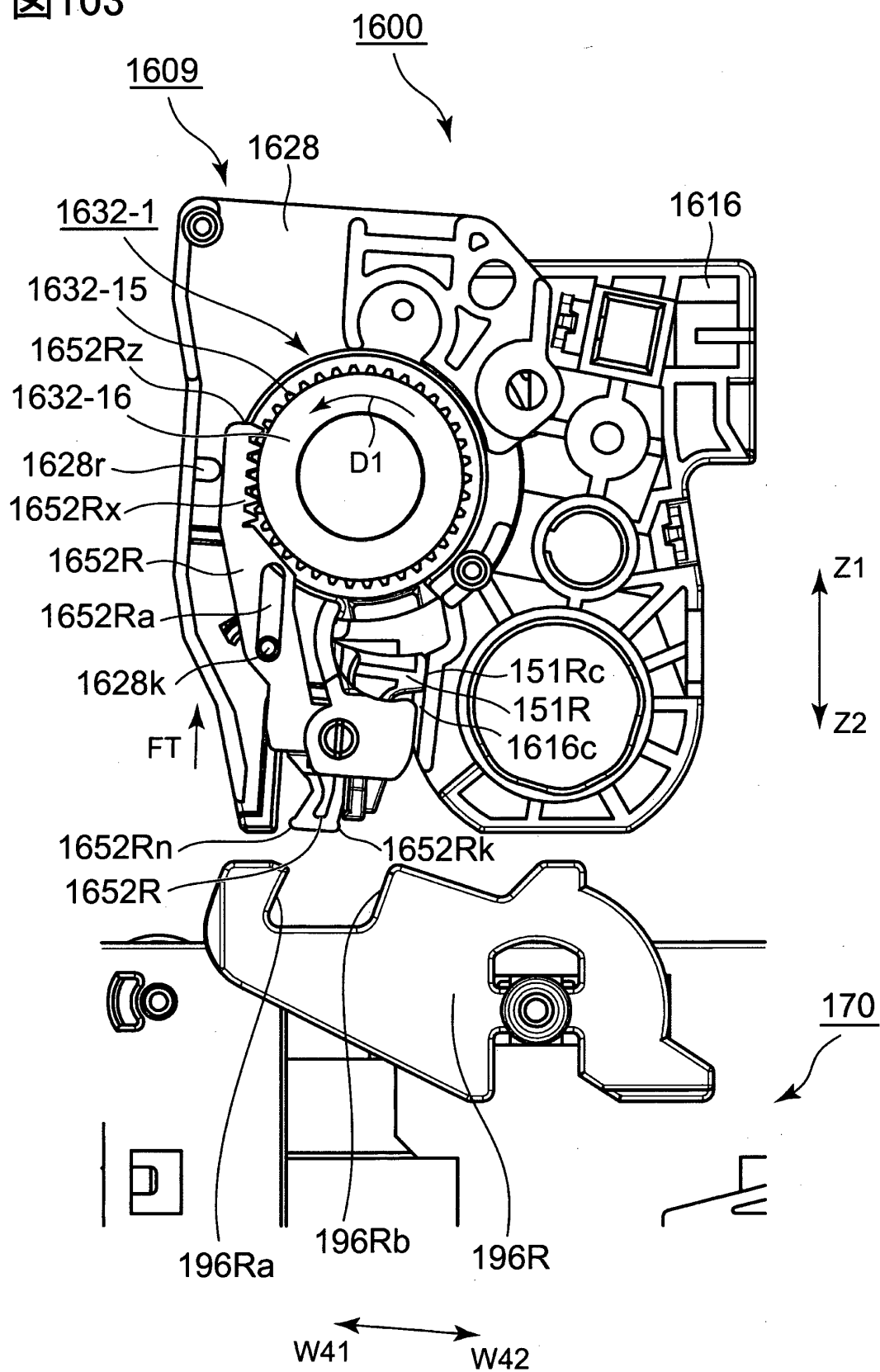


図107

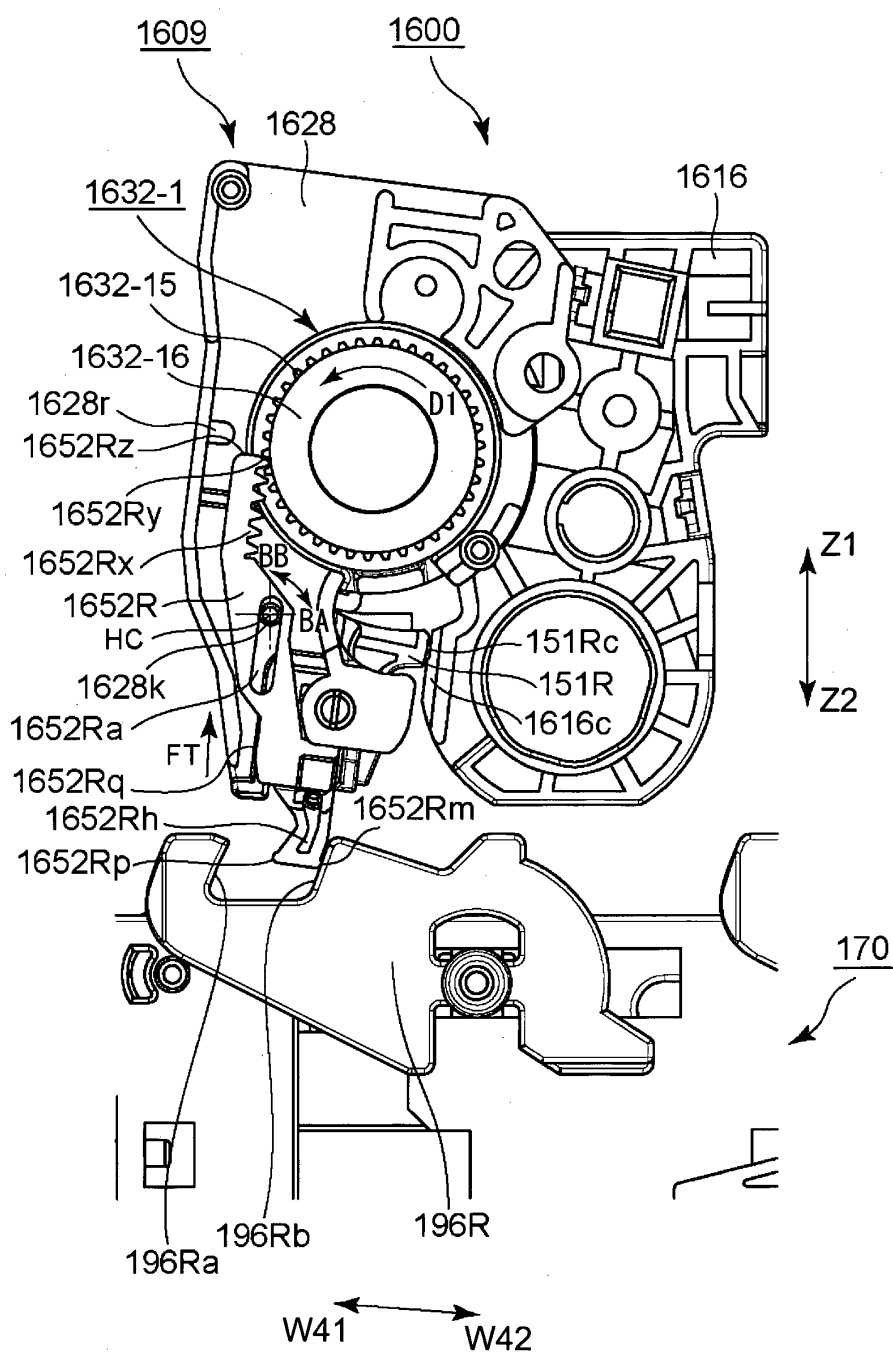


図108

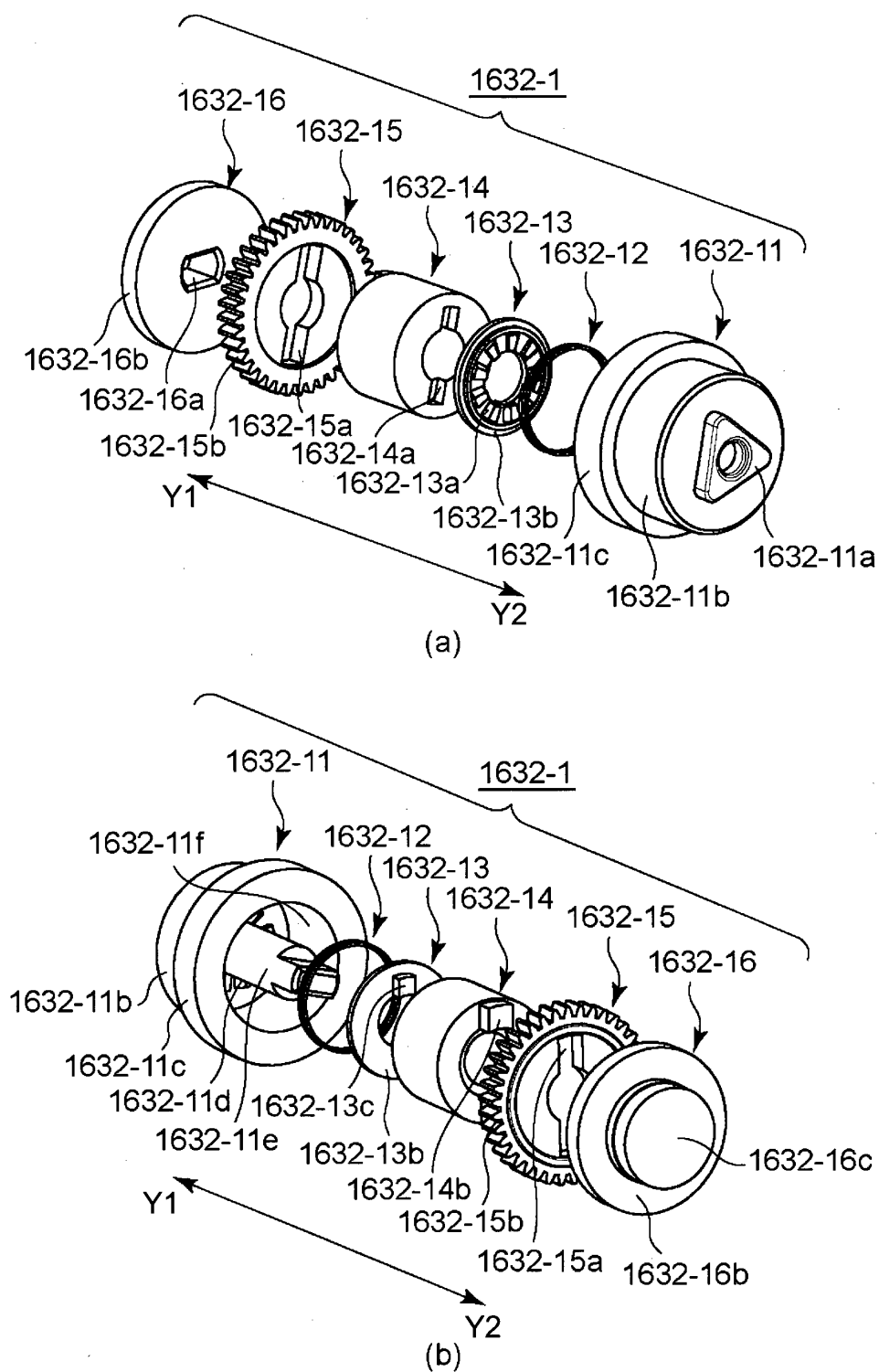


図109

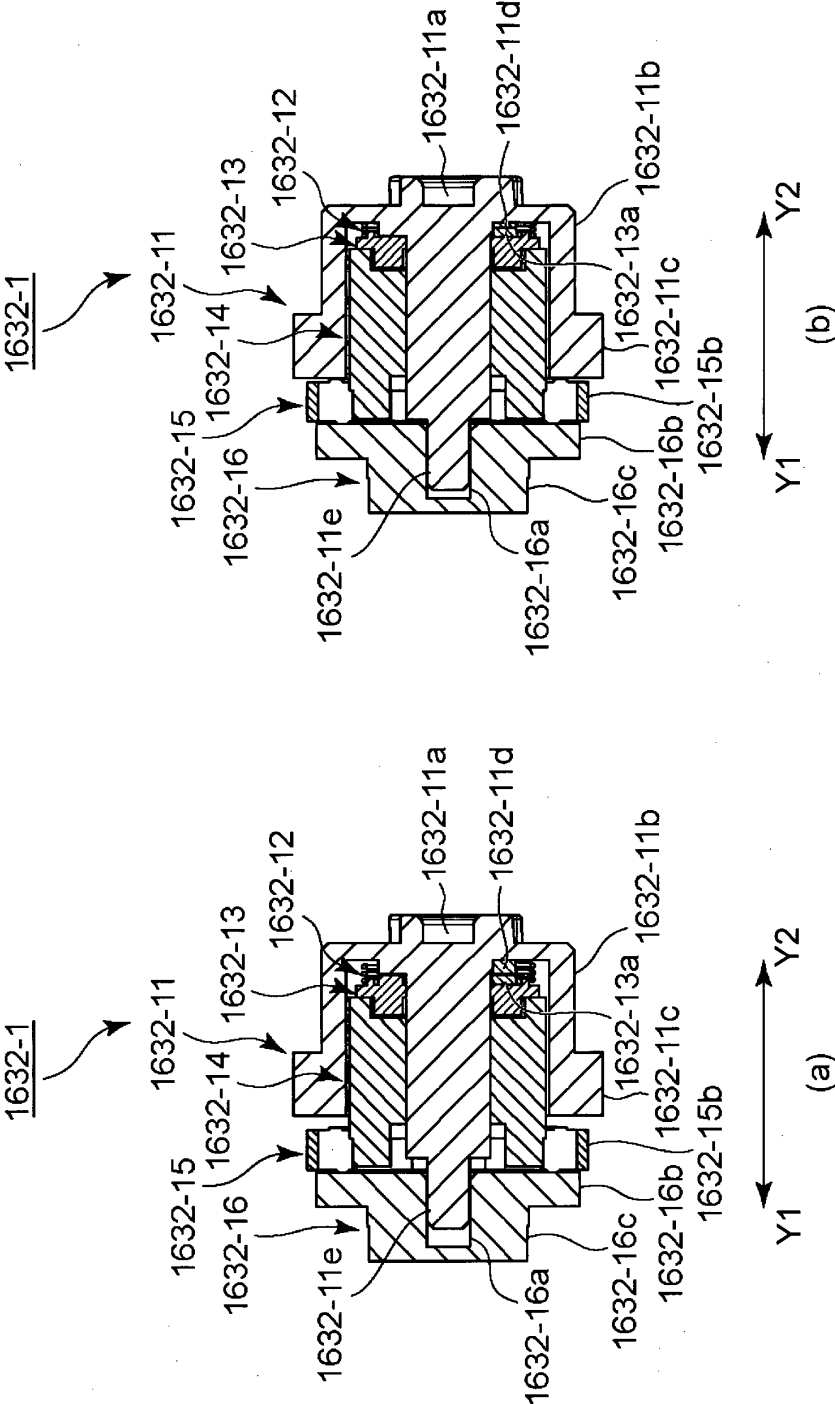


図110

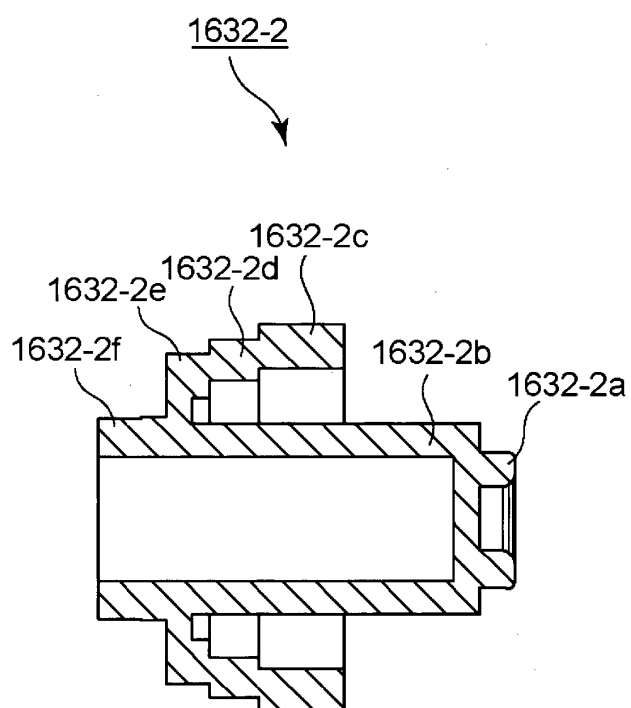


図111

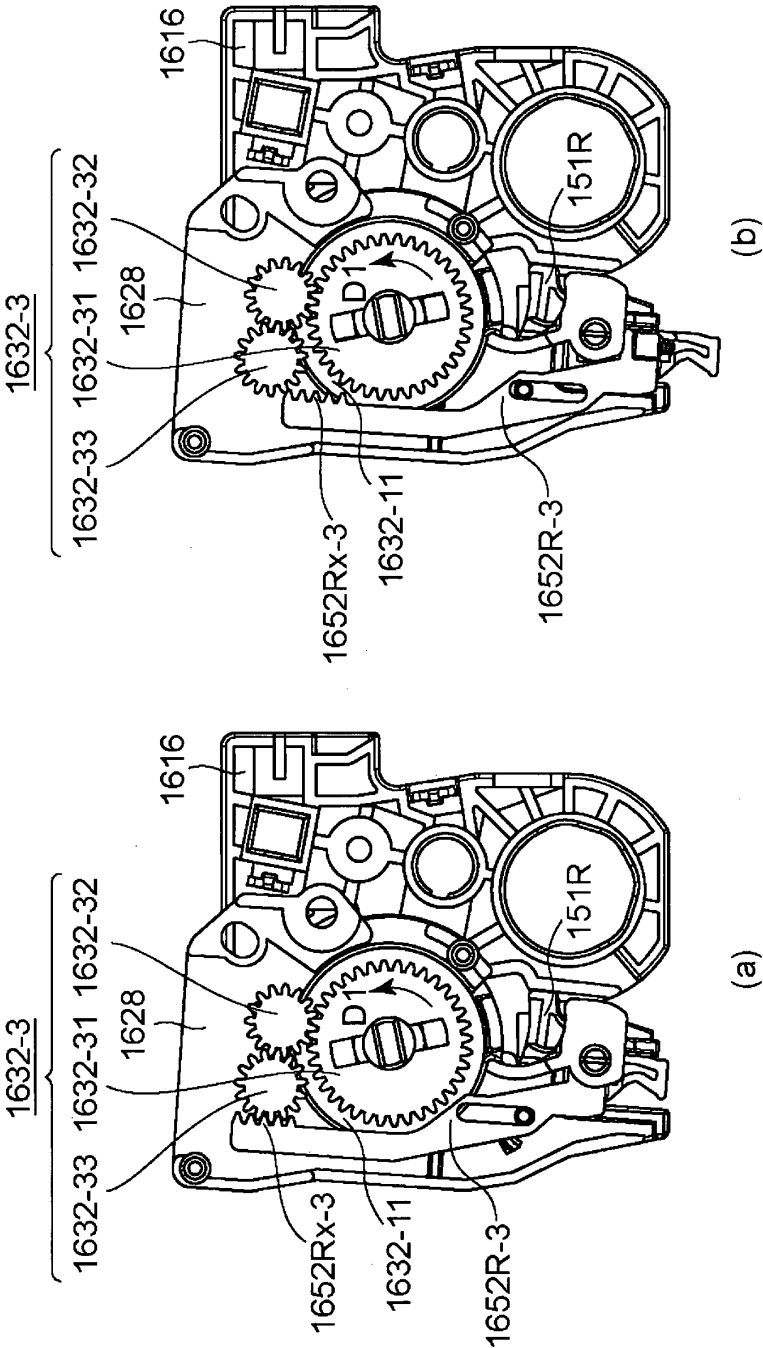


図112

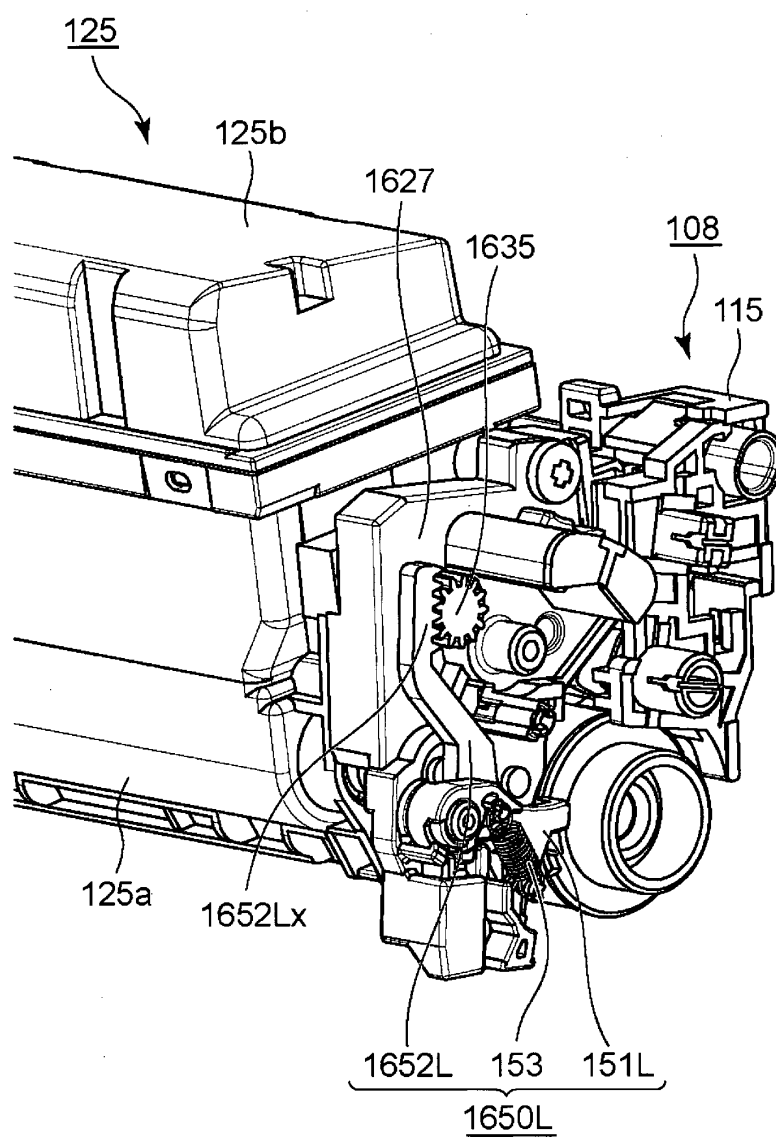


図113

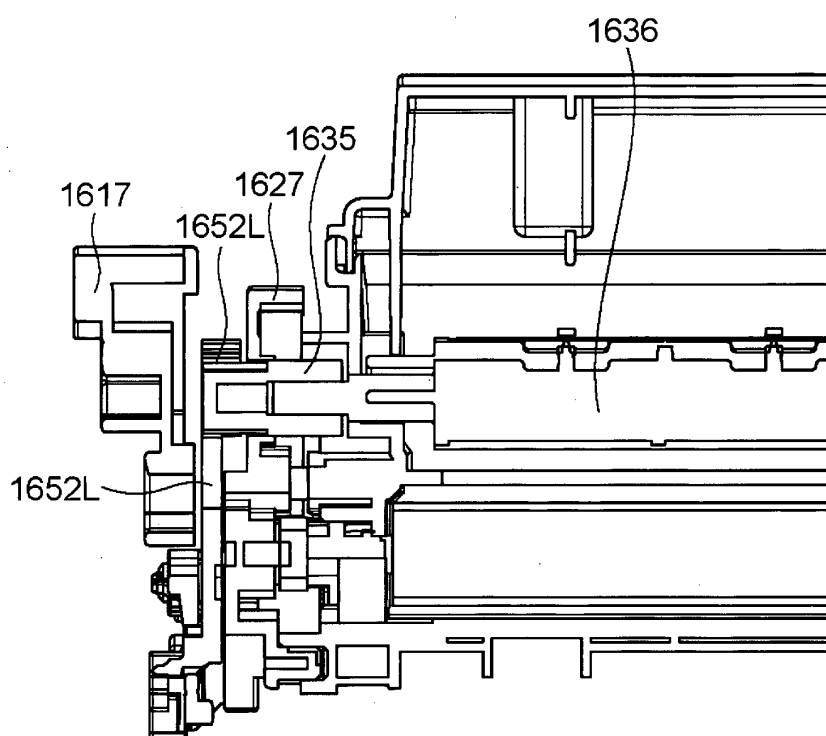


図114

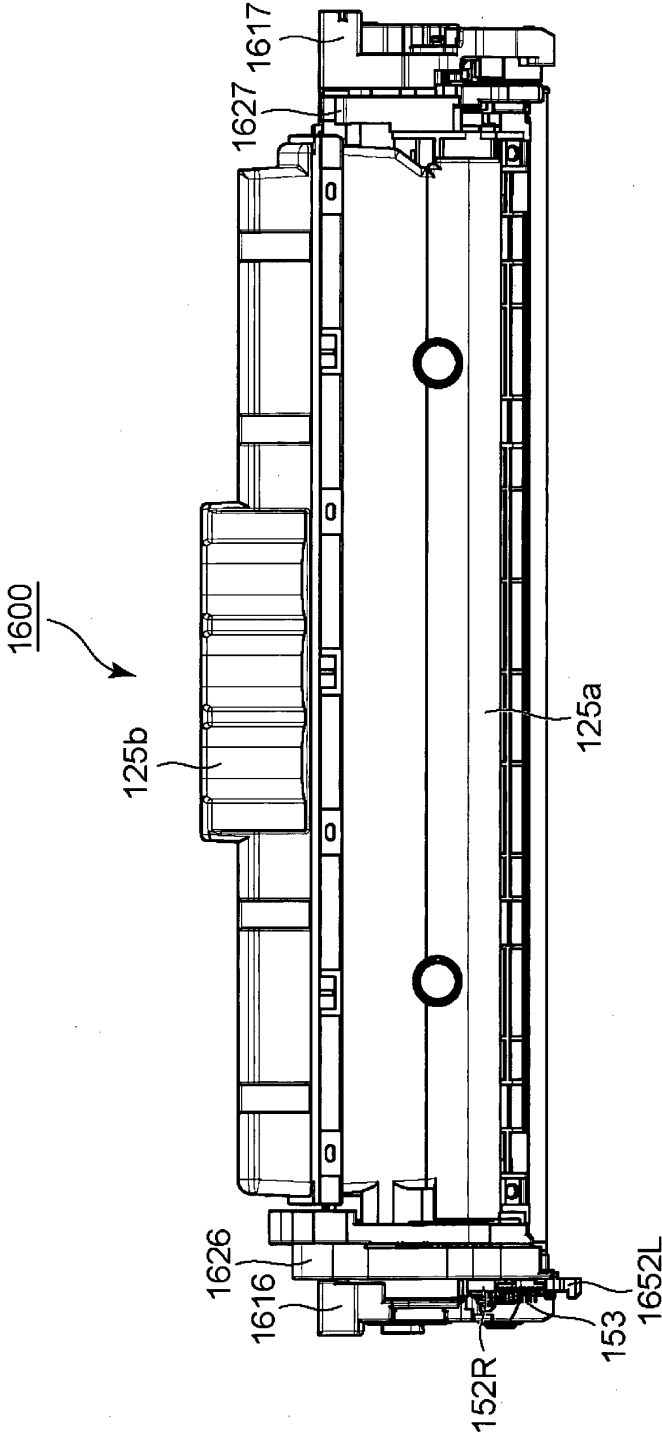


図115

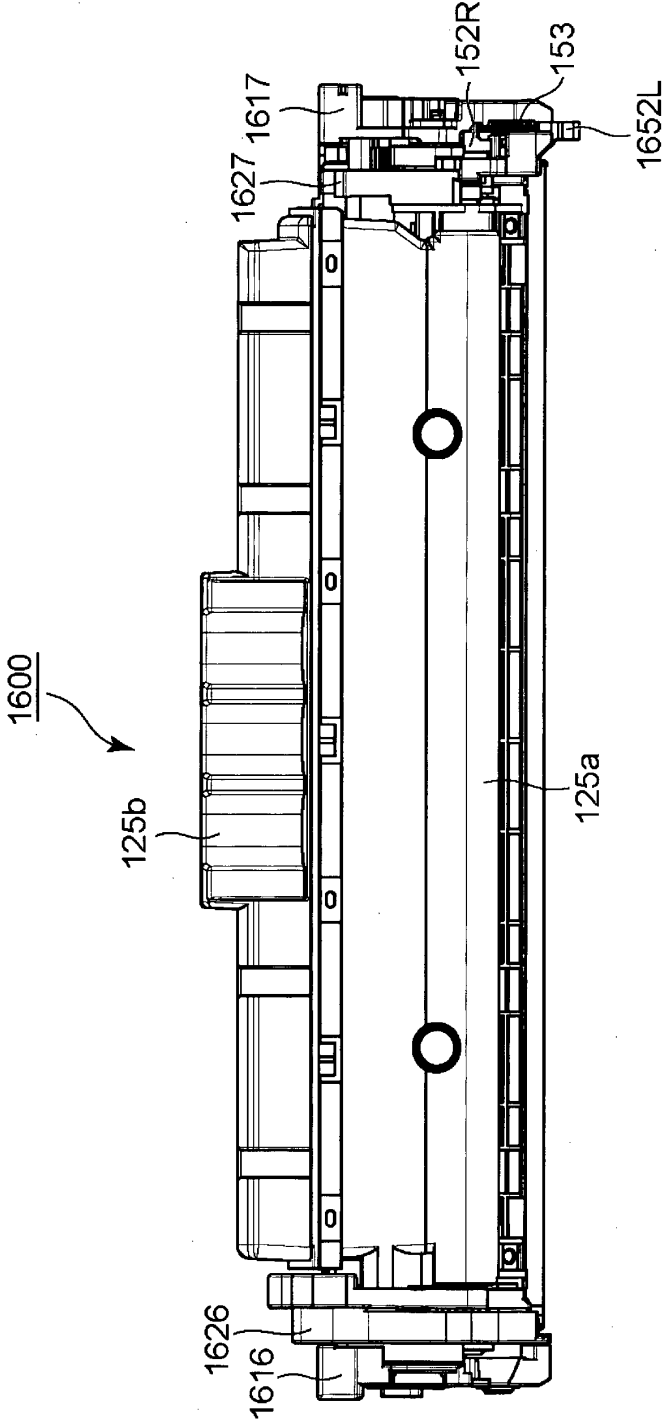


図 116

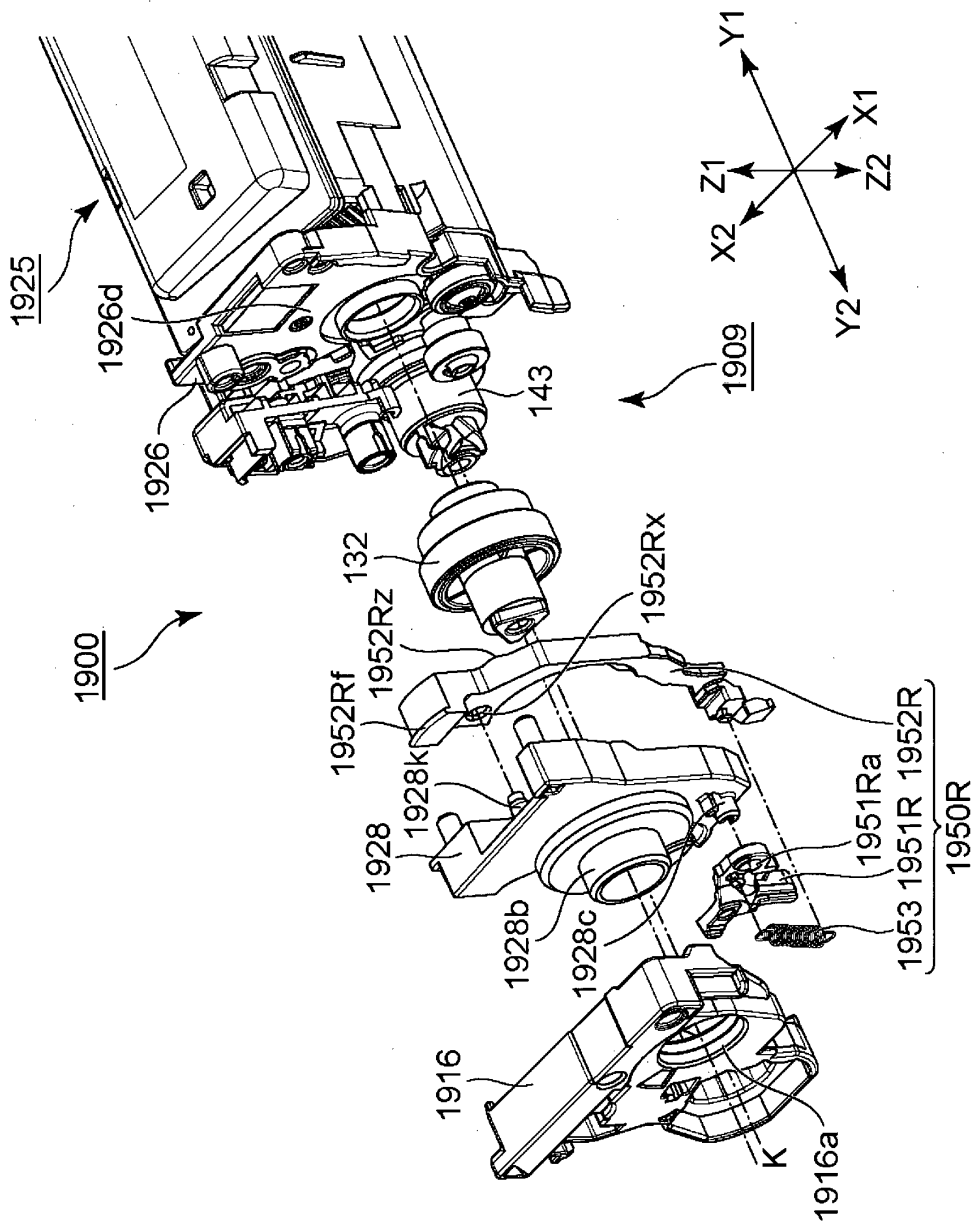


図 117

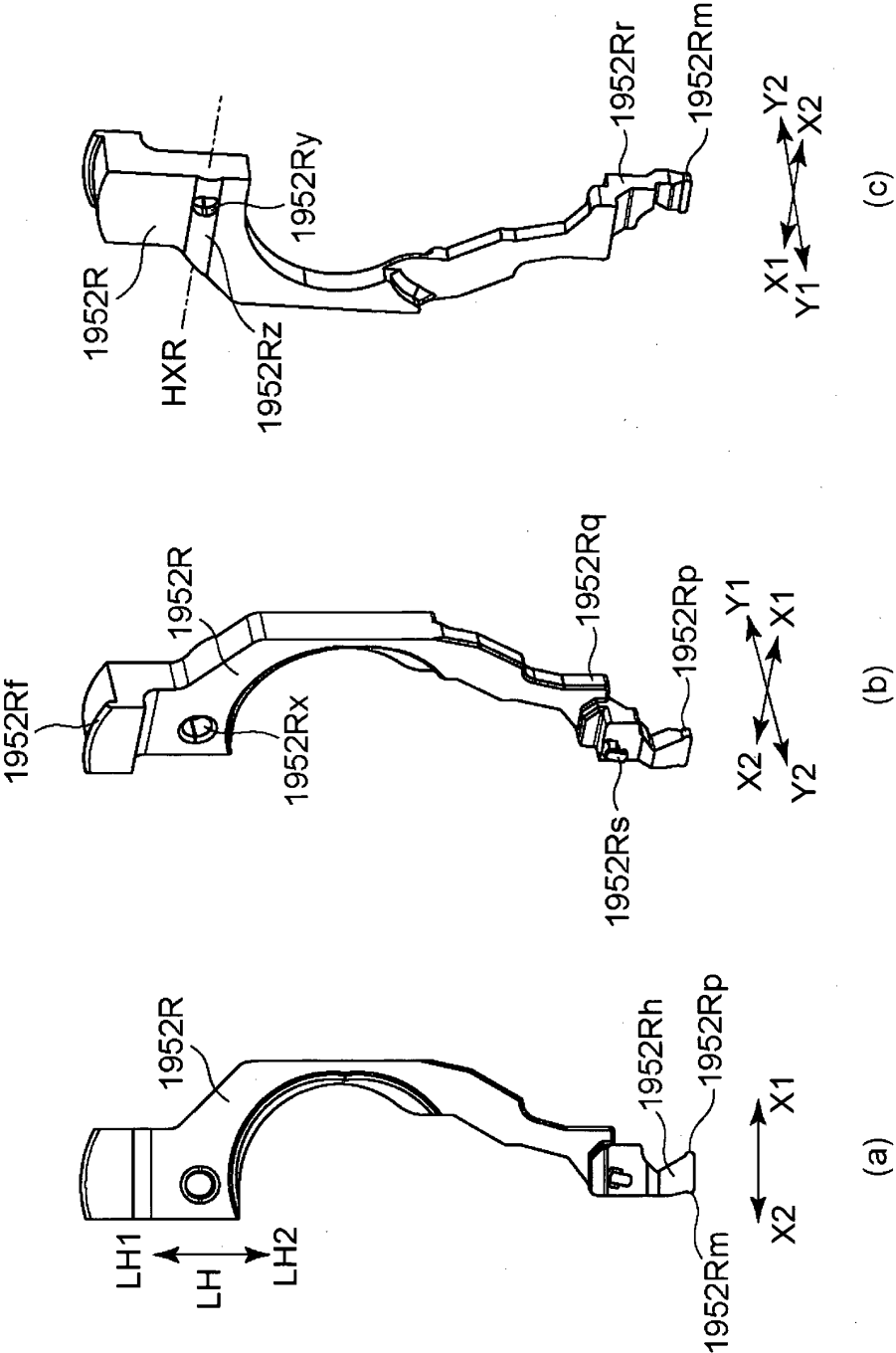


図118

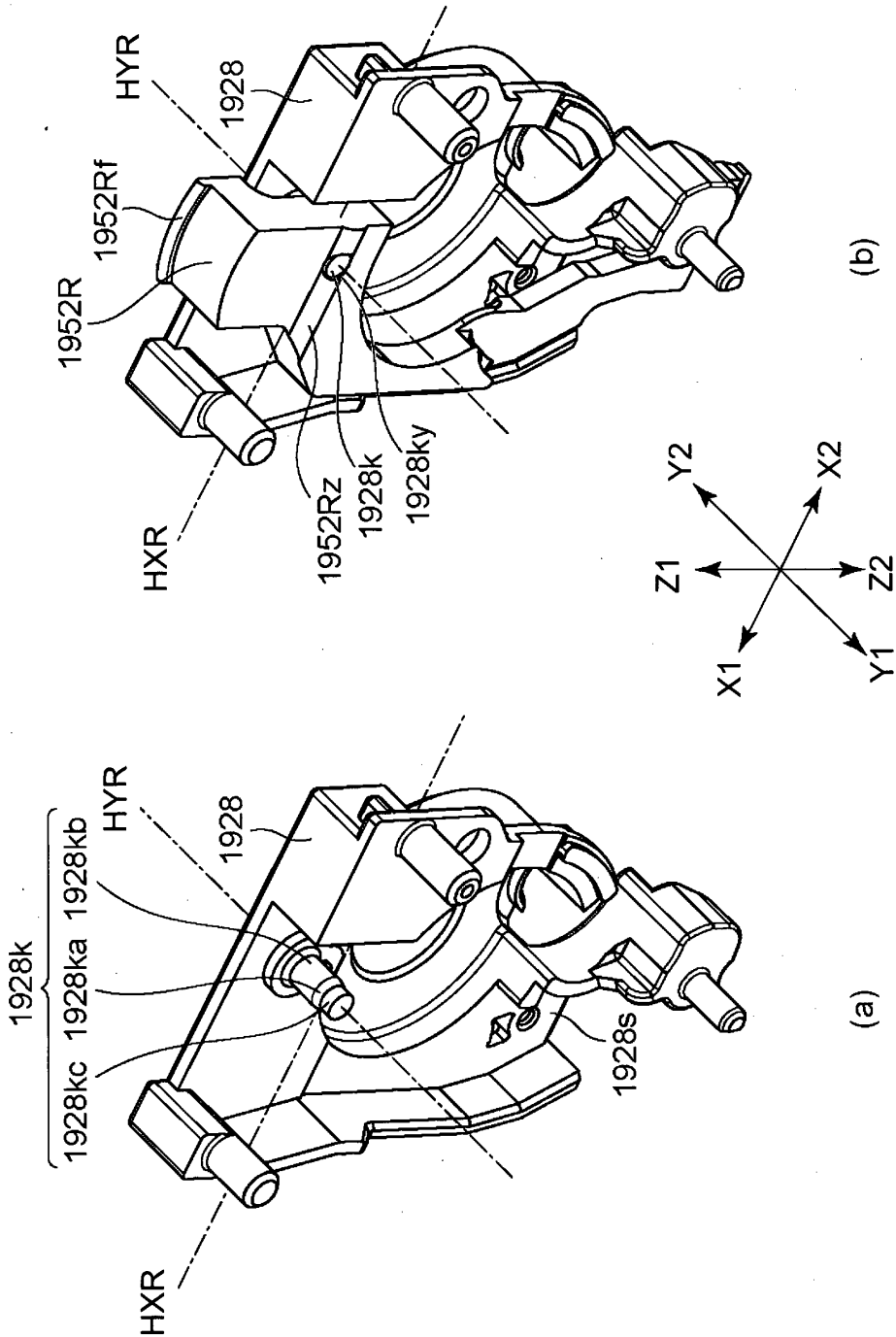


図120

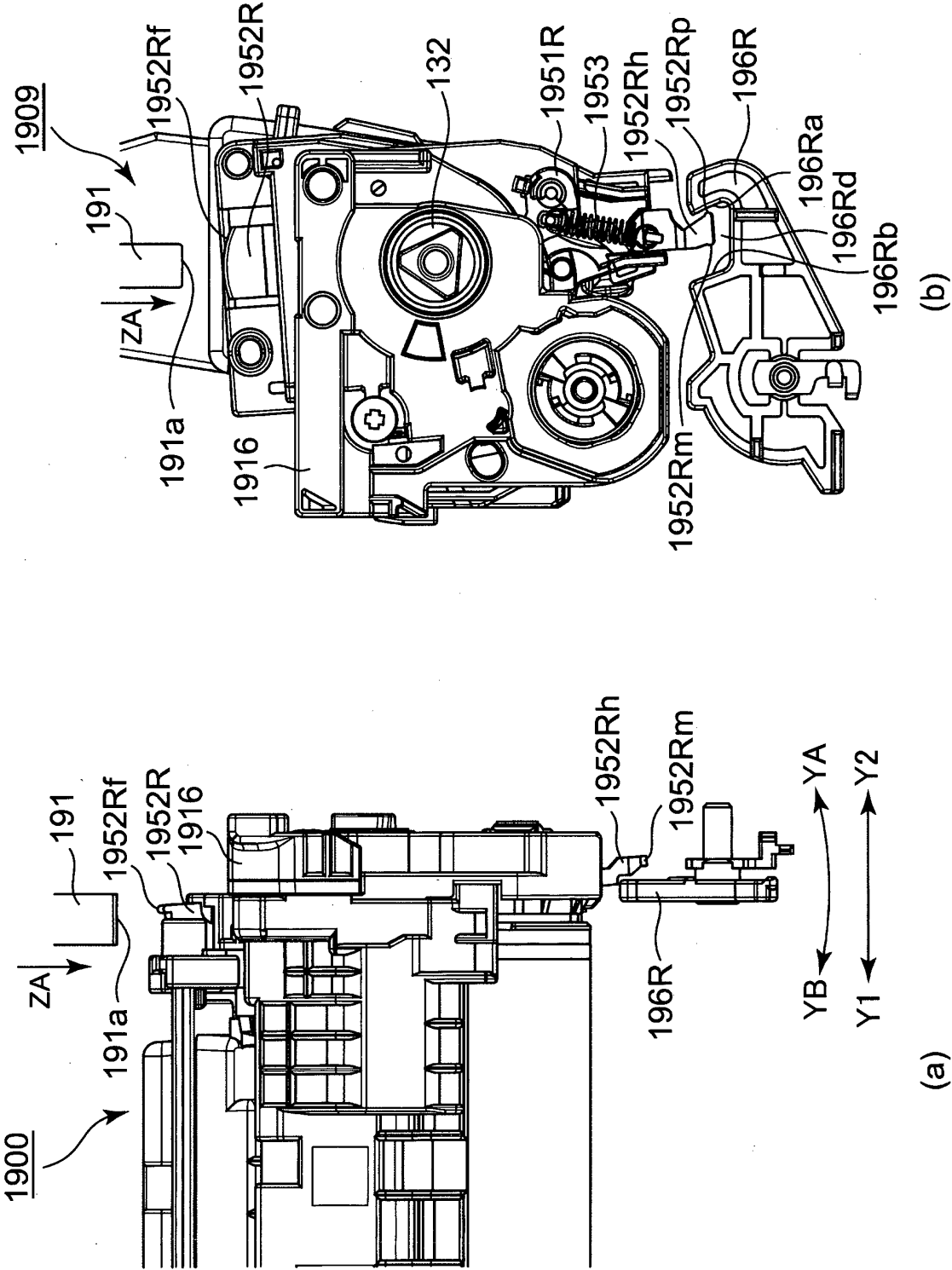


図121

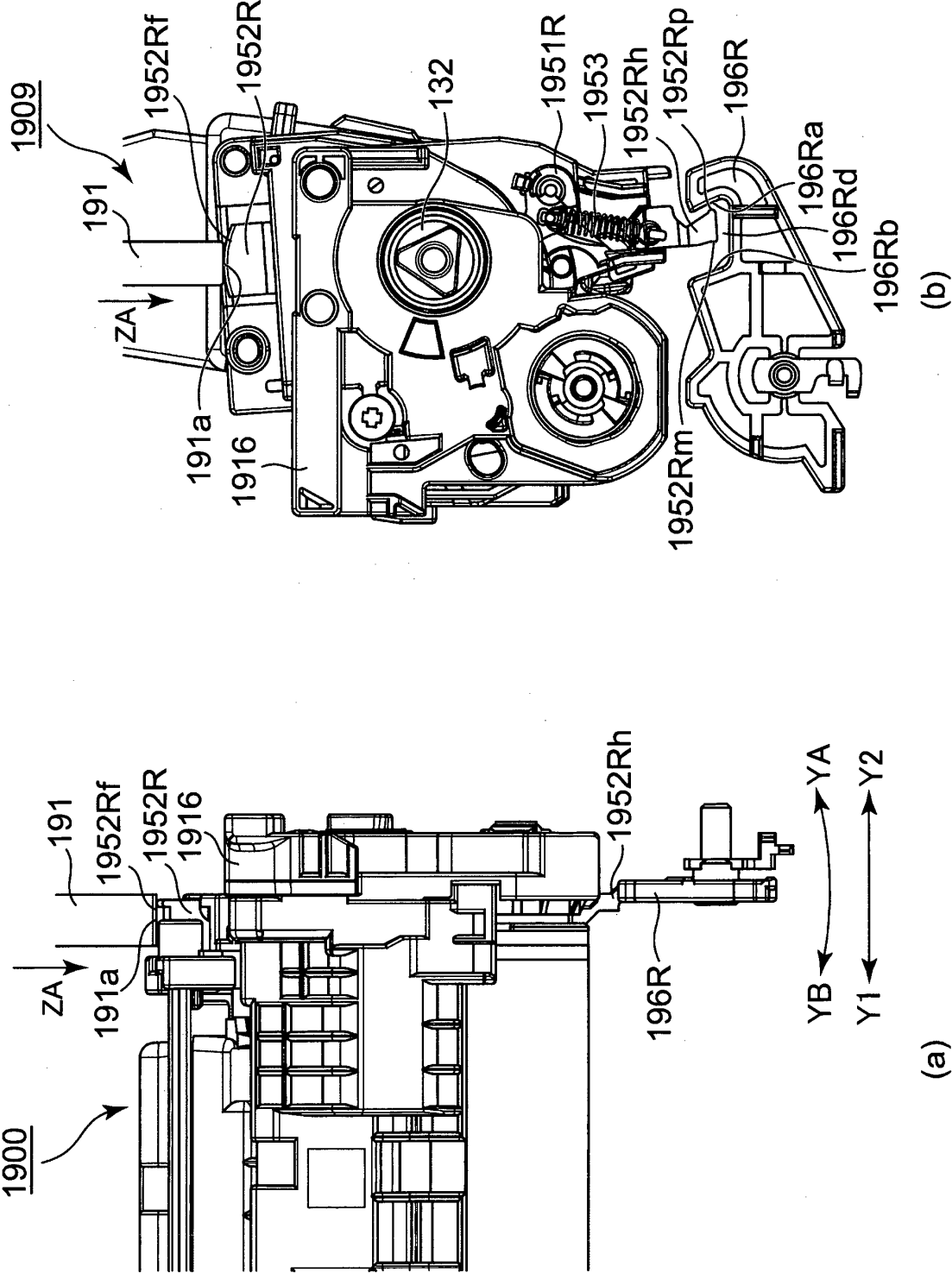


図122

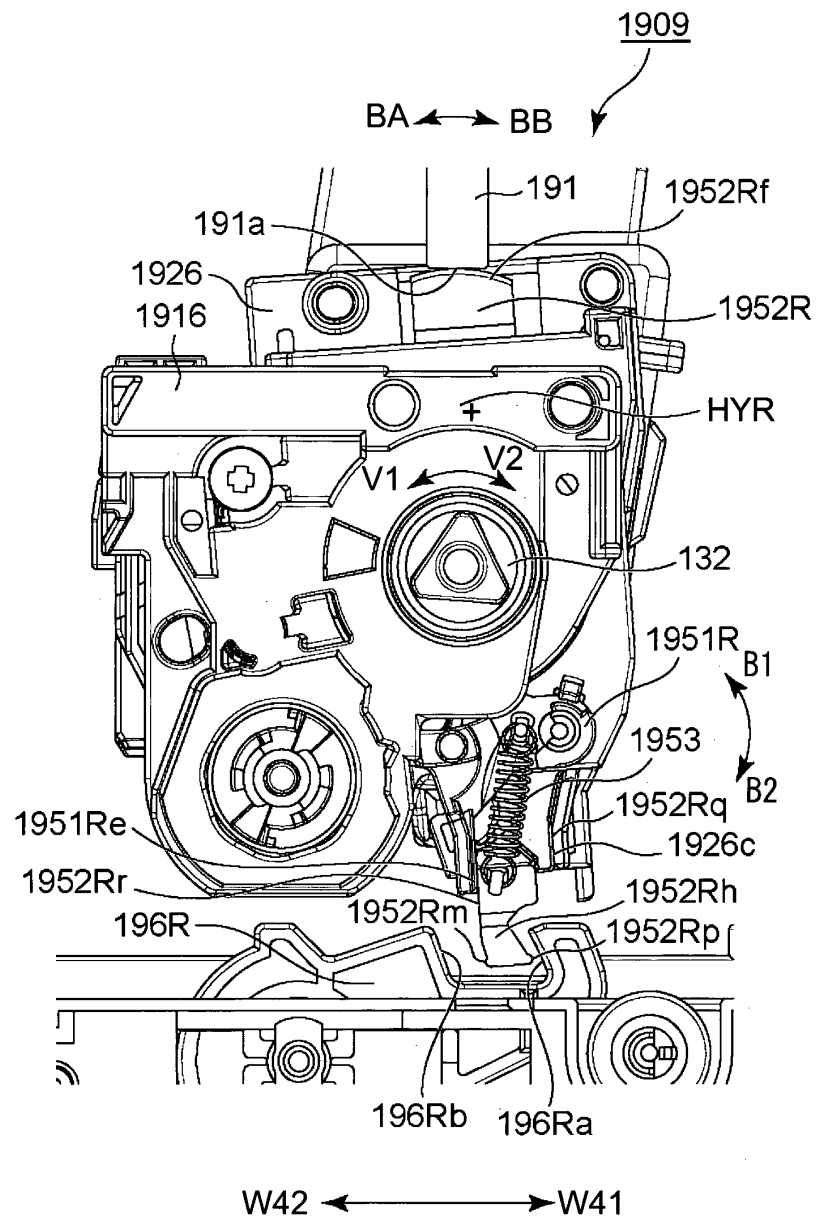


図123

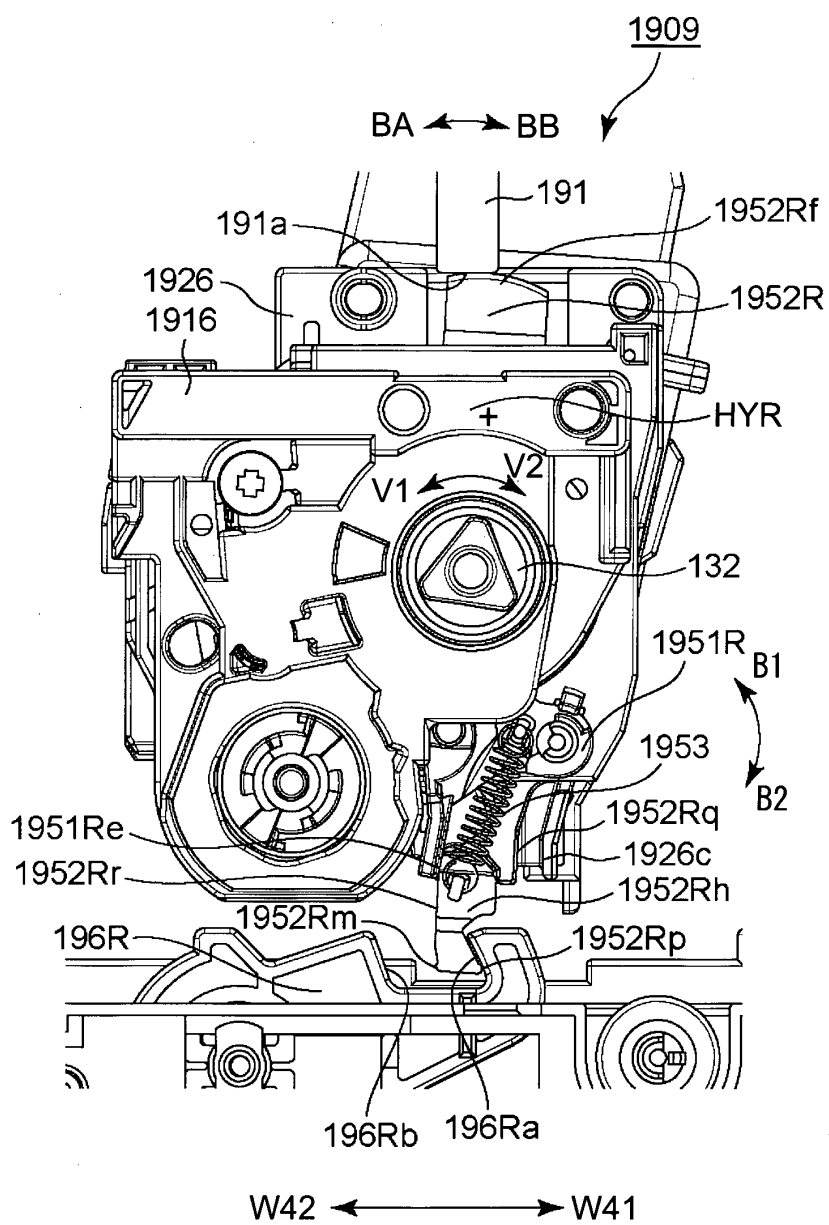


図124

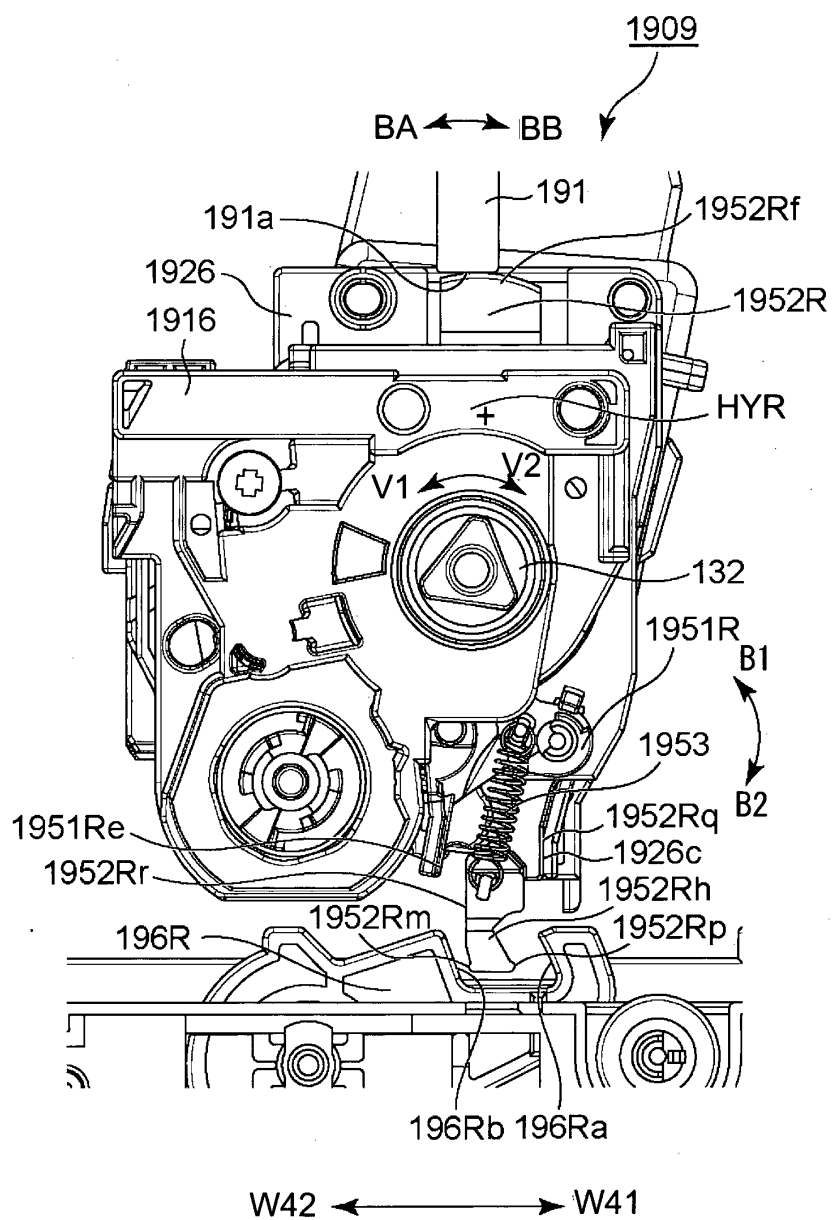


図125

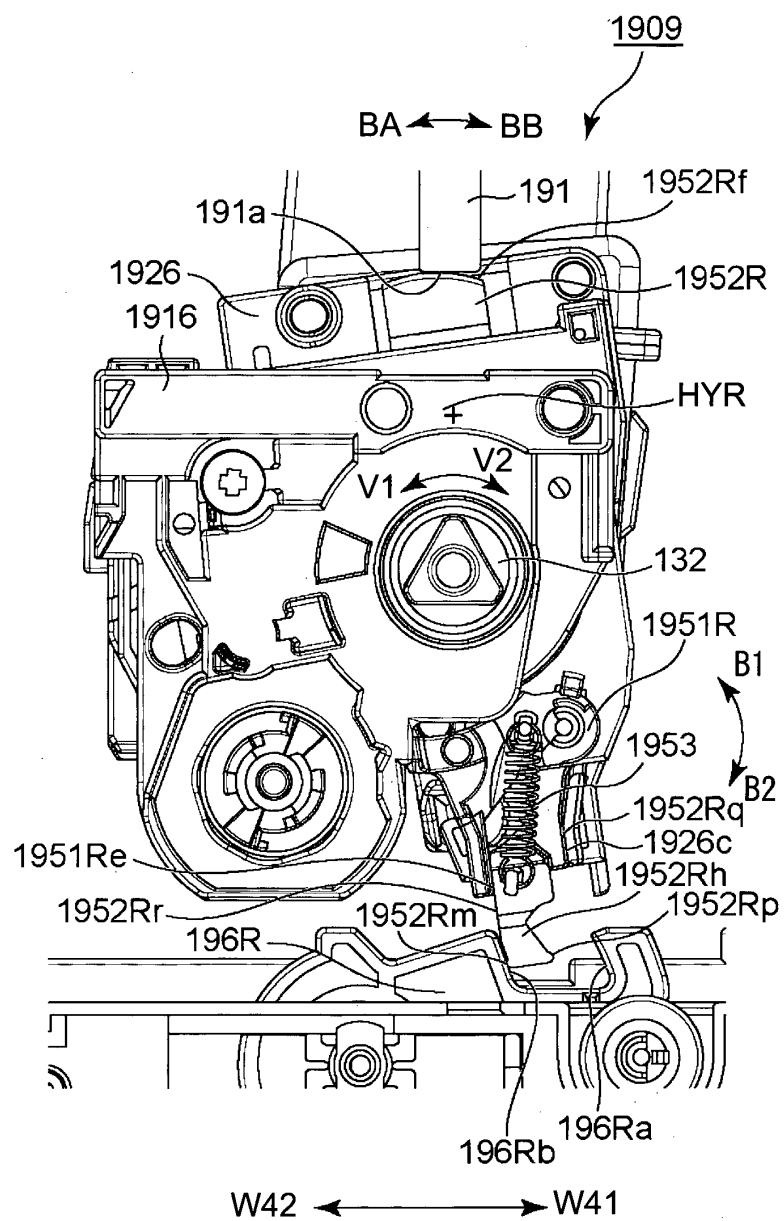


図126

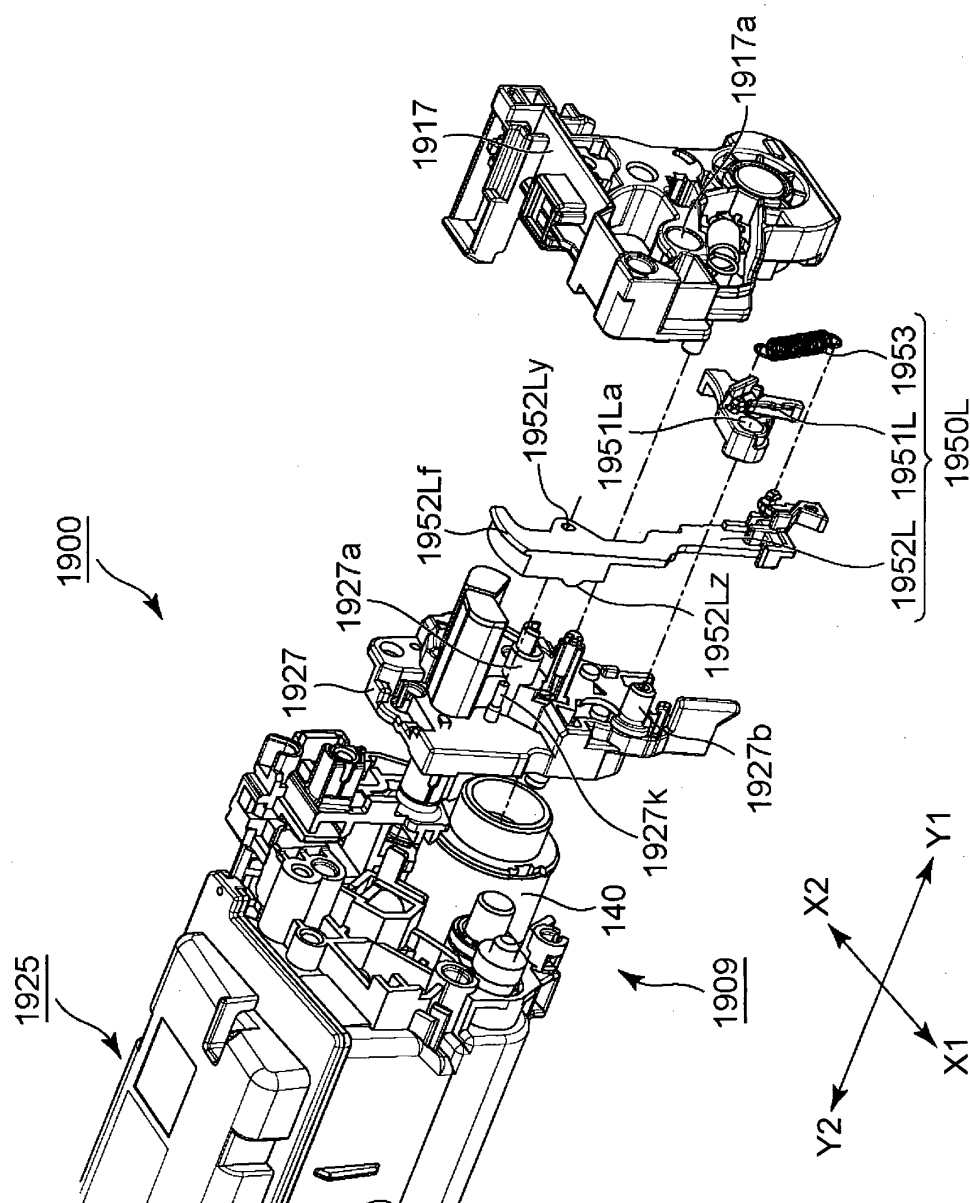


図127

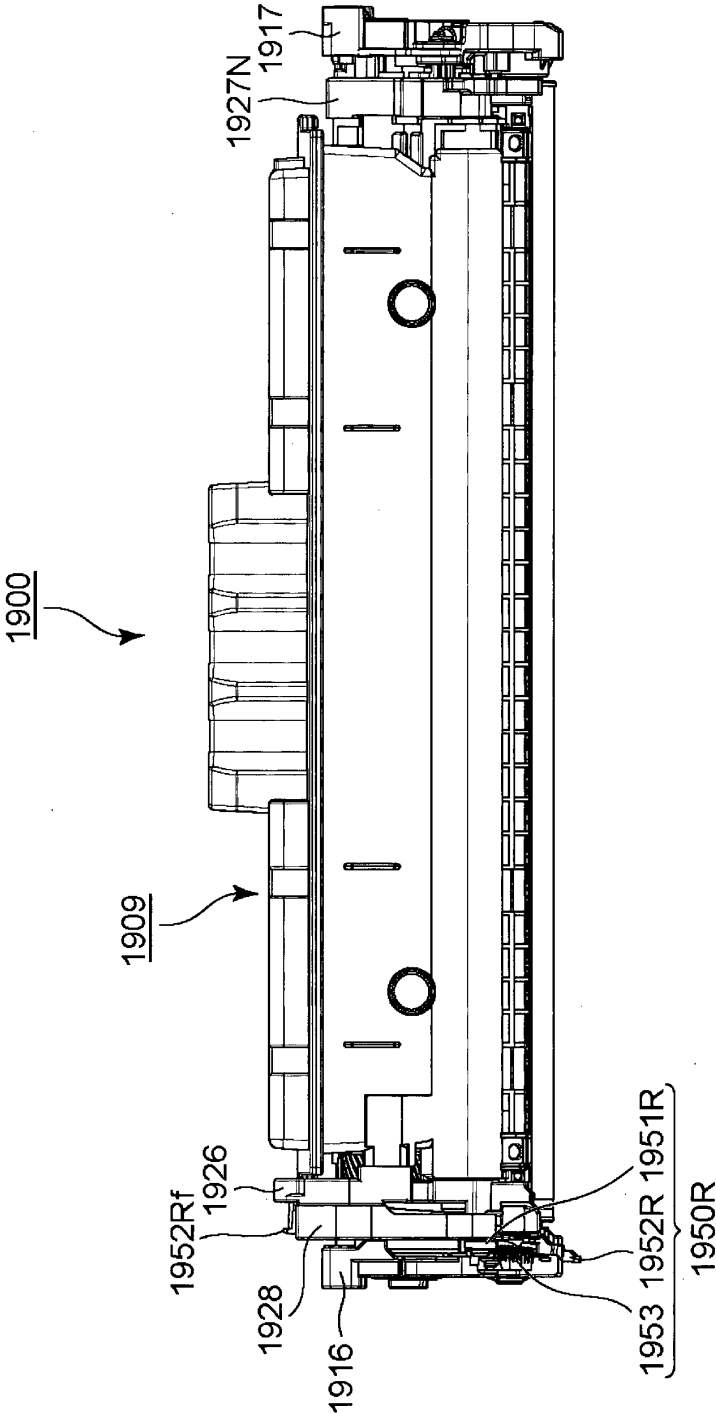


図128

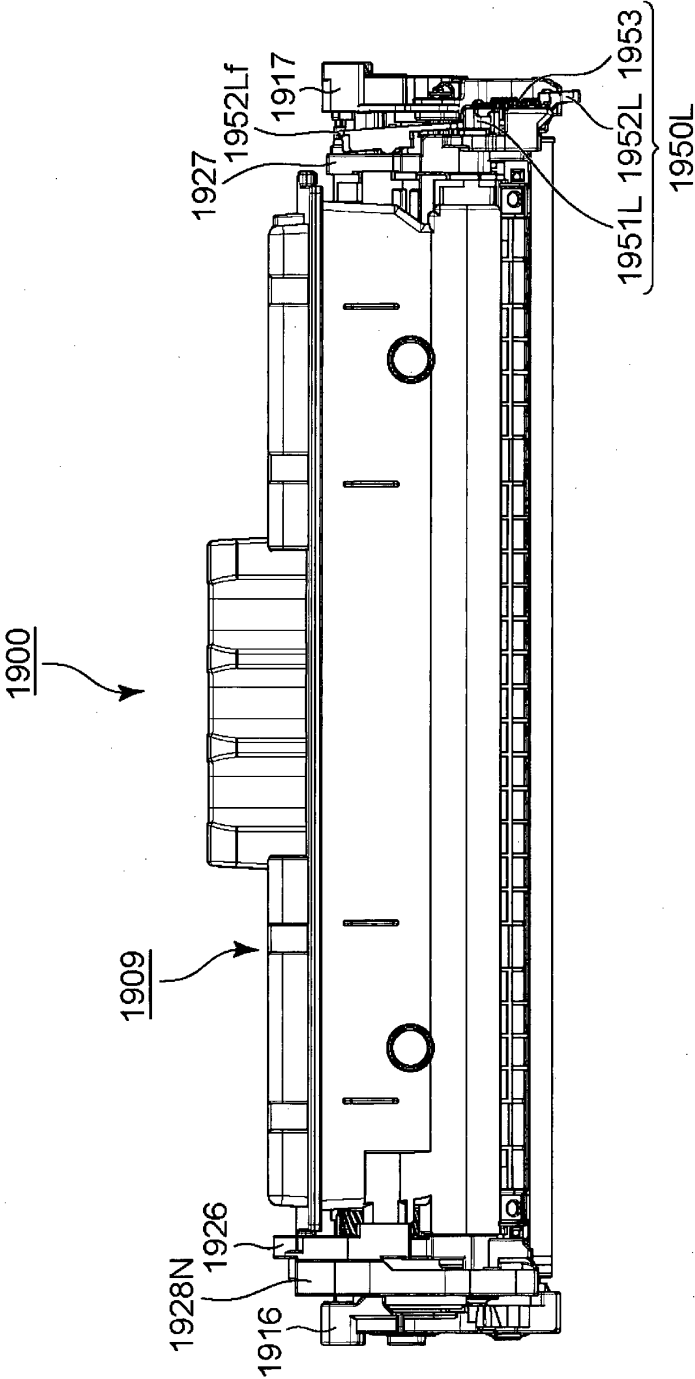


图 129

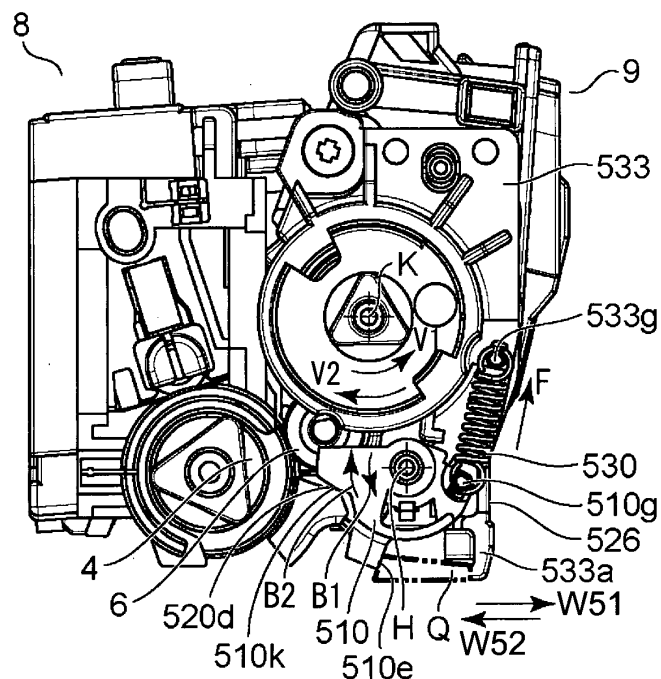
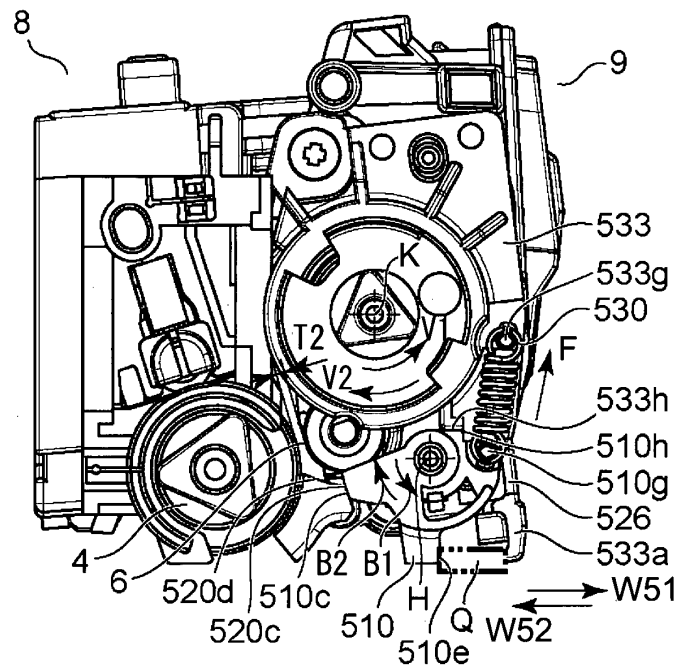


図131

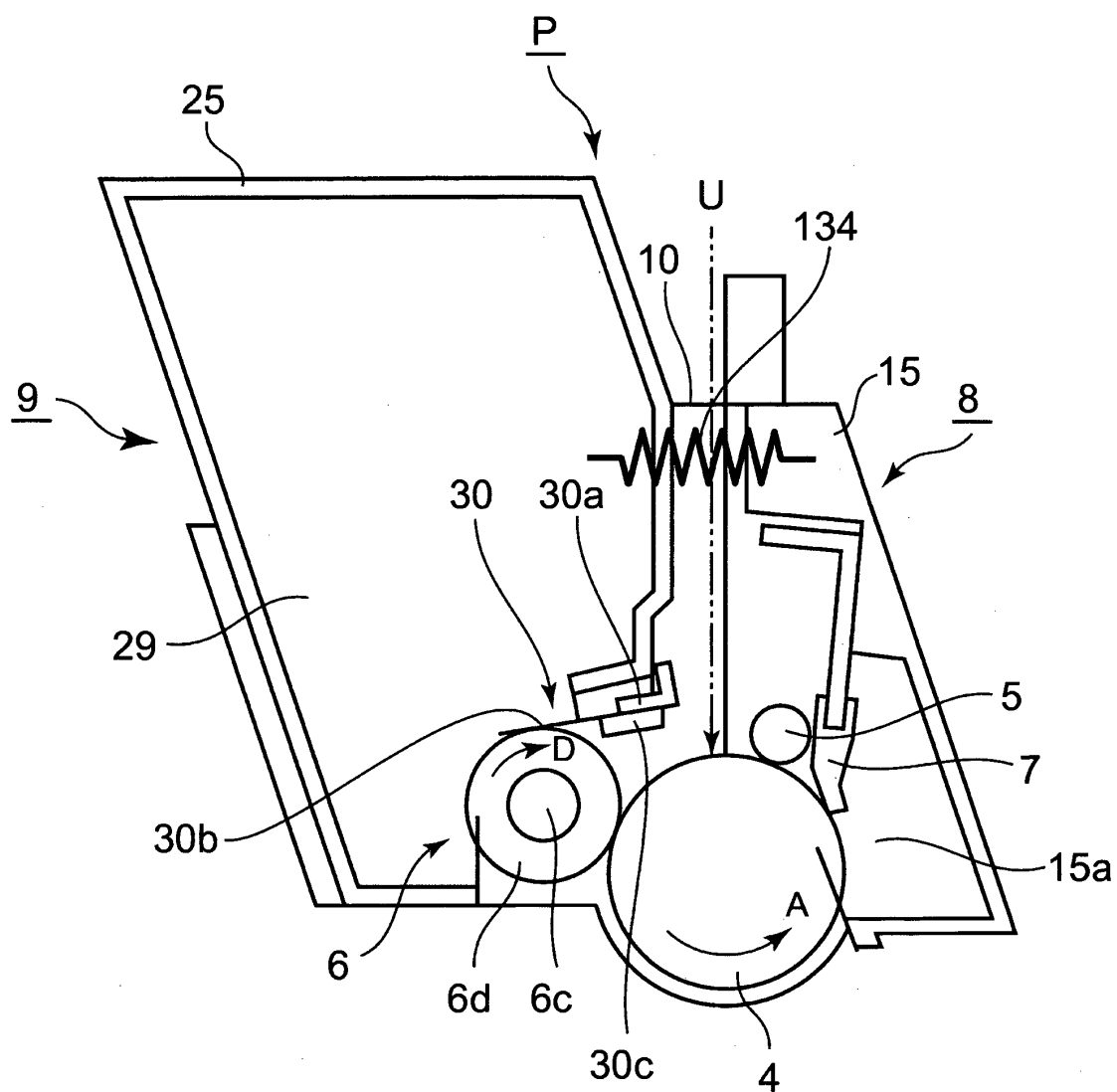


图 132

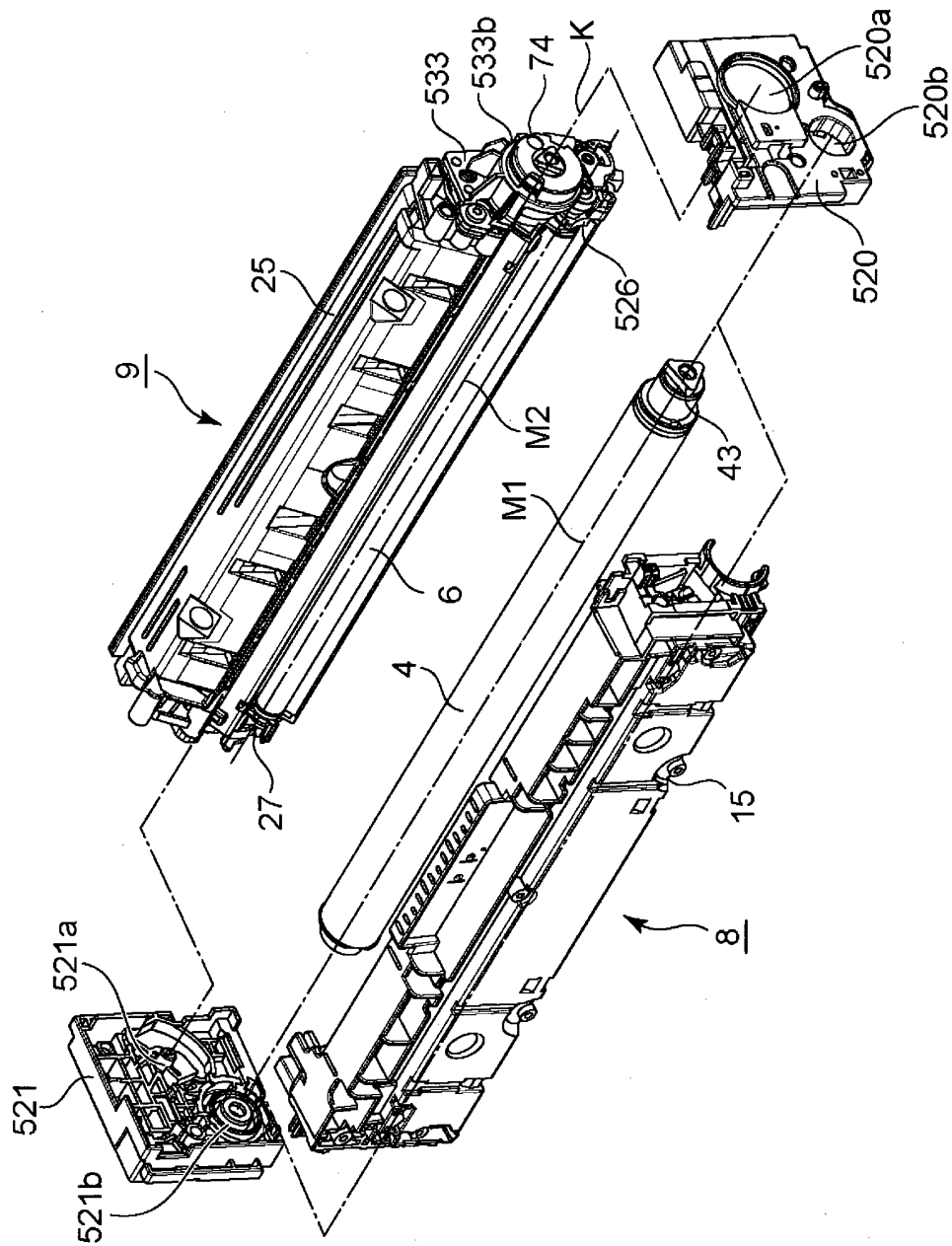


図134

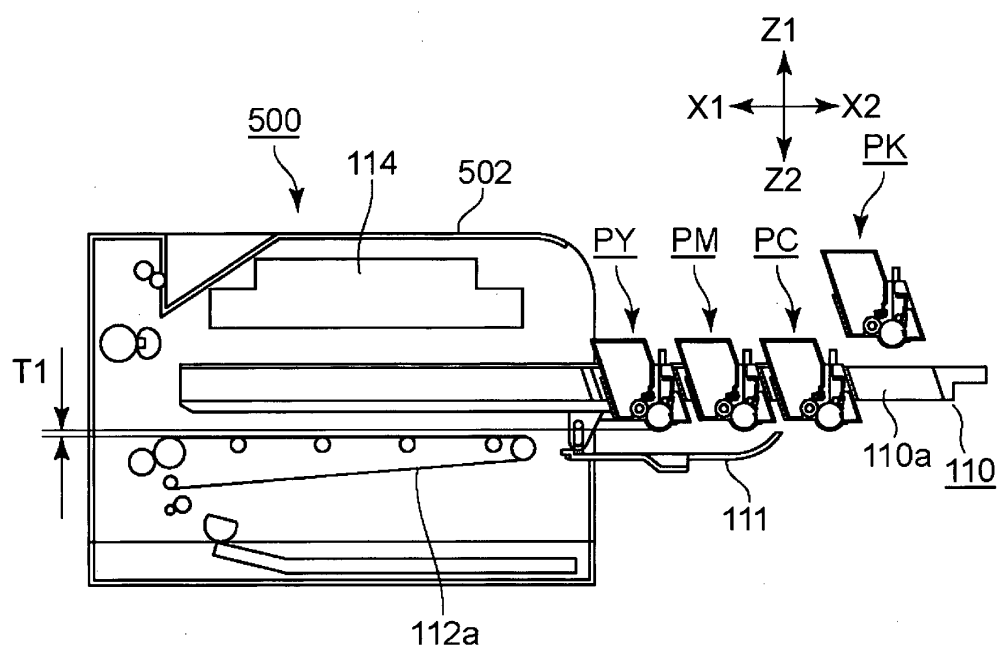
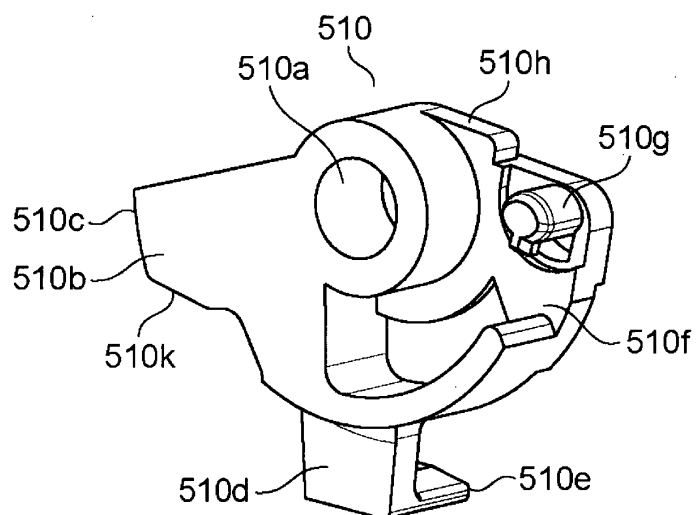
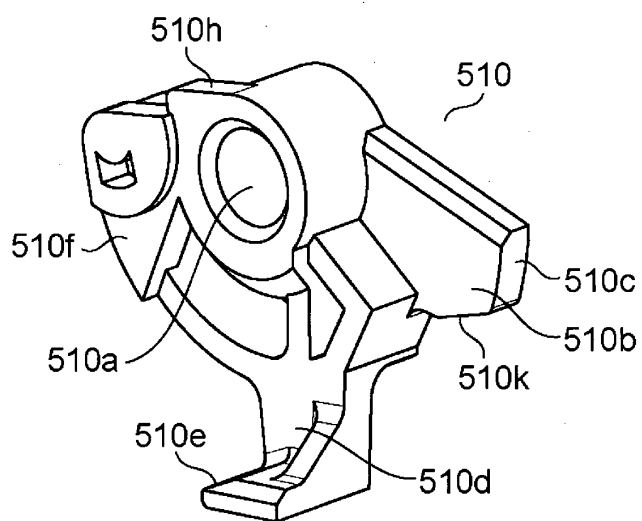


図135



(a)



(b)

図 136

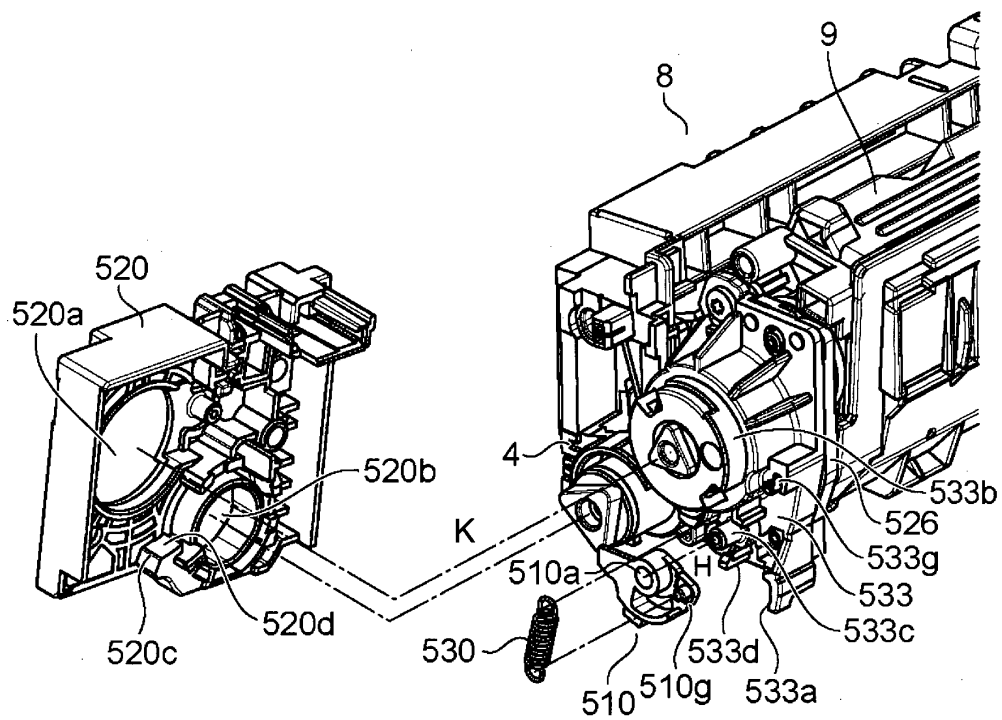


図137

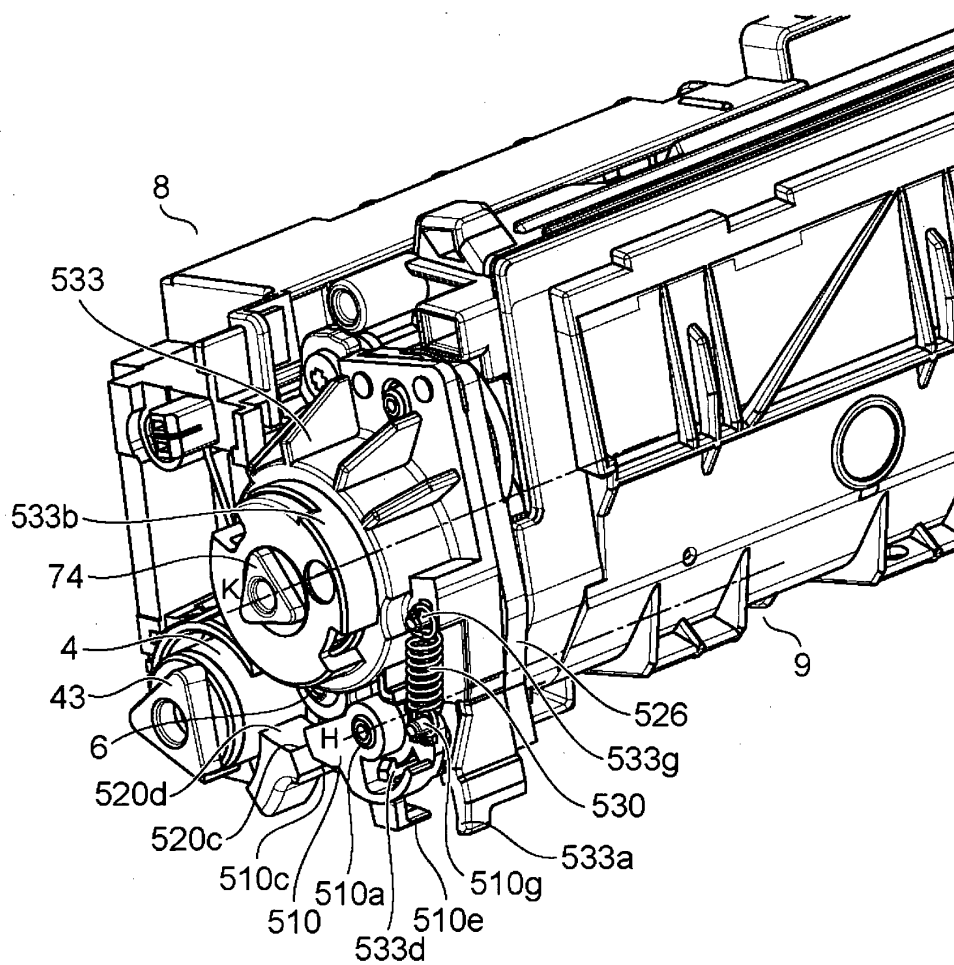
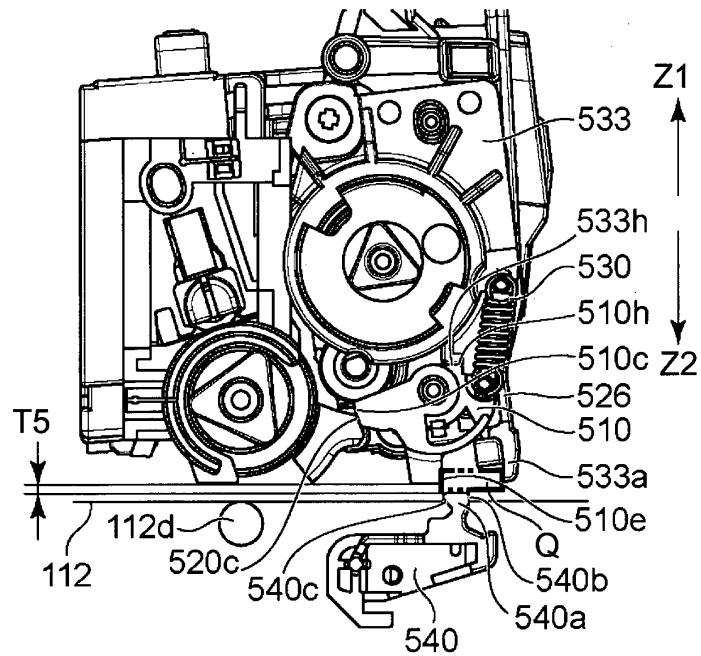
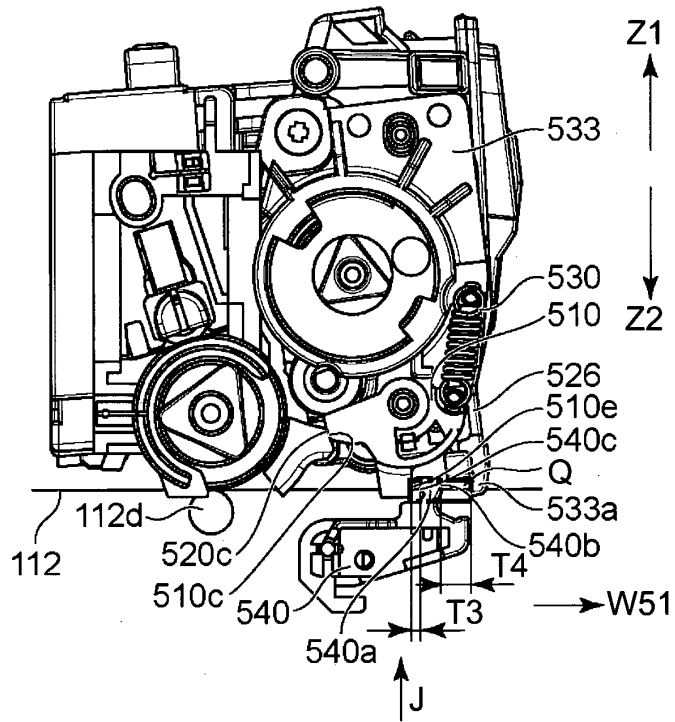


図 138



(a)



(b)

図140

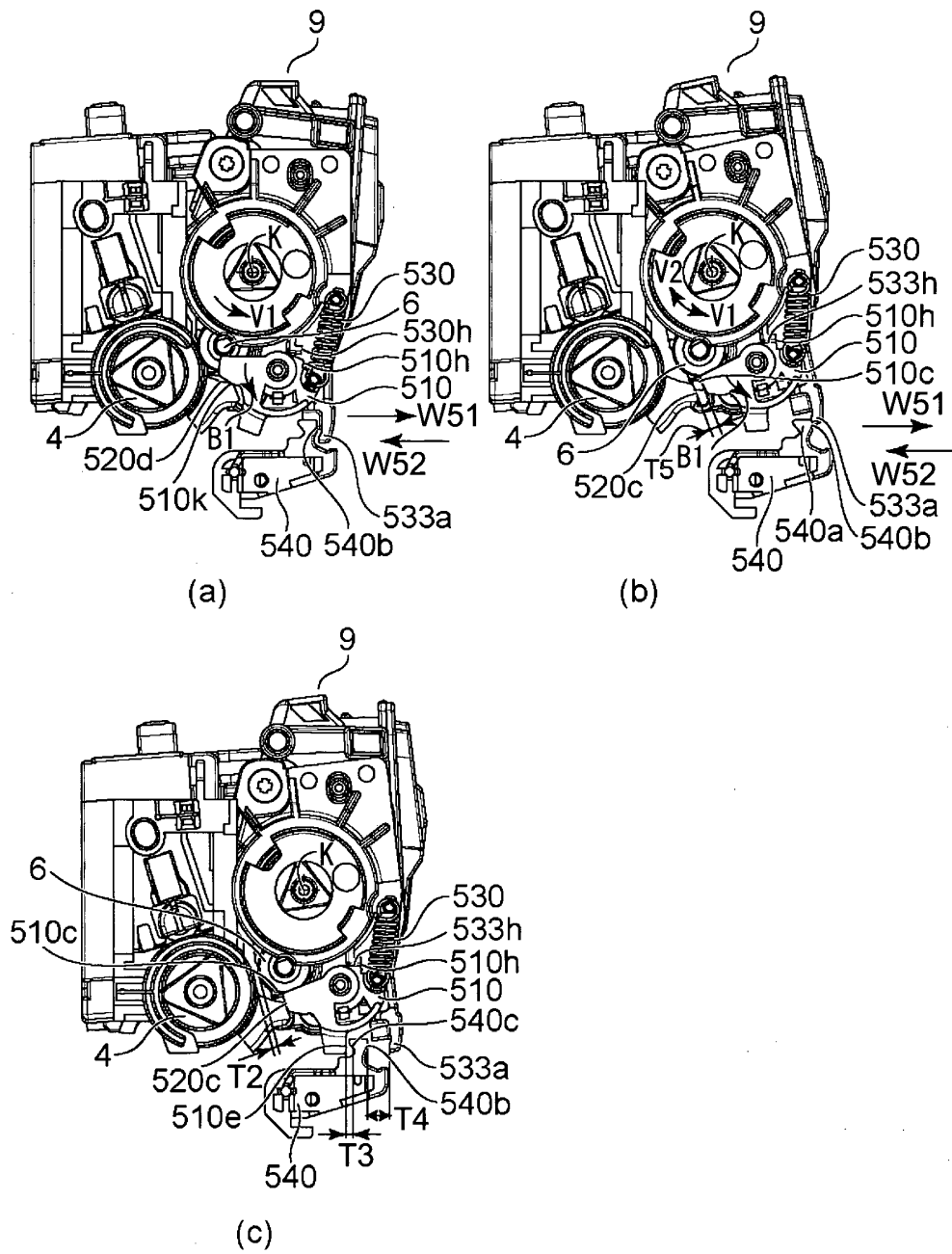


図 141

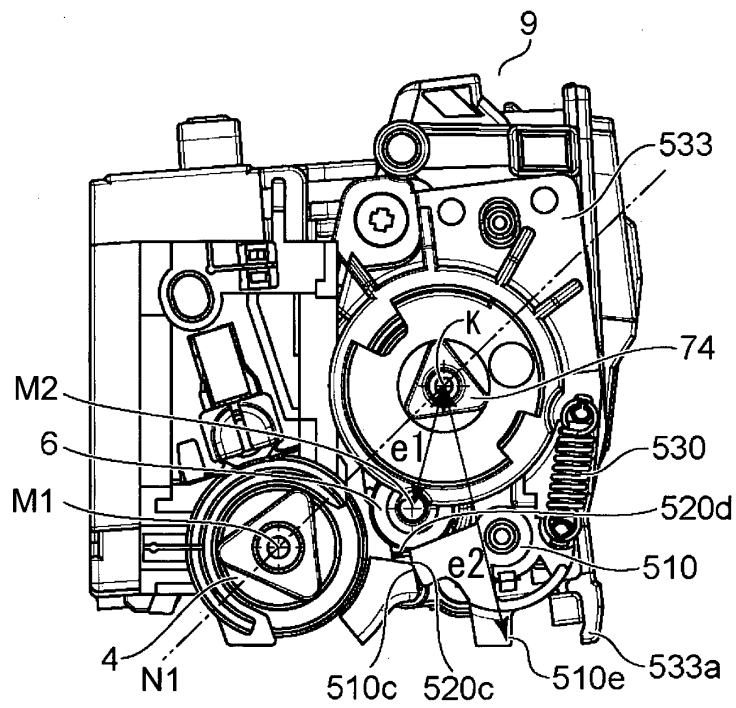


図142

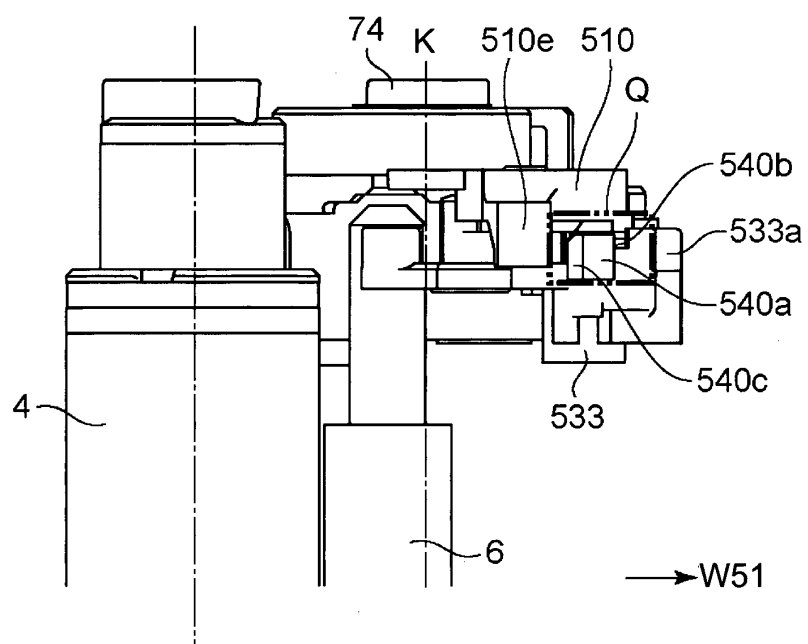


図143

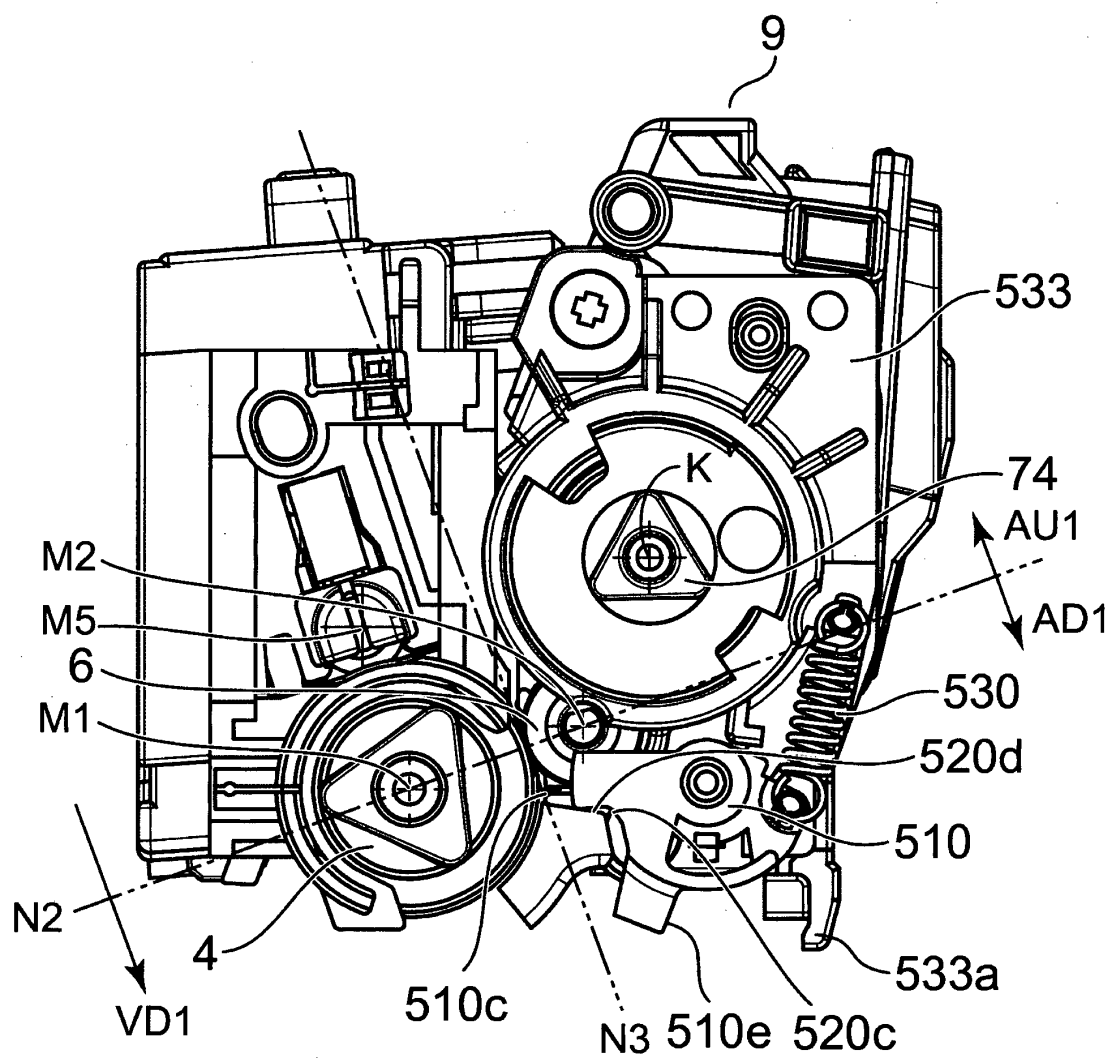


図 144

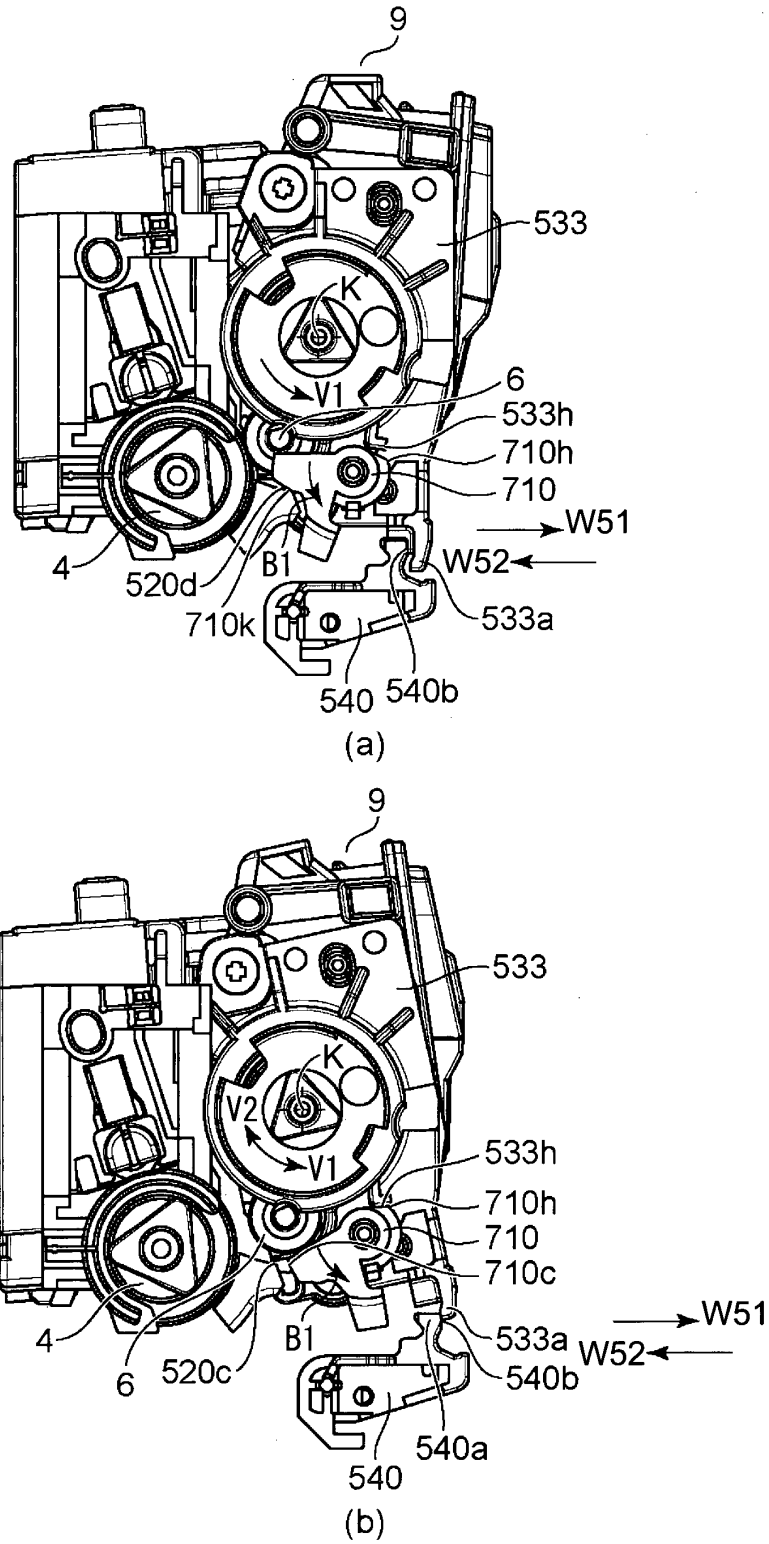


図145

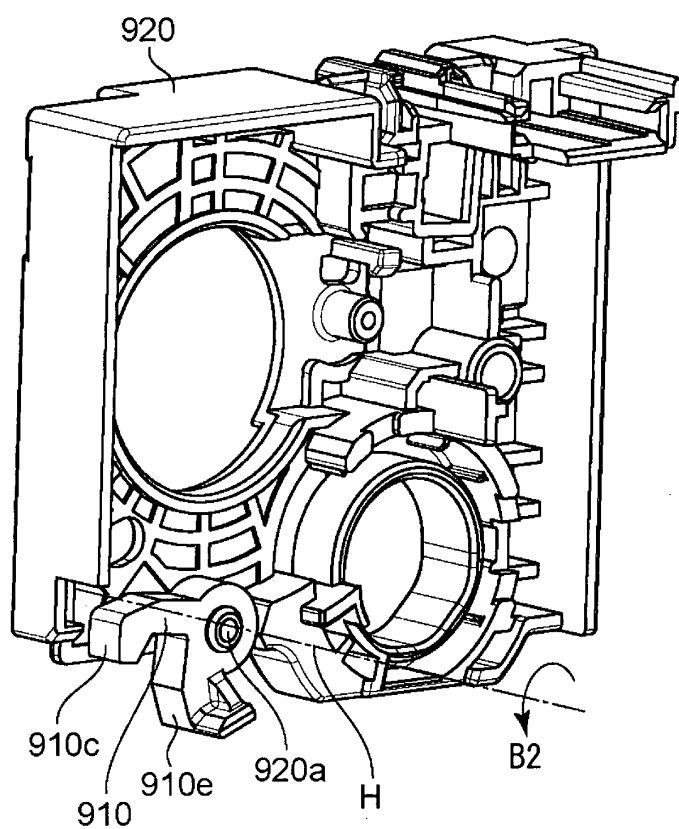


図146

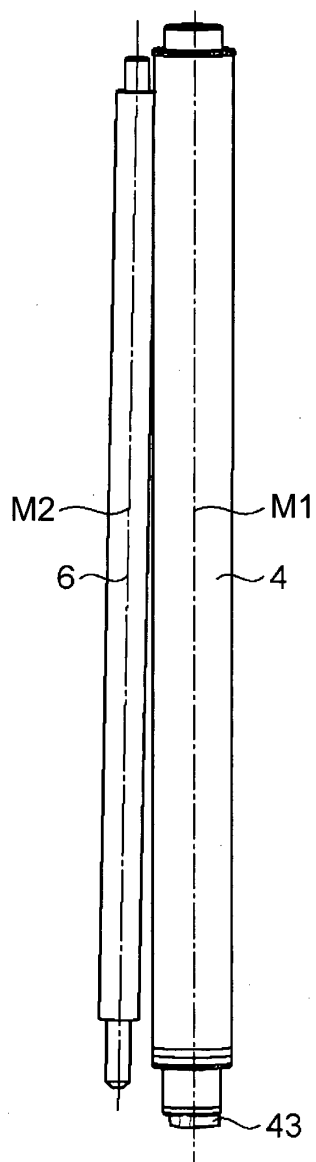


図147

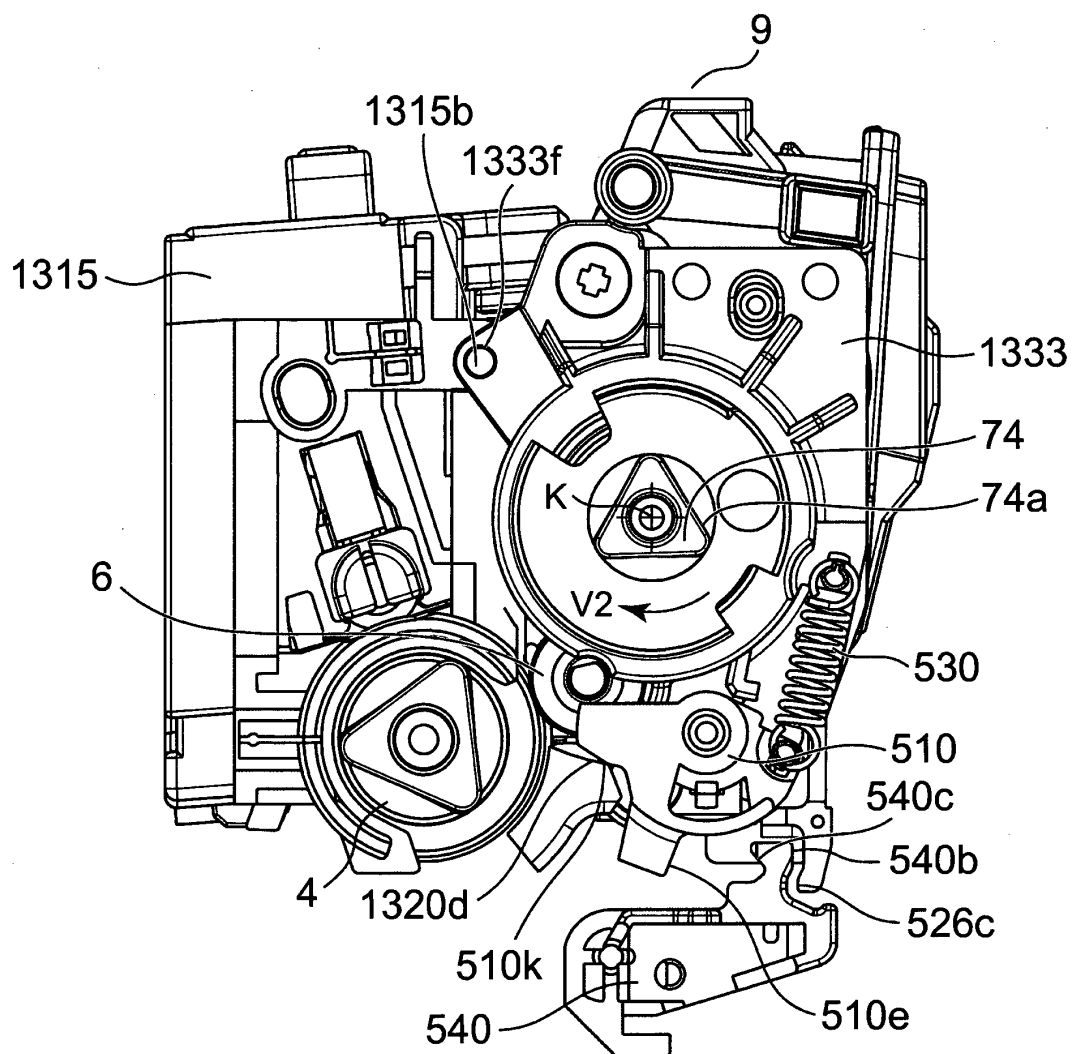


図 148

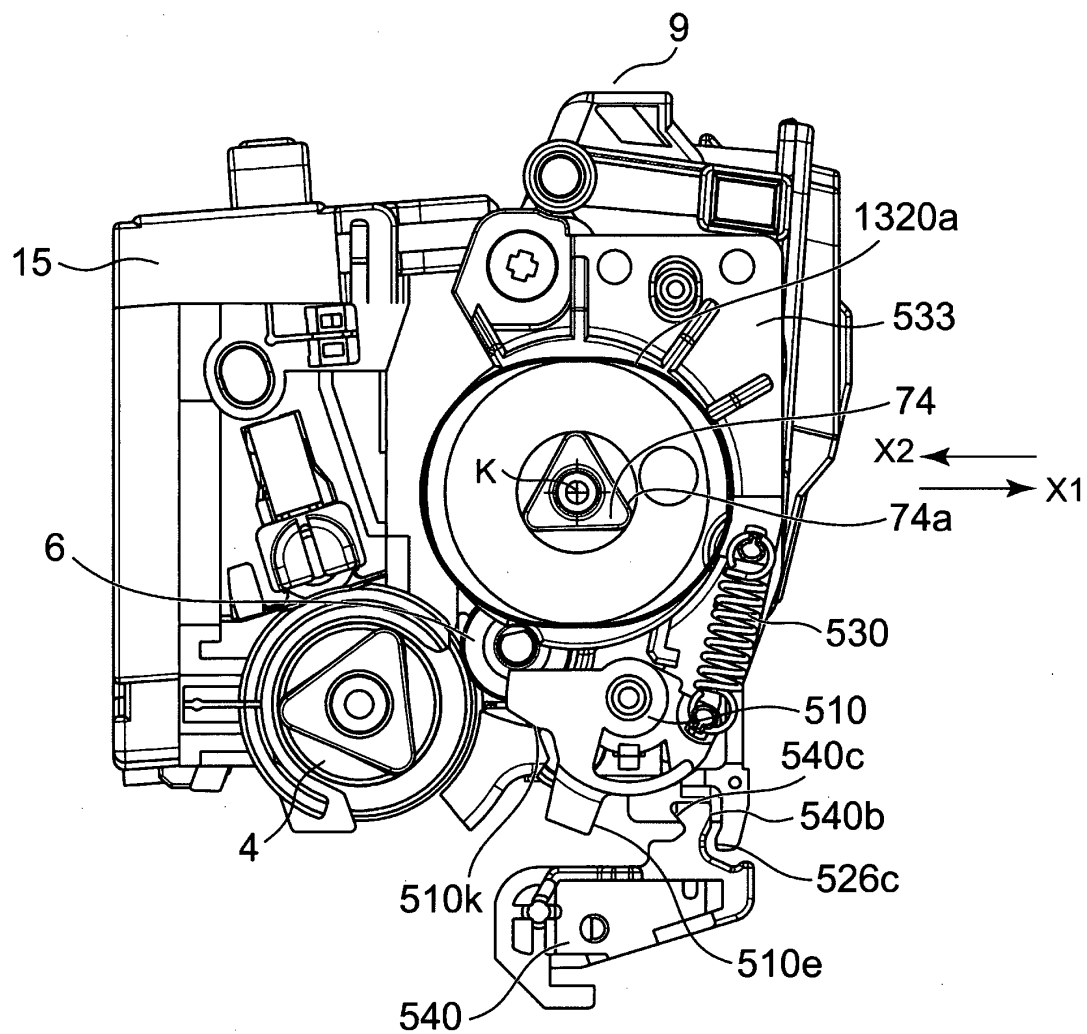


图 149

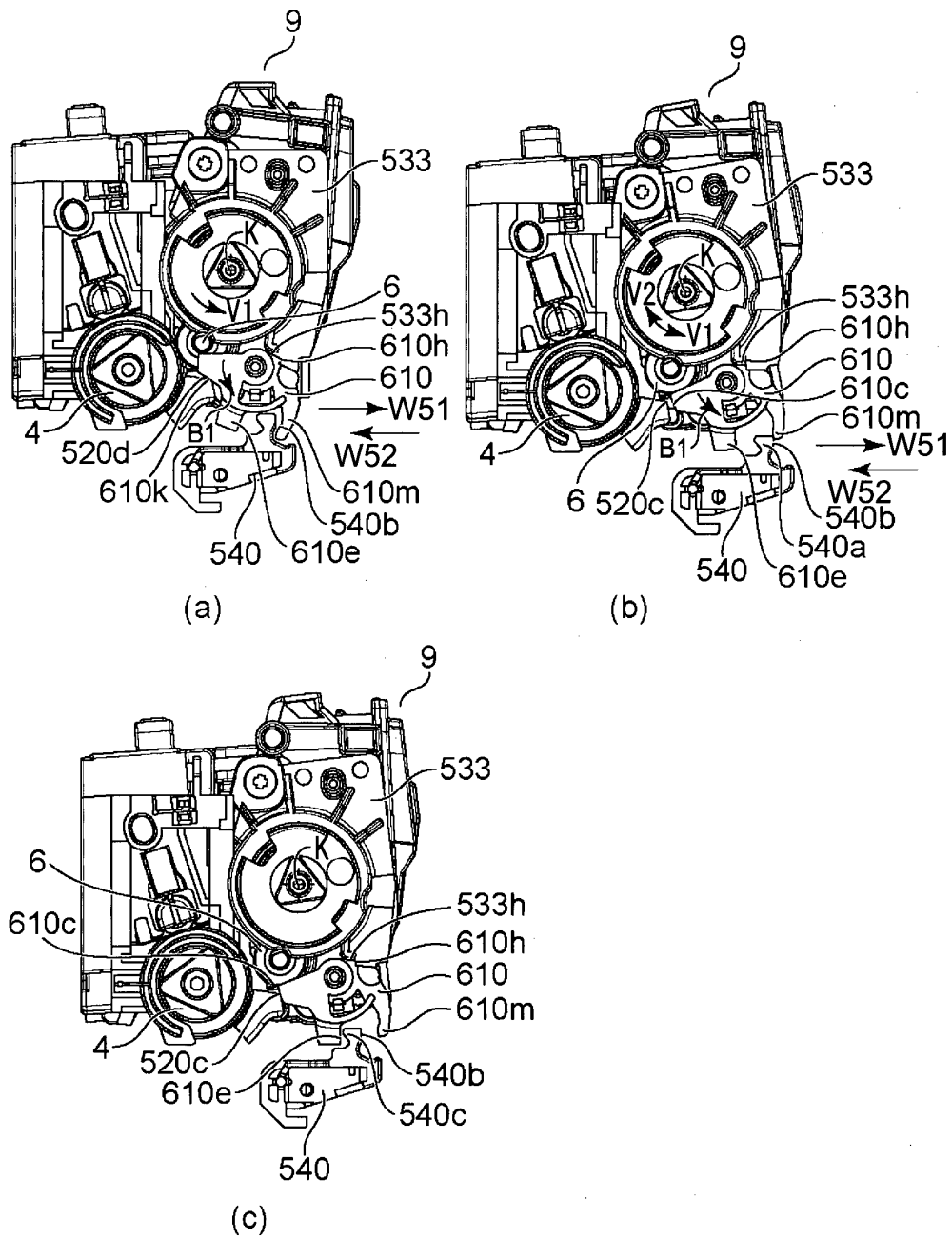


図 150

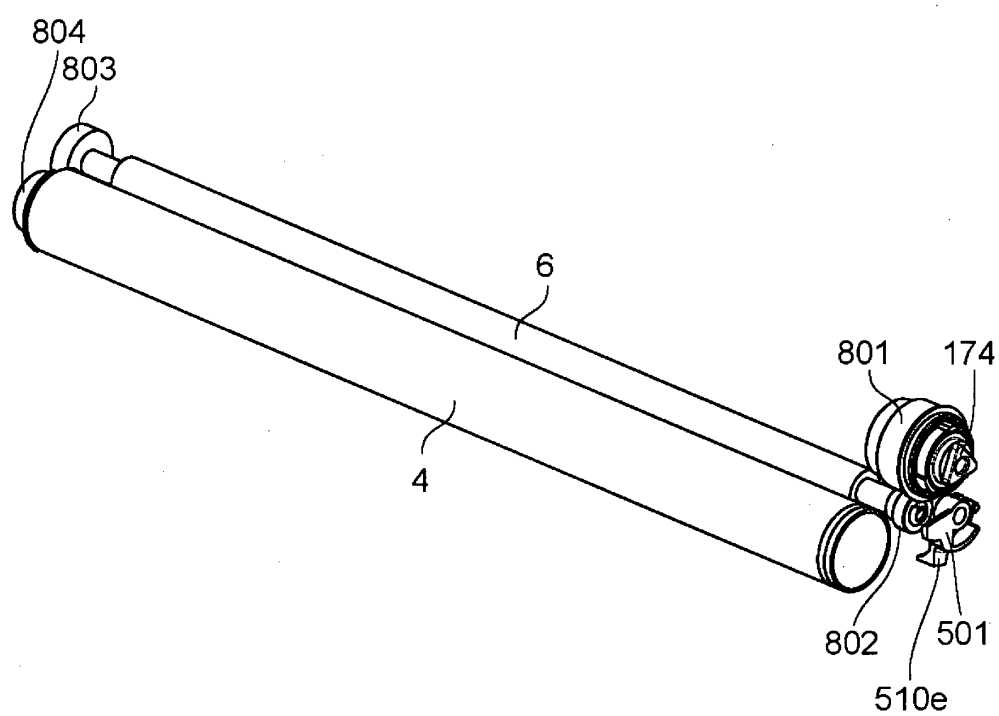


図151

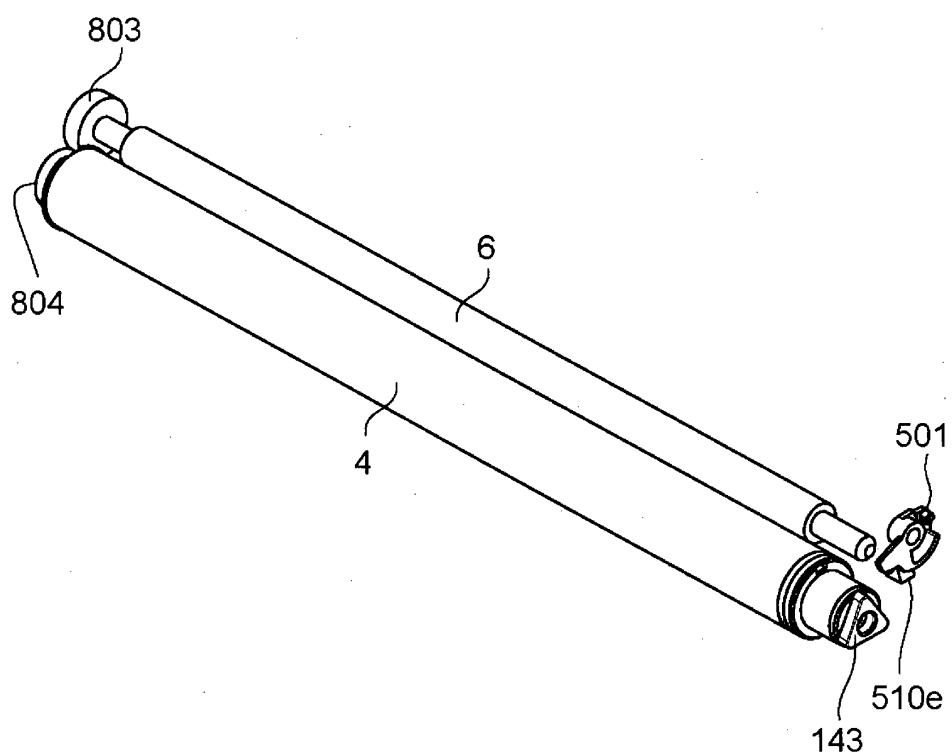
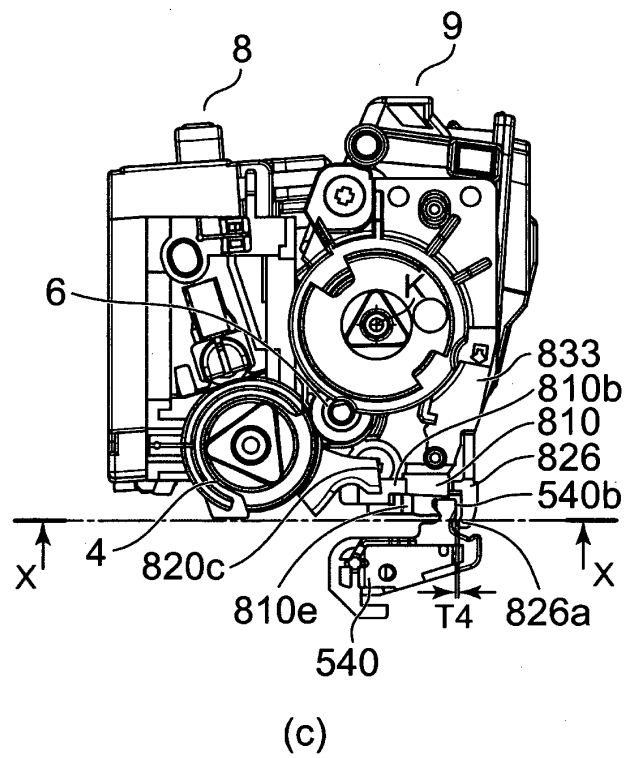
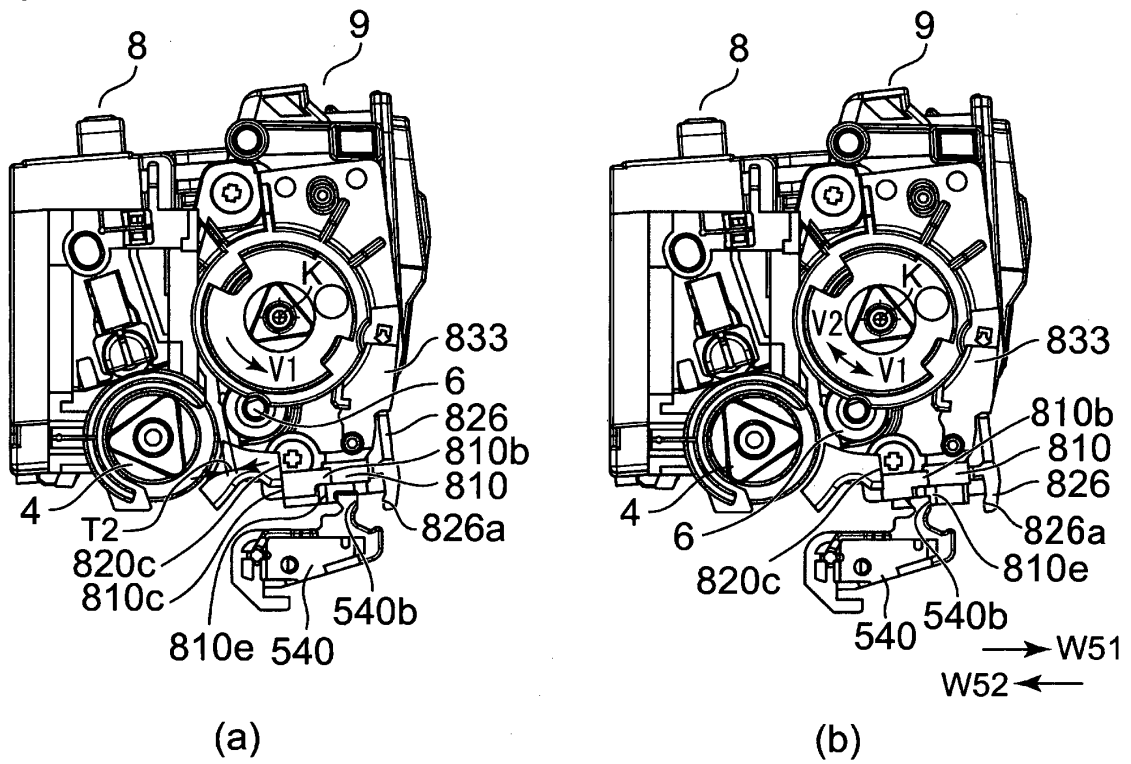


図152



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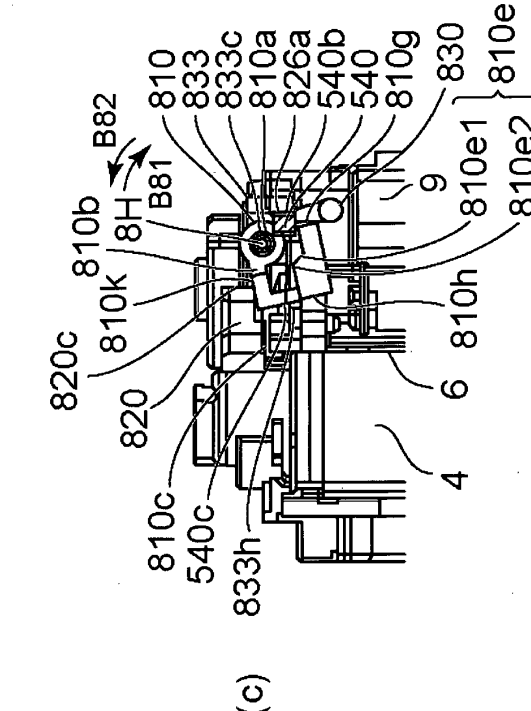
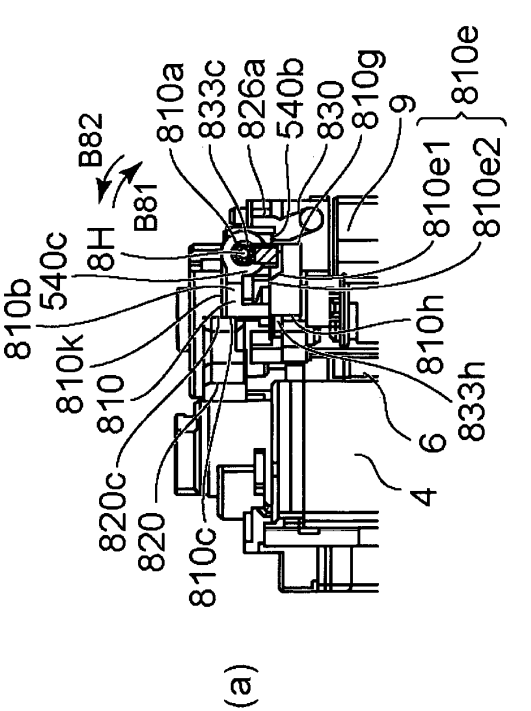
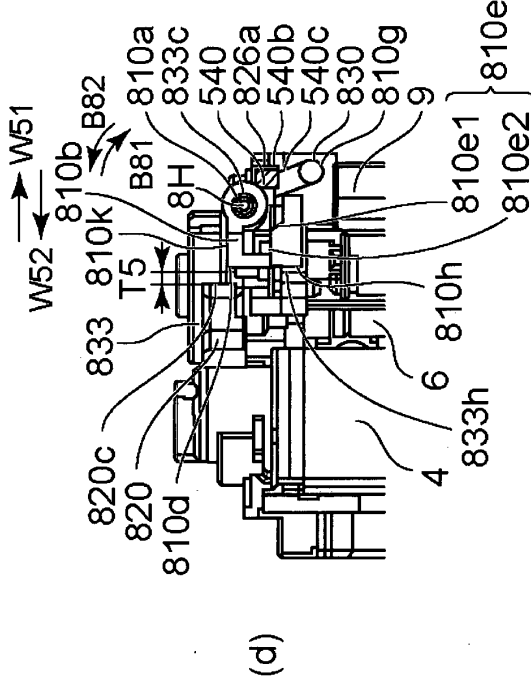
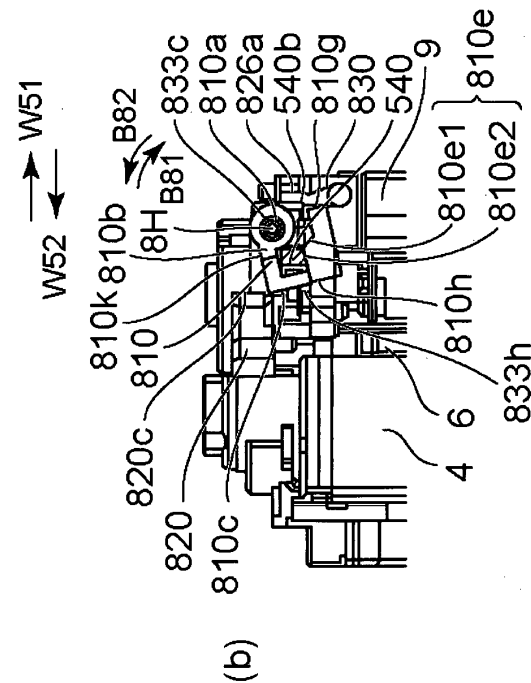


図154

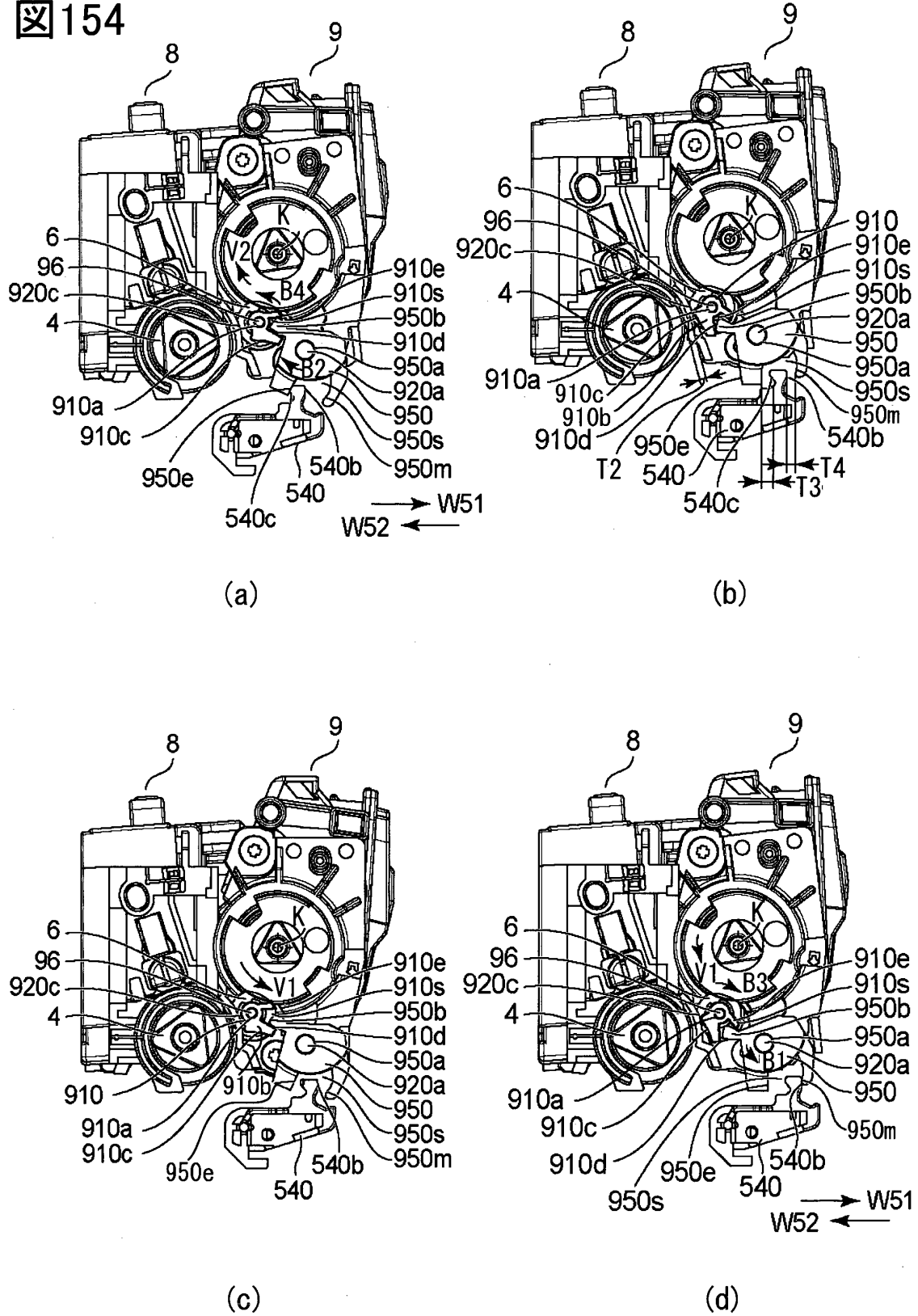


図155

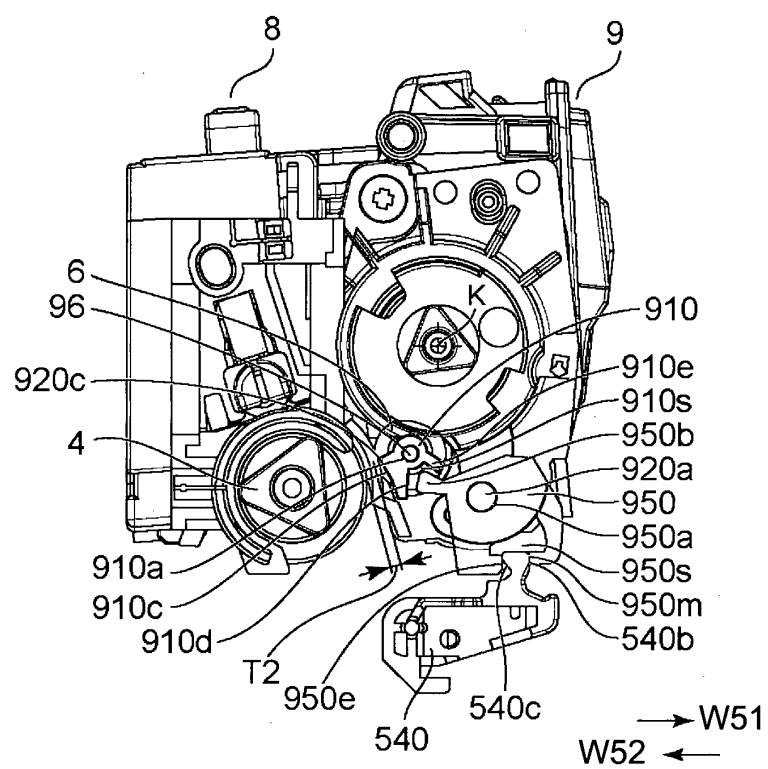
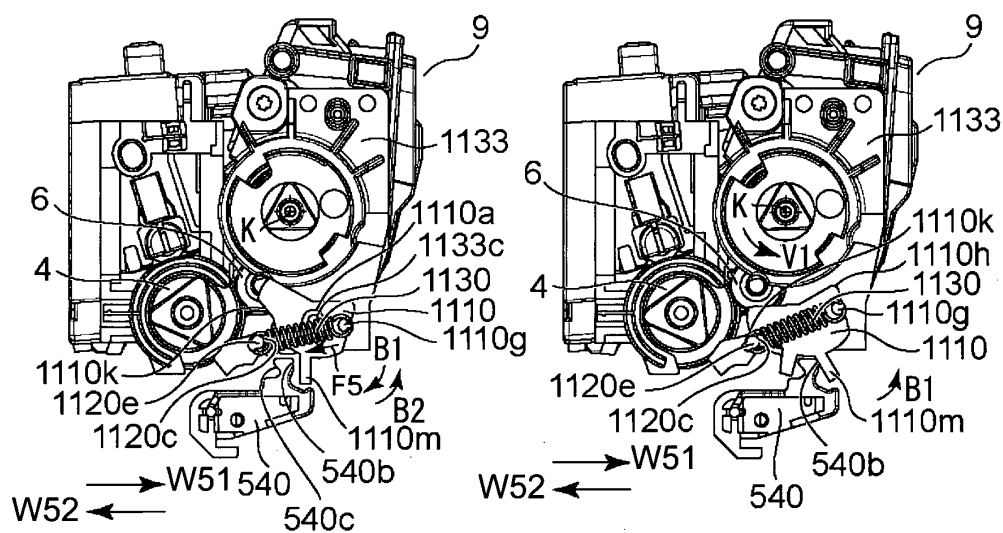
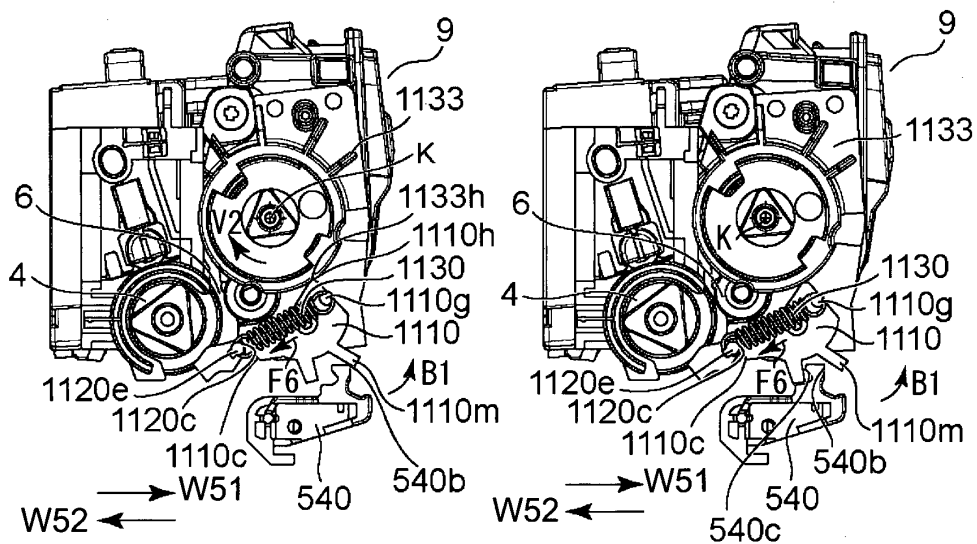


図156



(a)

(b)



(c)

(d)

図157

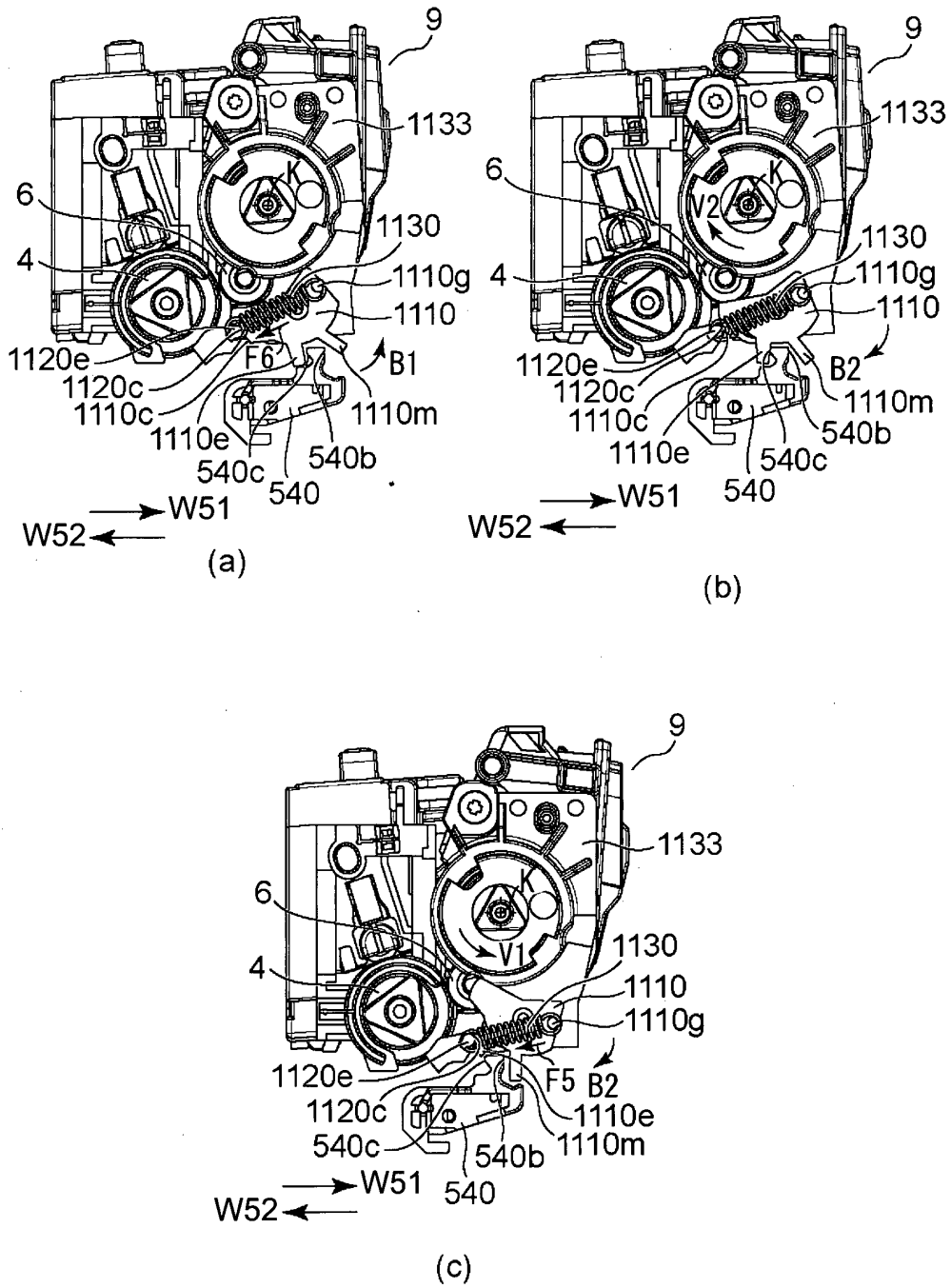


図158

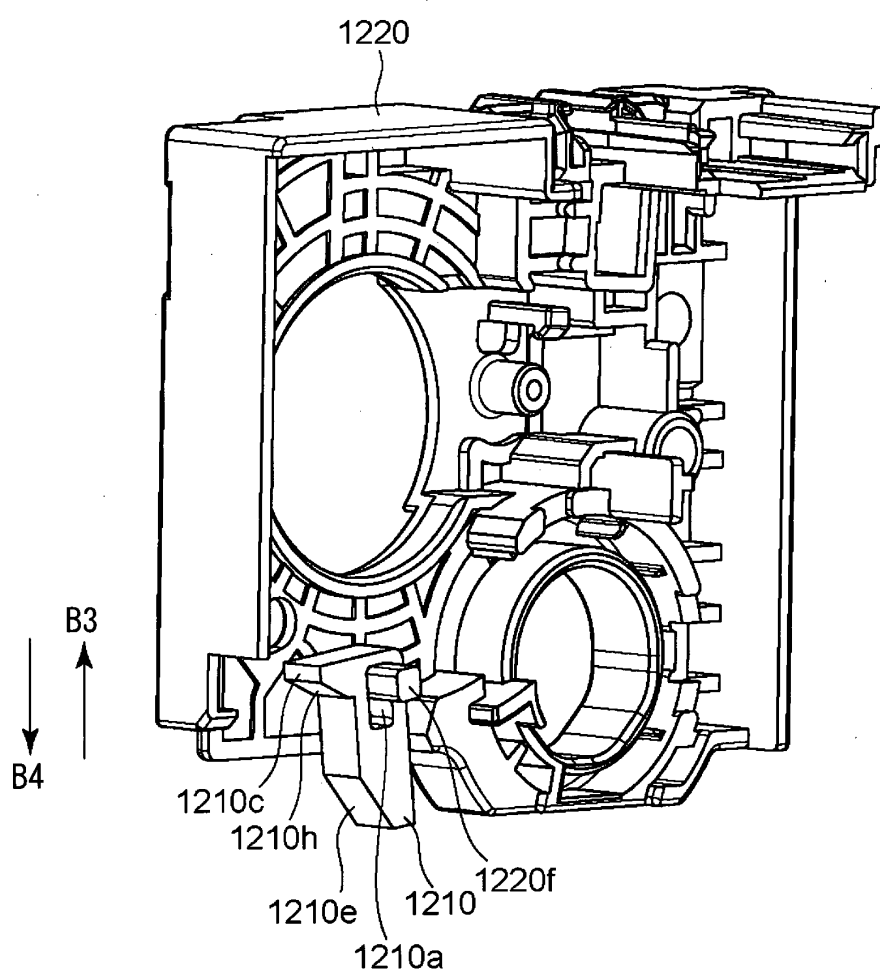


図159

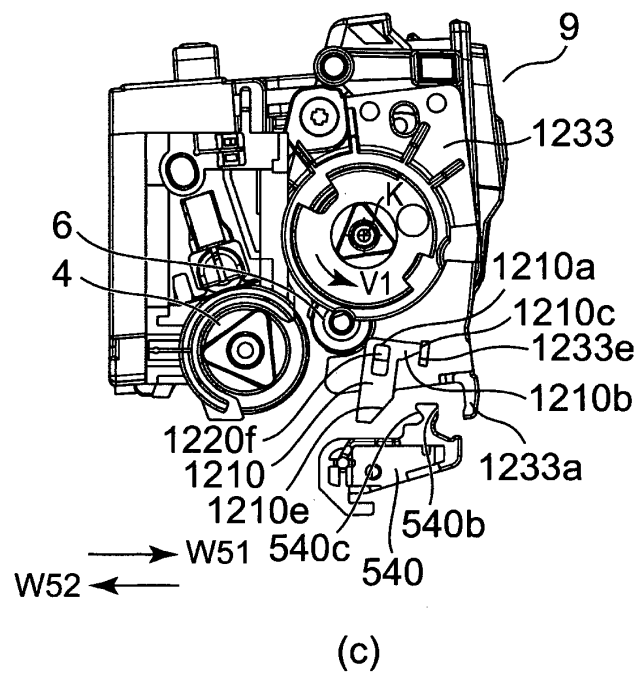
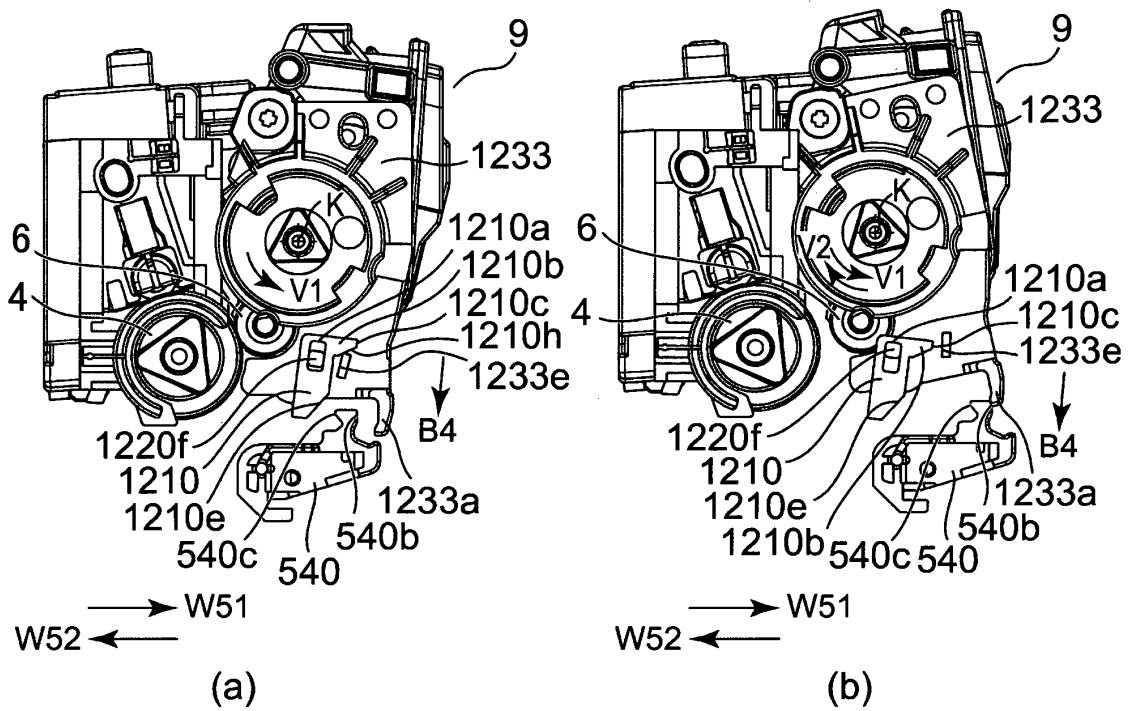


図160

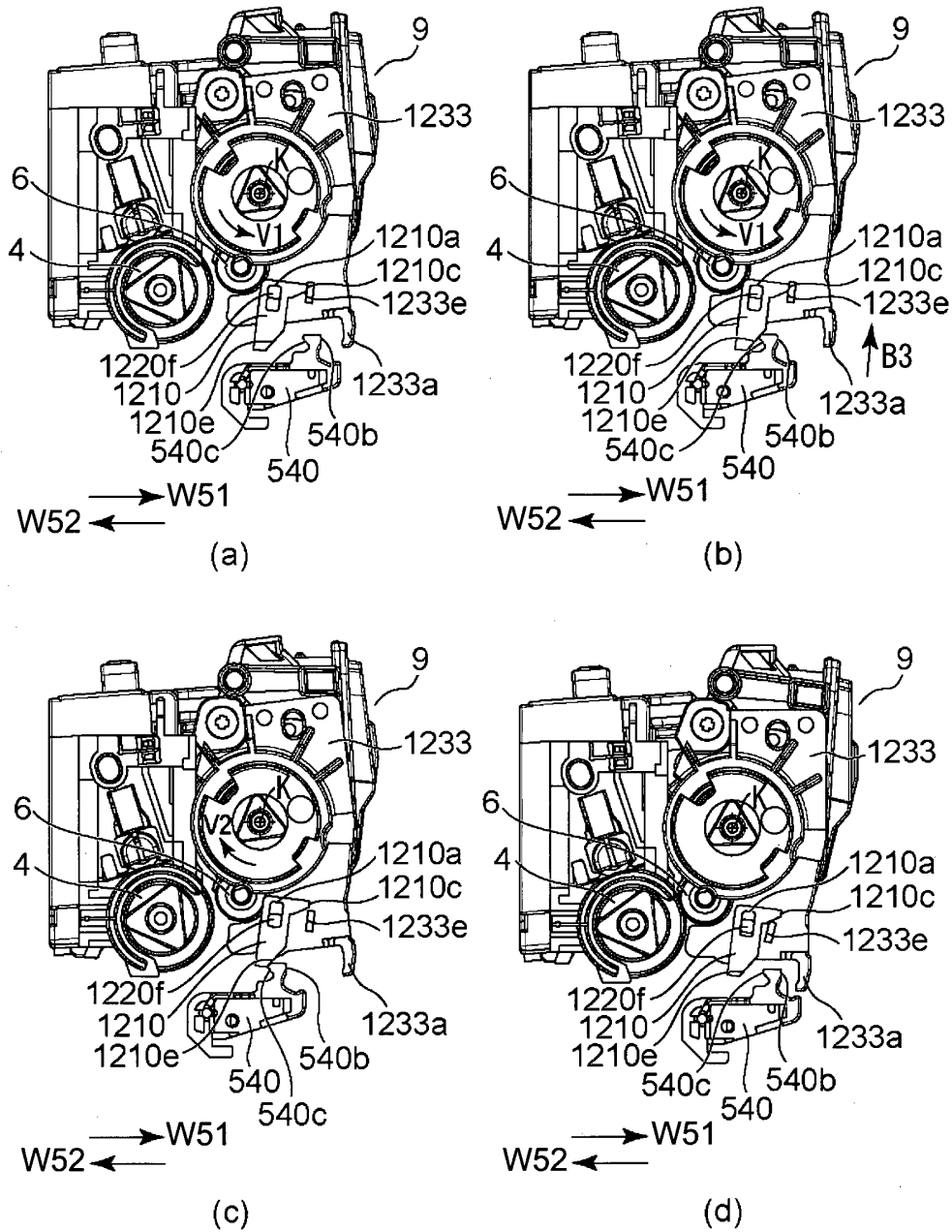
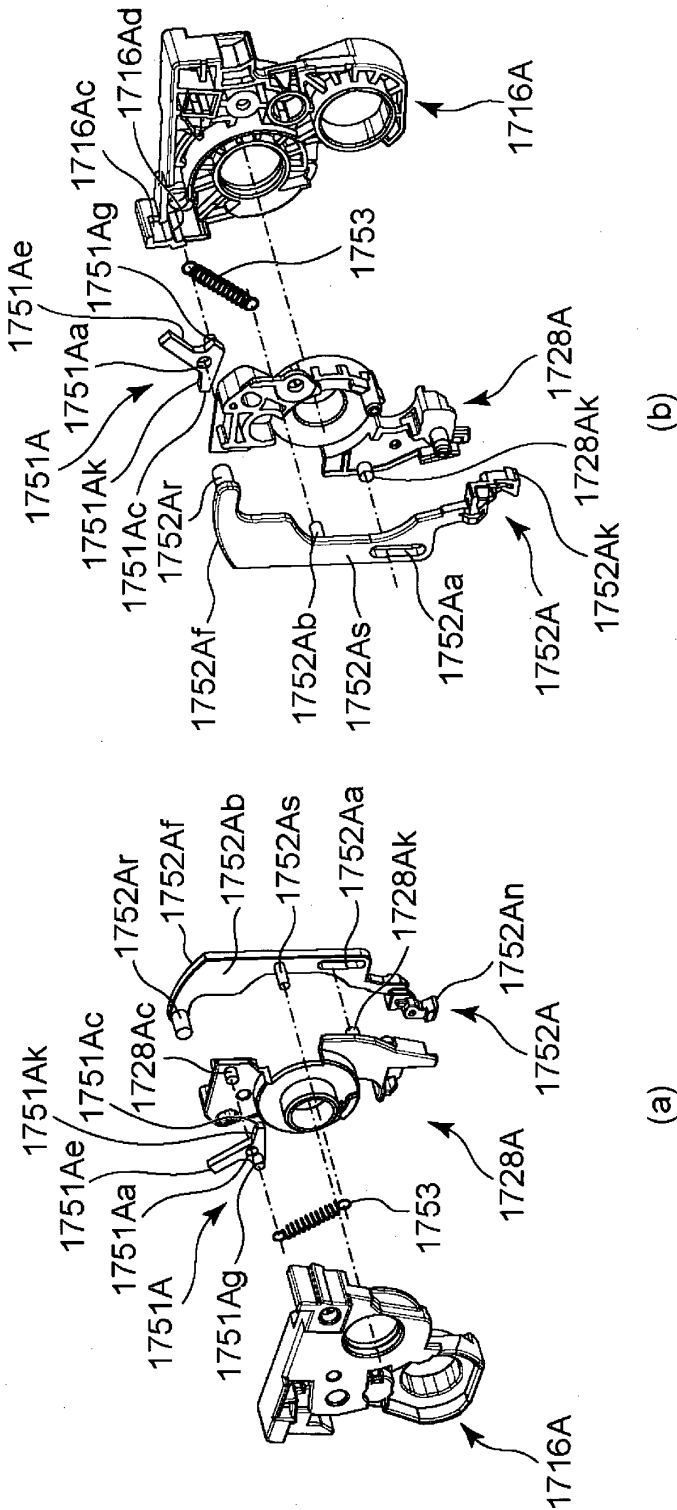


図161



162

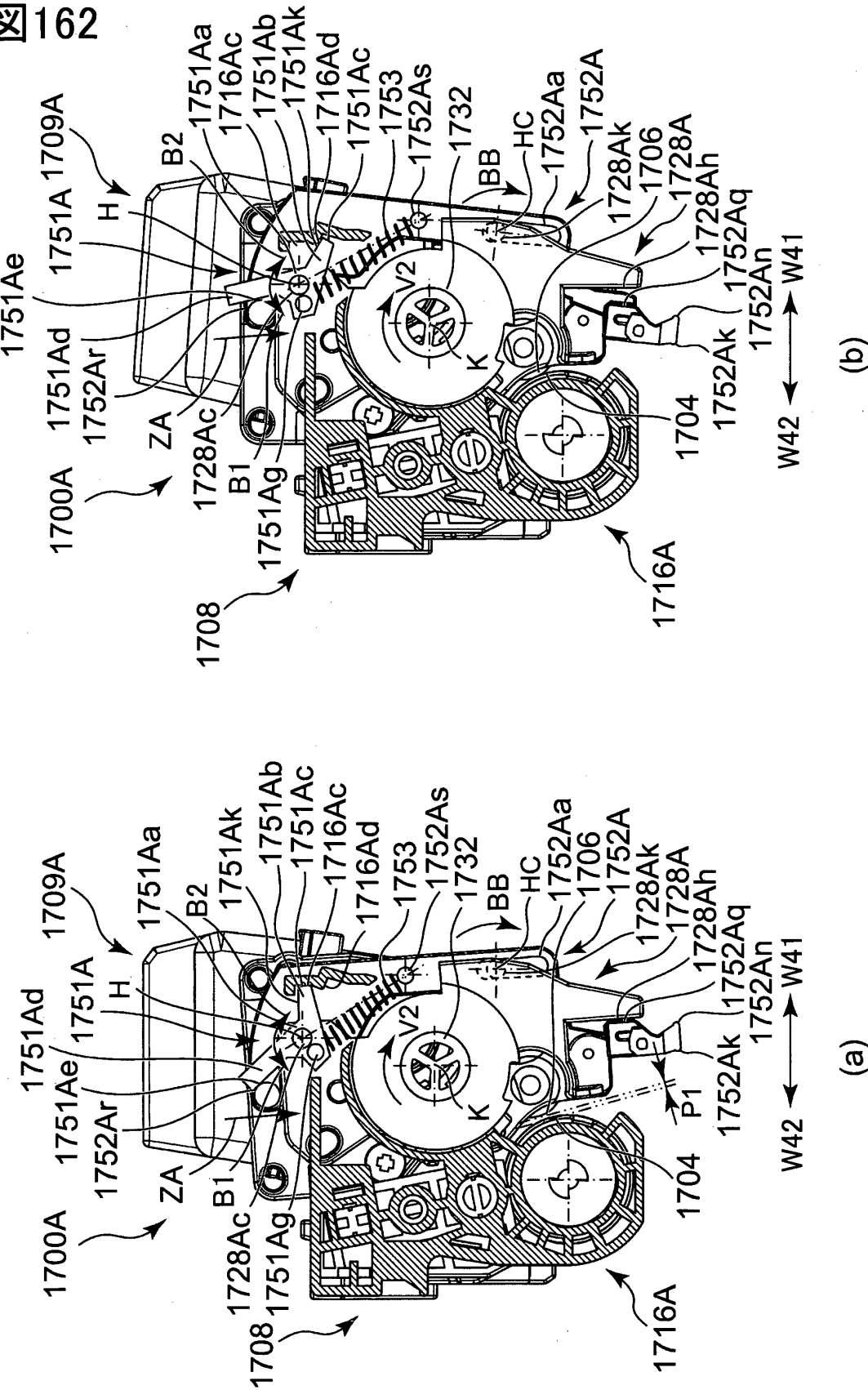


图 163

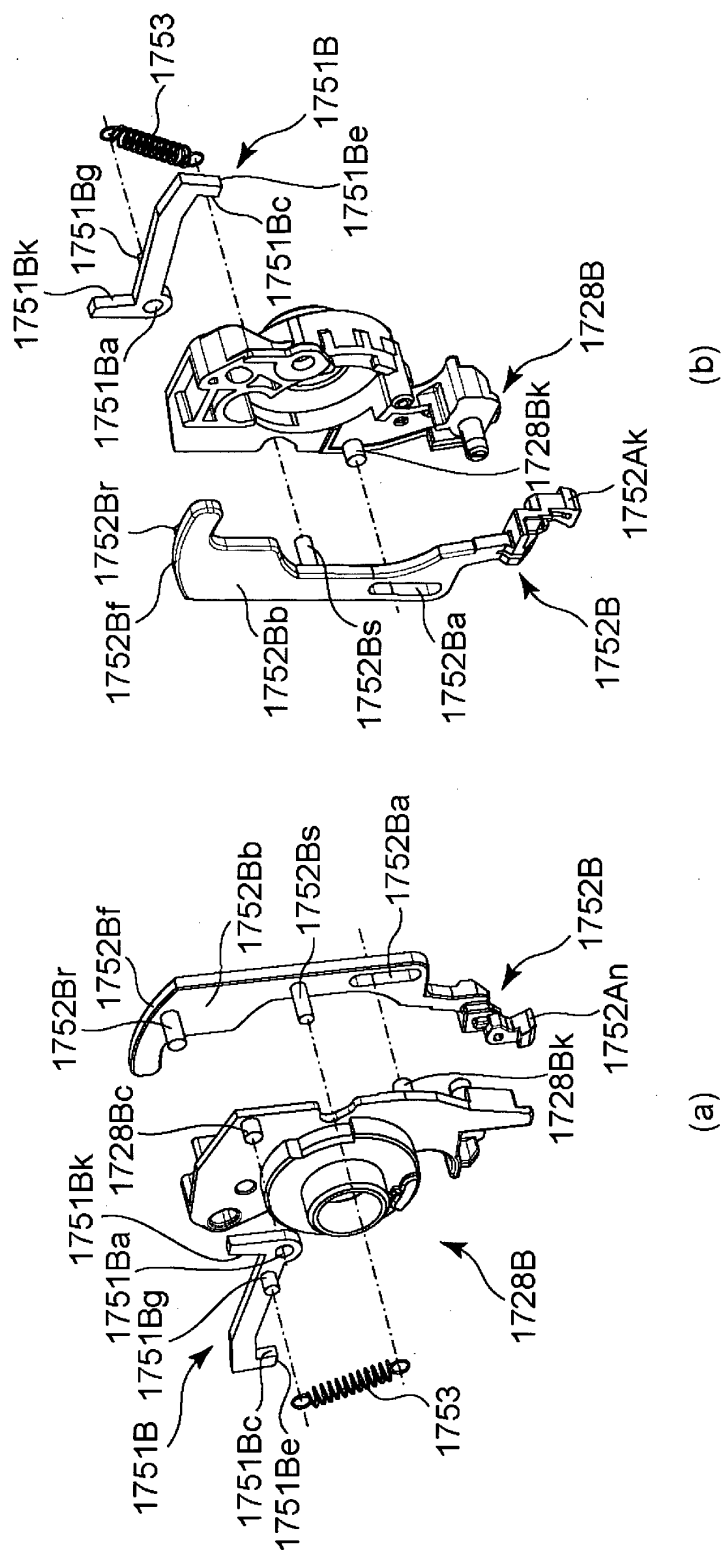


図165

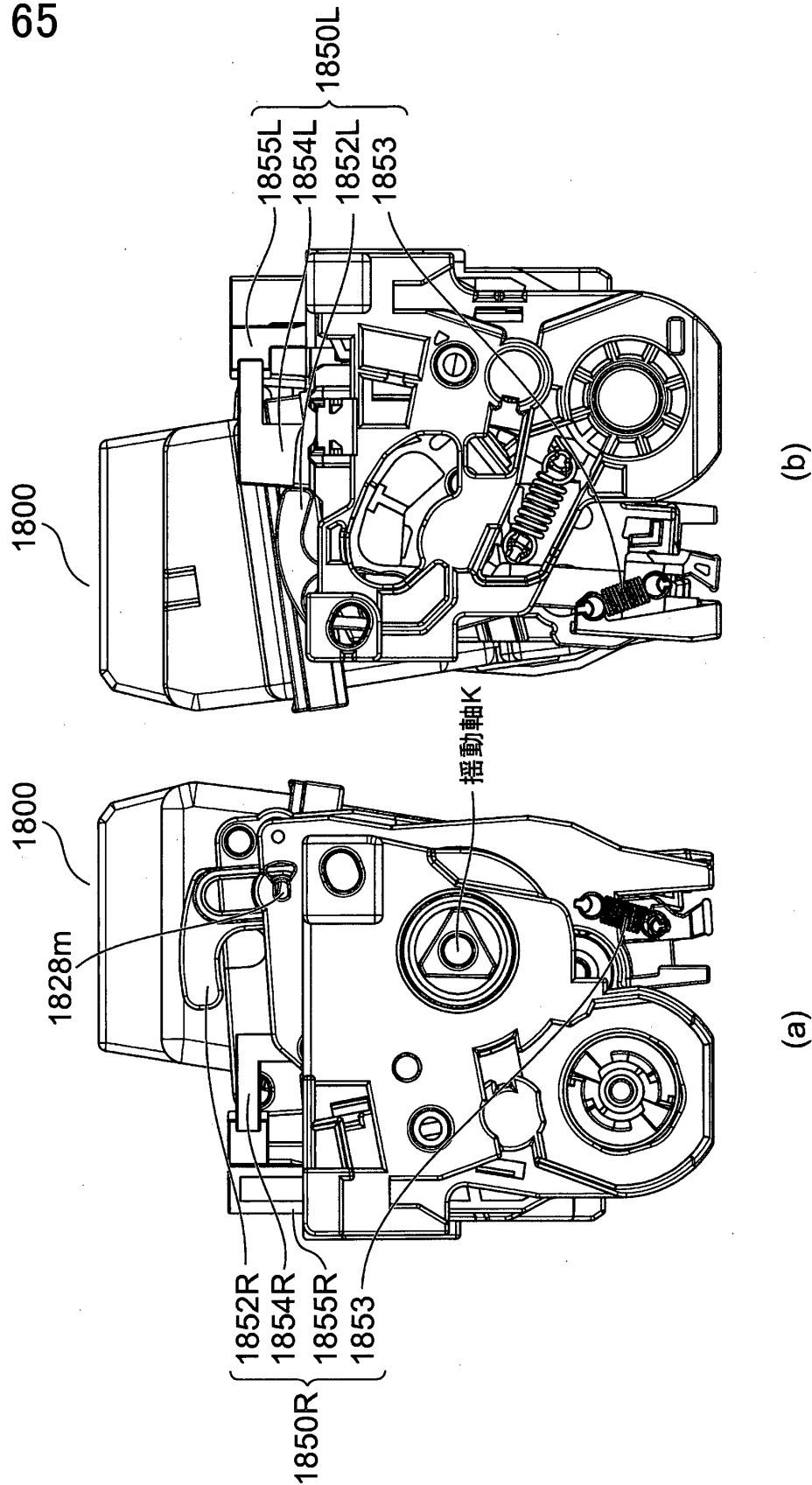


図166

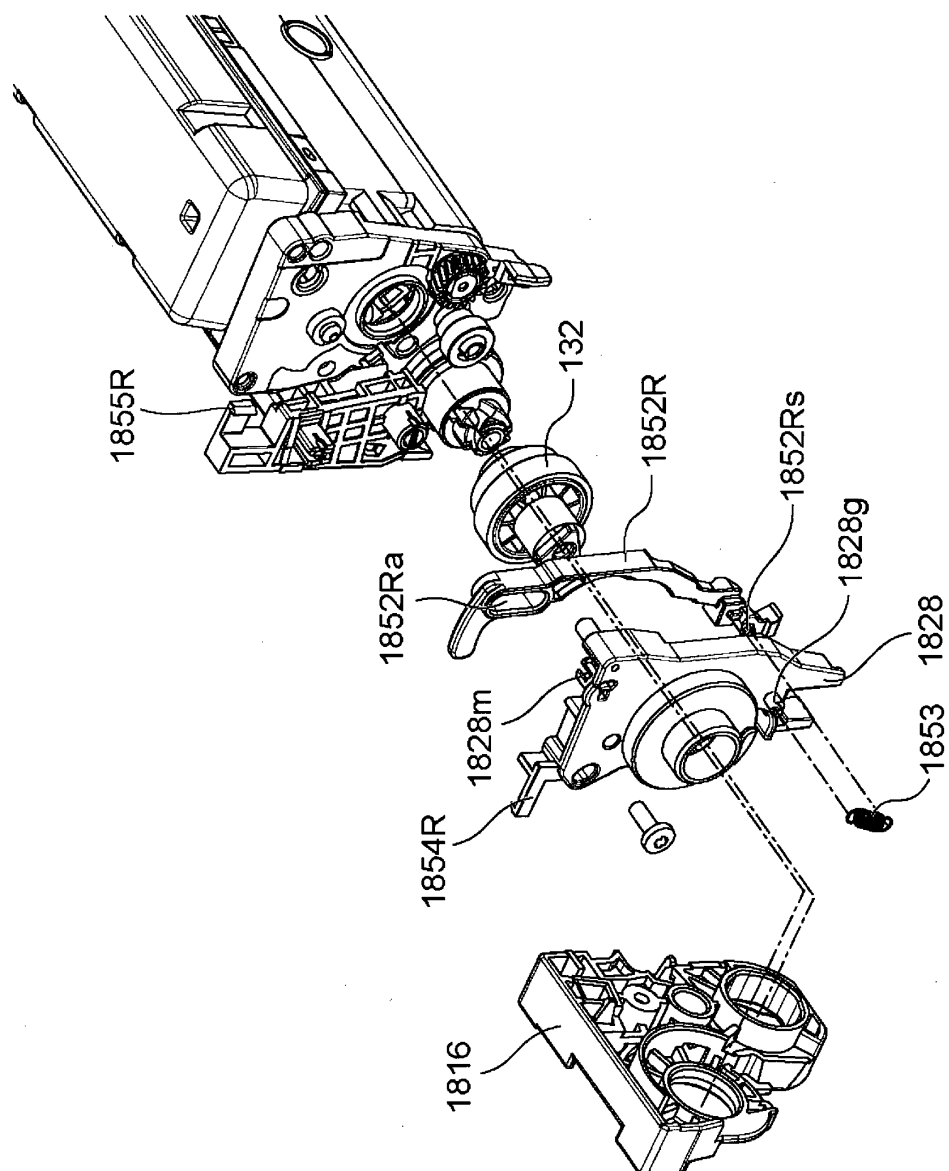


図167

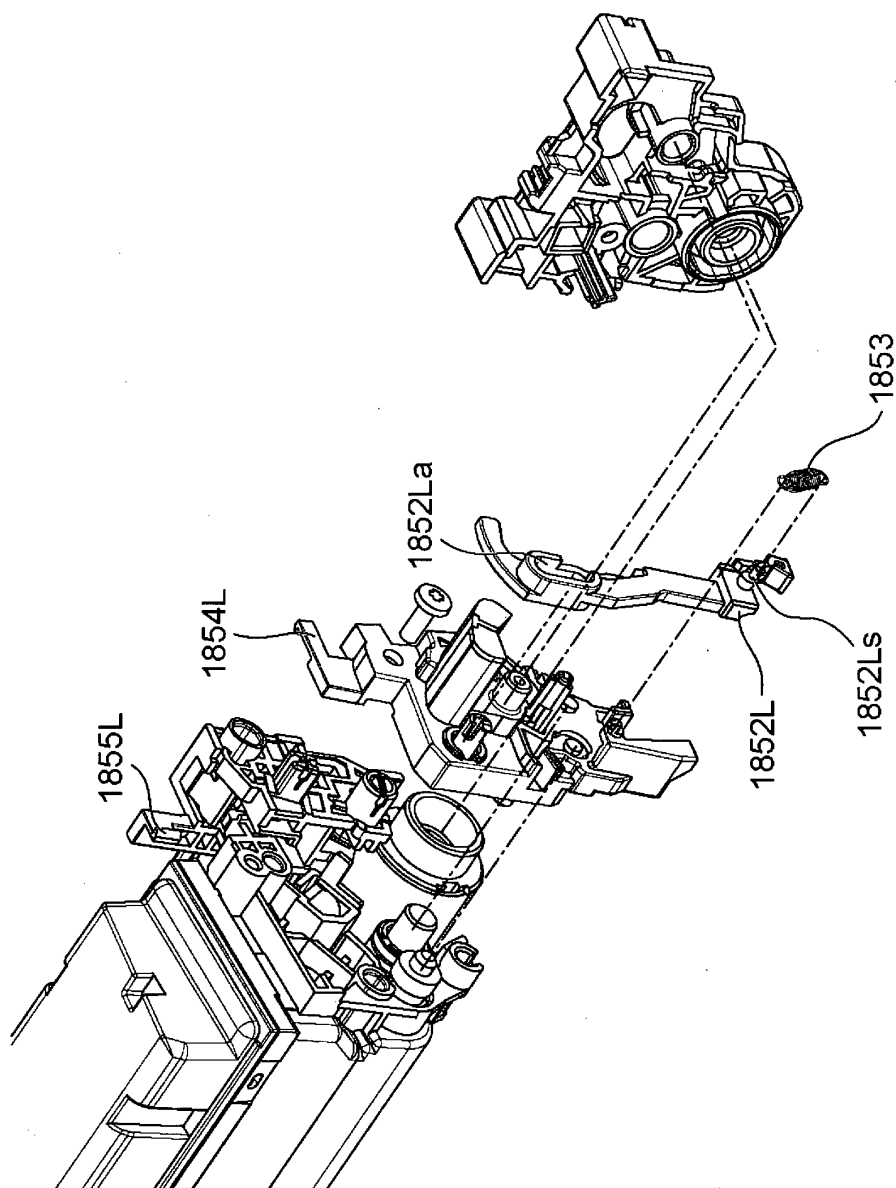


図 168

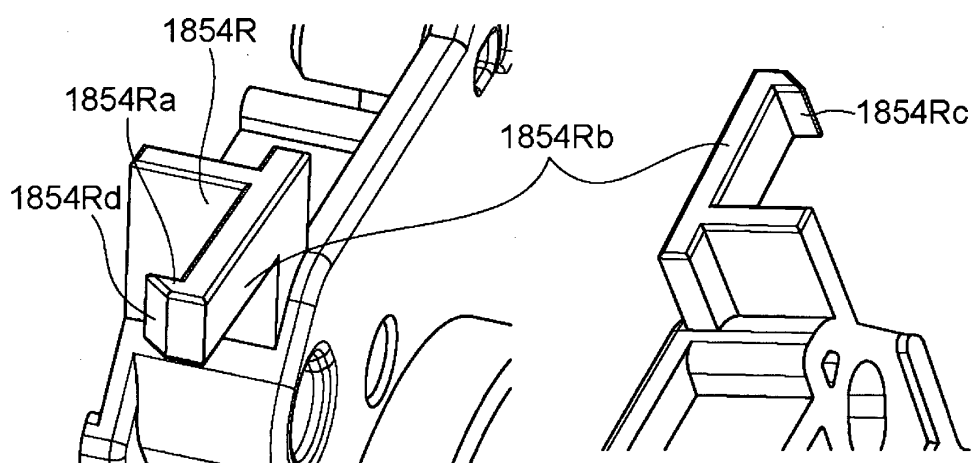


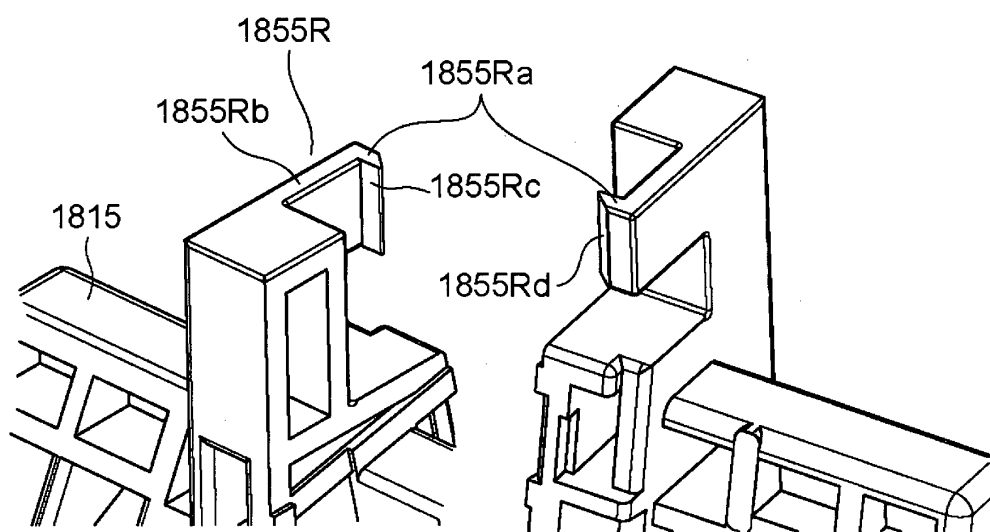
図 169

図170

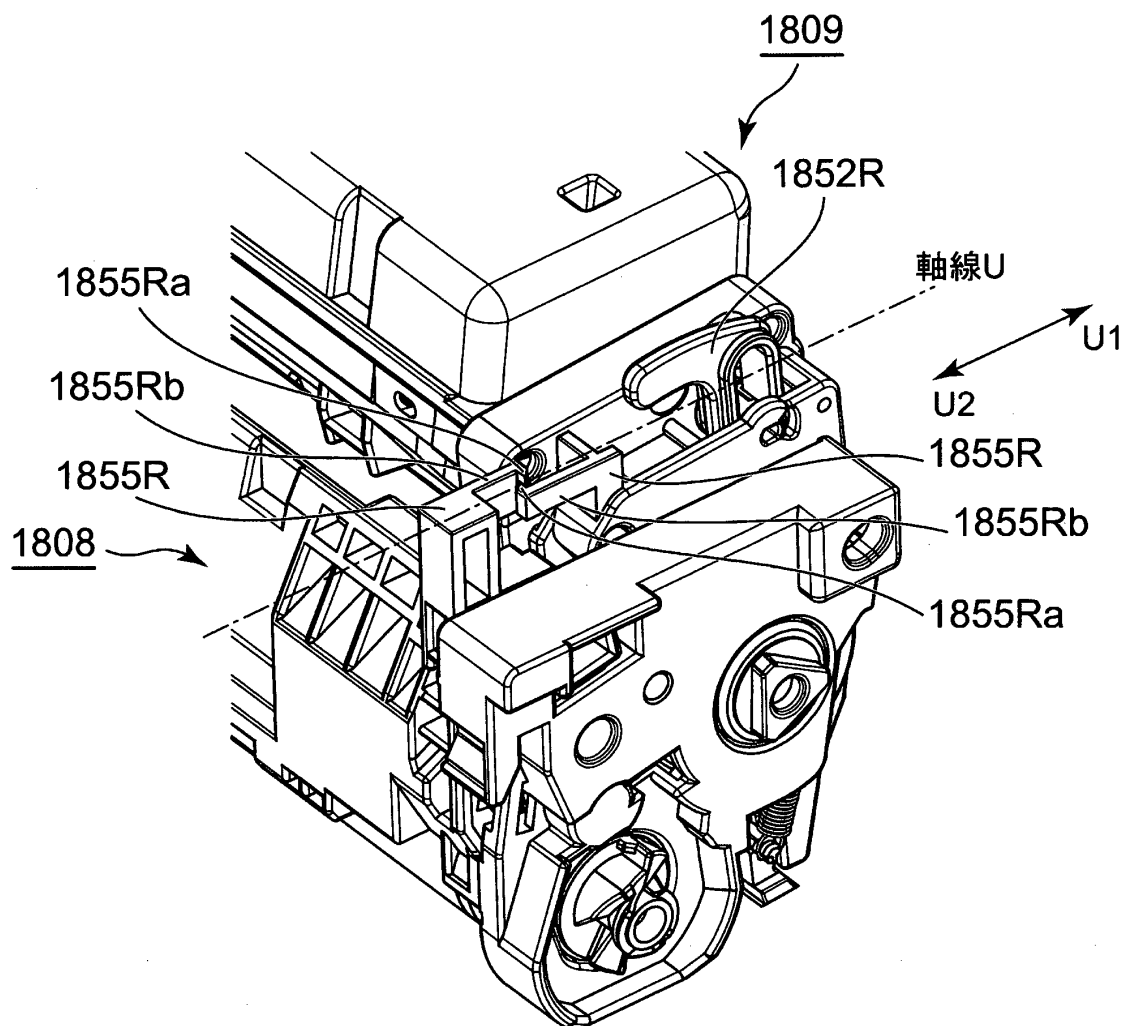


図171

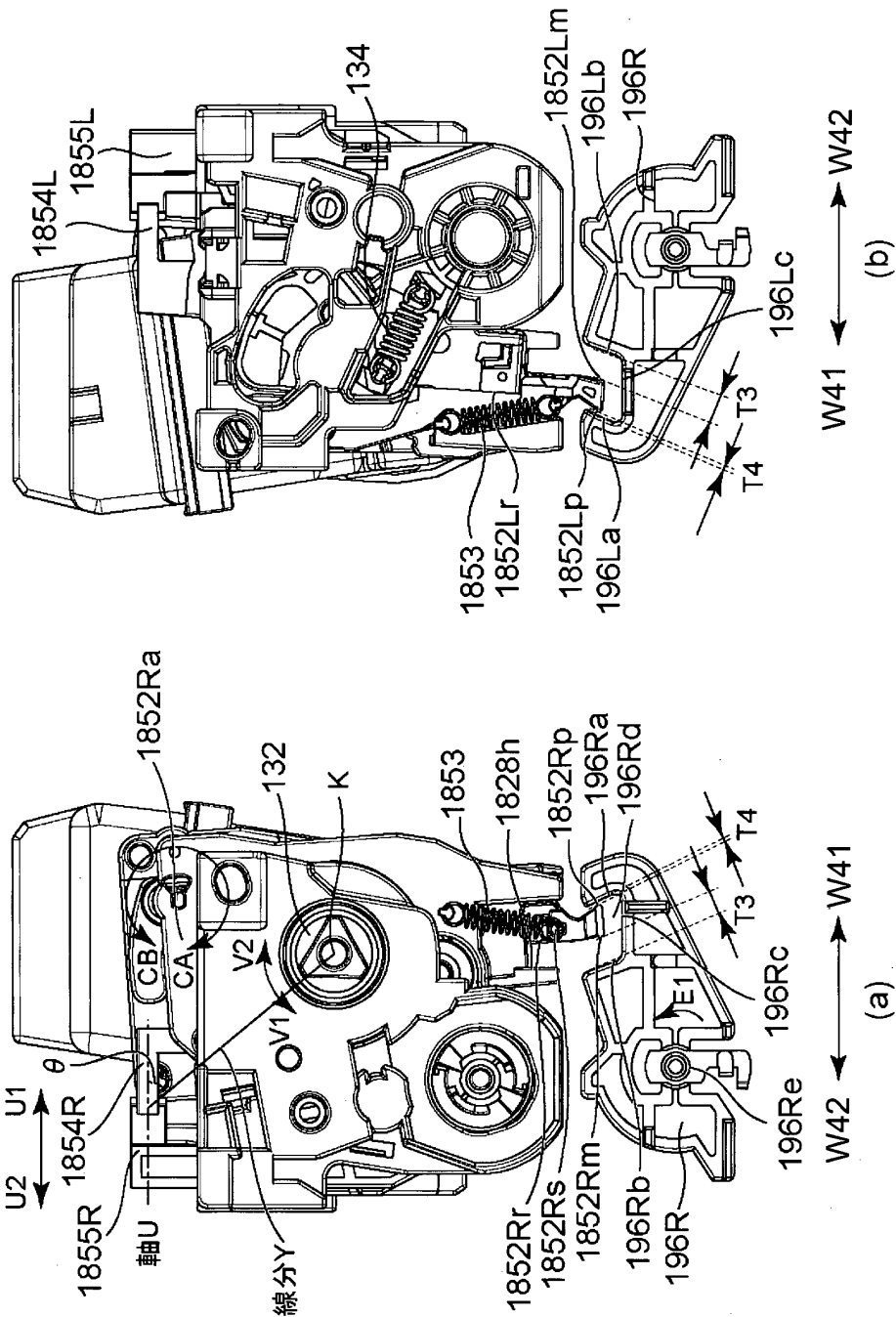


図172

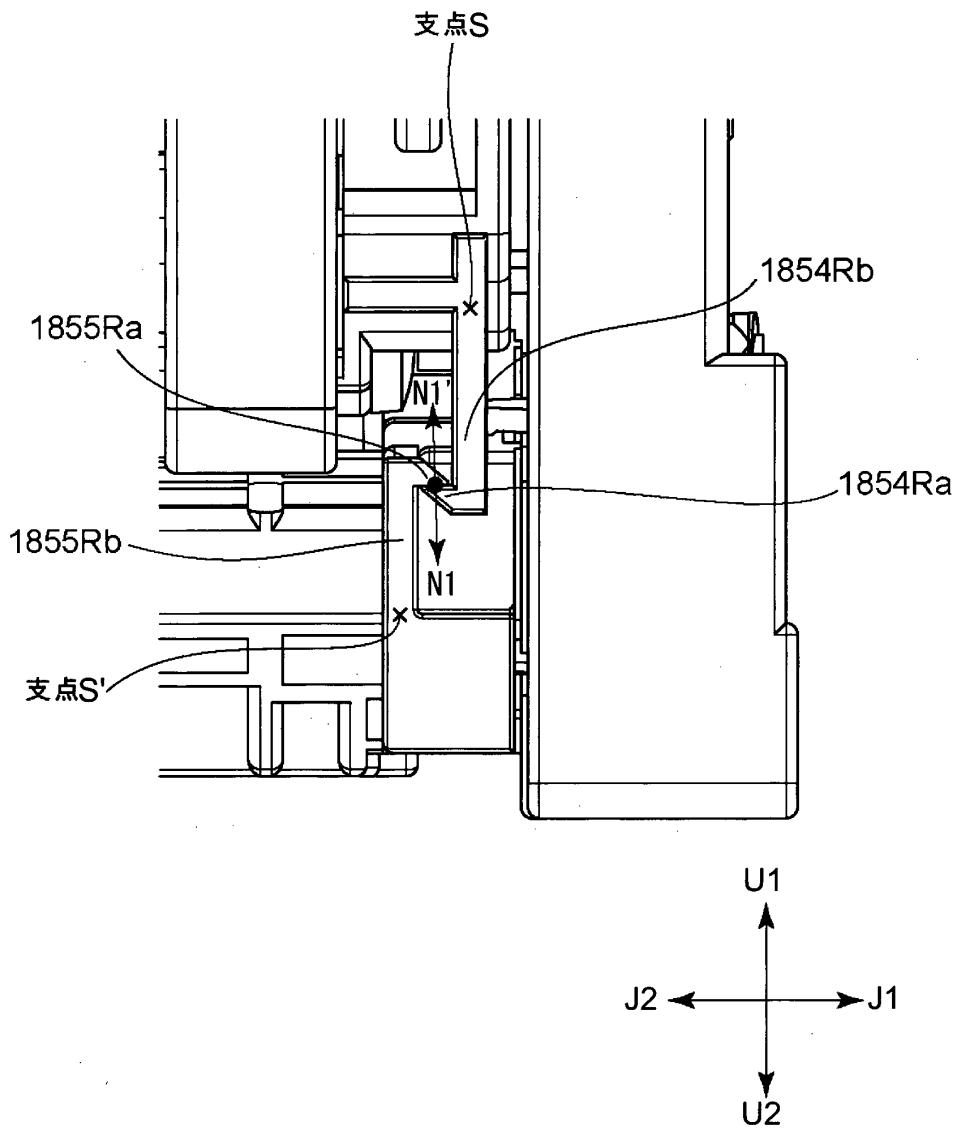


図173

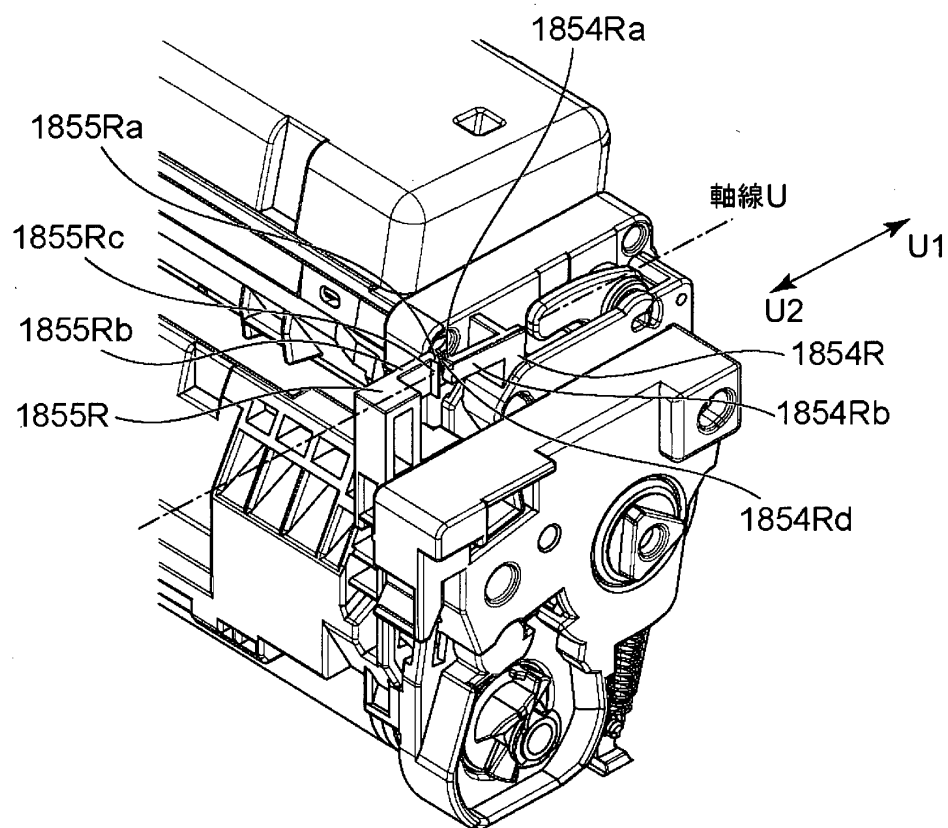


図174

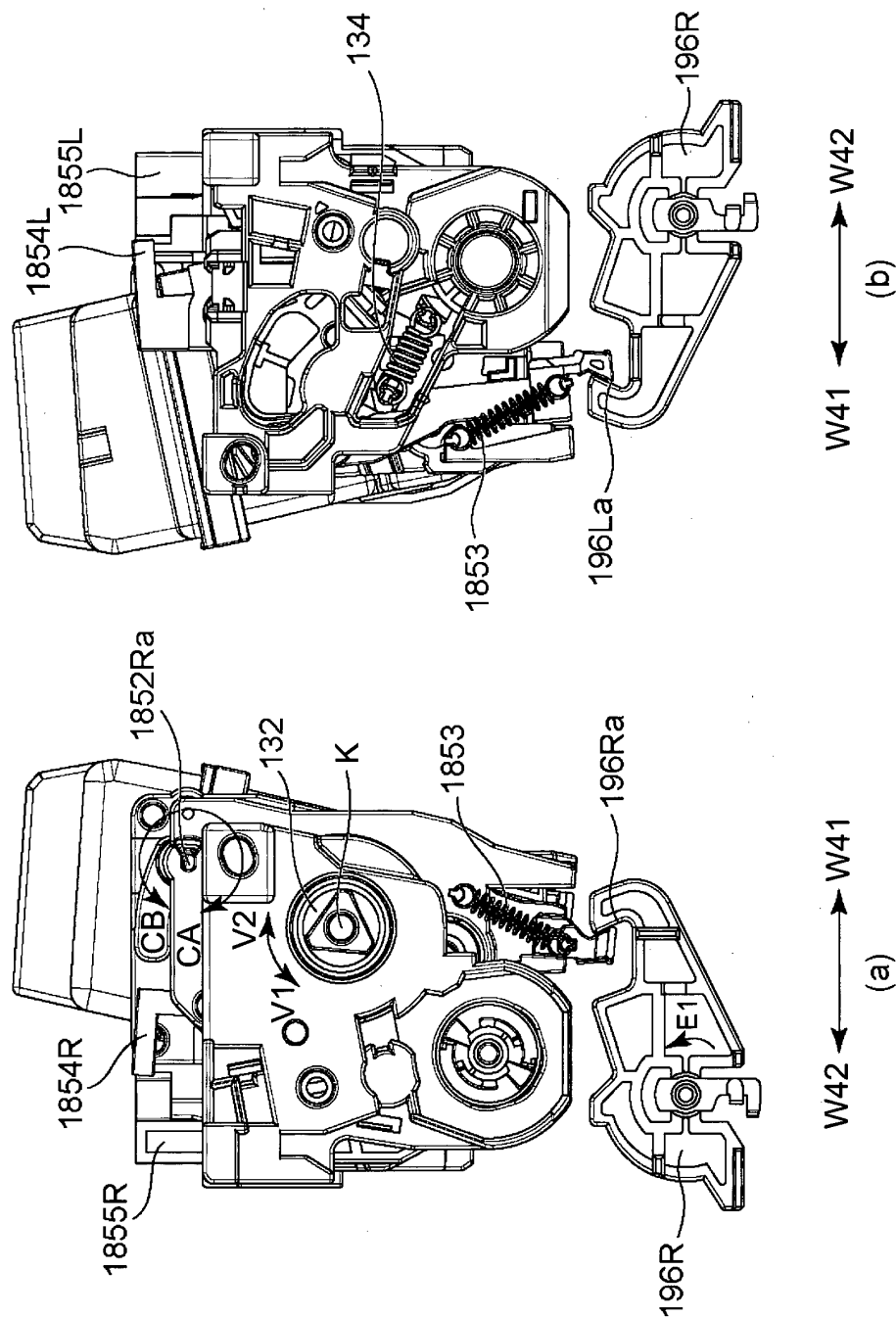


図176

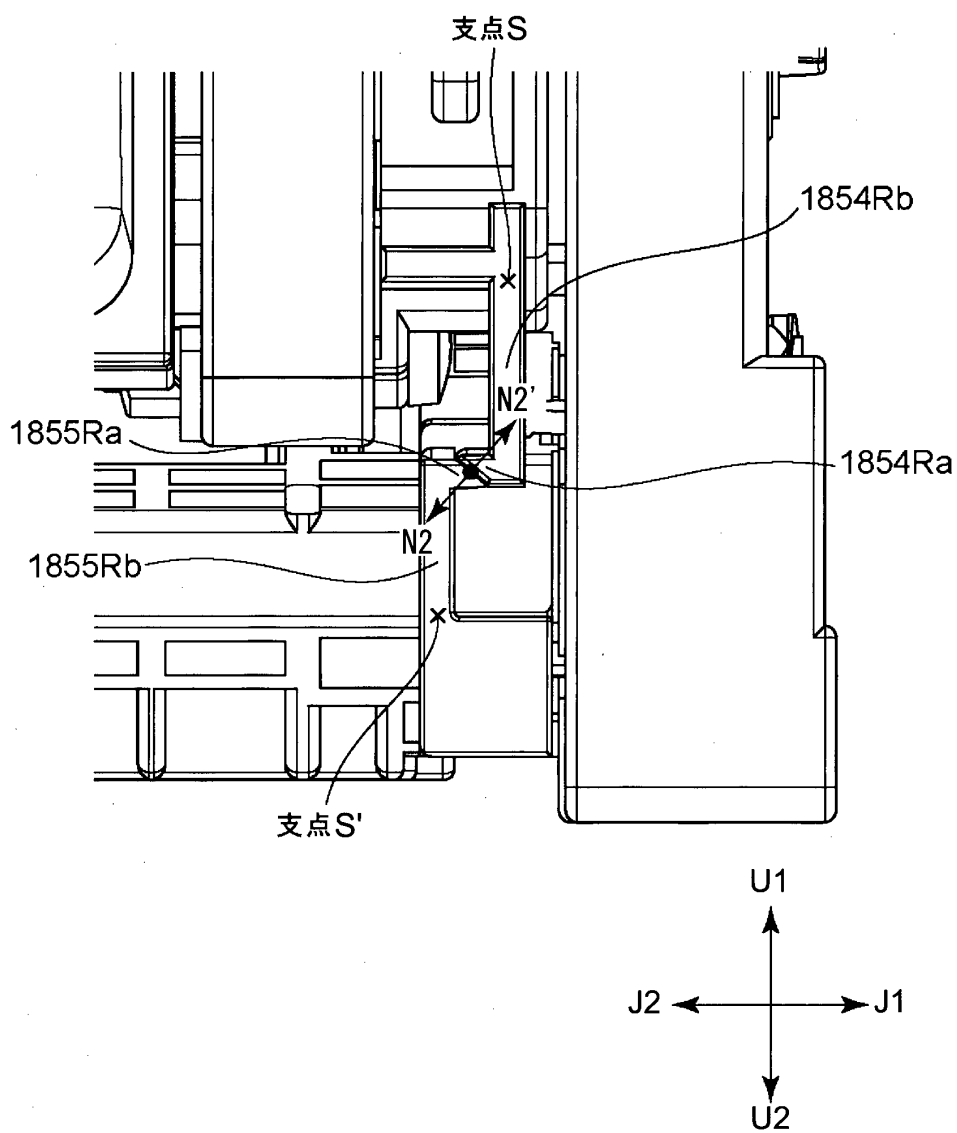


図177

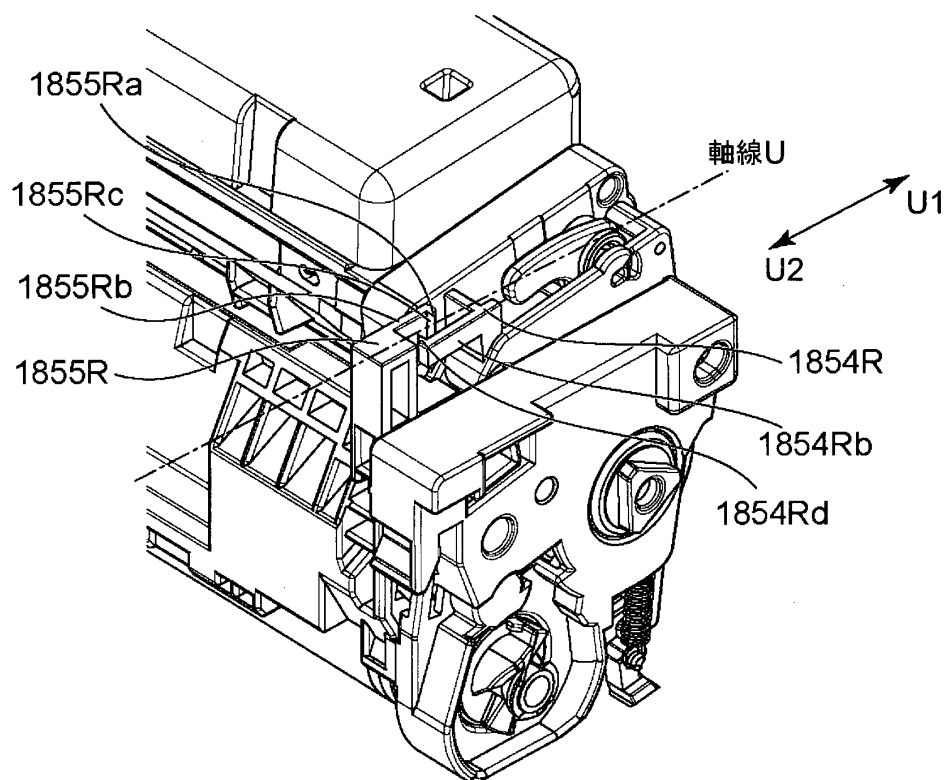


図178

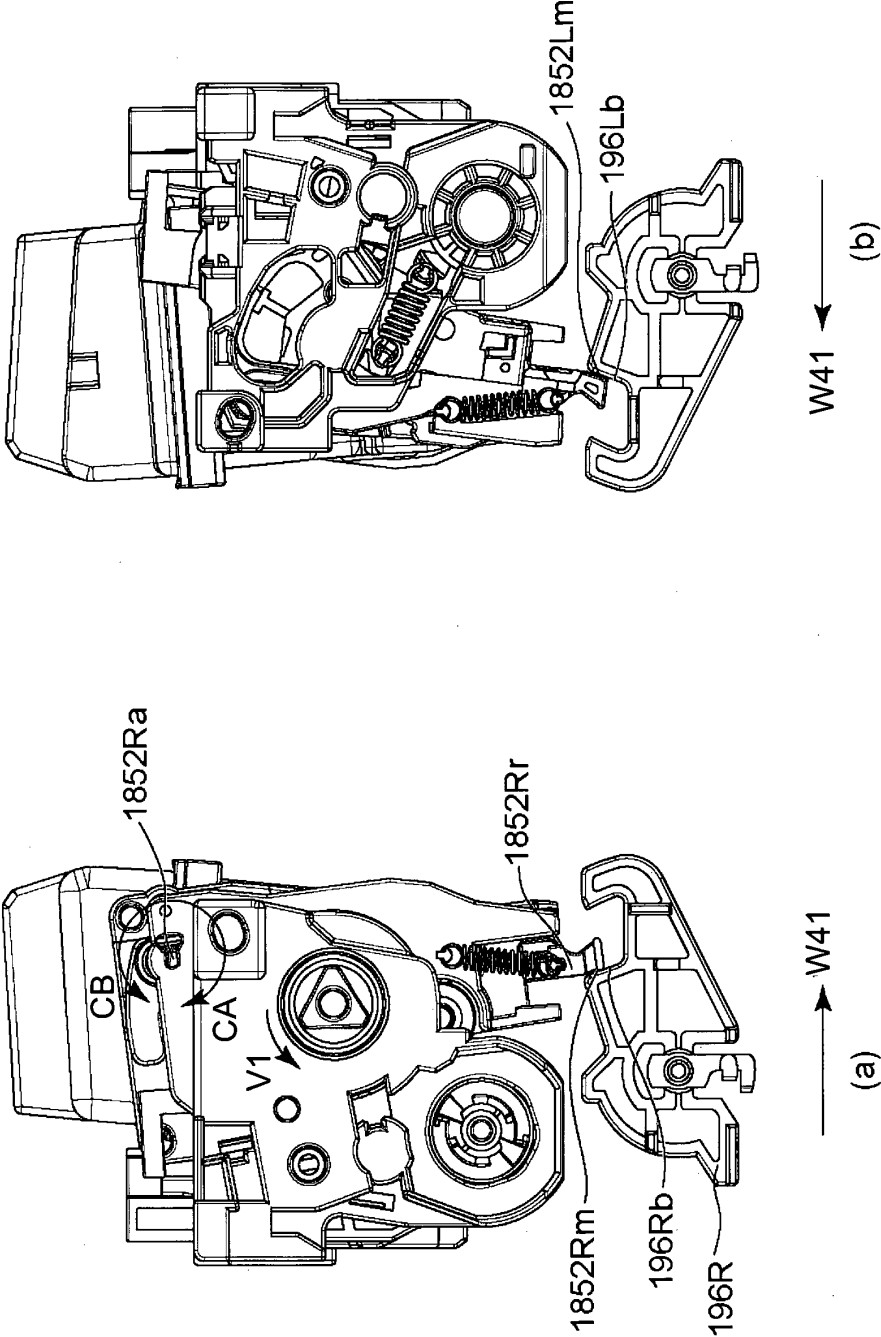


図179

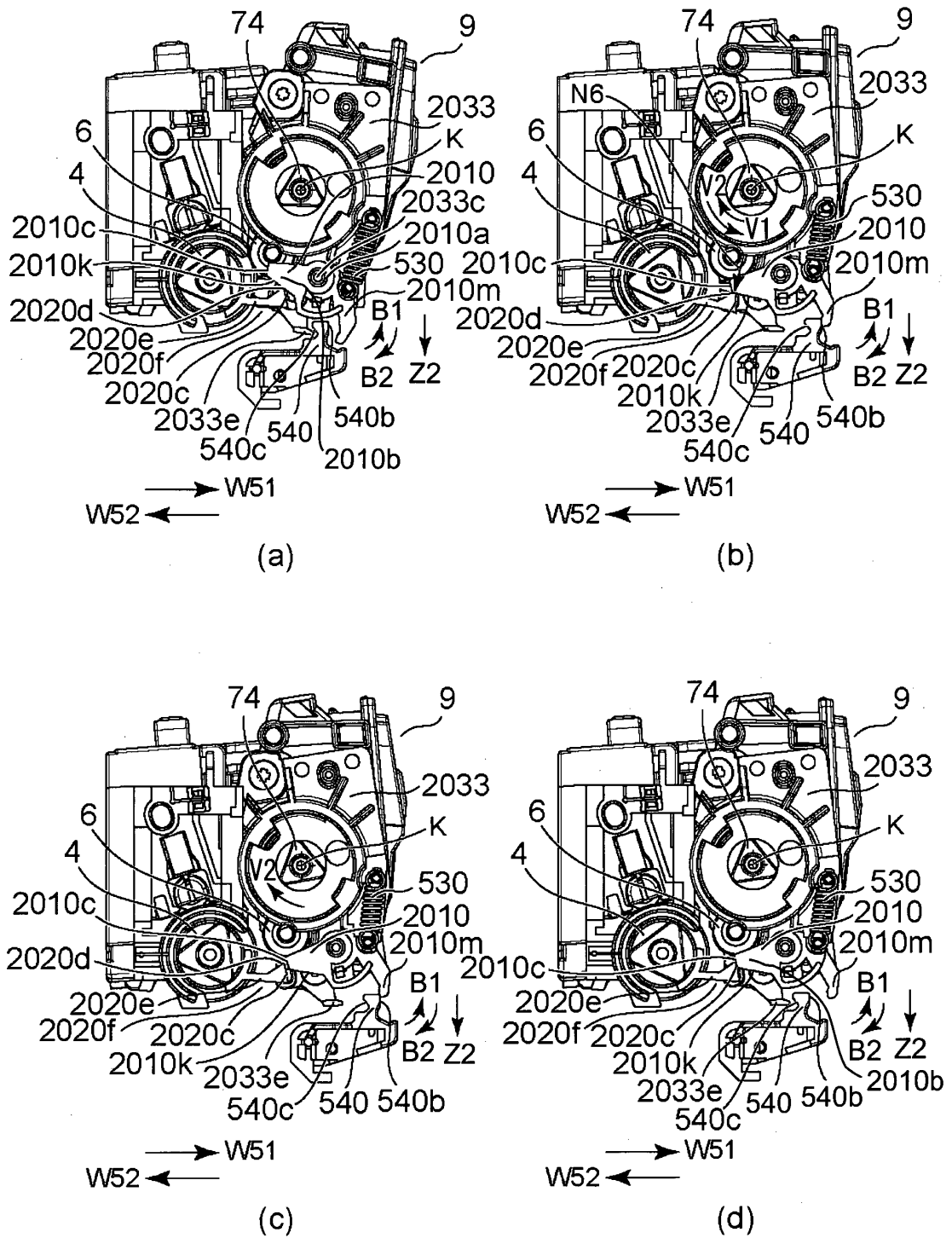


図180

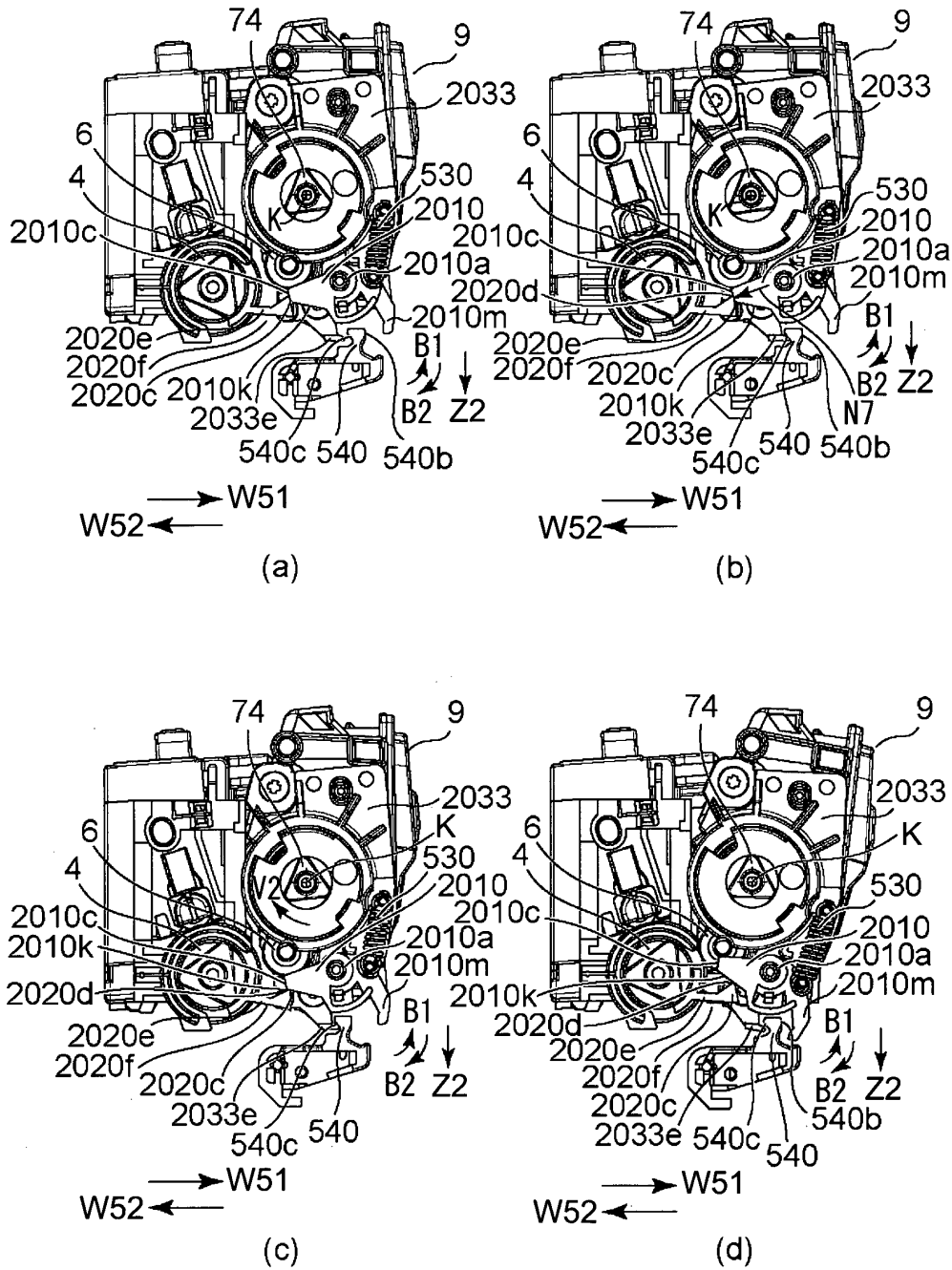


図181

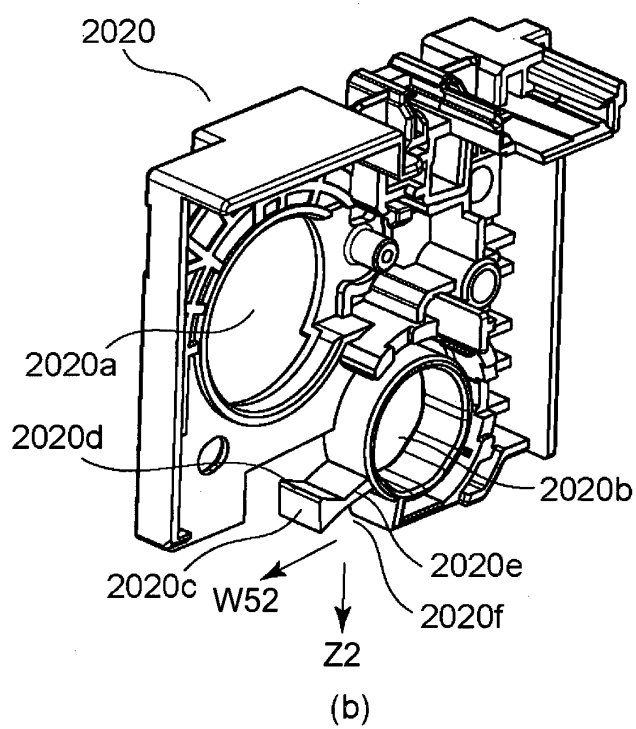
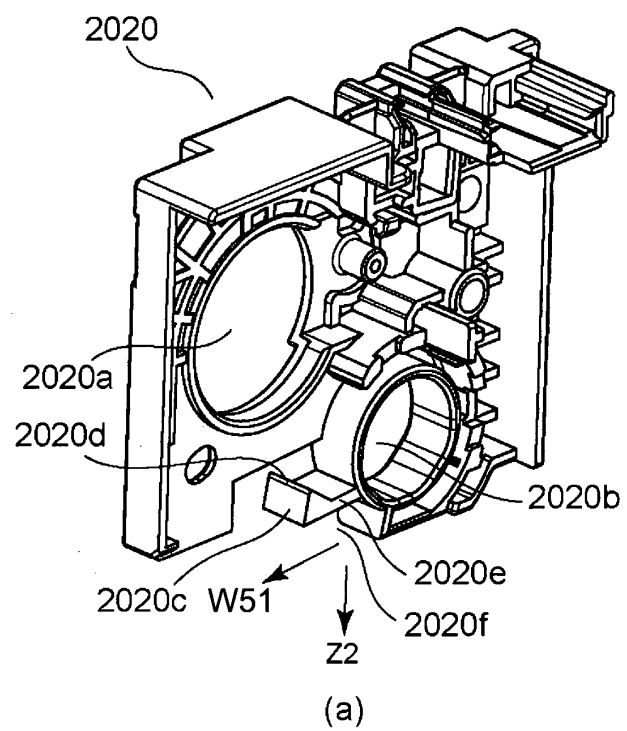


図182

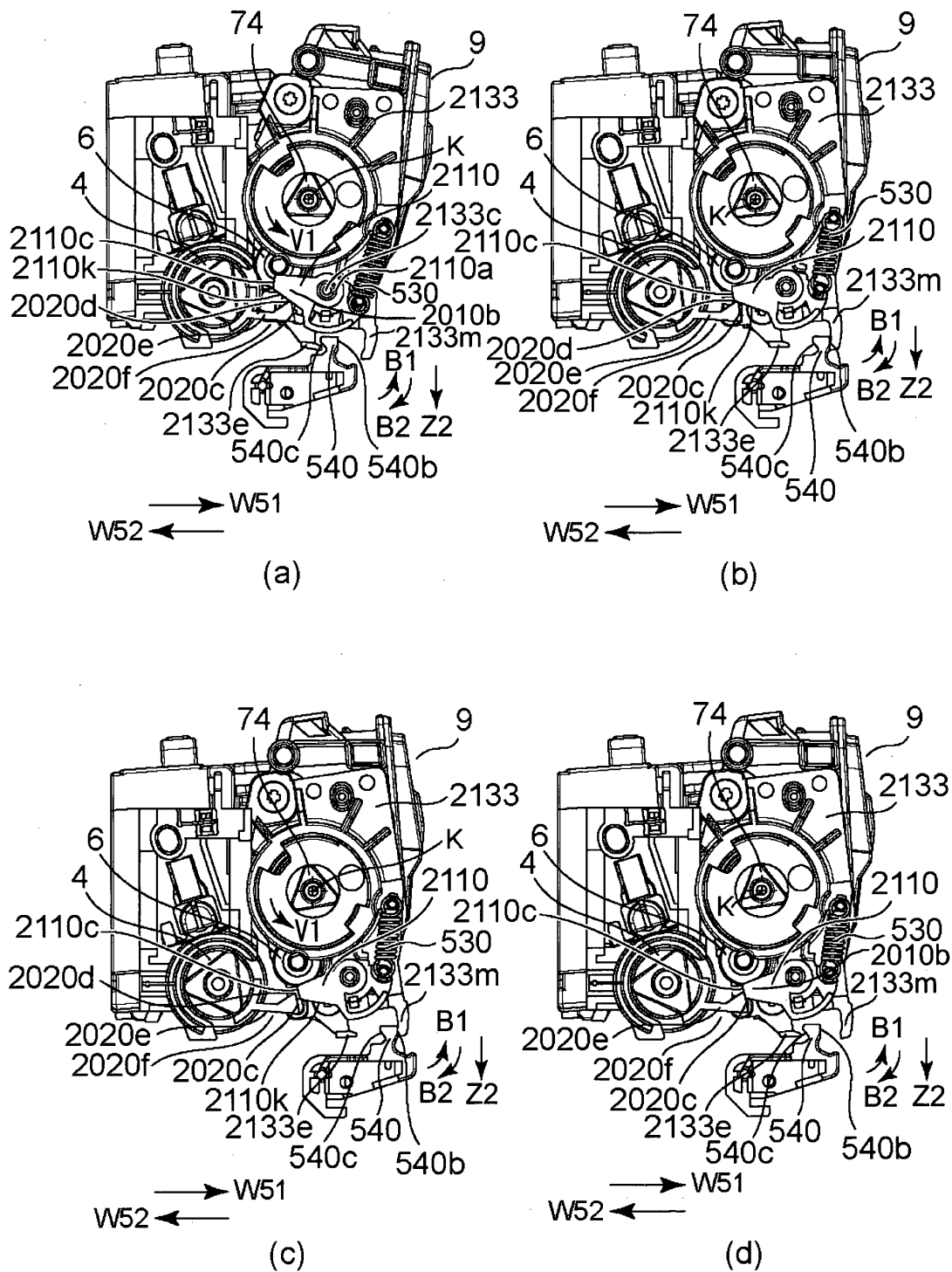


图 183

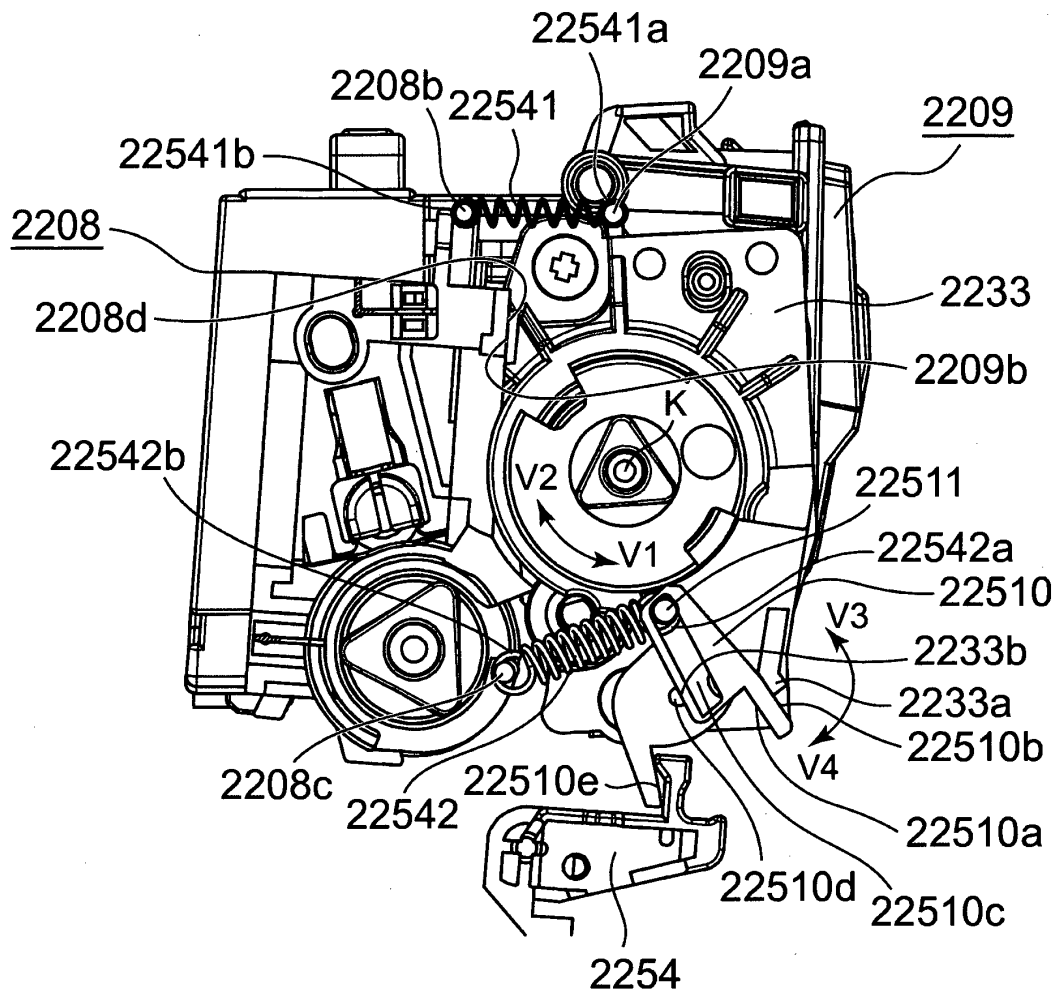
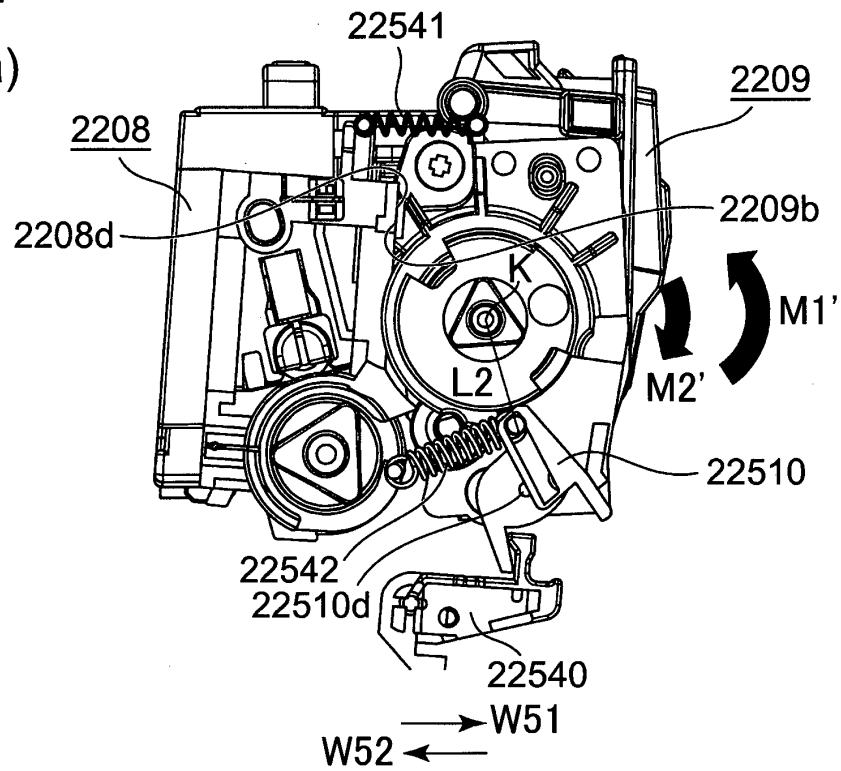


図184

(a)



(b)

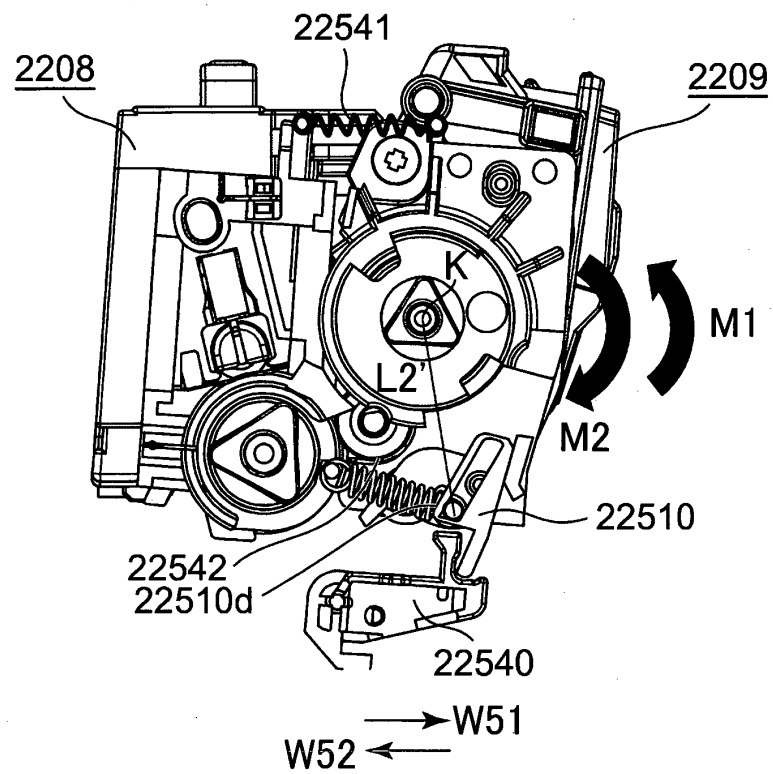


図185

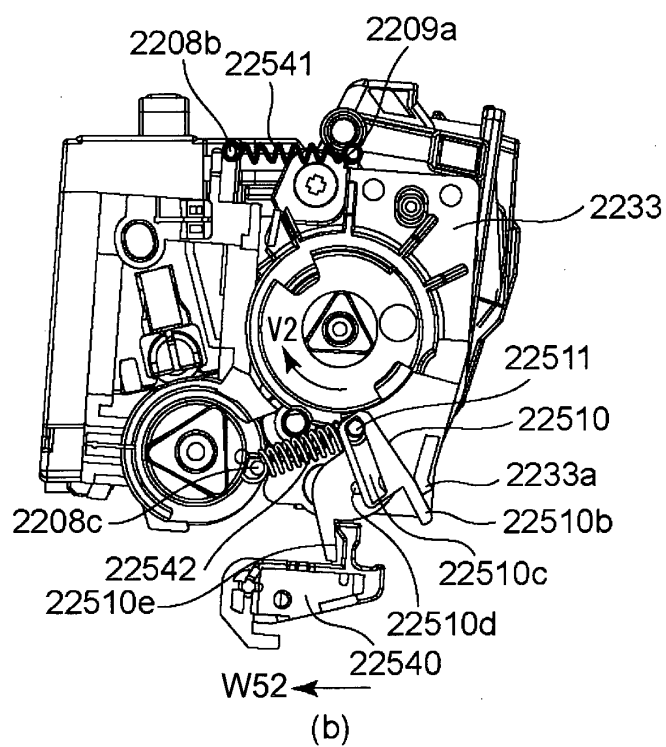
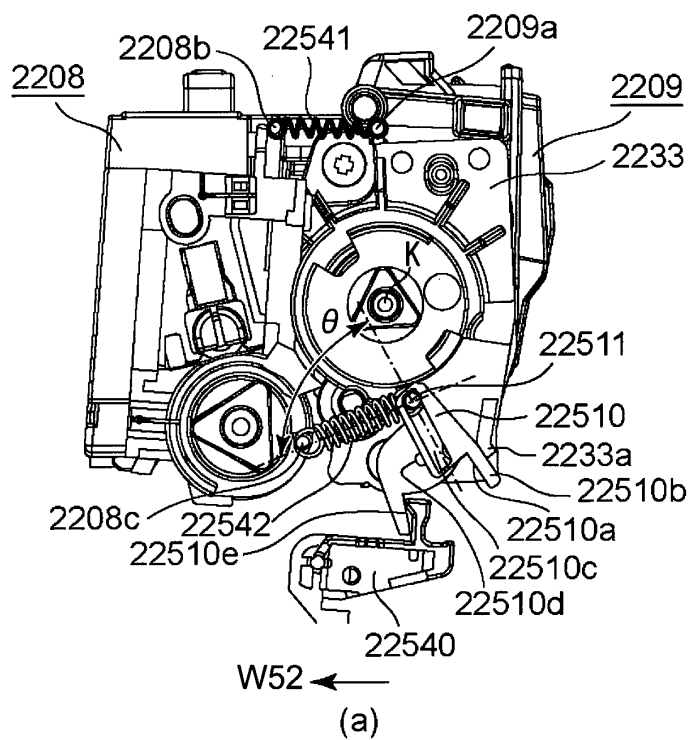


図187

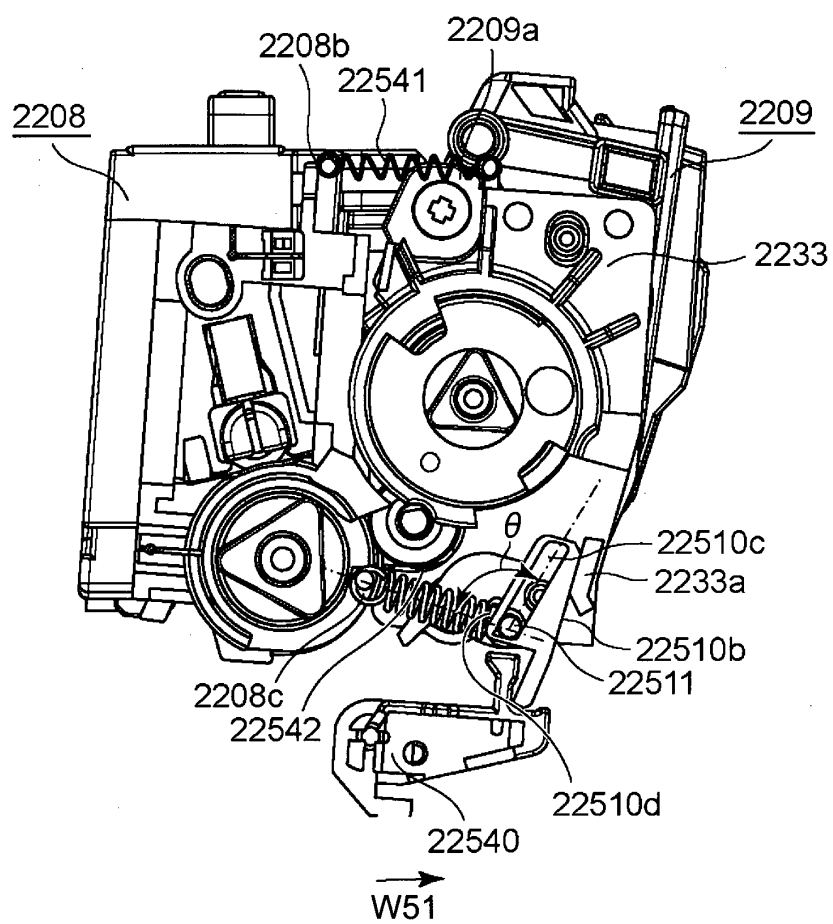


図 188

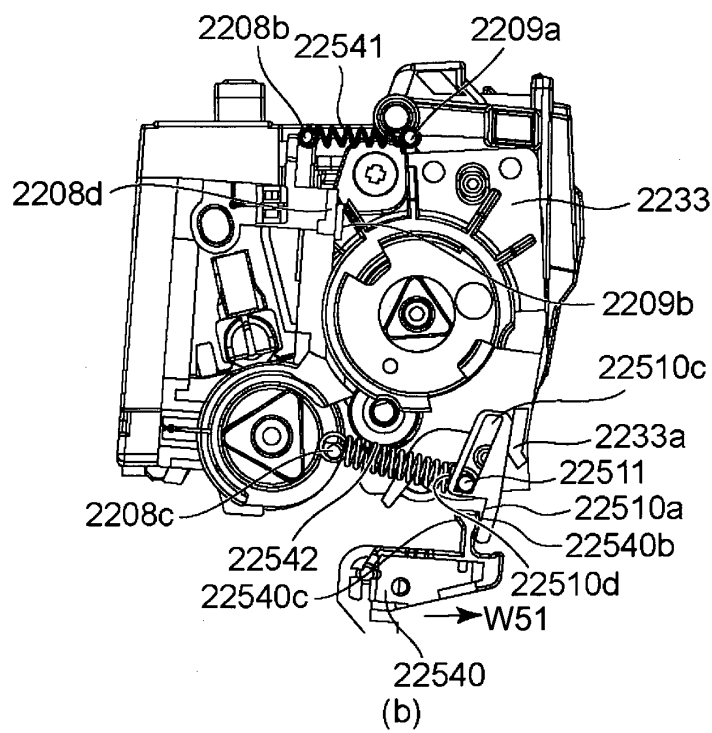
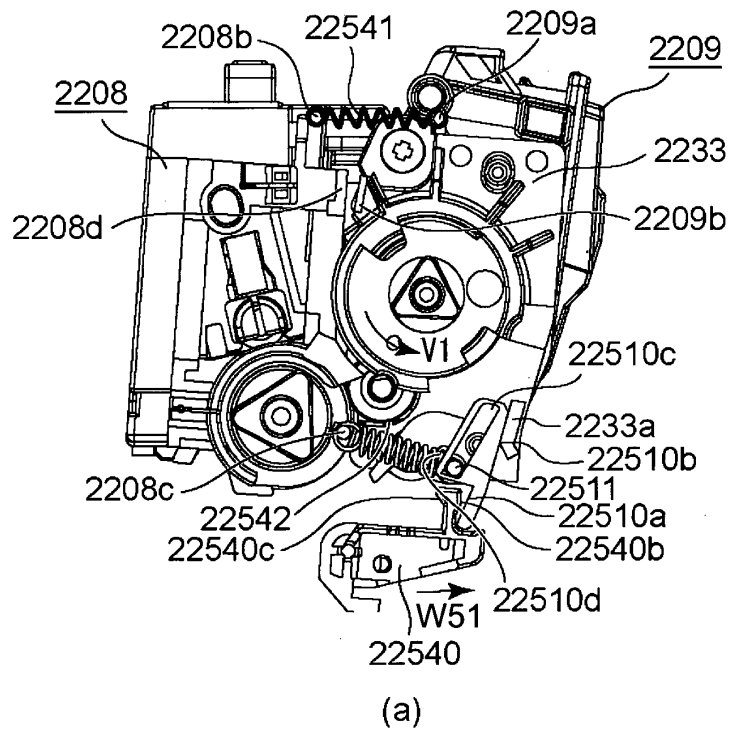


図 189

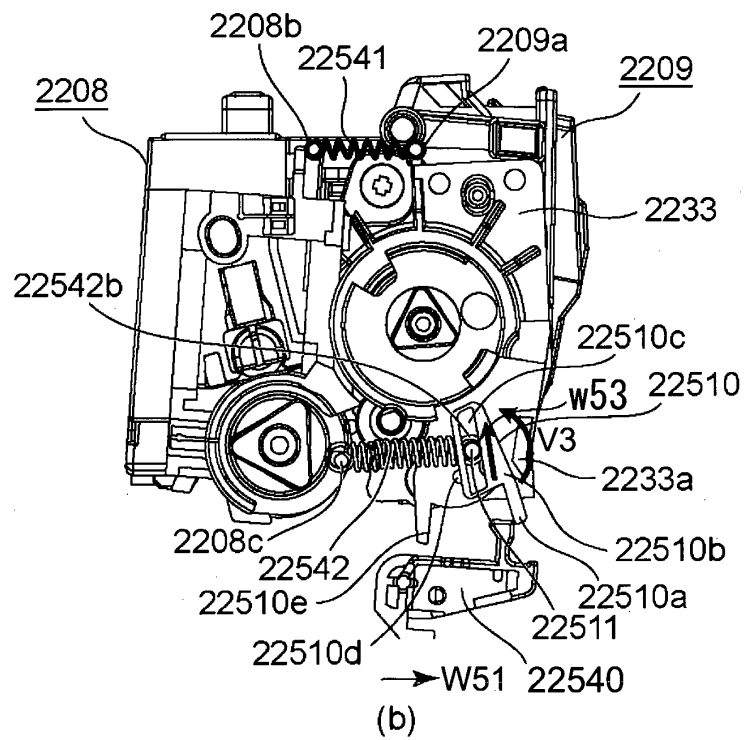
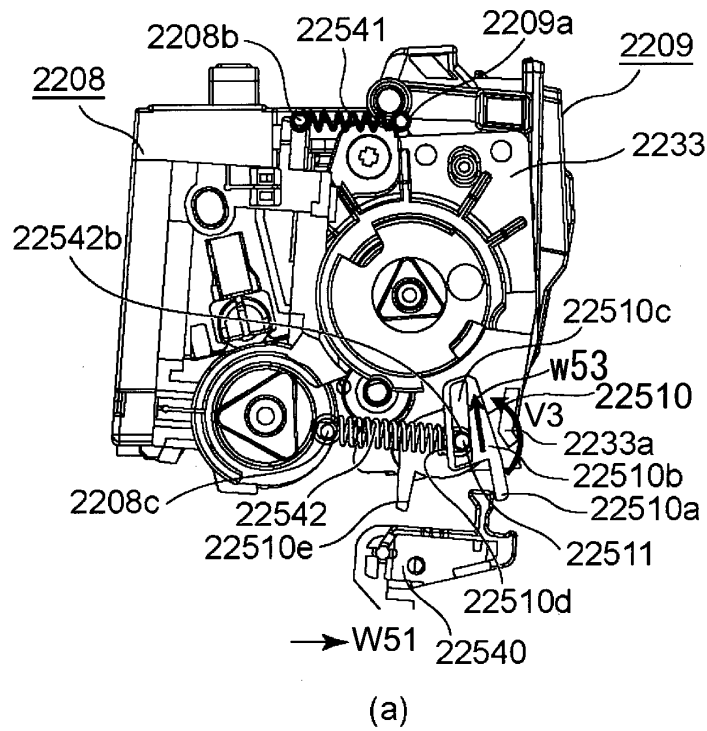
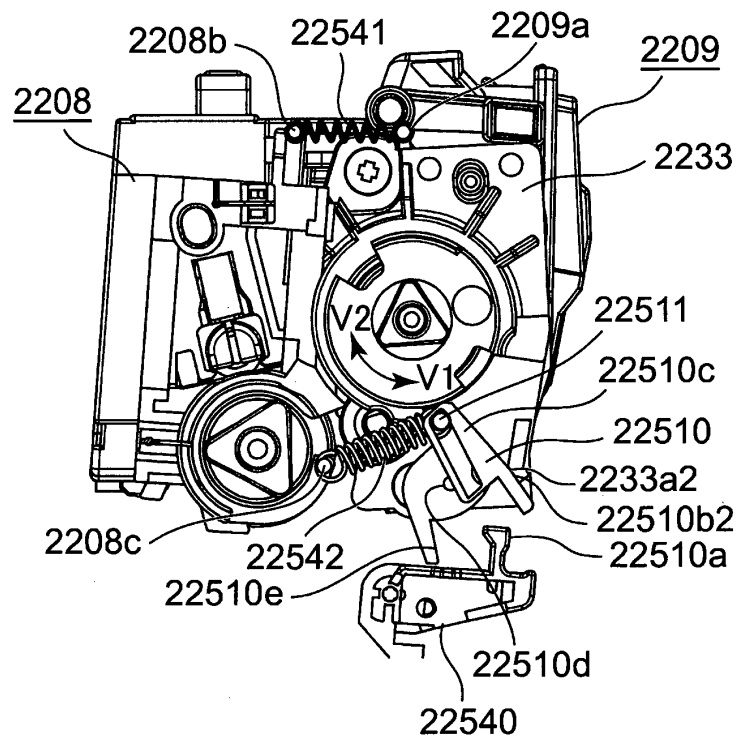


図 190

(a)



(b)

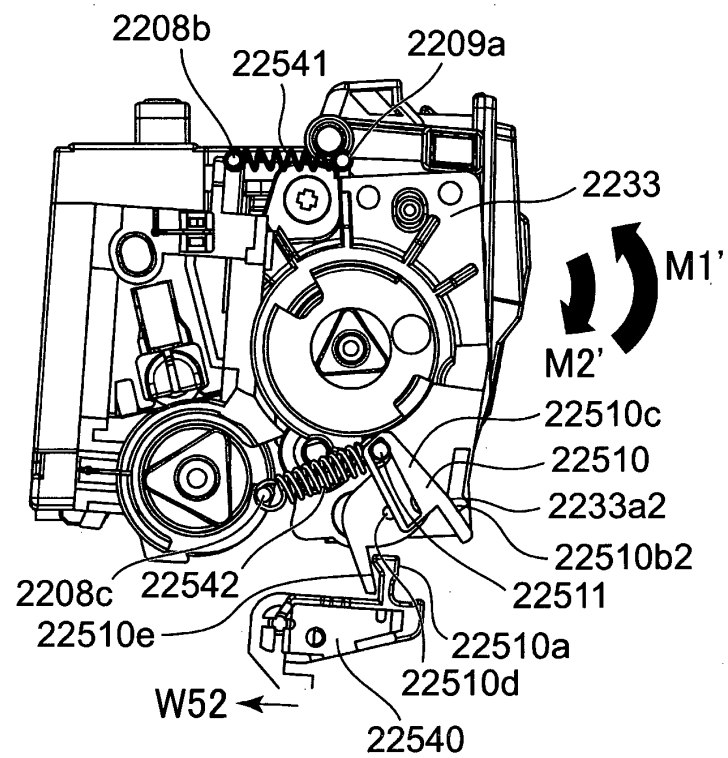


図192

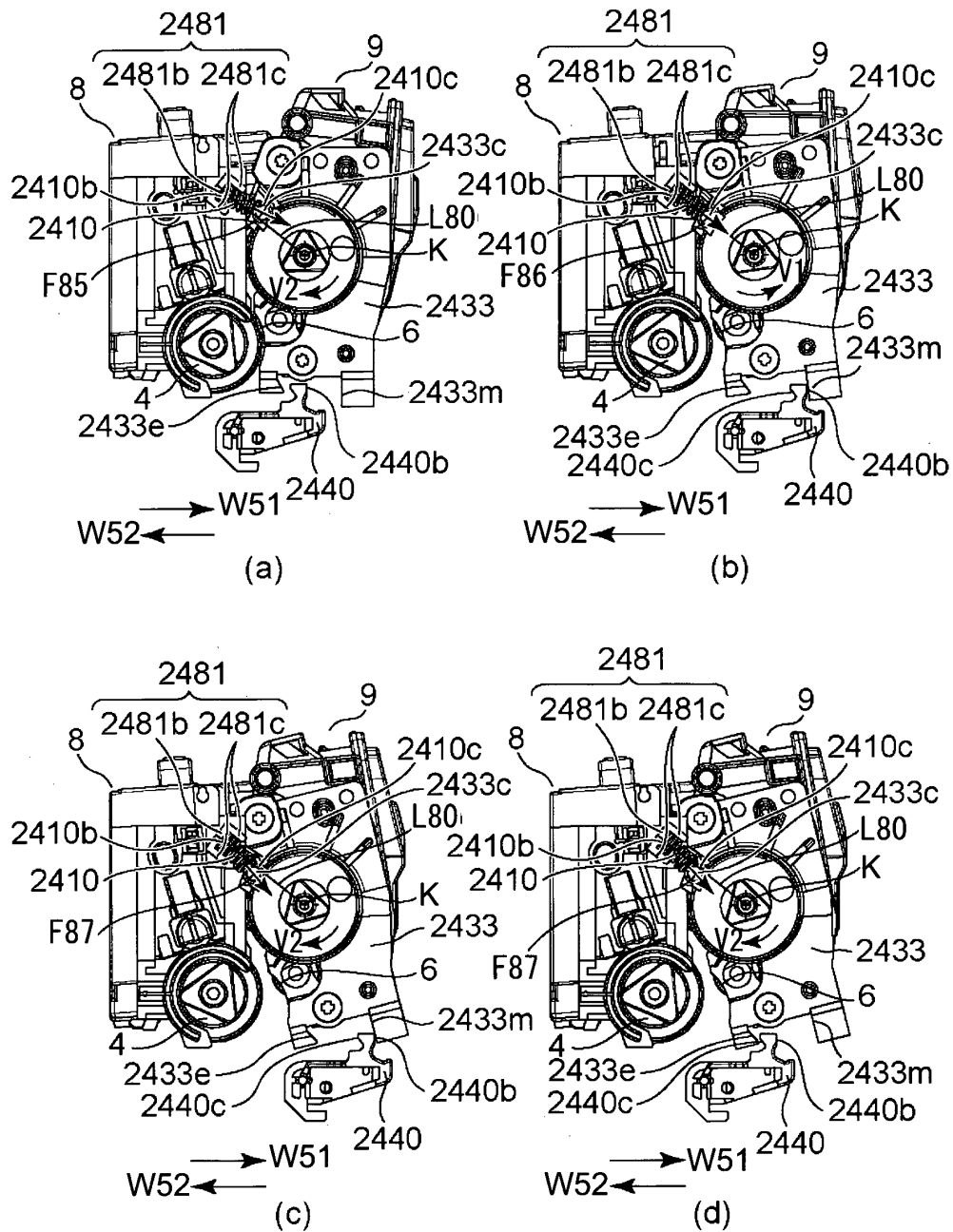


図193

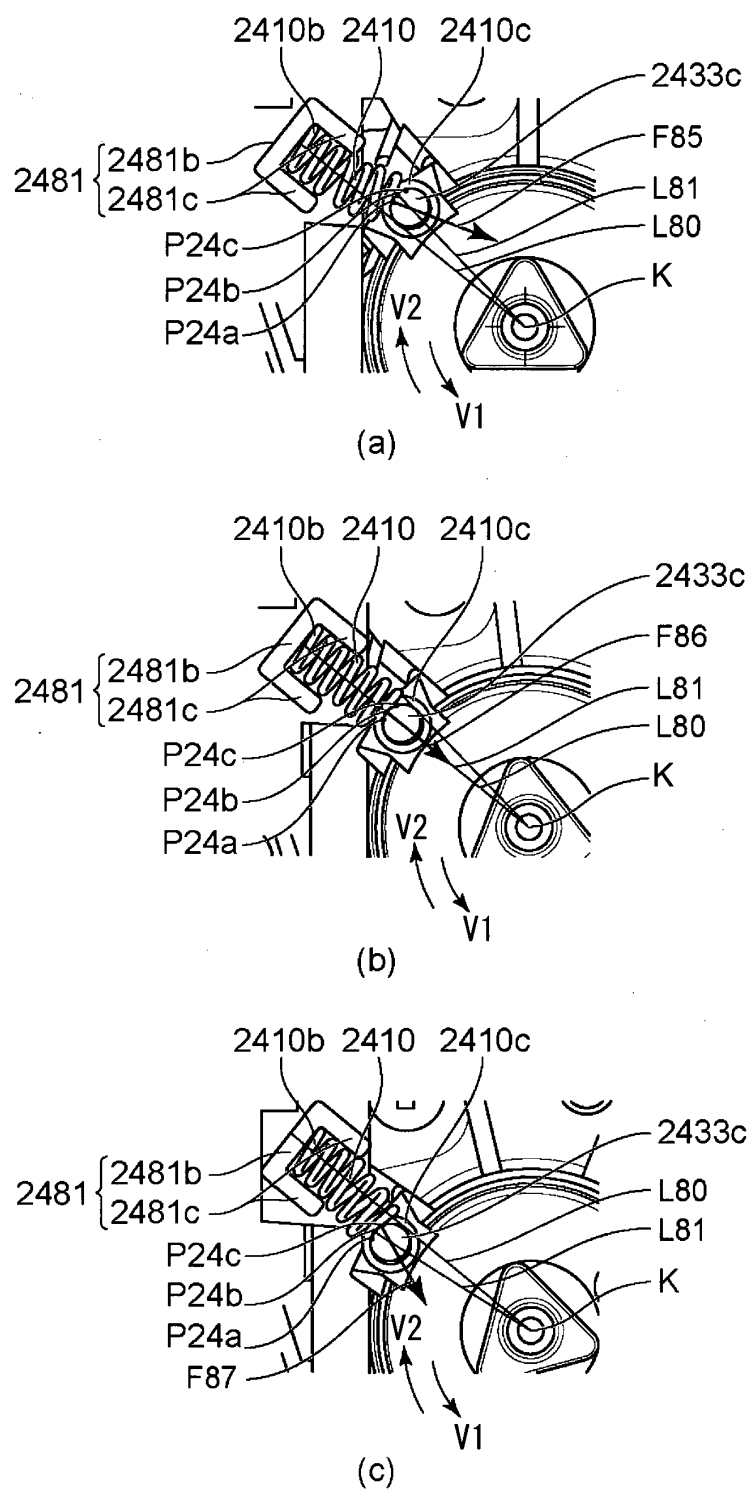


図194

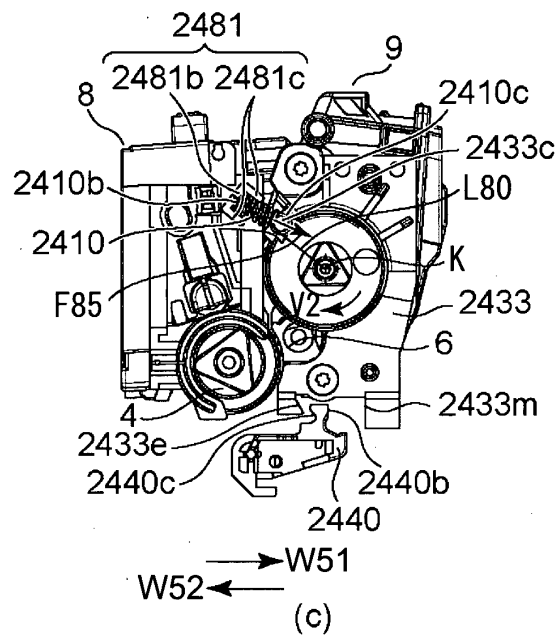
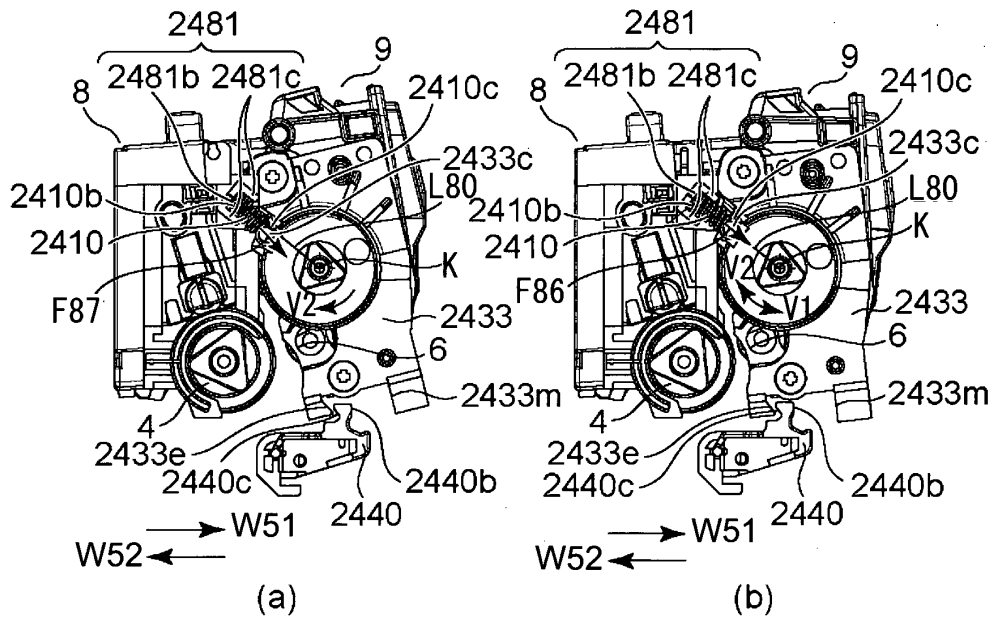


図 195

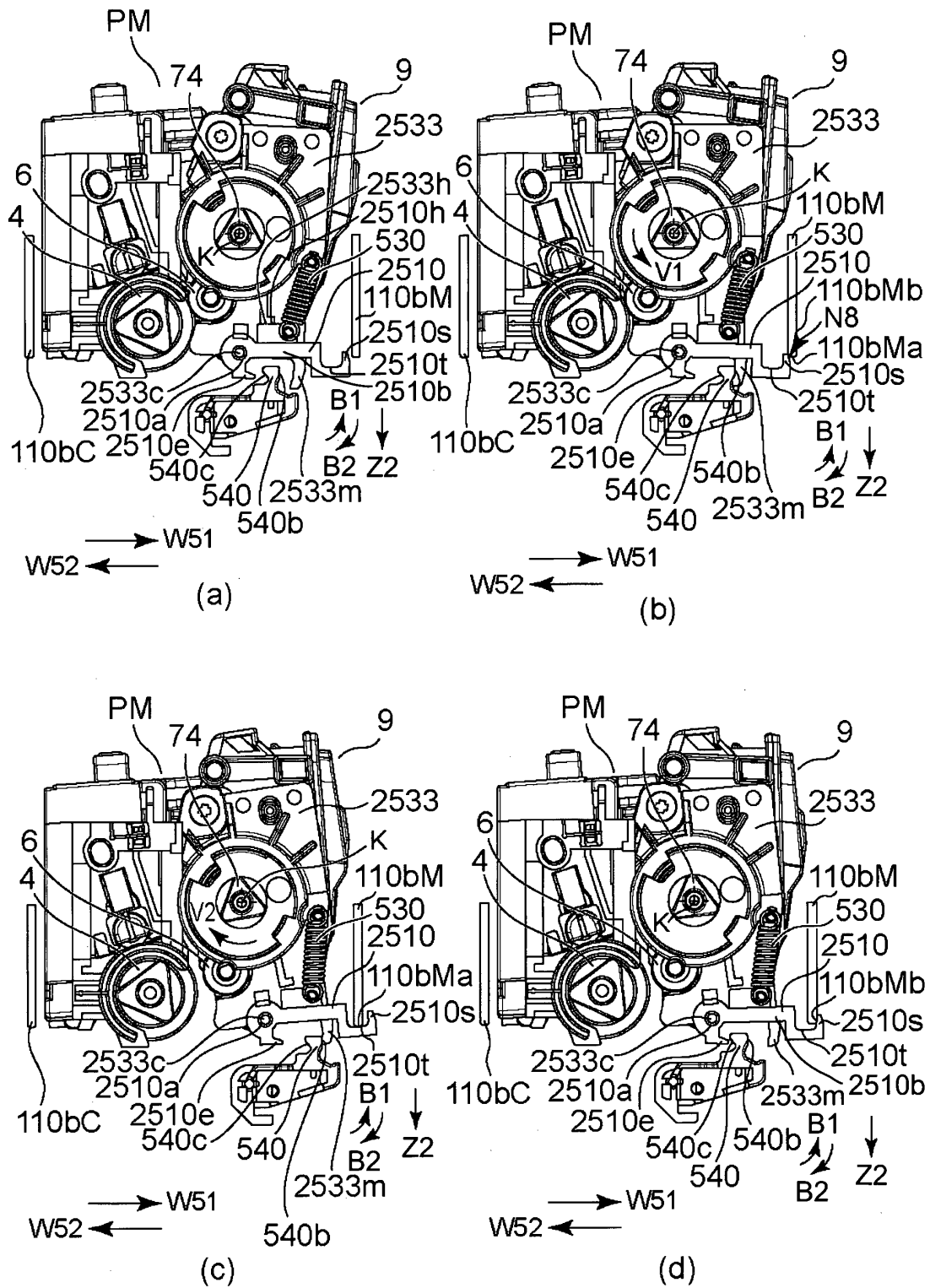


図 196

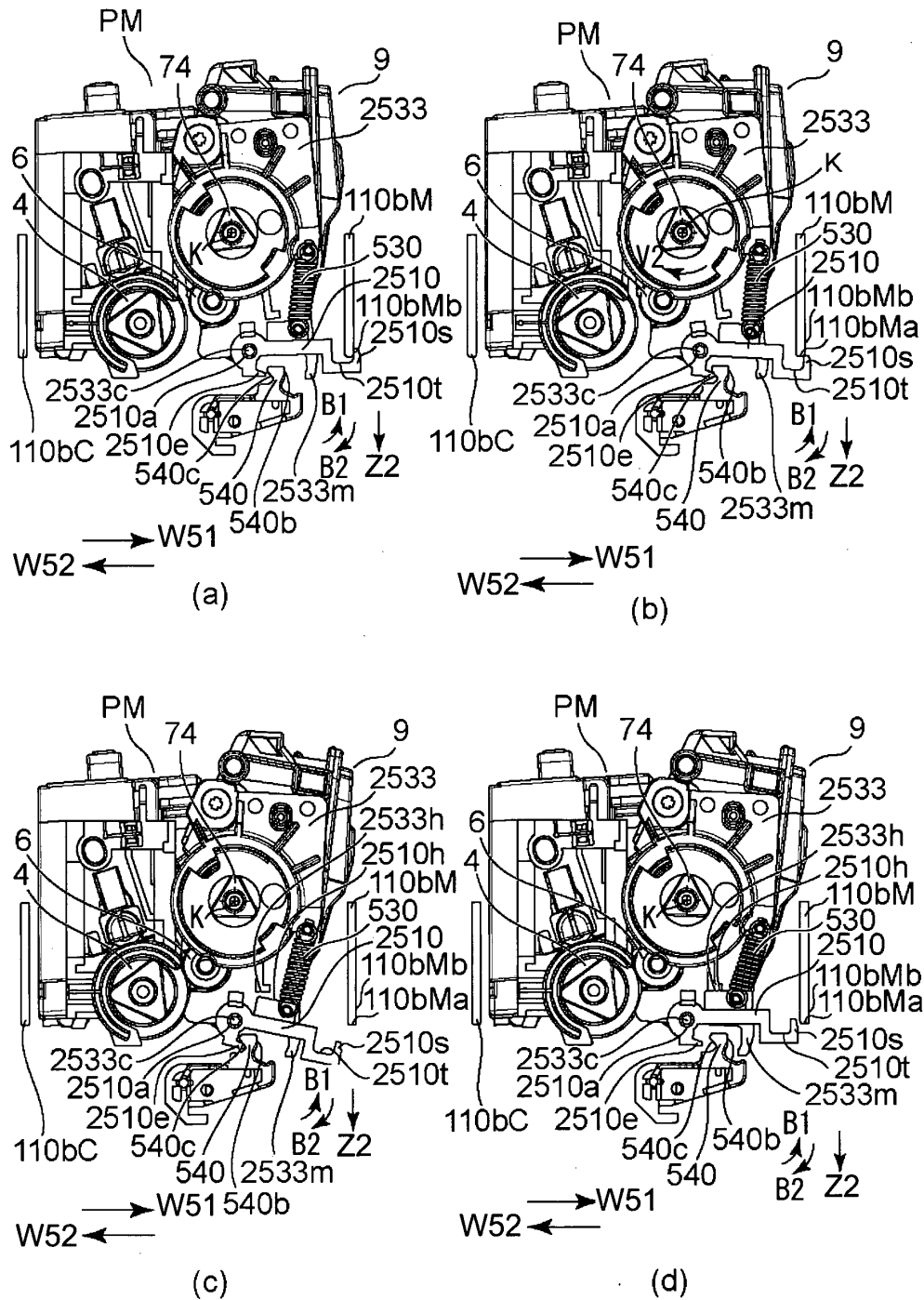


図198

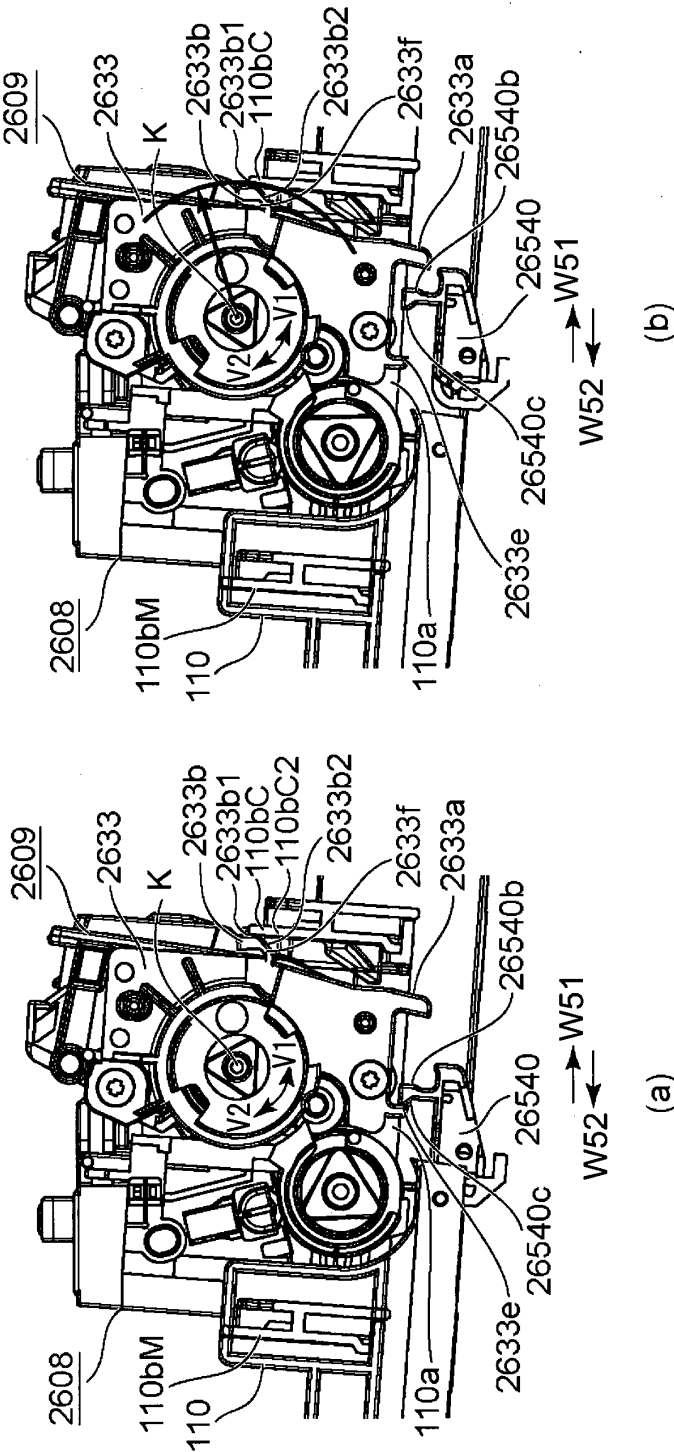


図 199

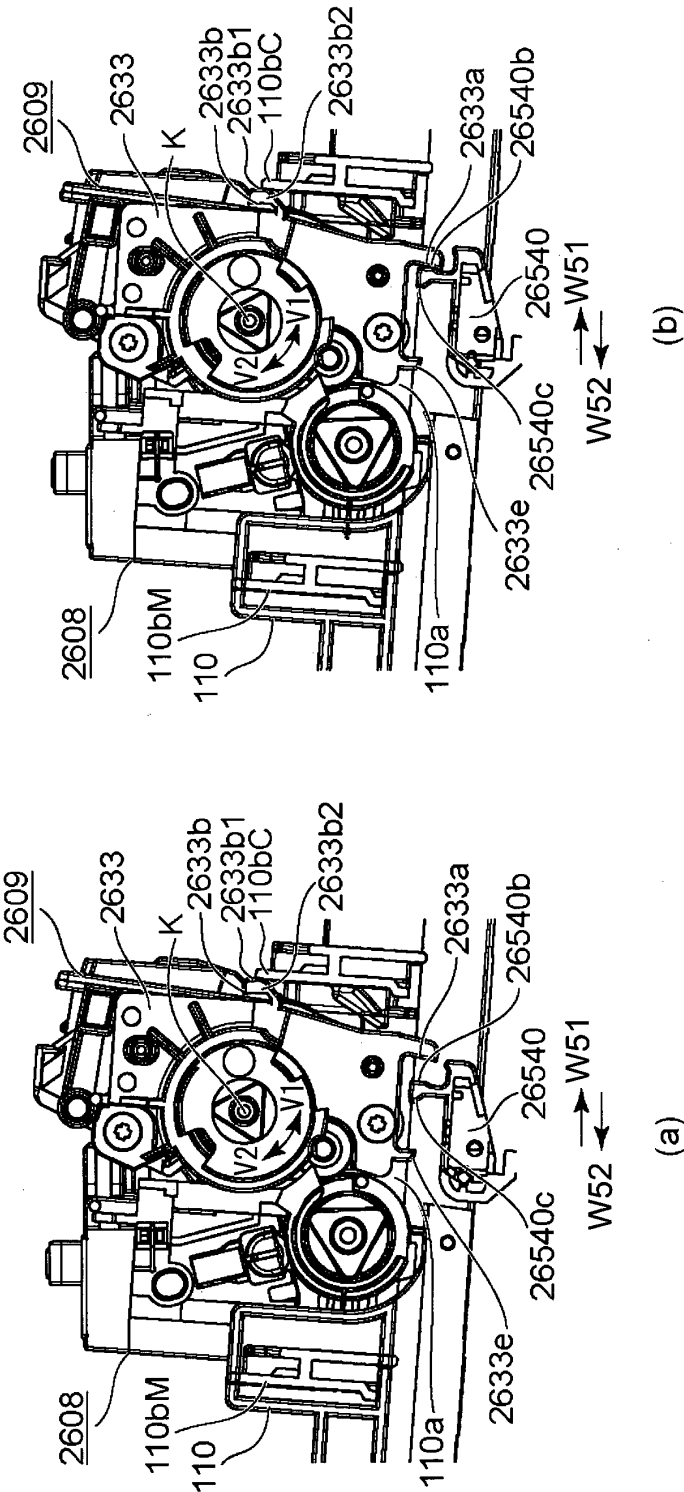


图 200

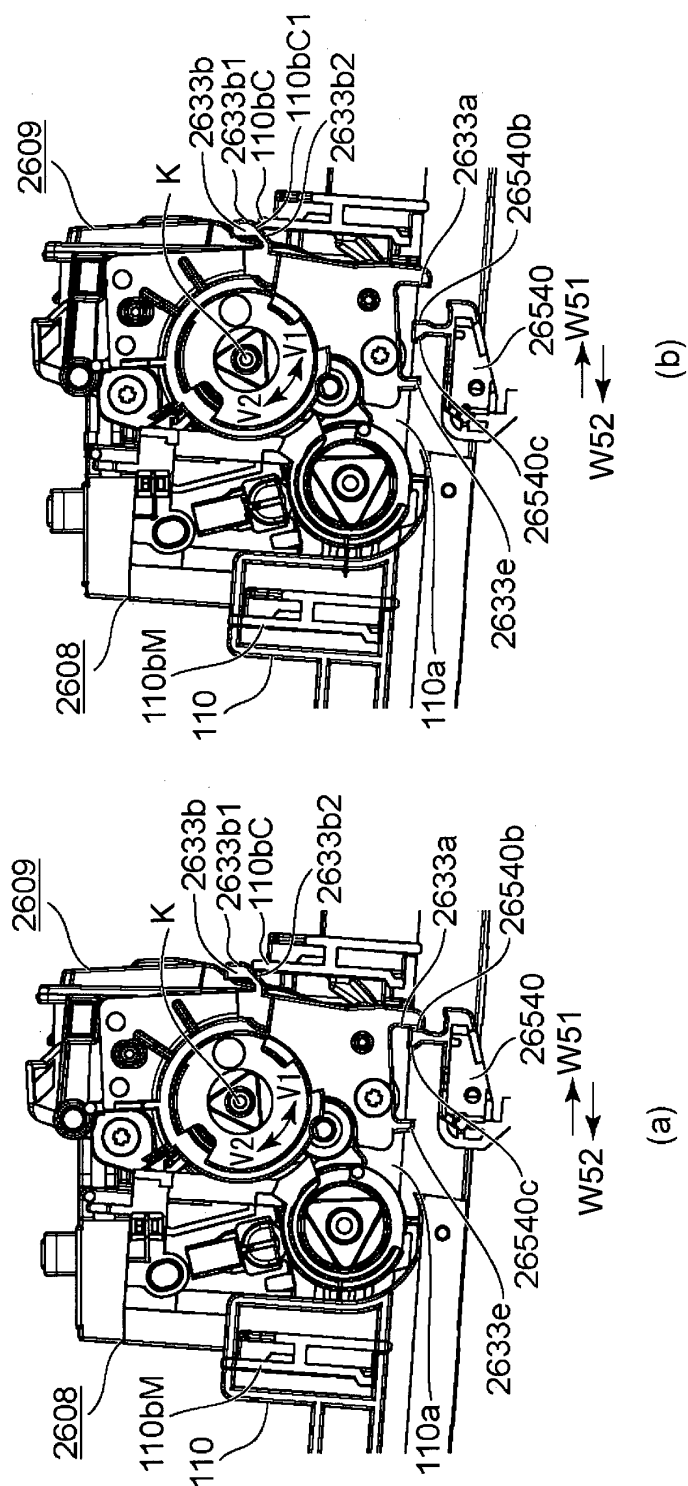


図201

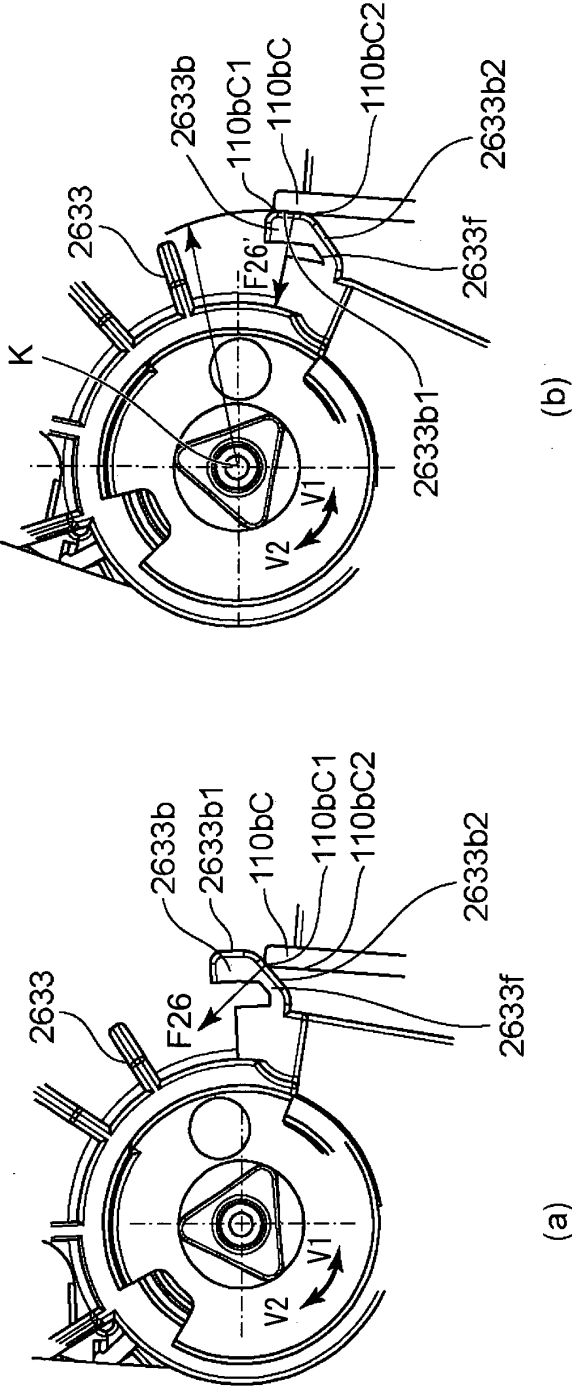


図202

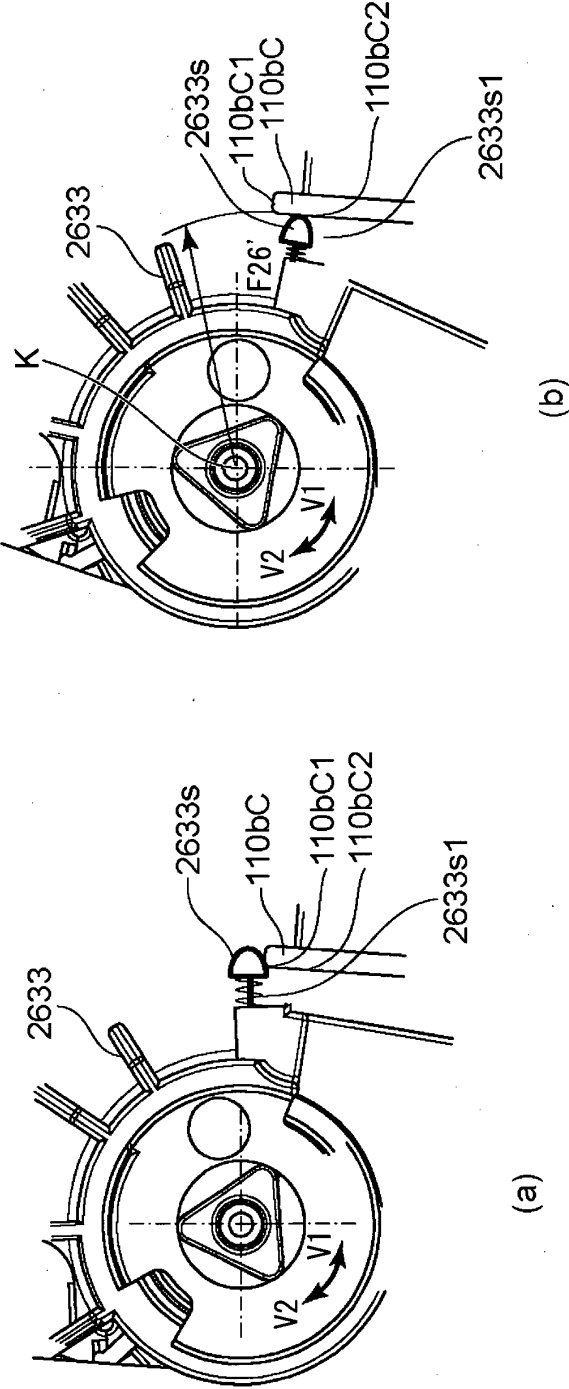


图 203

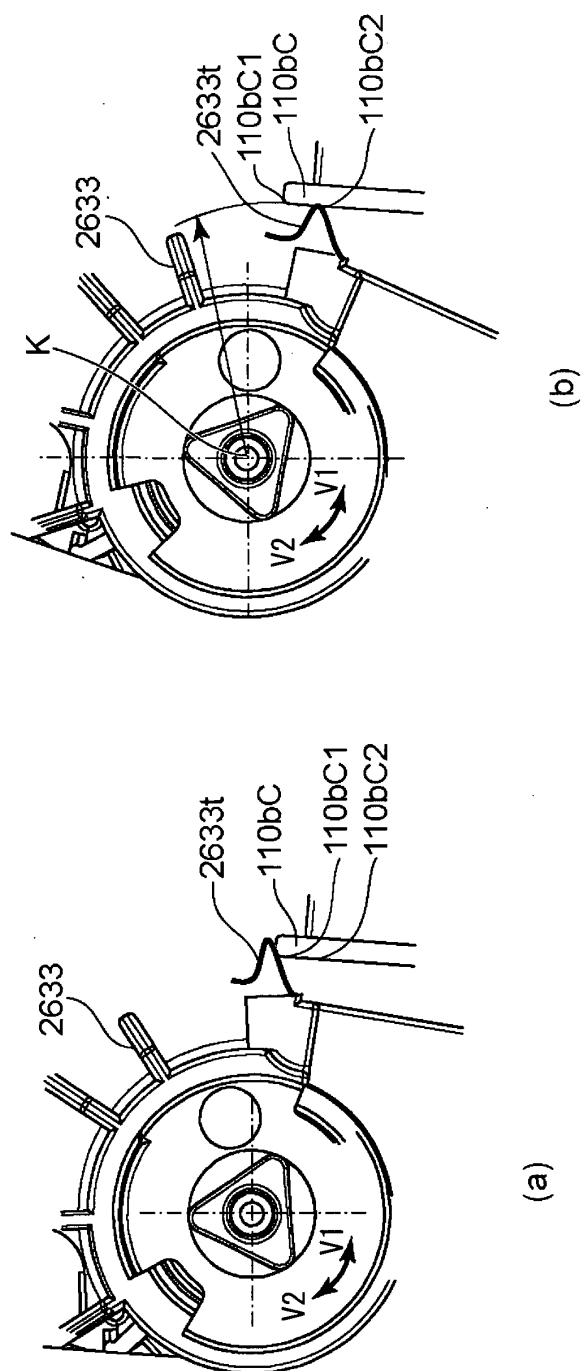


図204

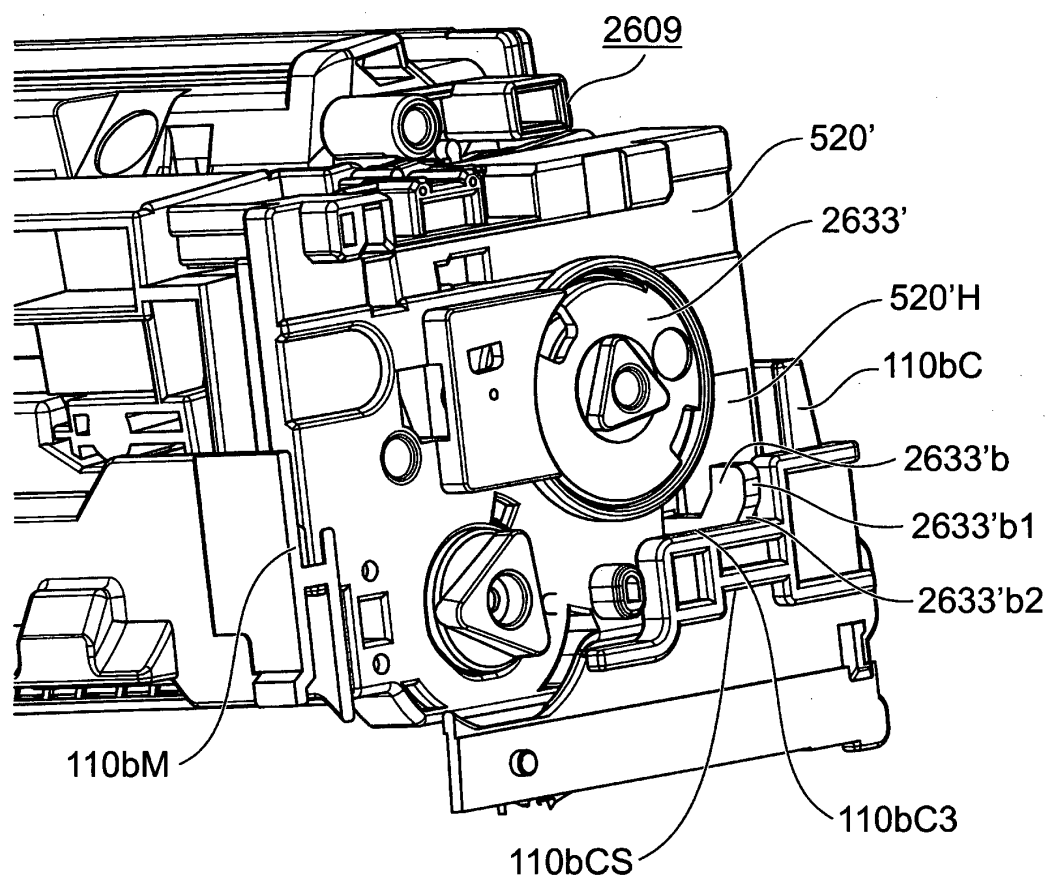


図205

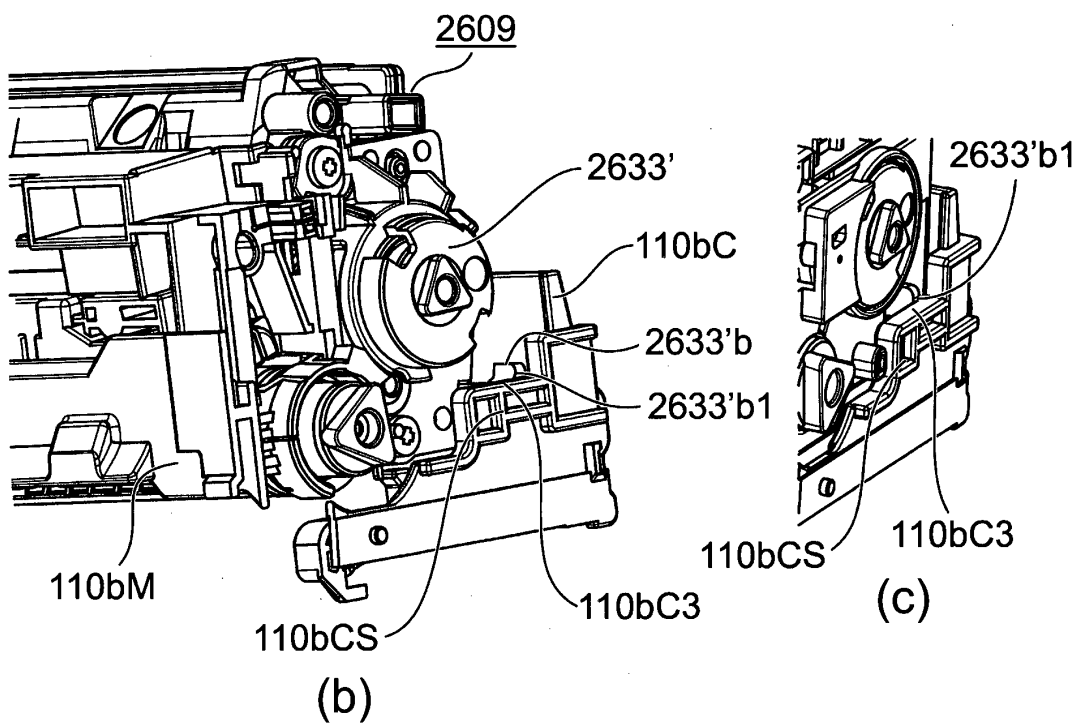
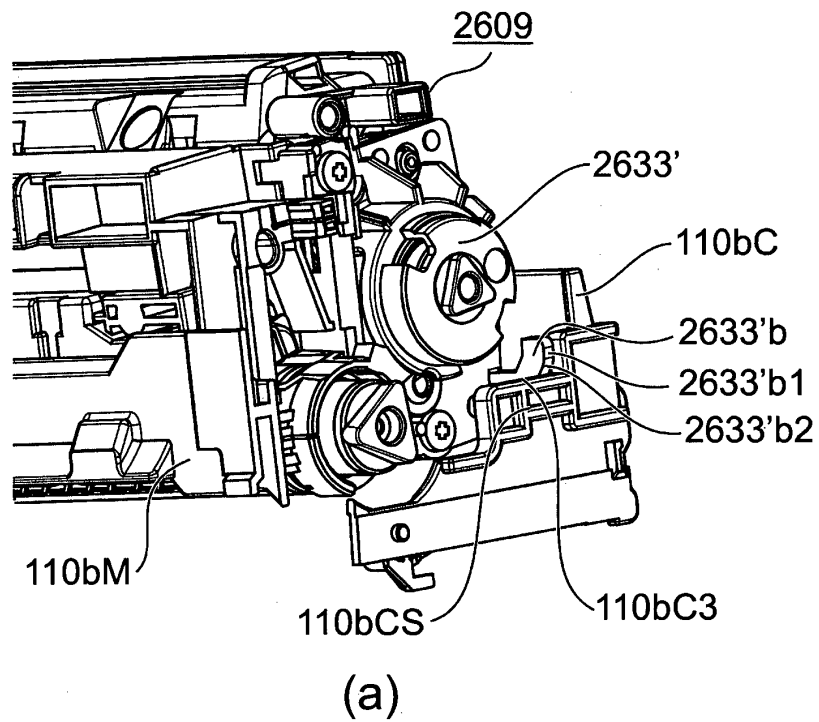


図206

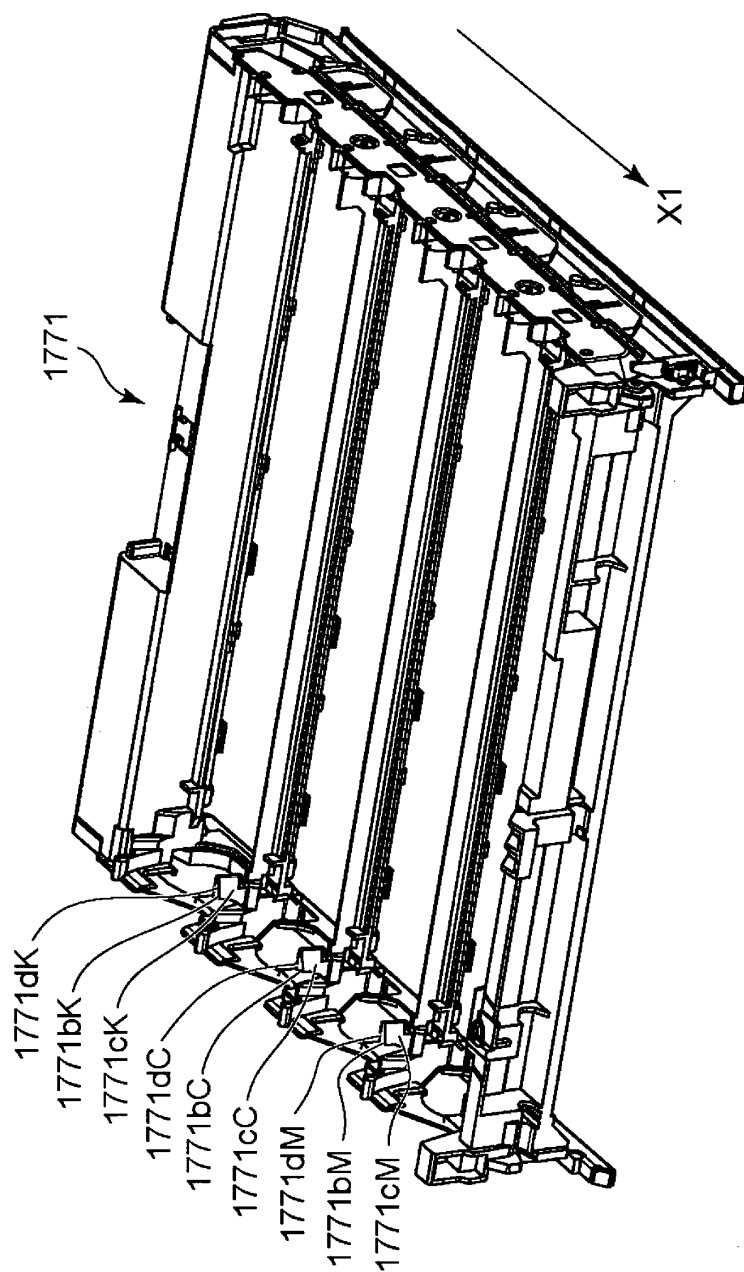


図207

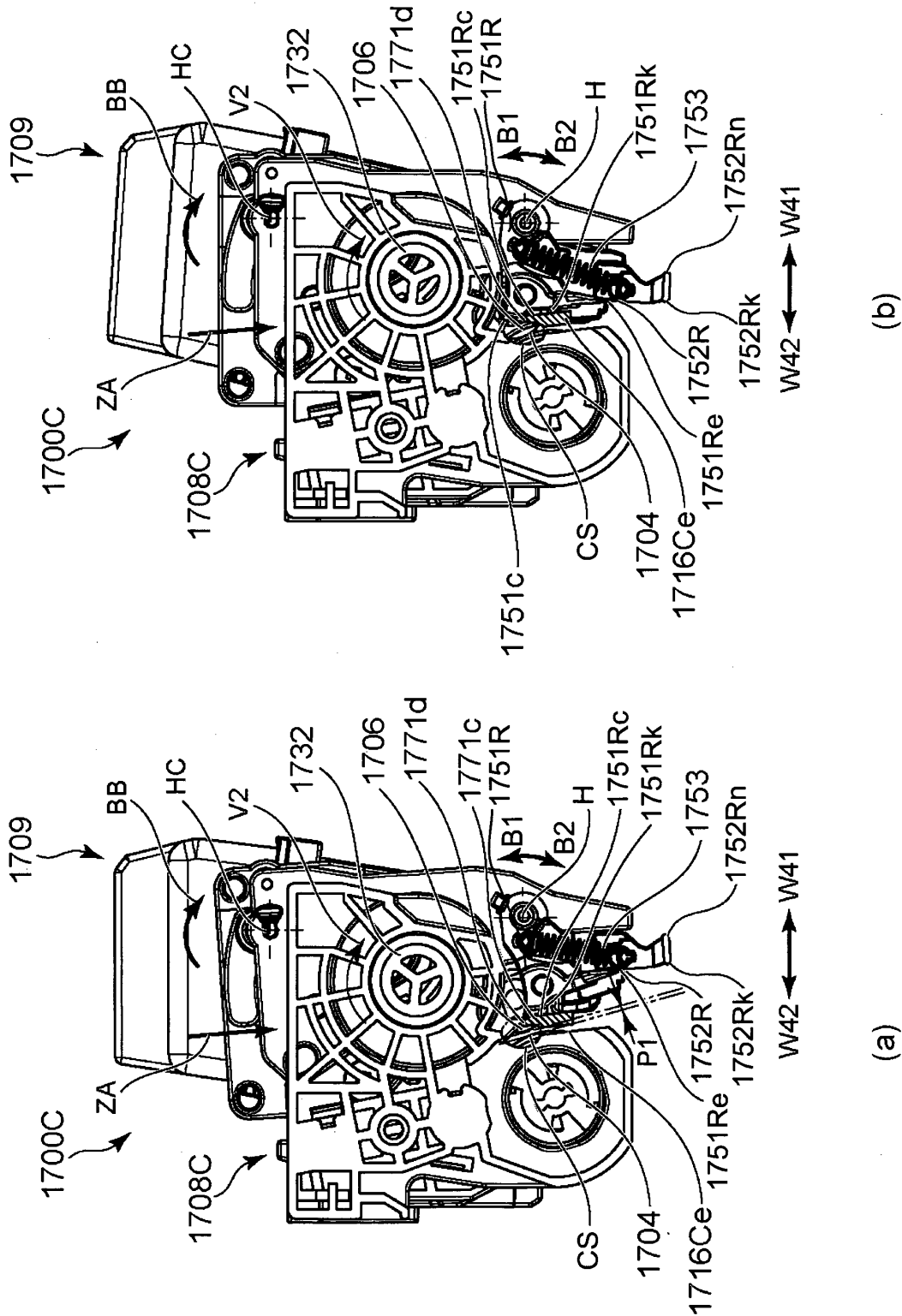


図208

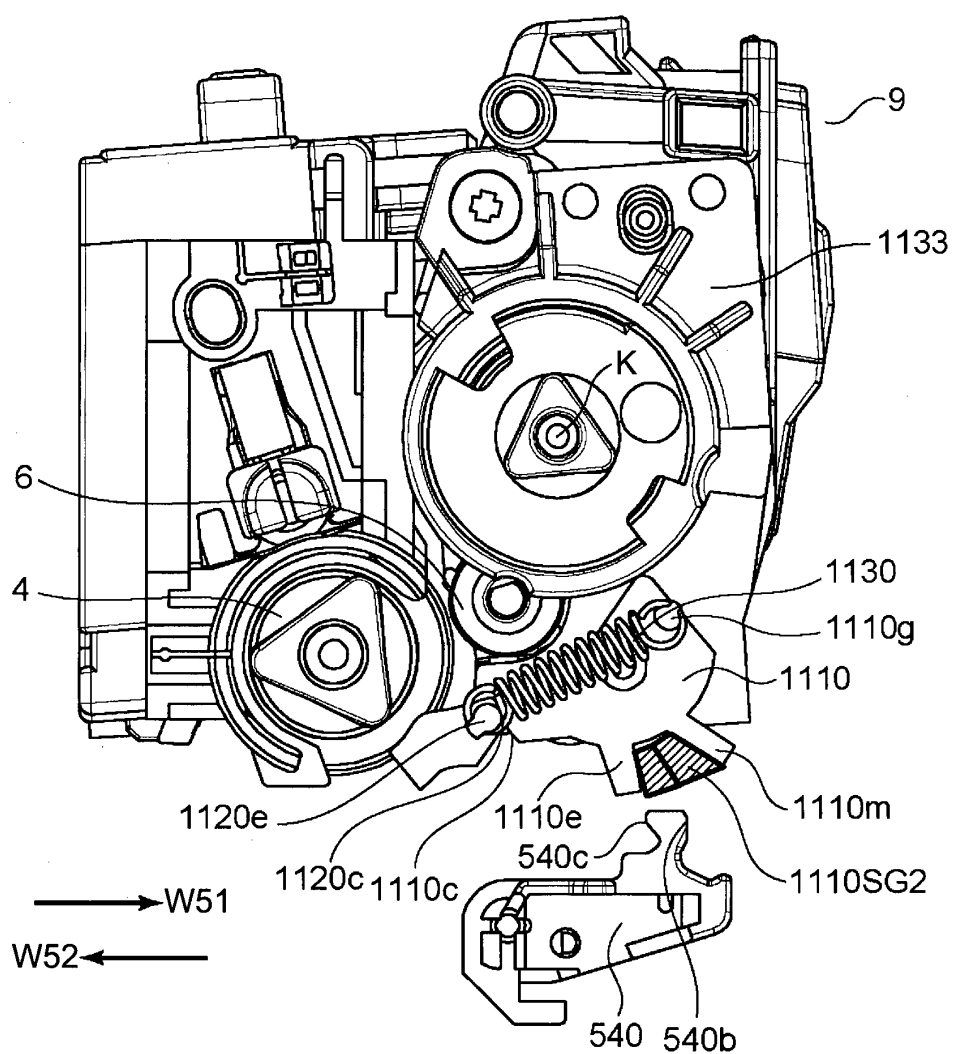


図209

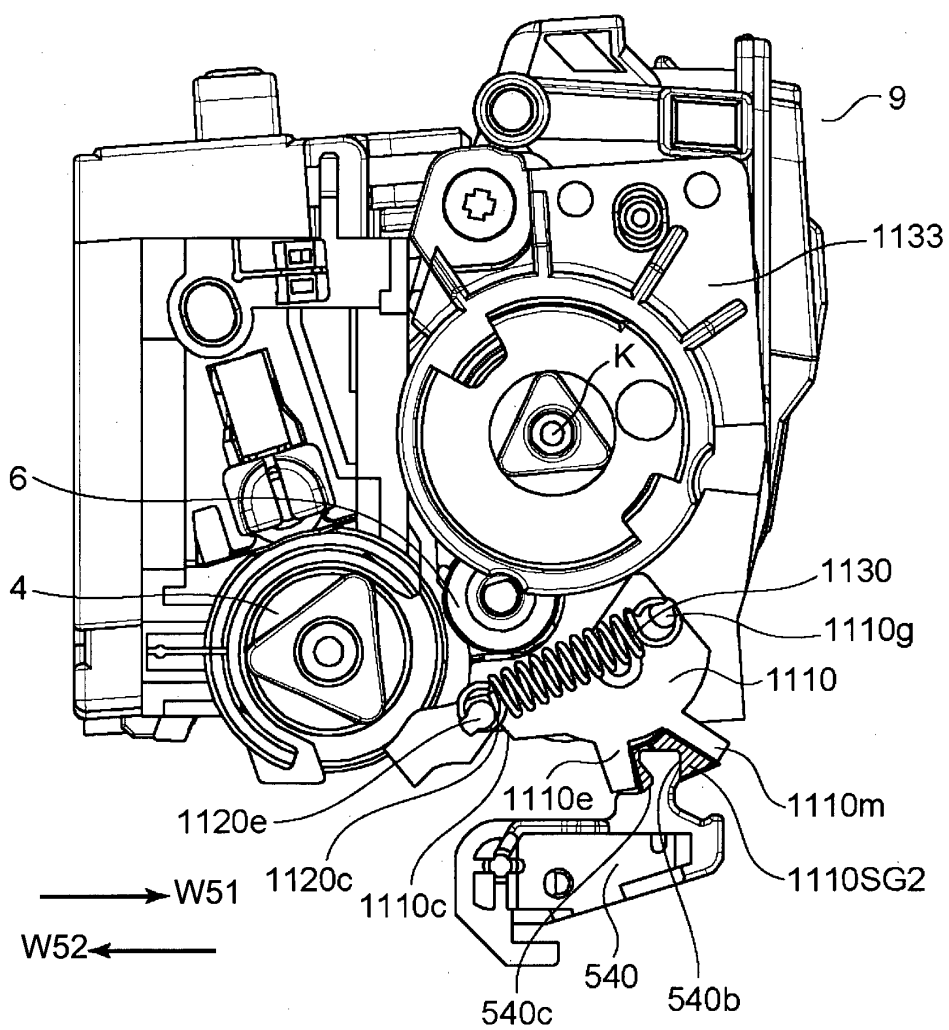


図210

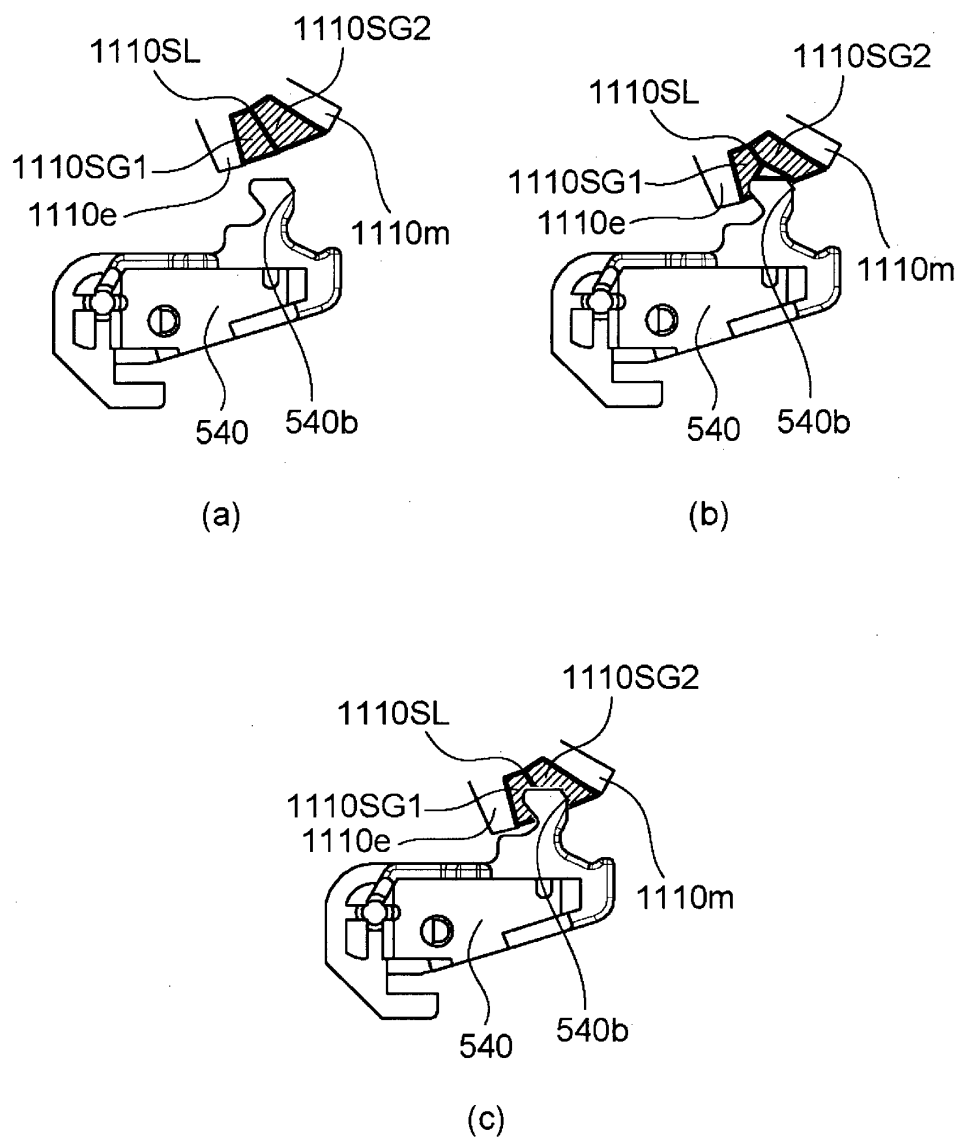


图 211

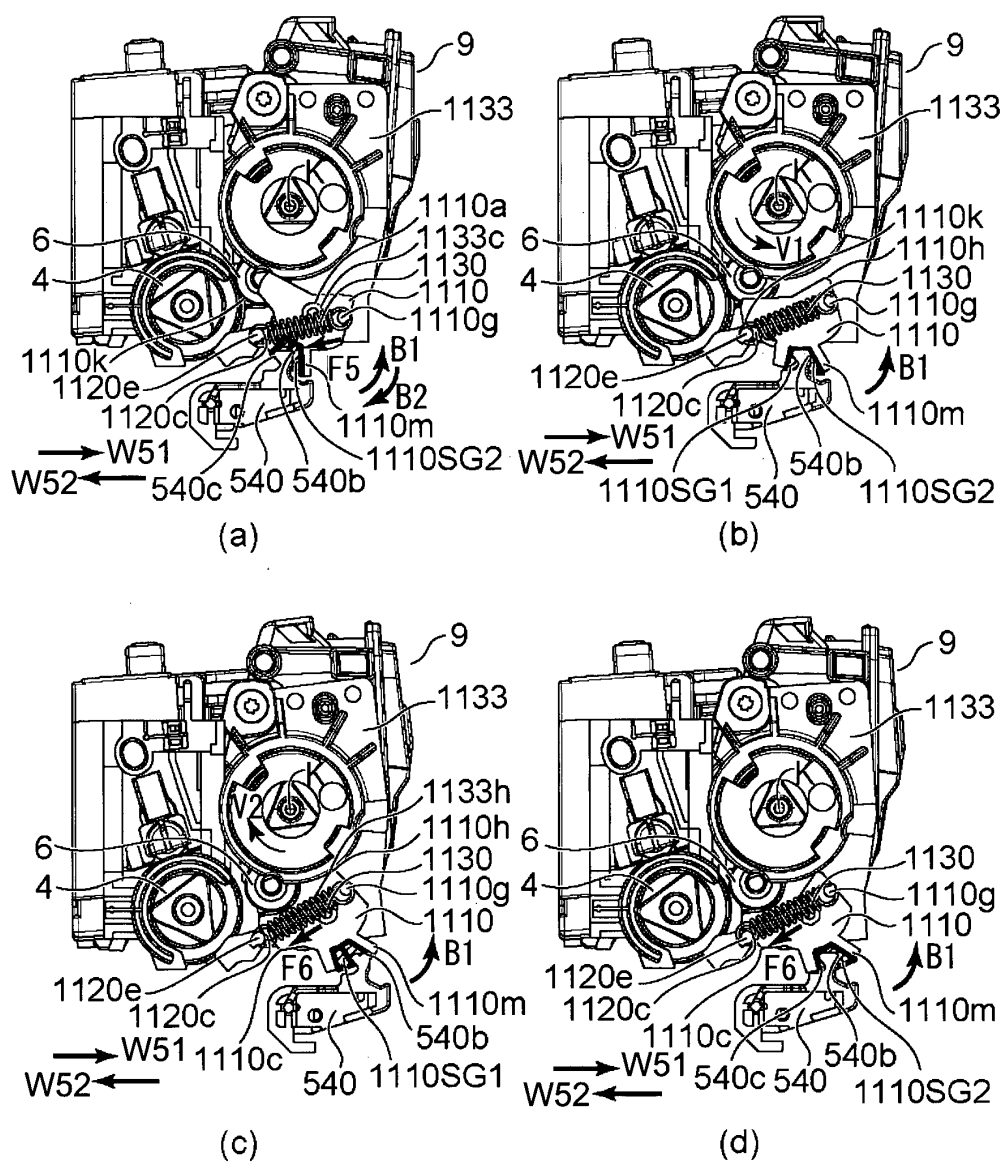


図212

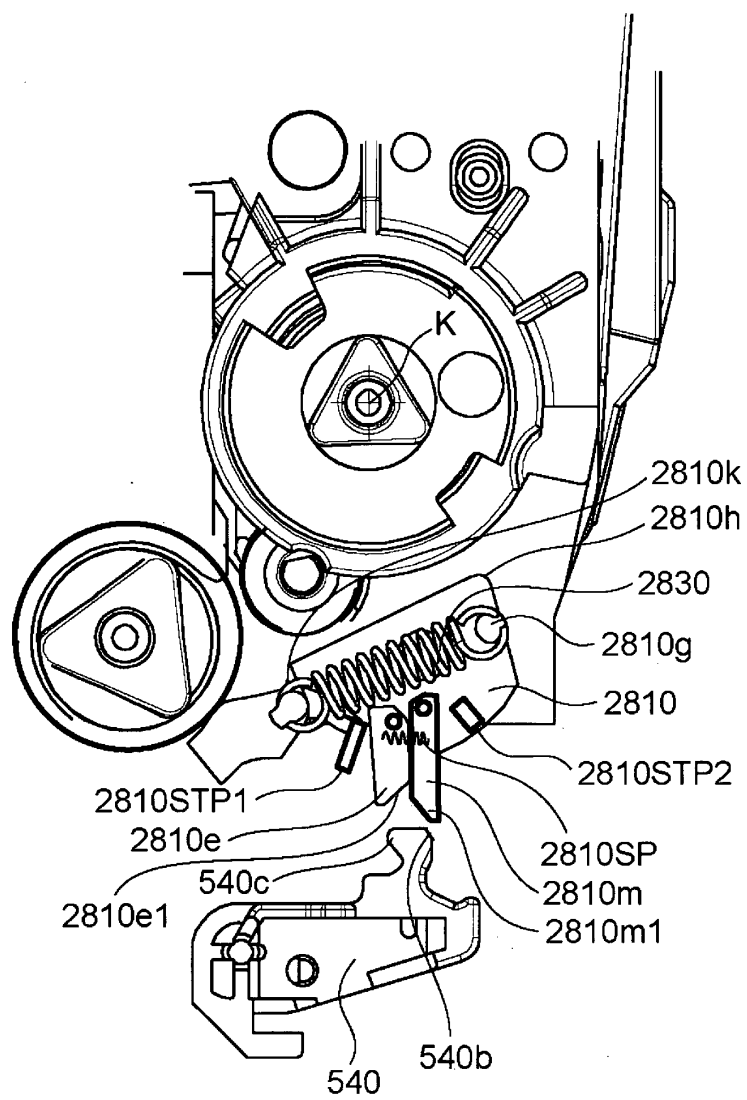


図213

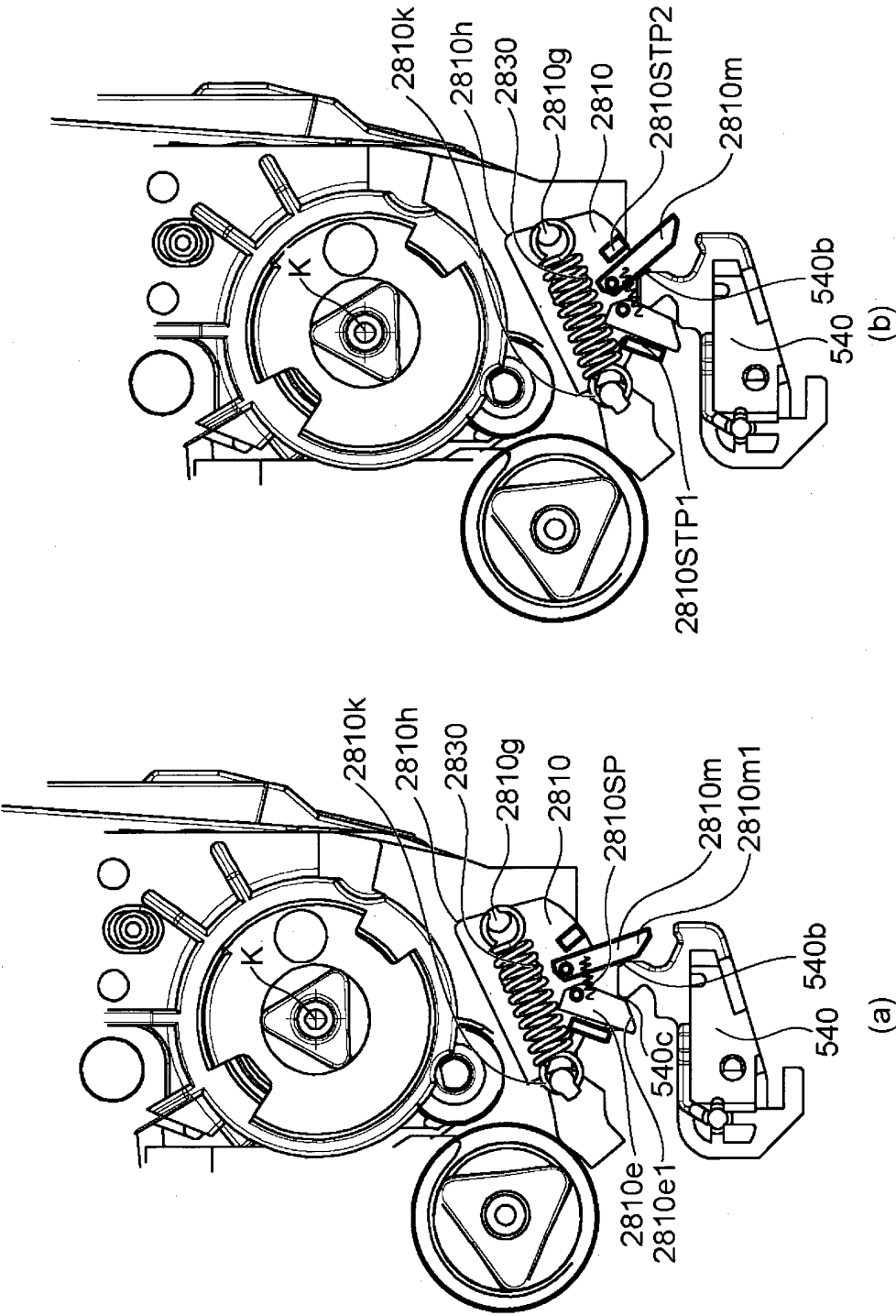


图214

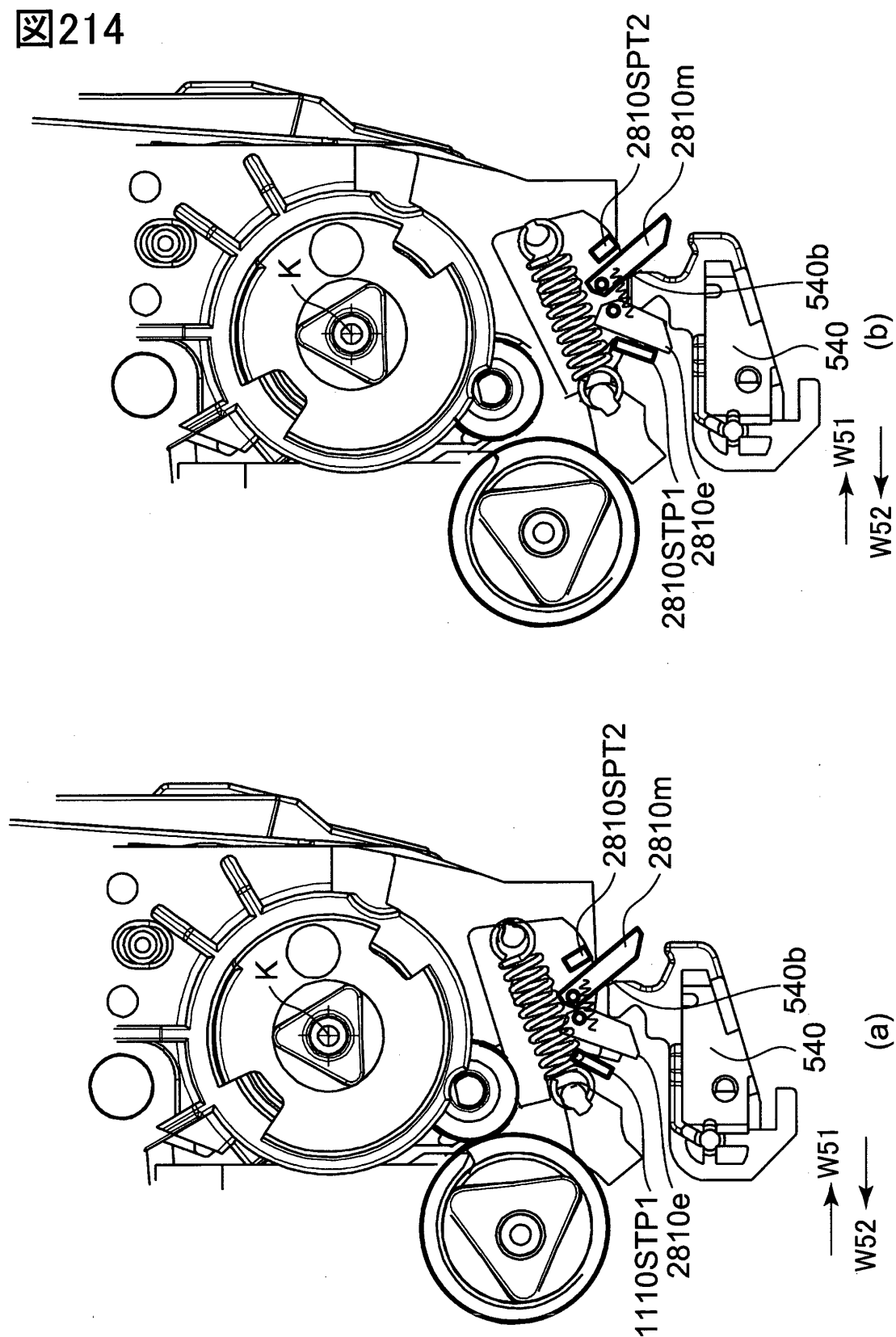


図215

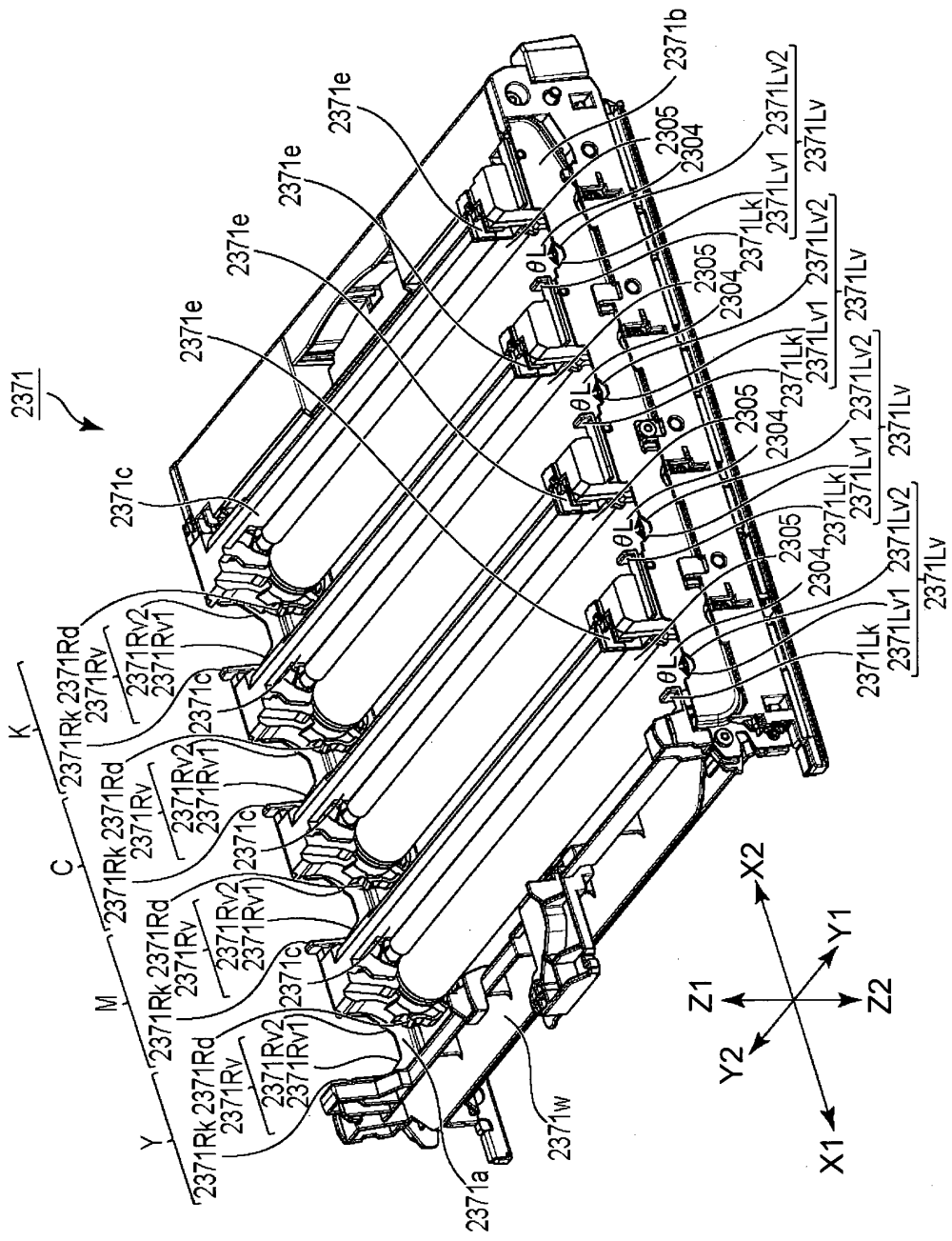


图217

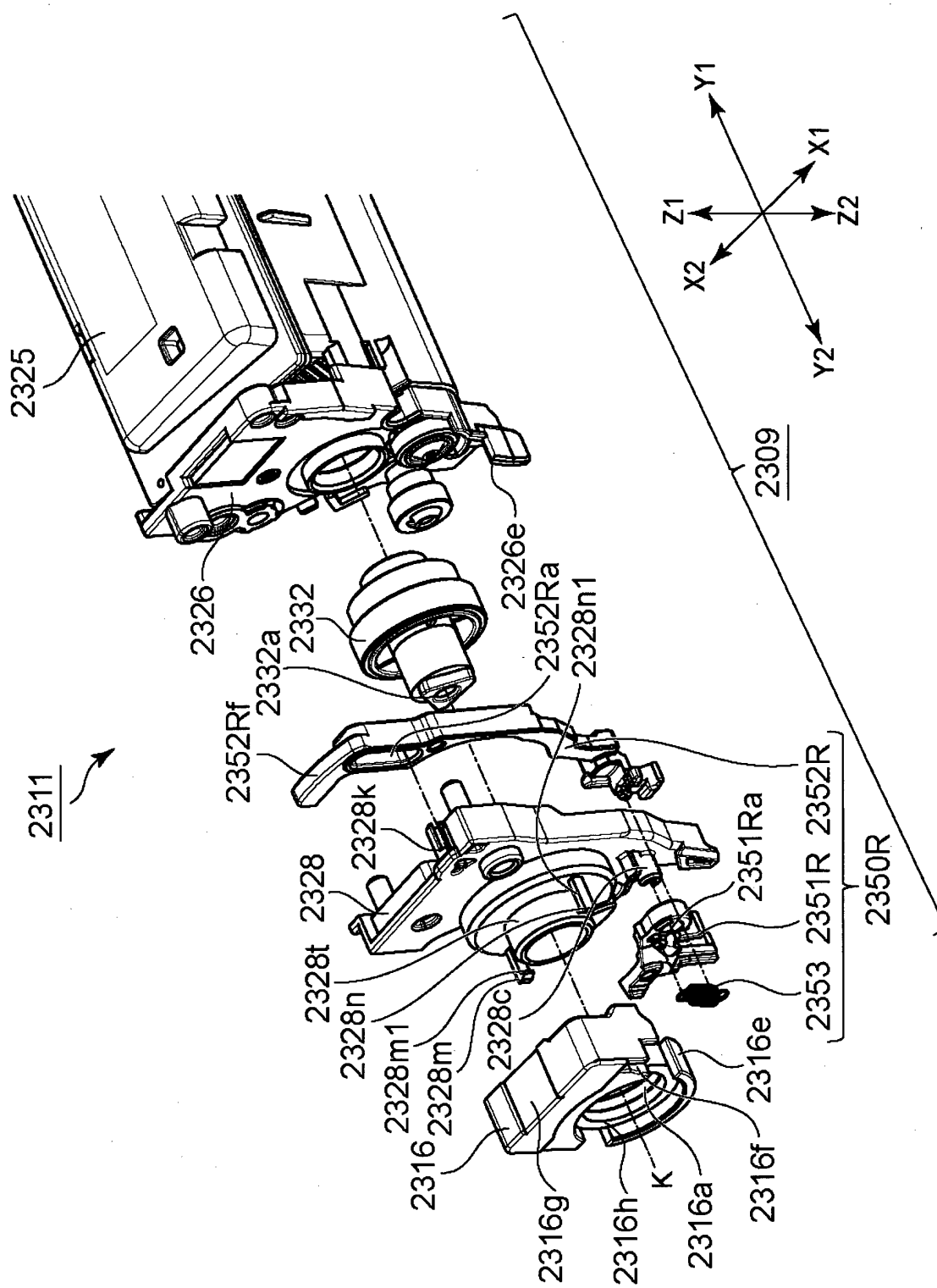


图218

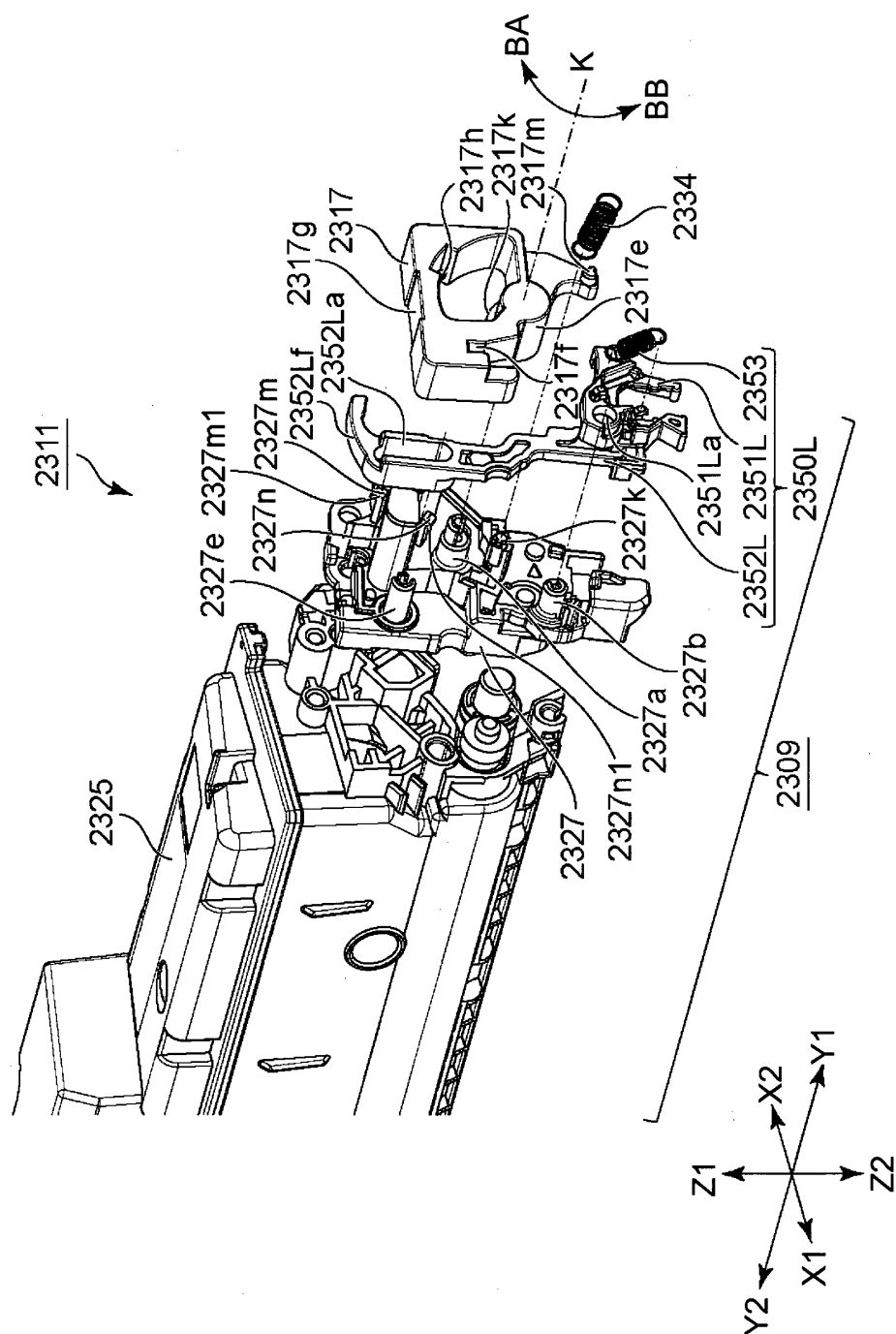


図219

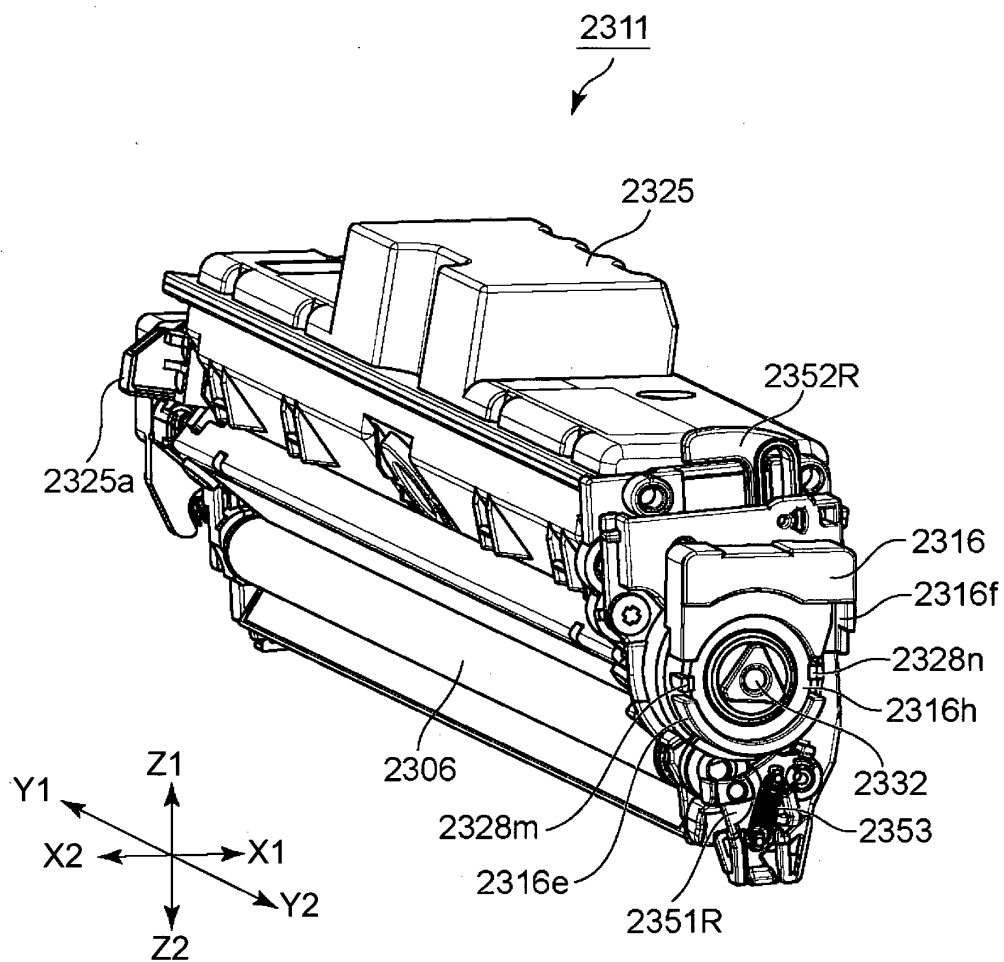
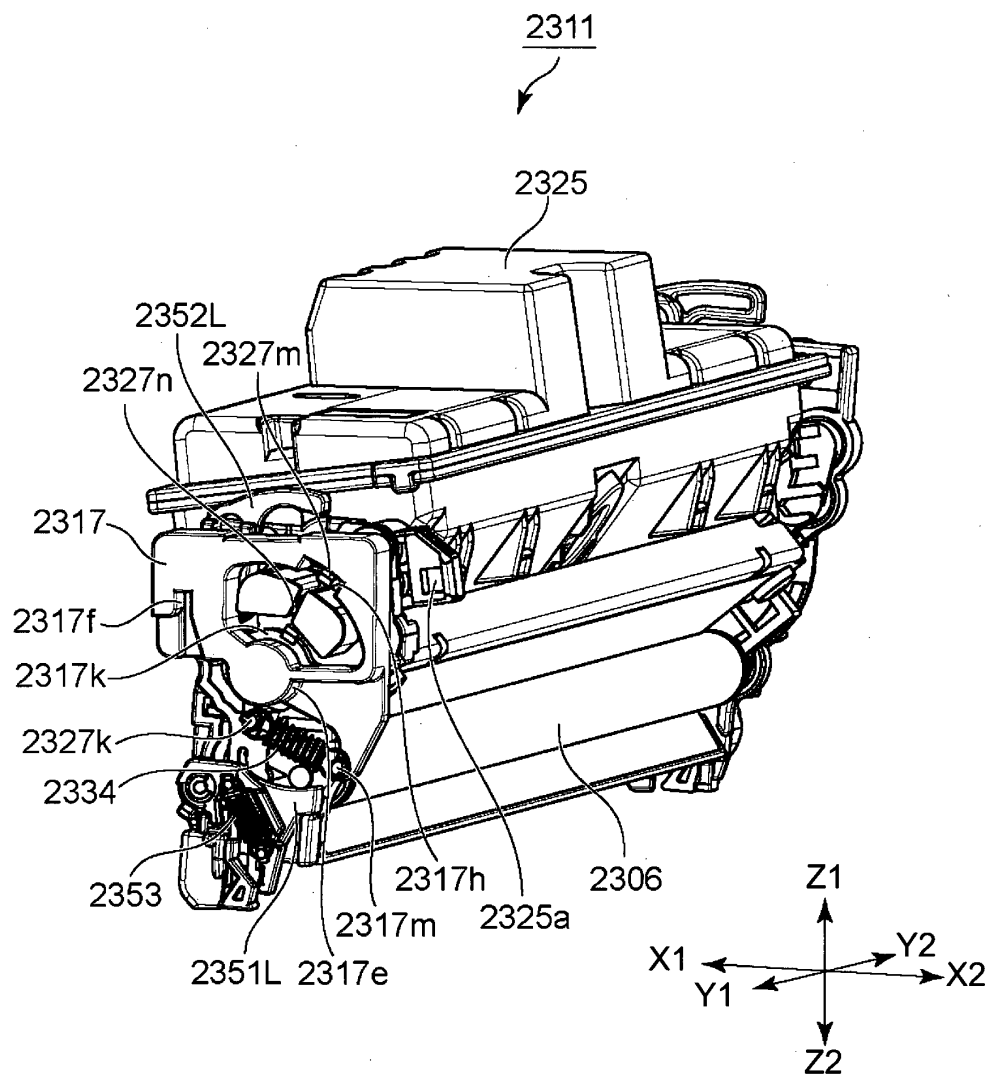
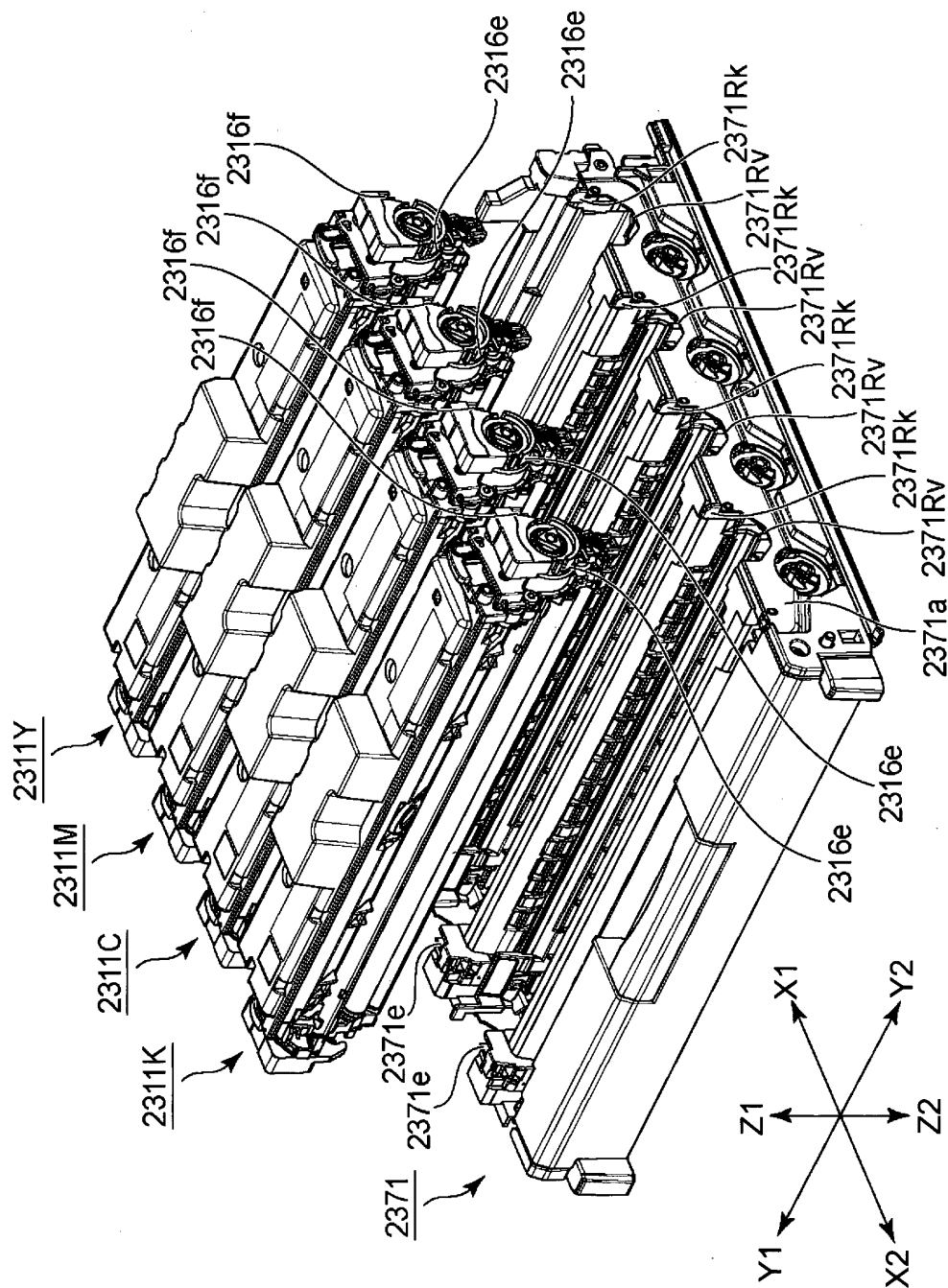


図220



221



222

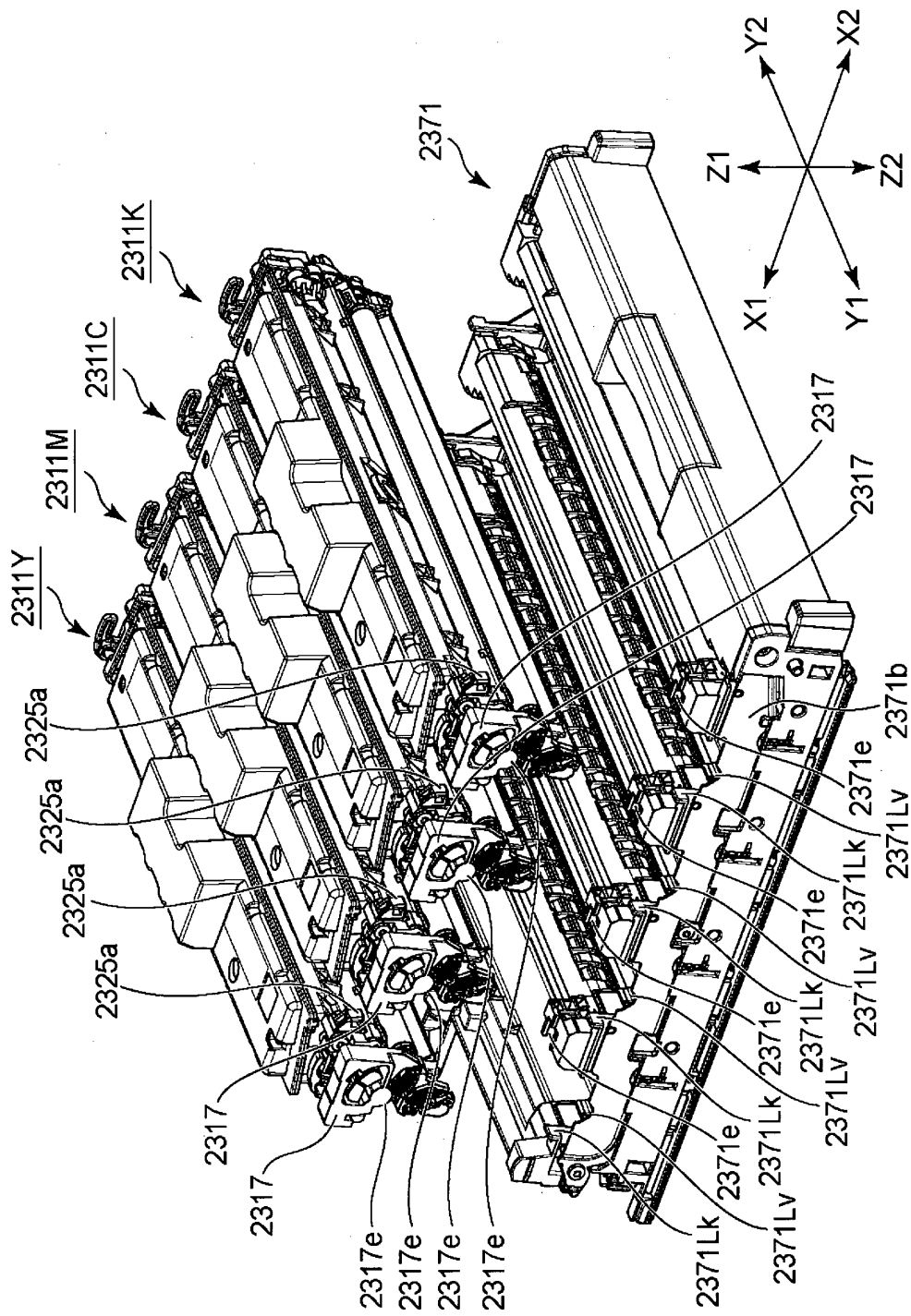


図223

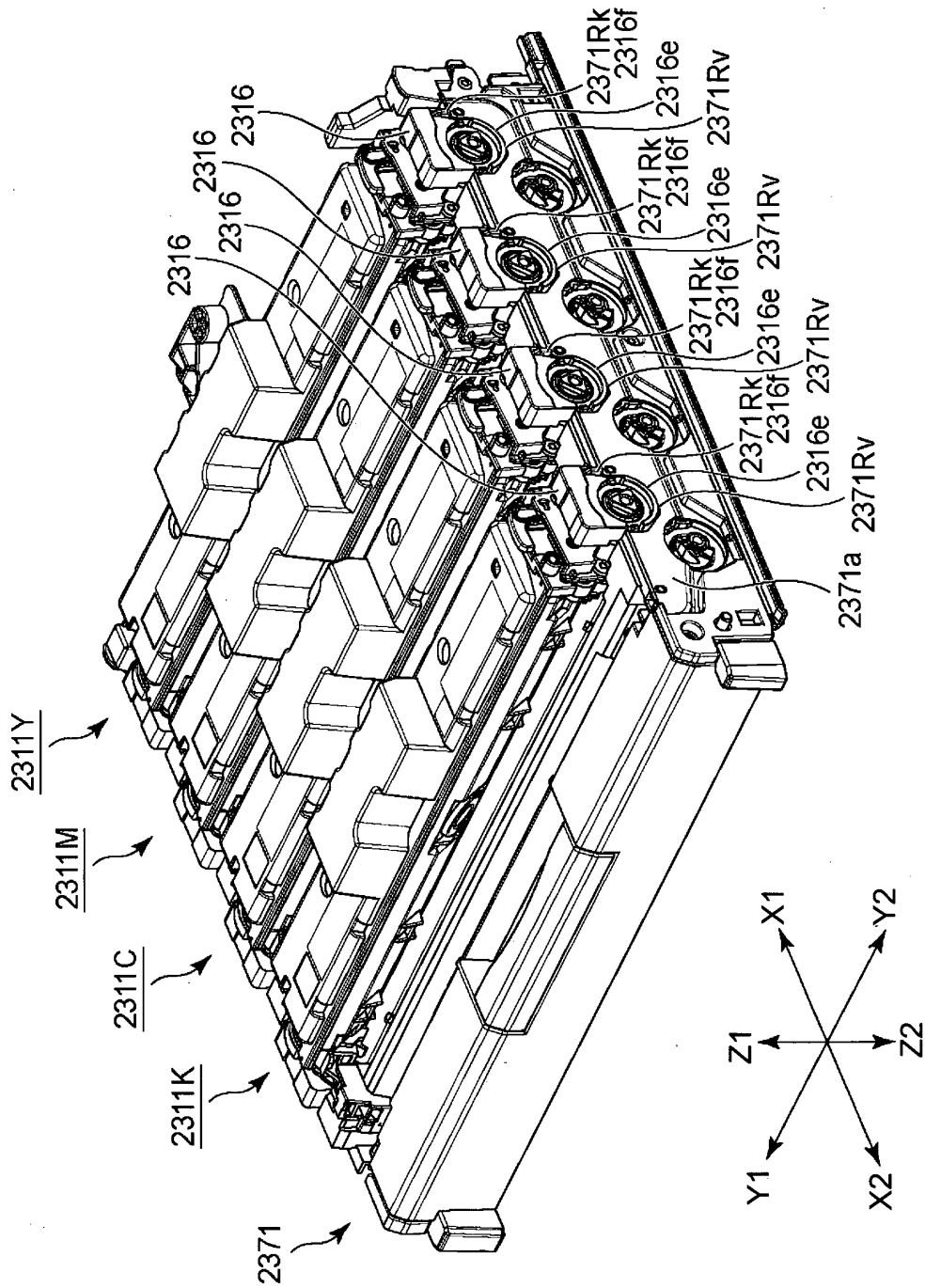


图224

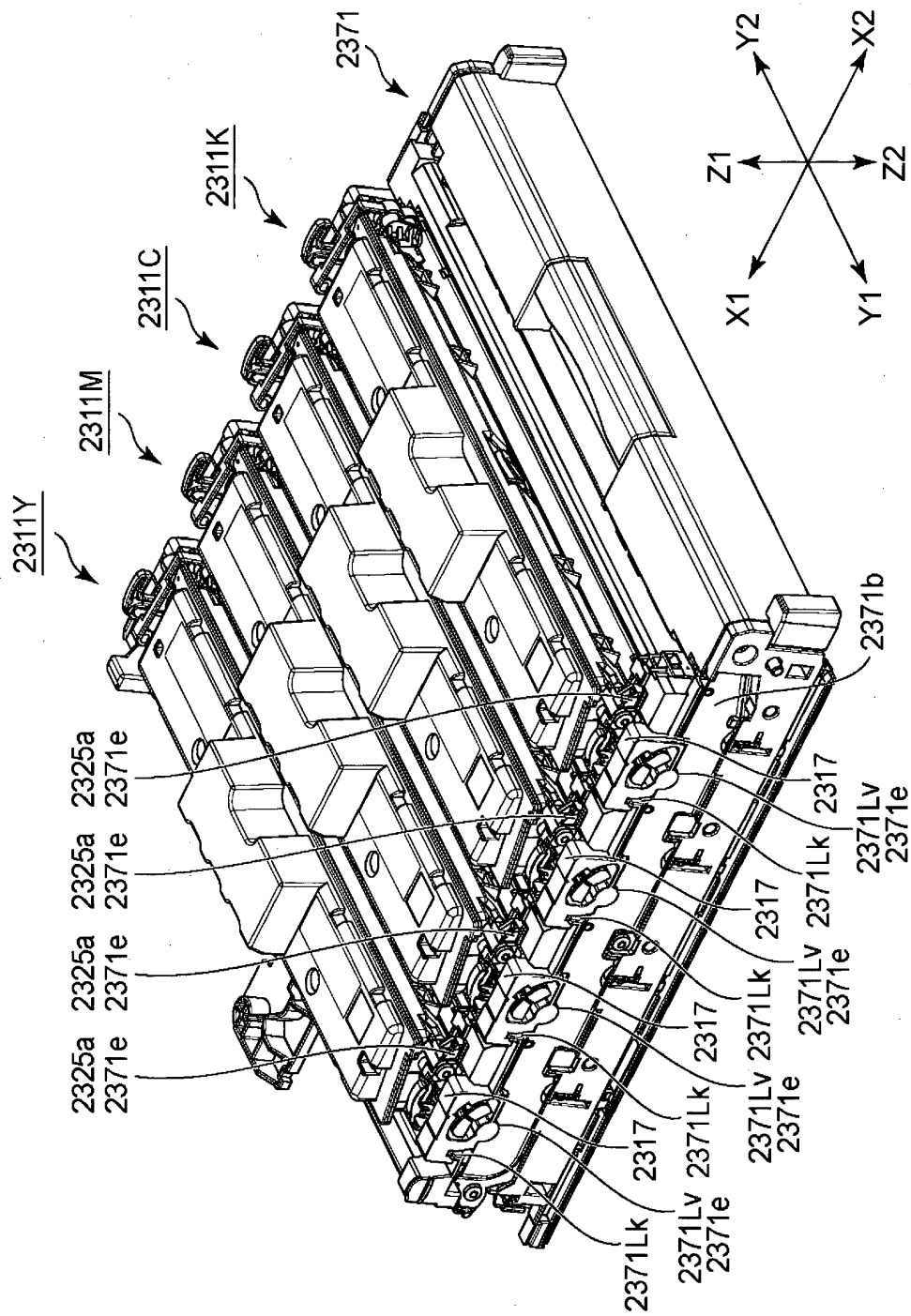


图 225

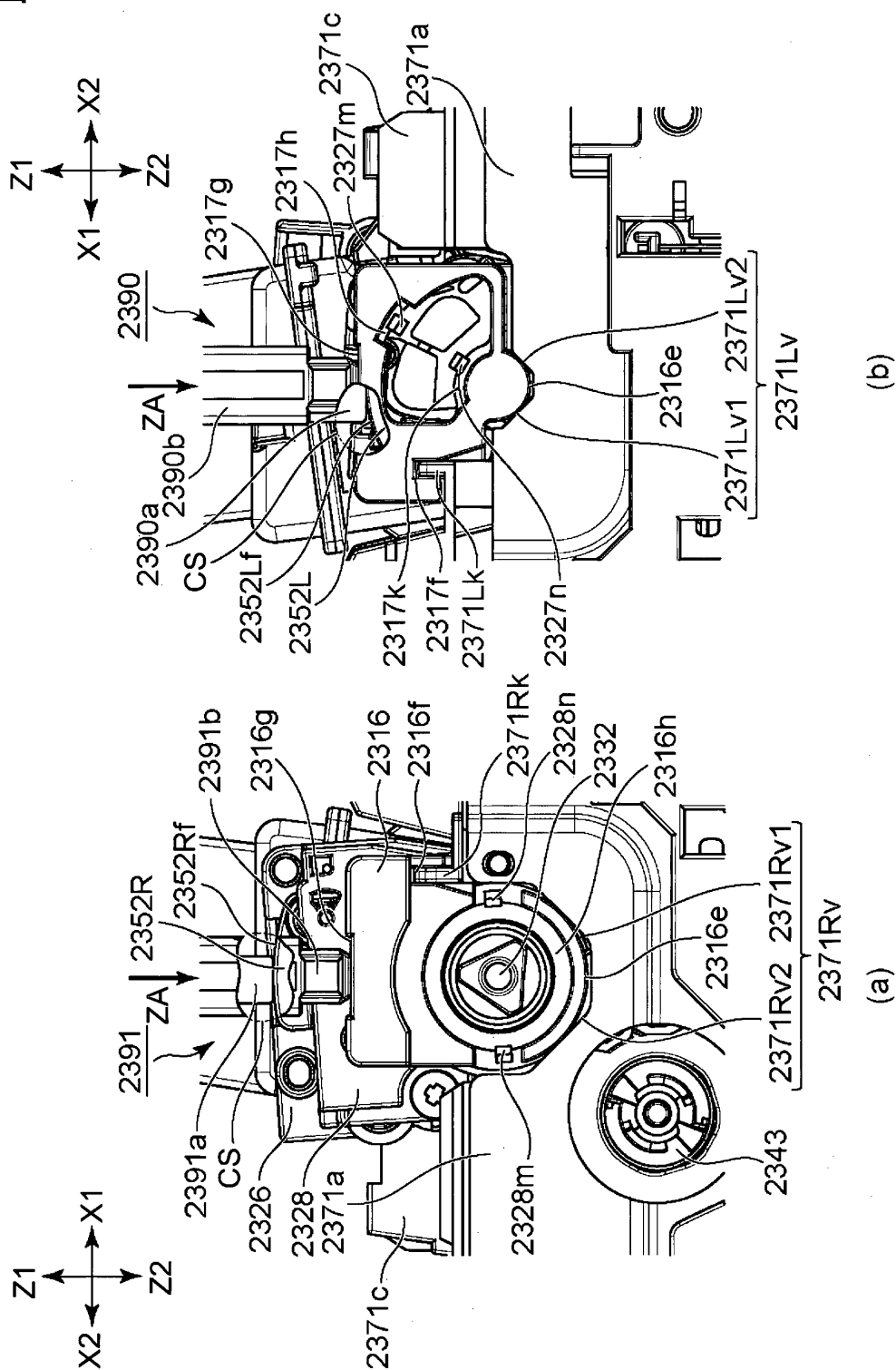


図226

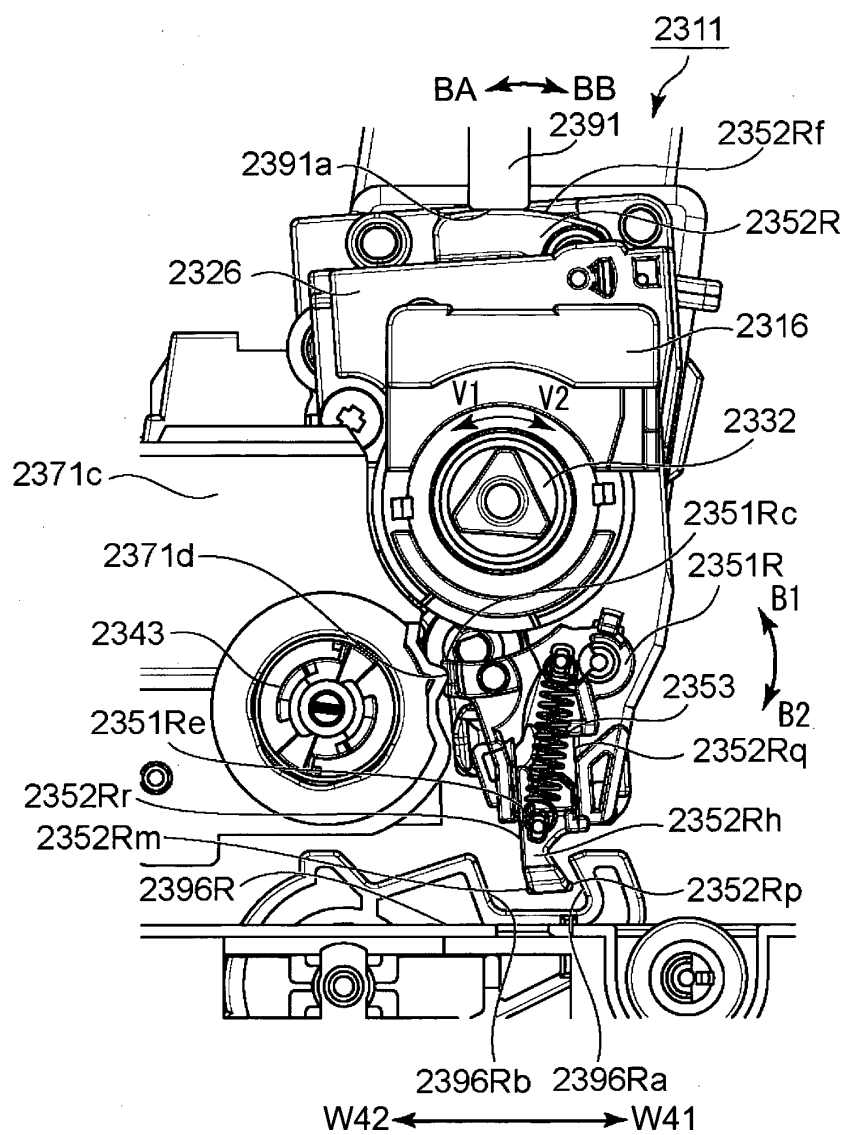


図227

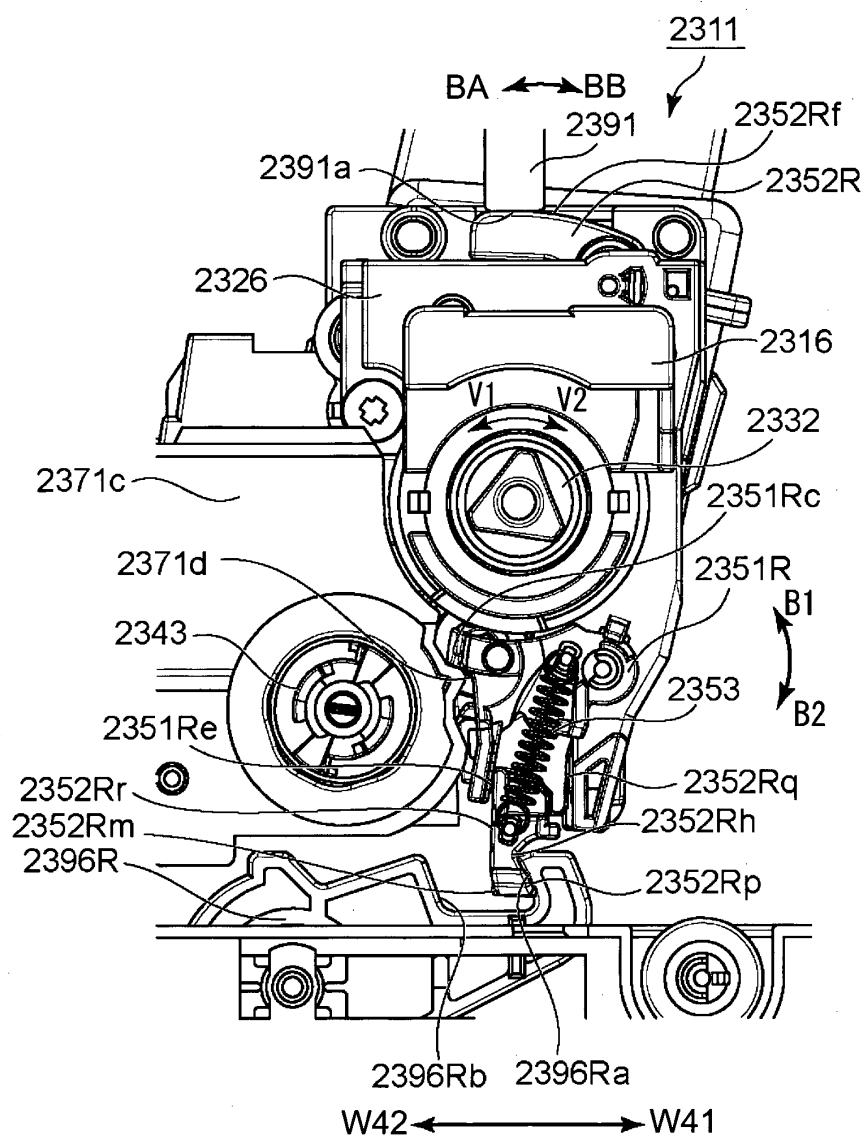


図228

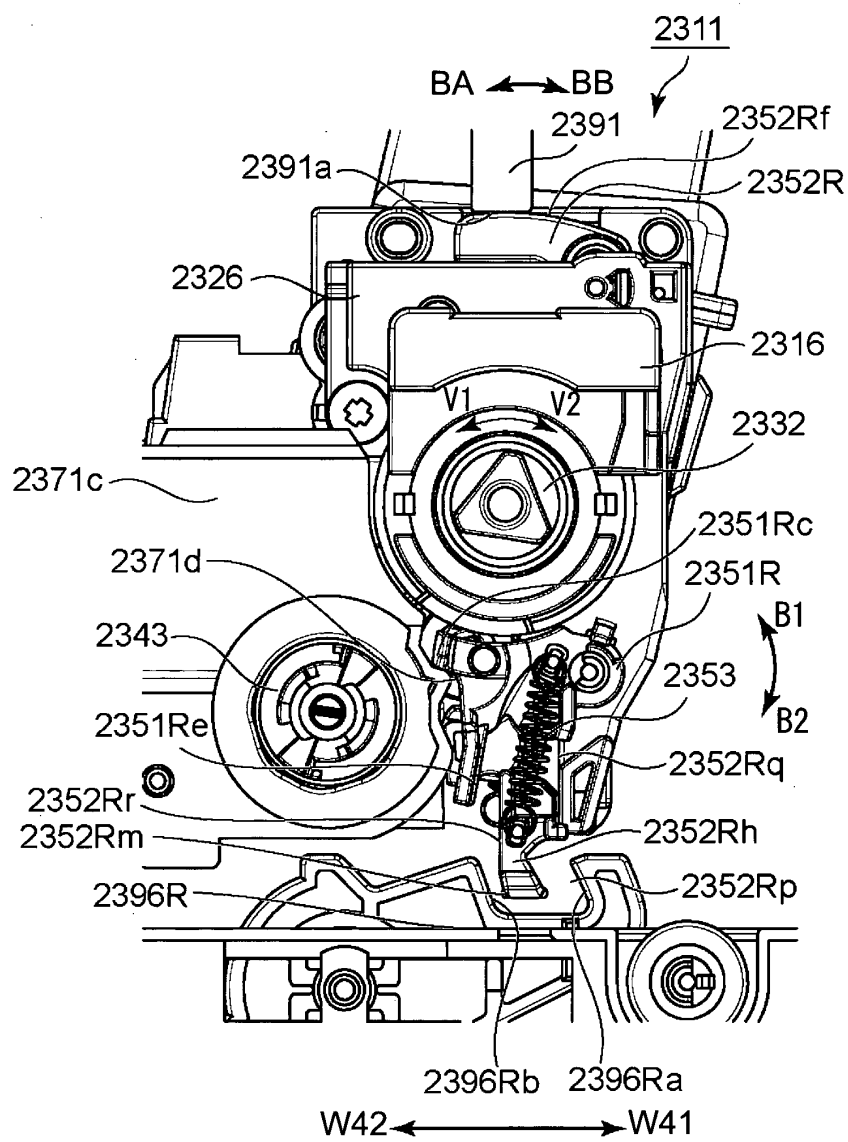
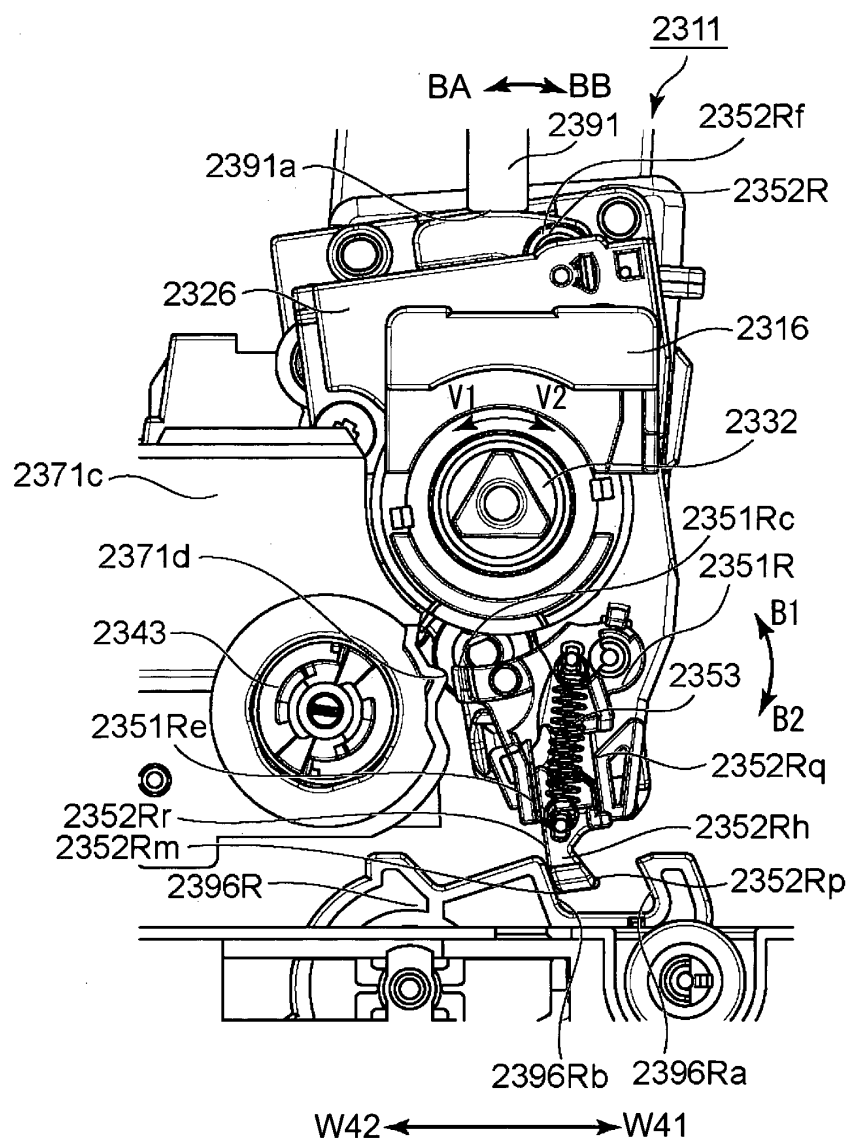


図229



230

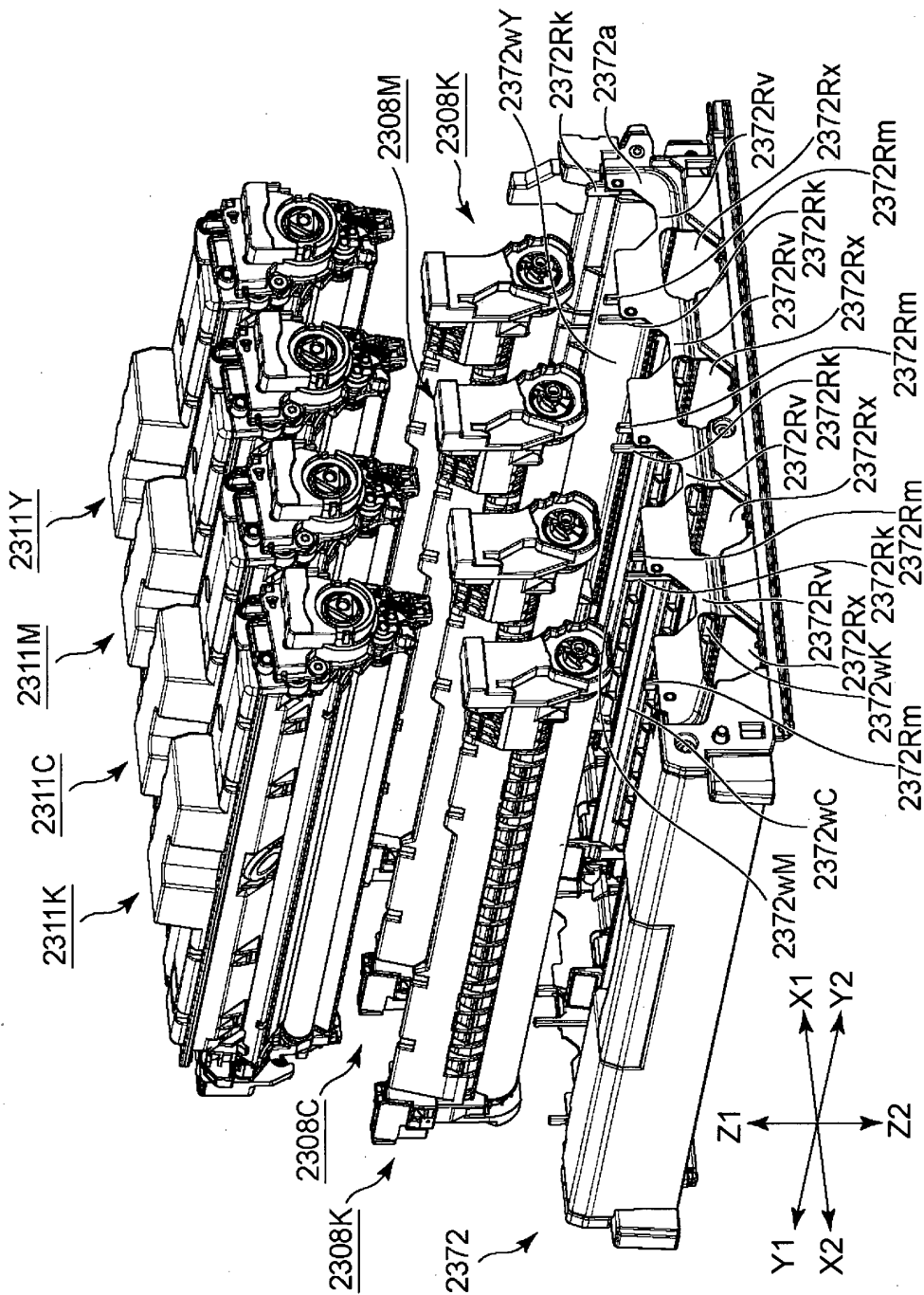
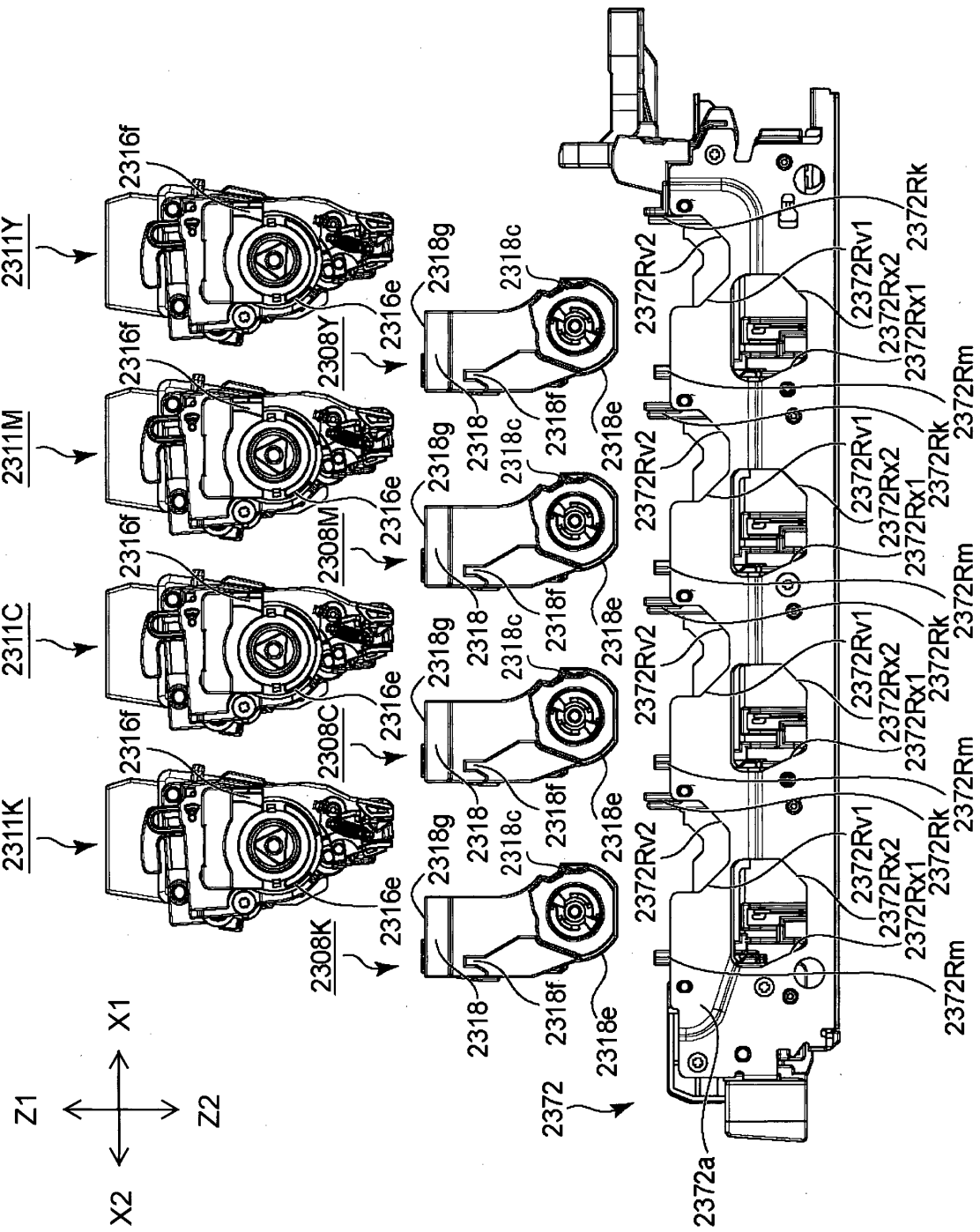


図231



232

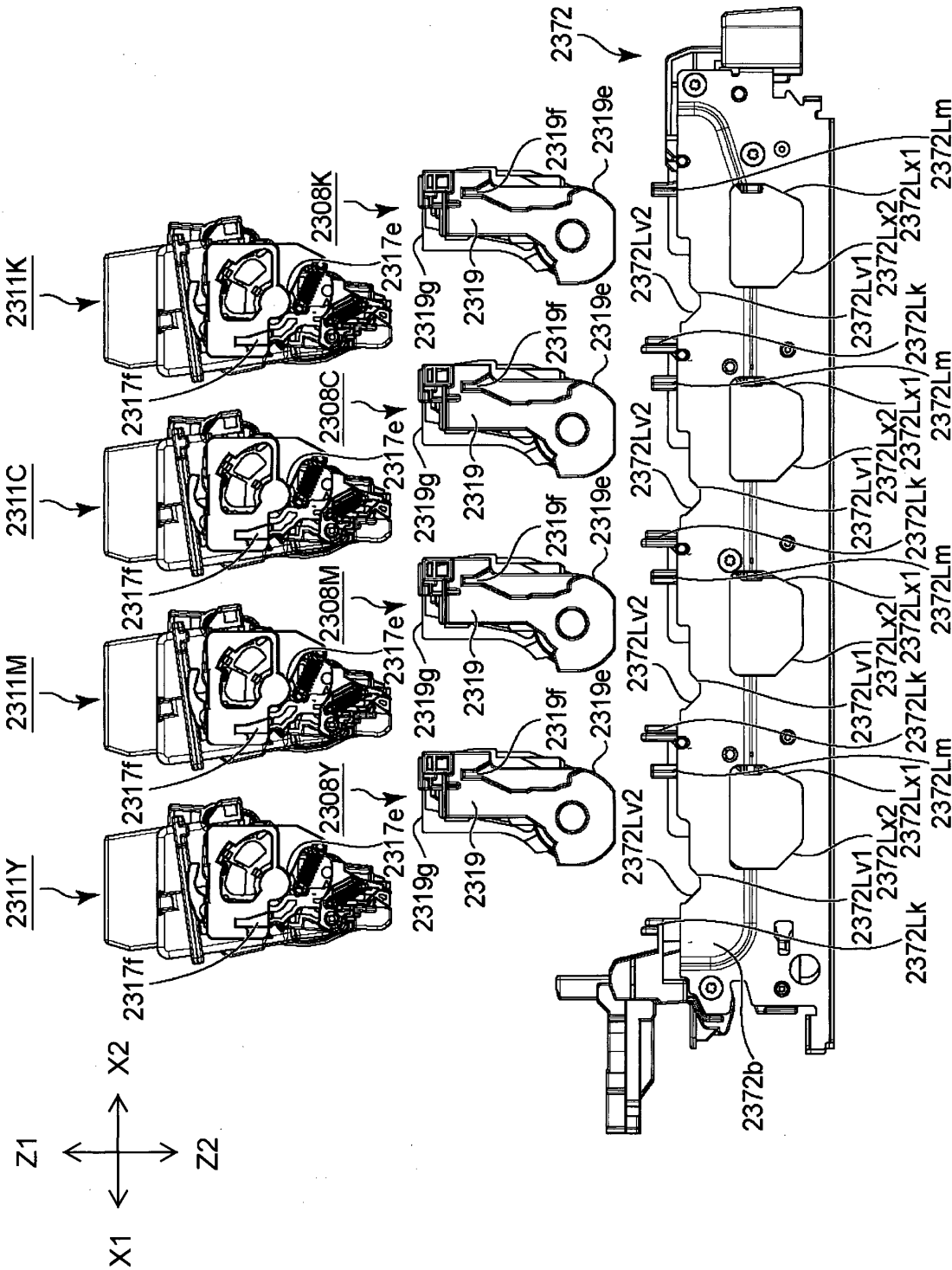


図 233

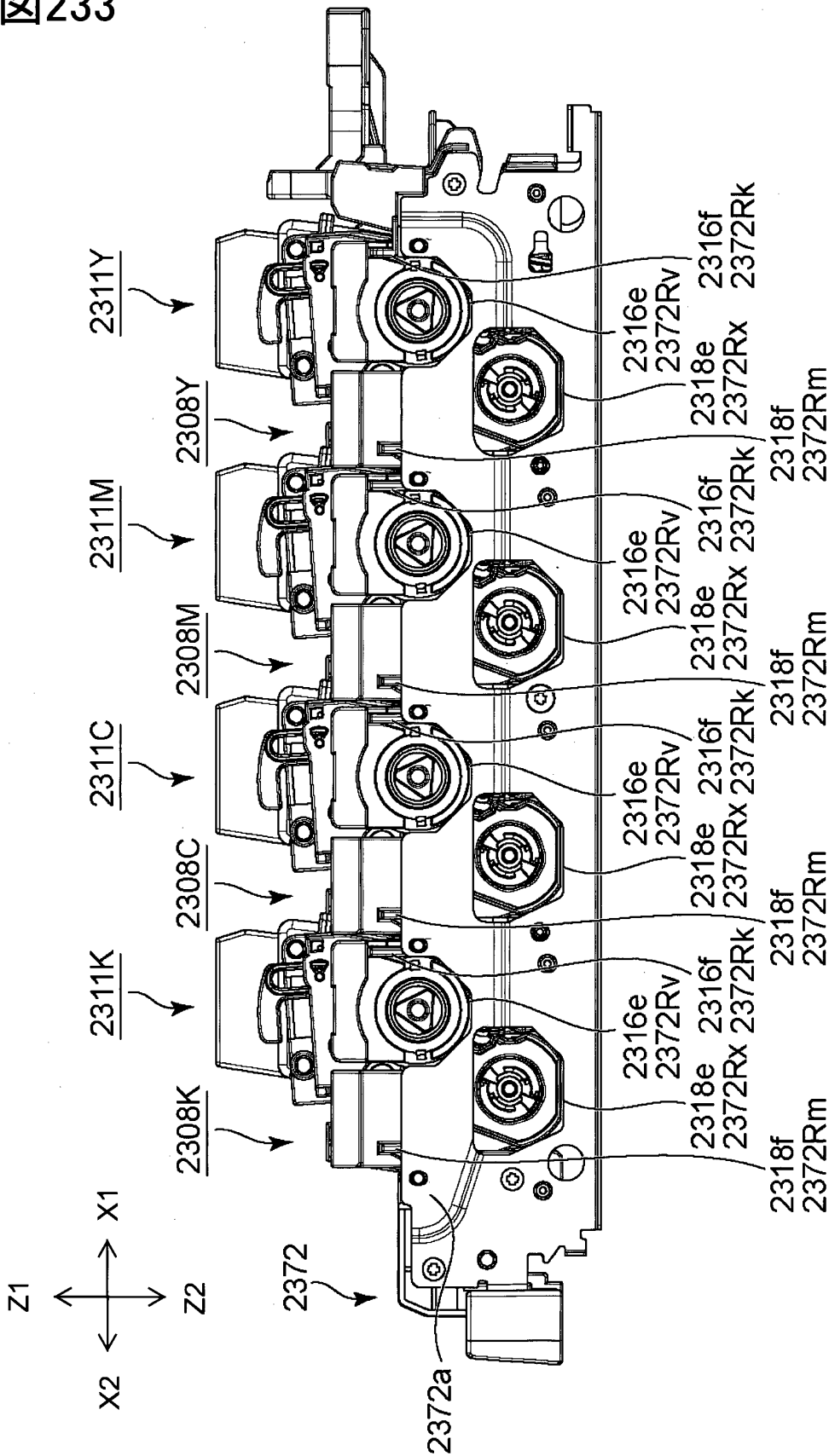


図234

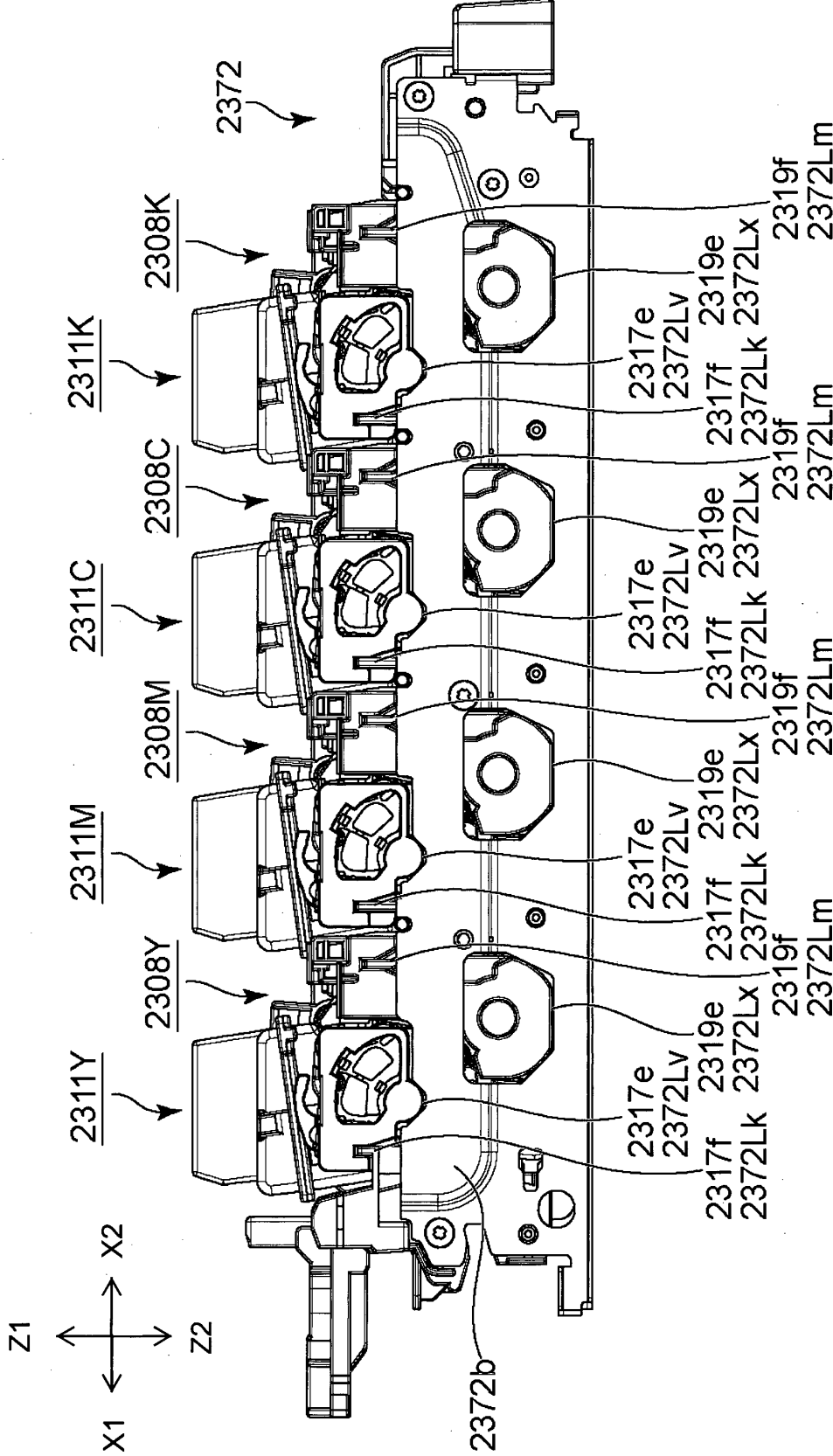


図235

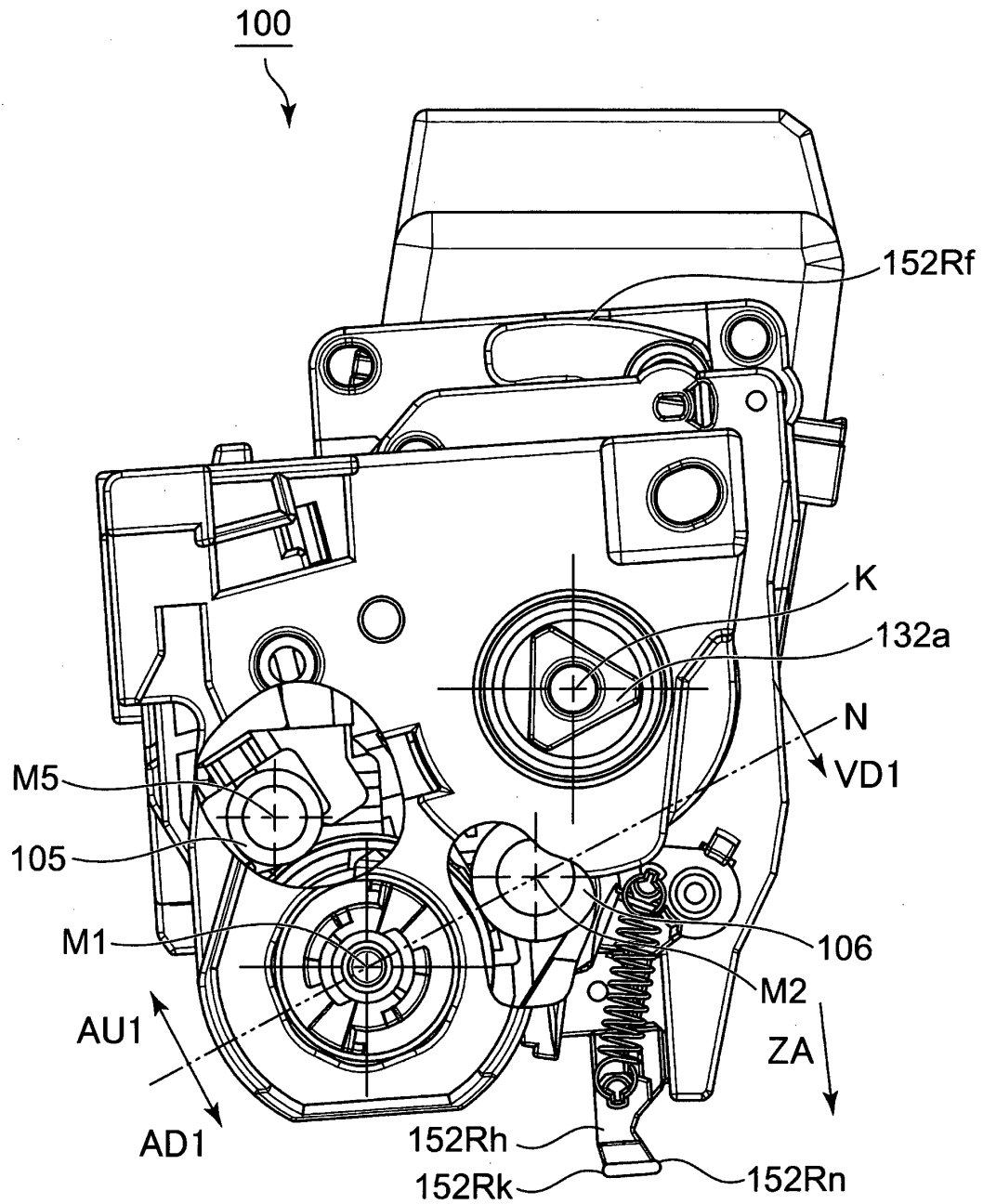


図236

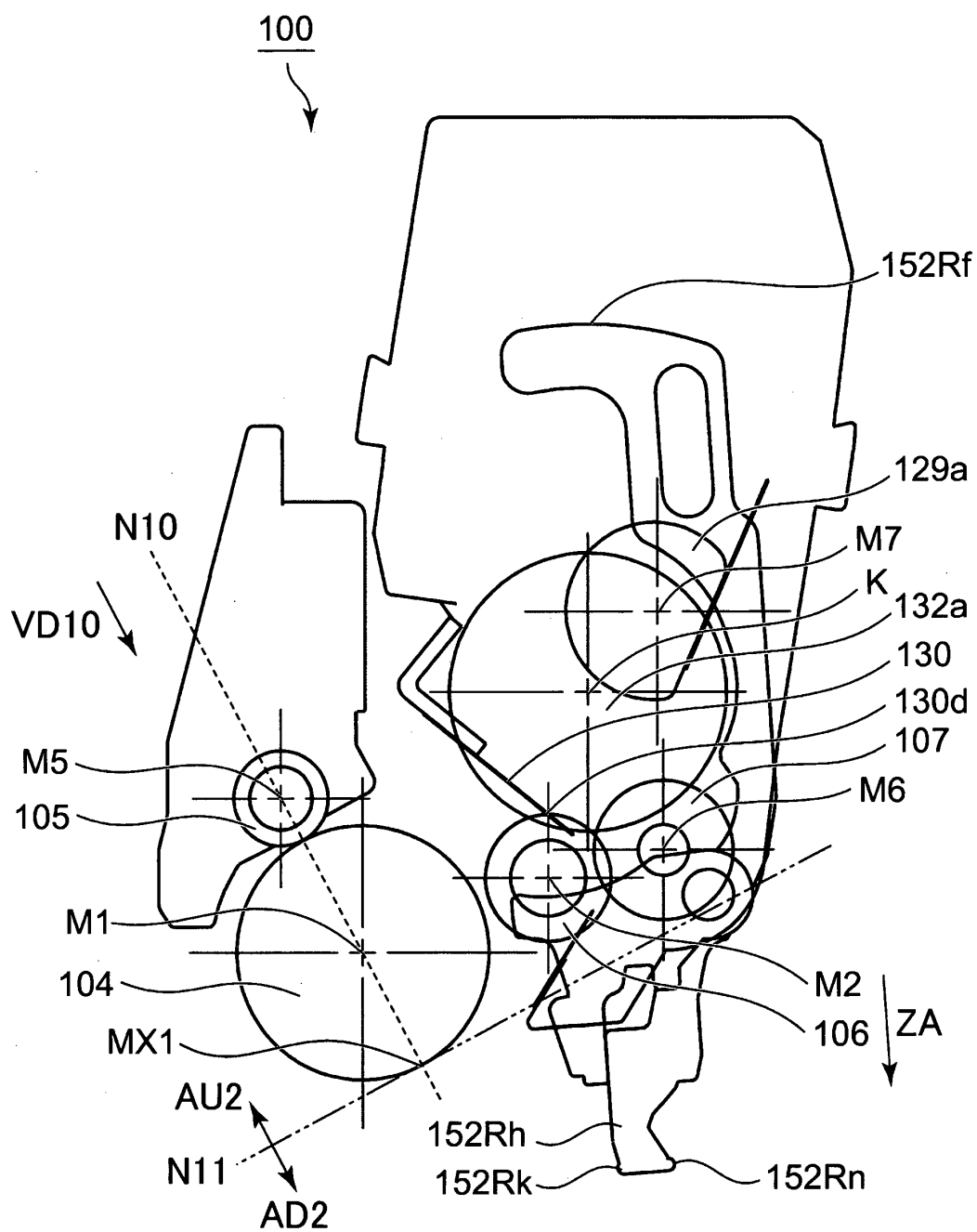


図237

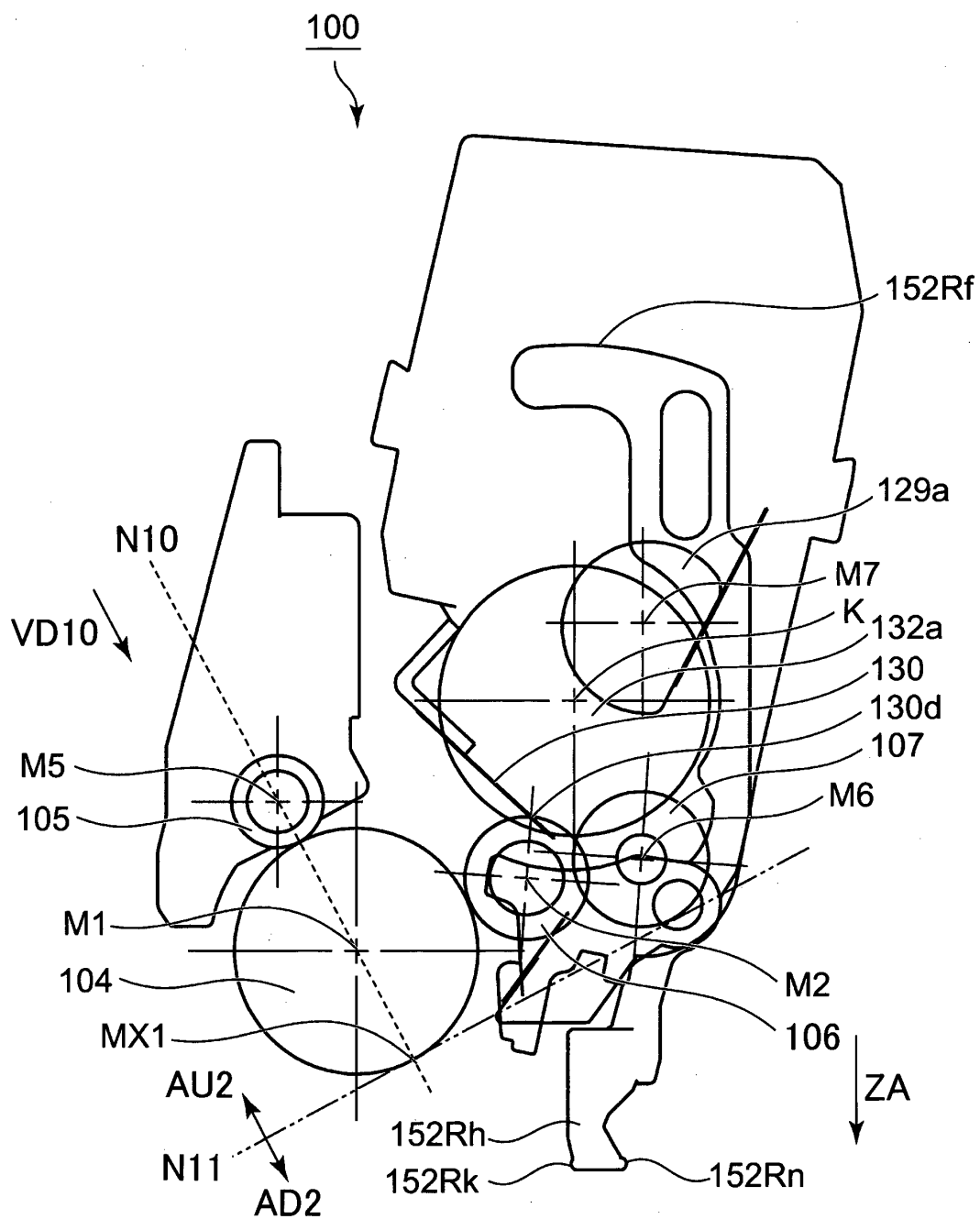


図238

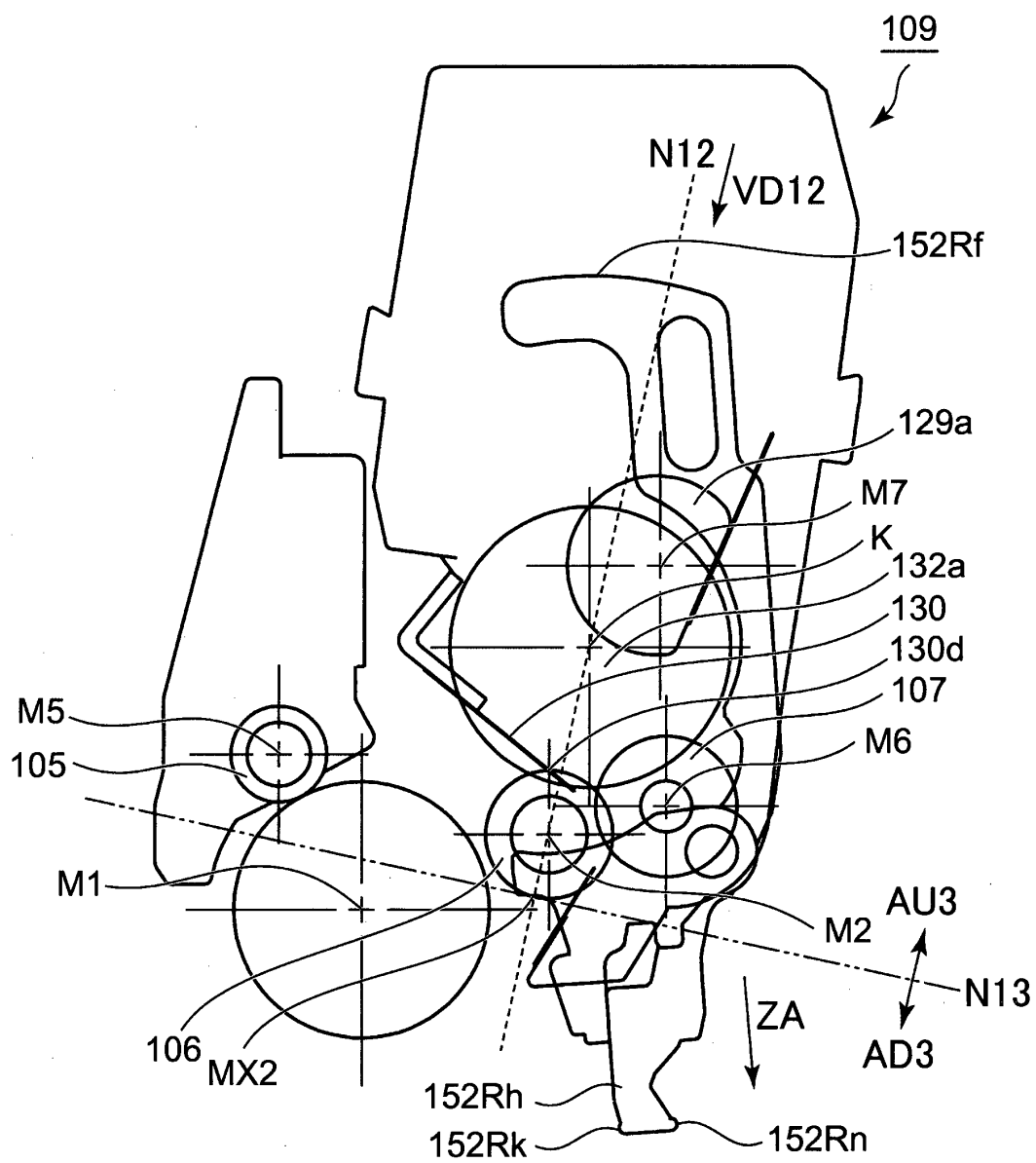


図239

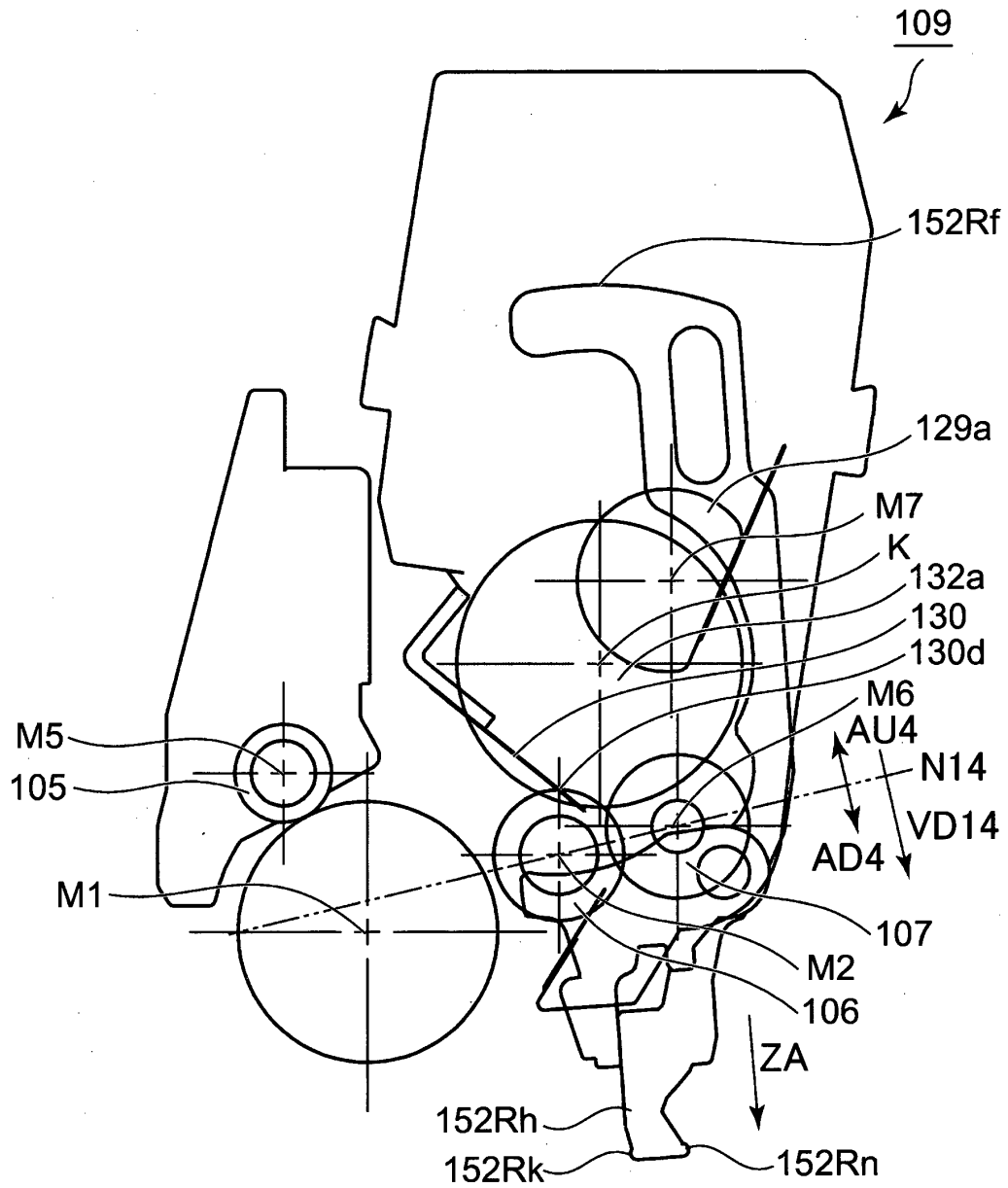


図240

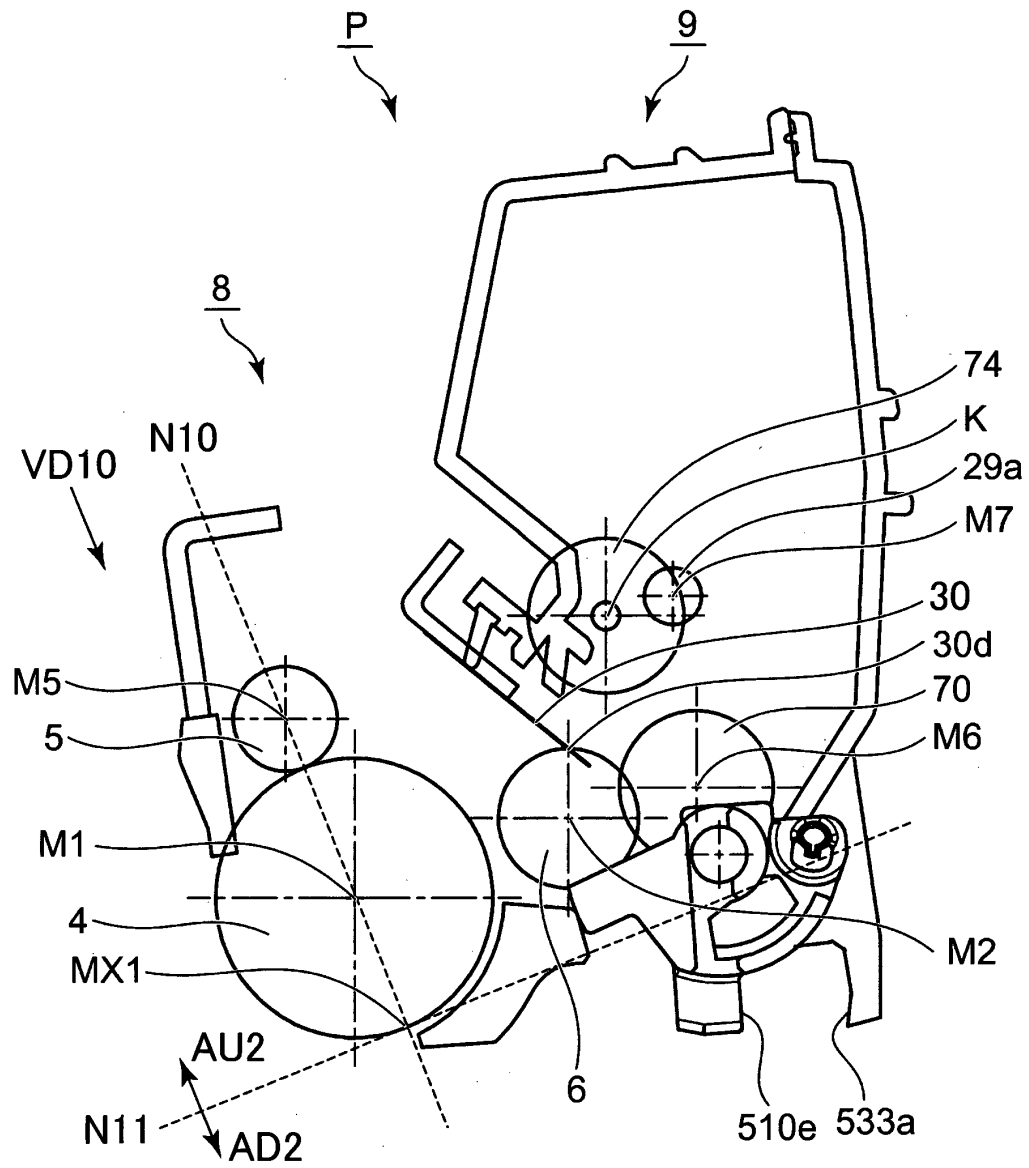


図241

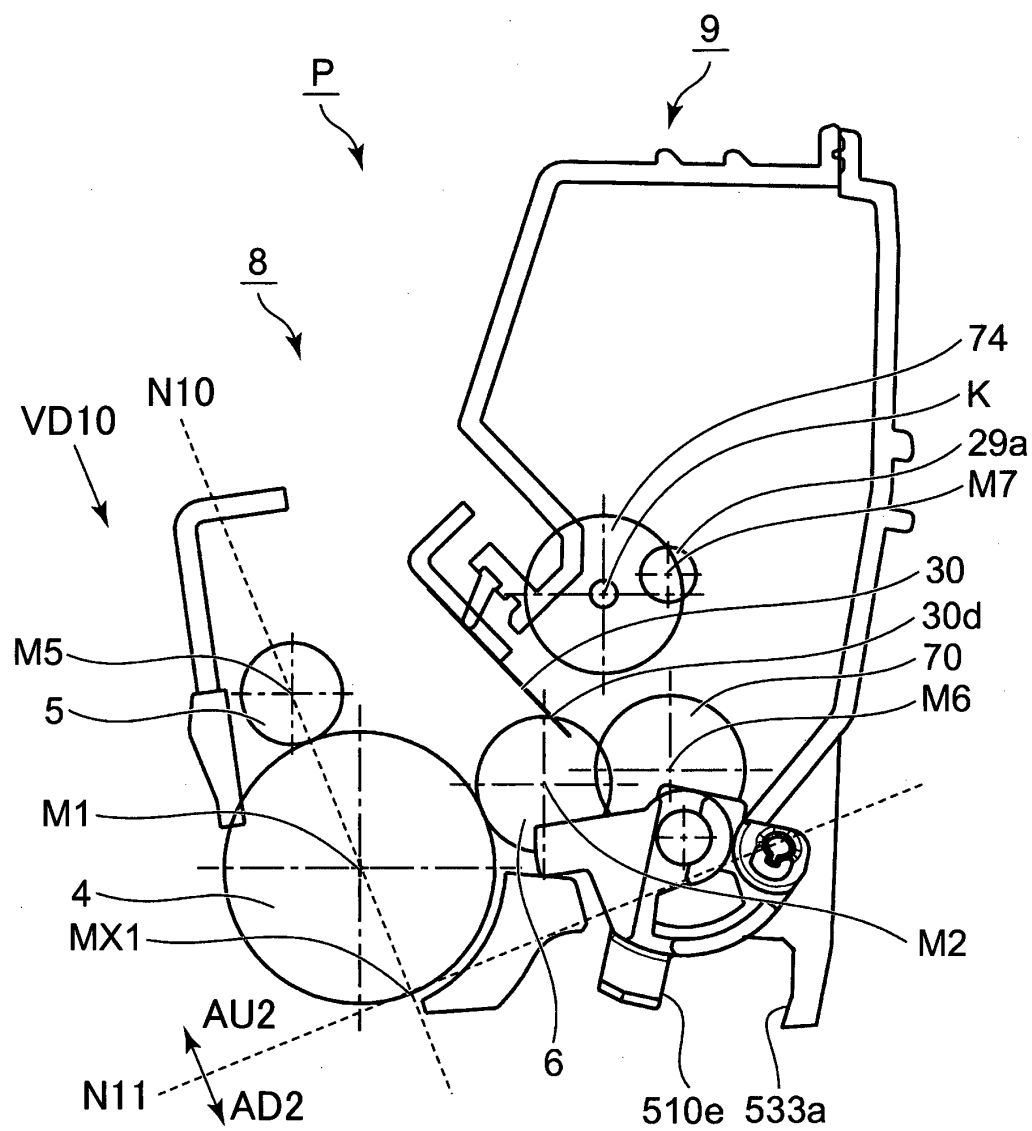


図242

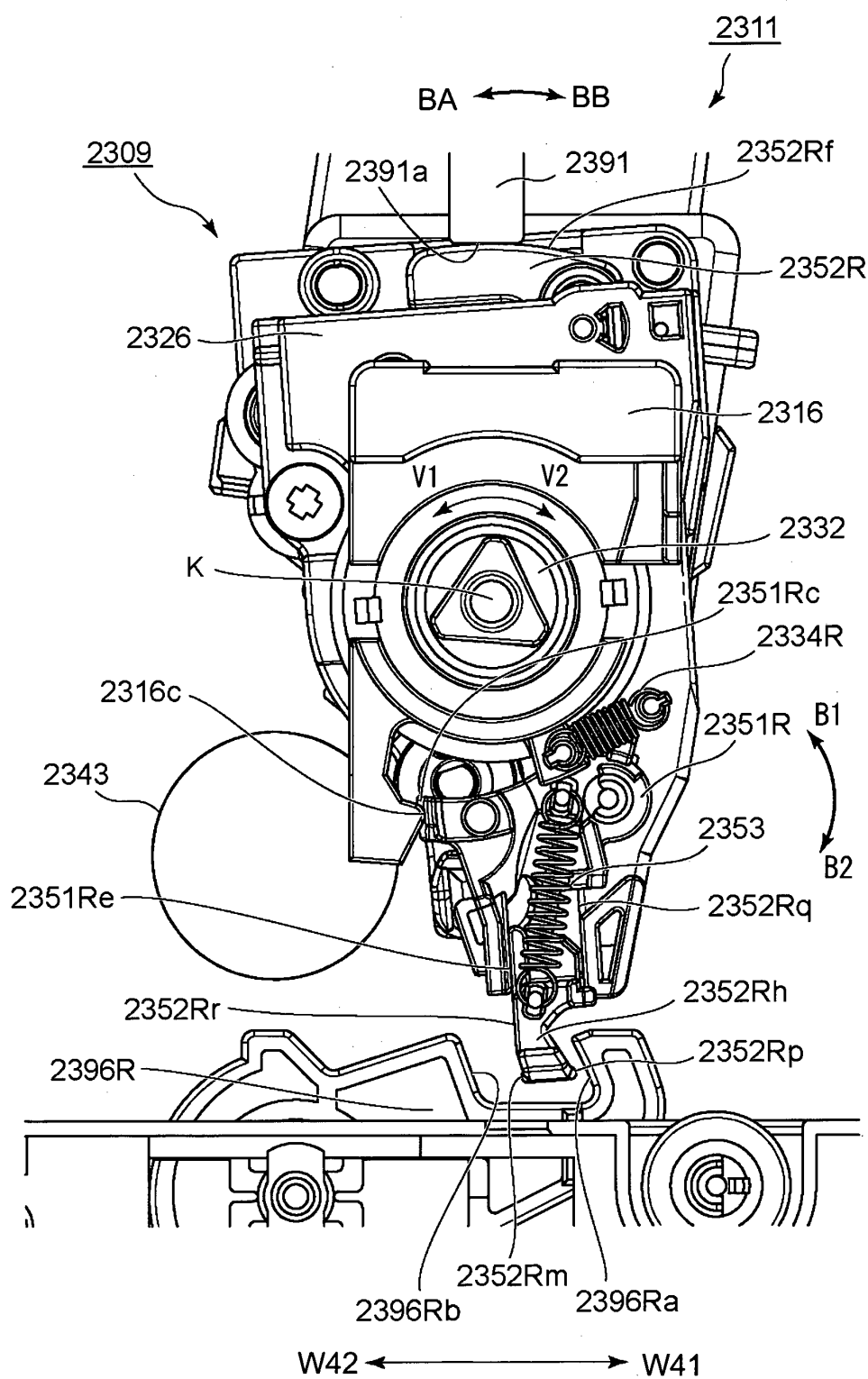


図243

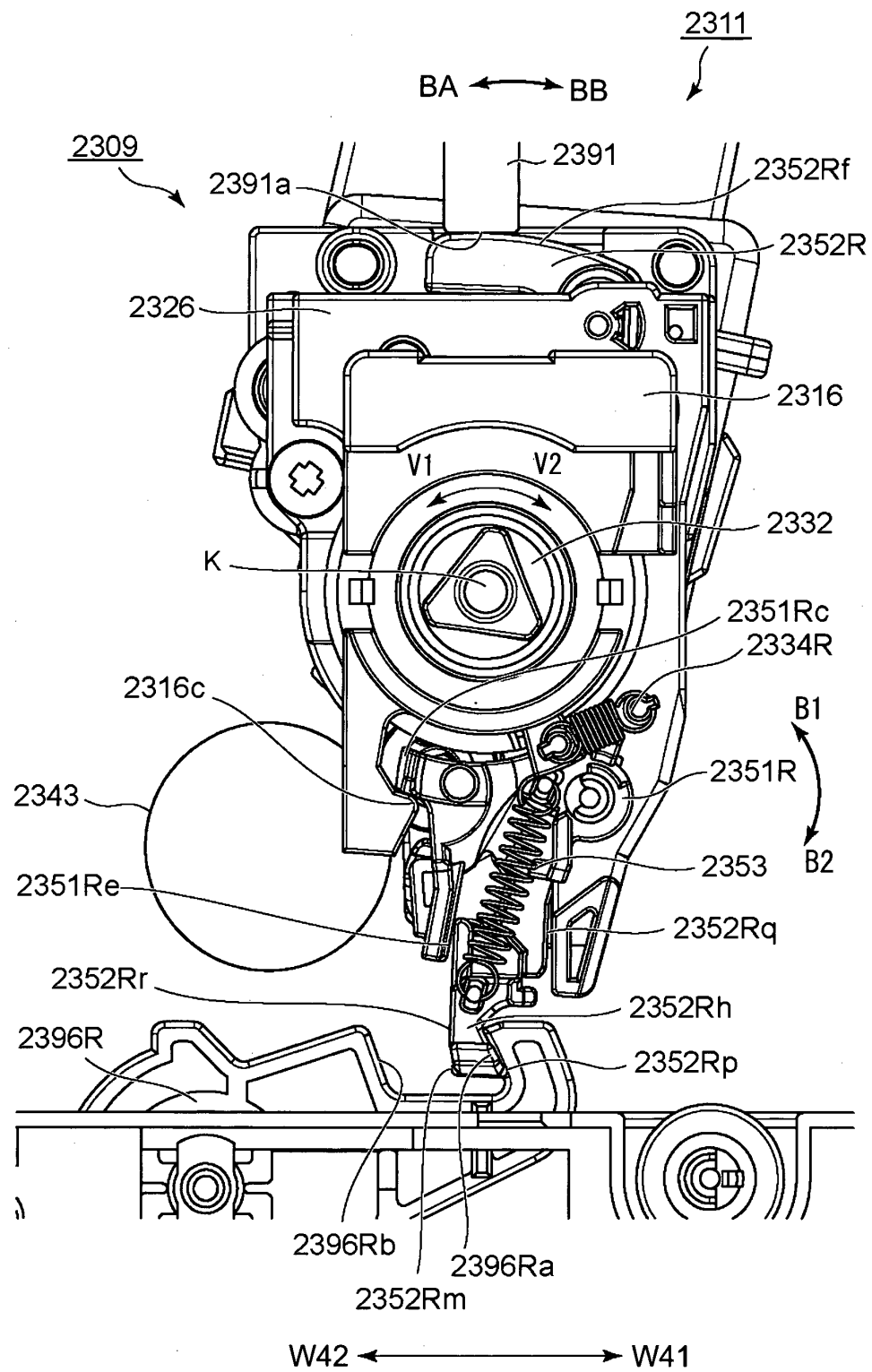


図244

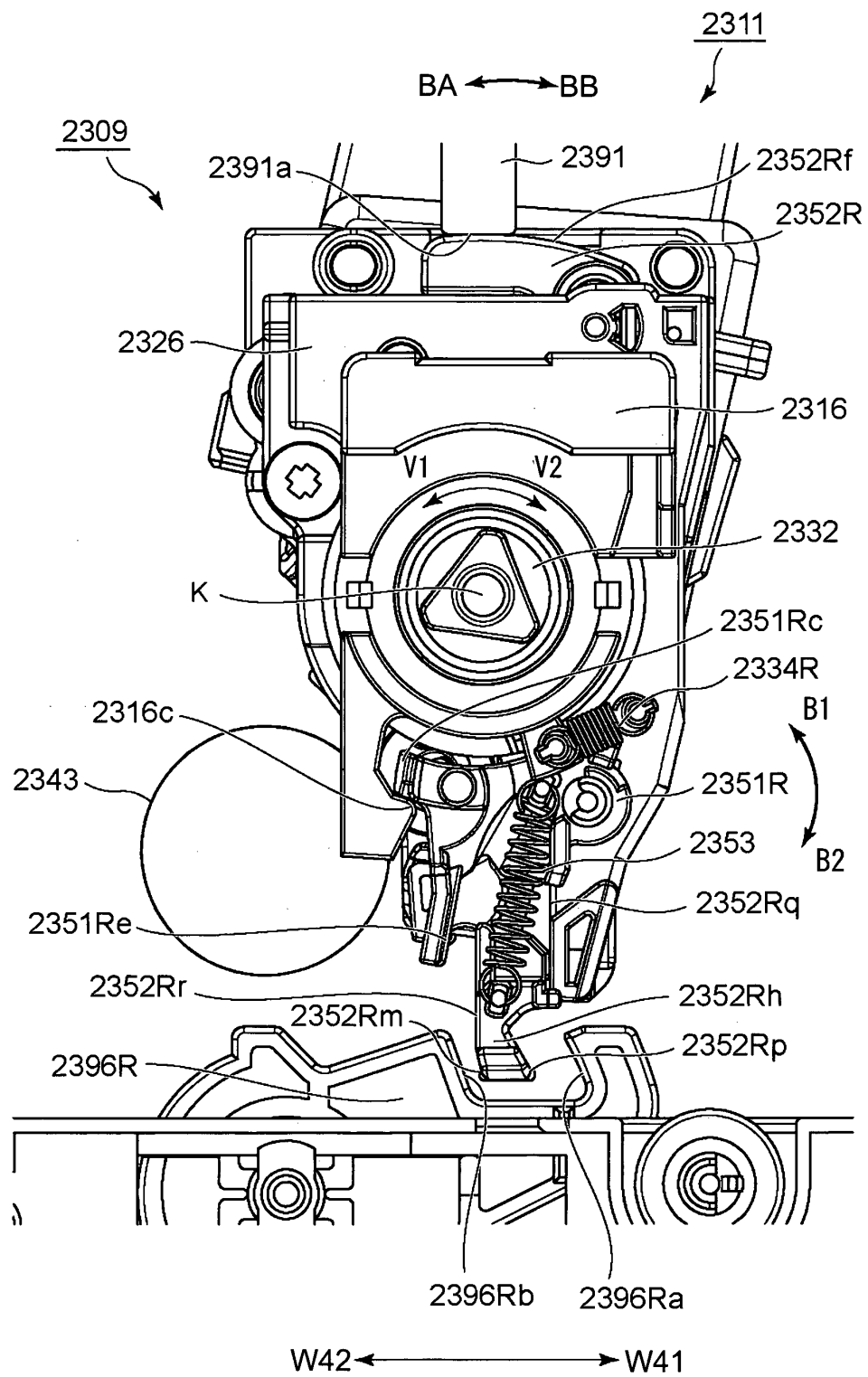


図245

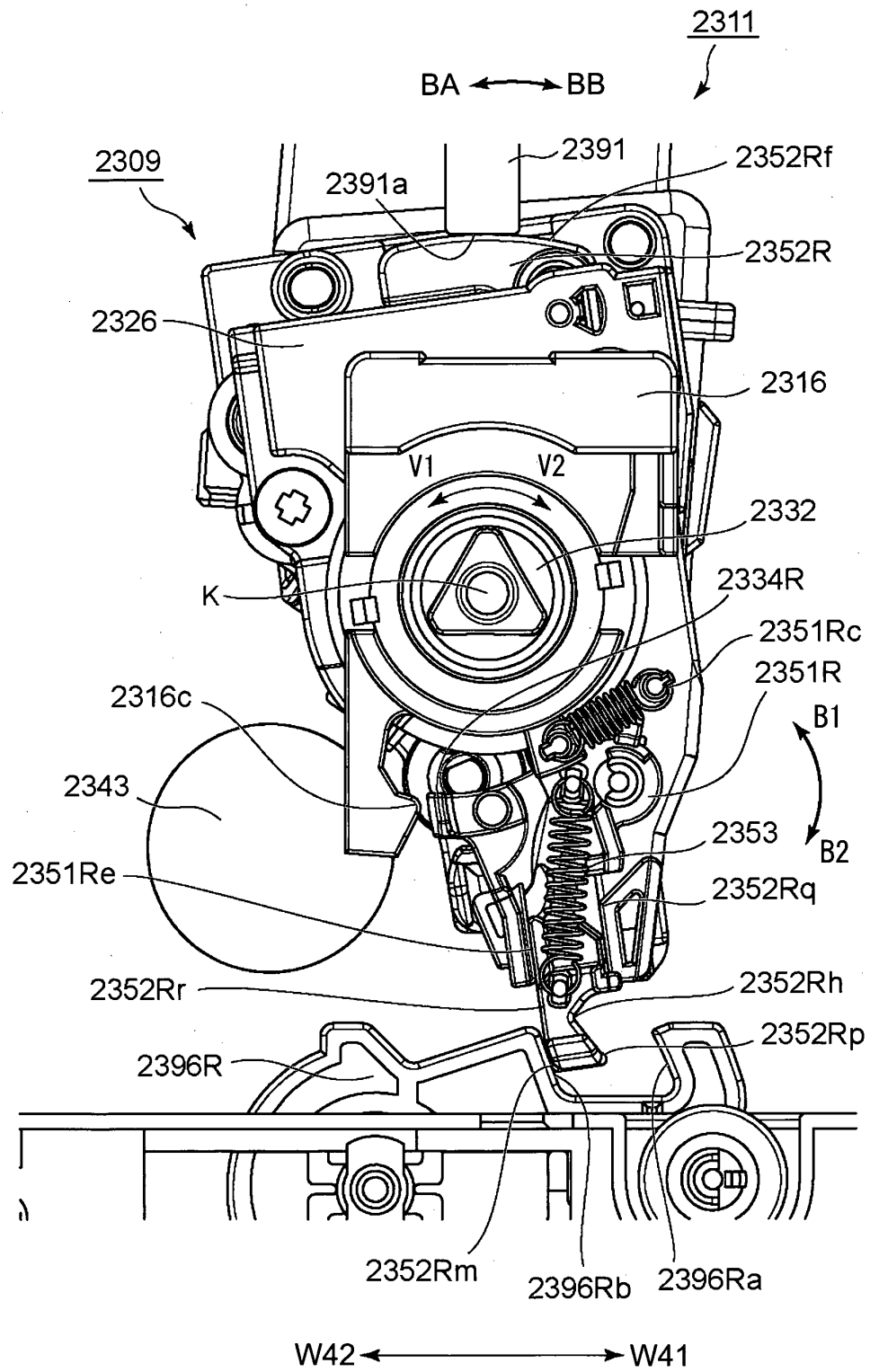
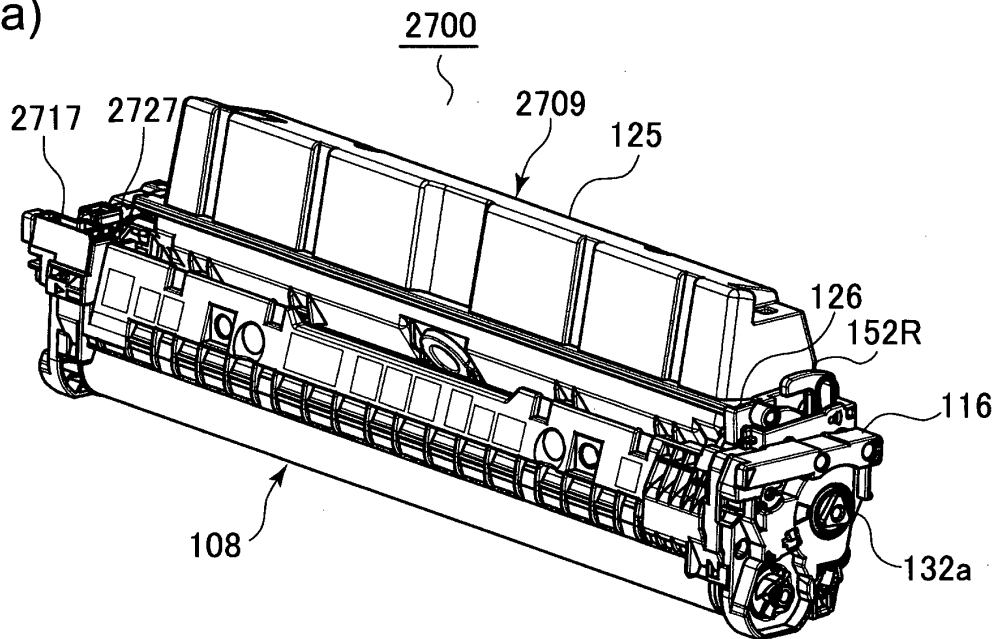


図246

(a)



(b)

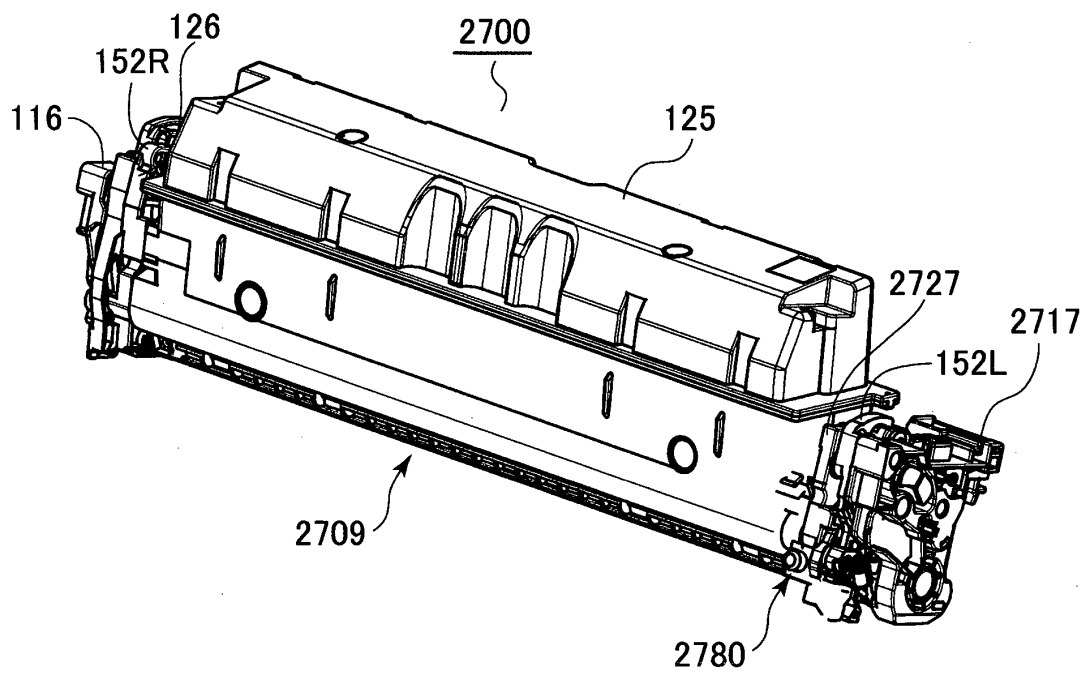


图247

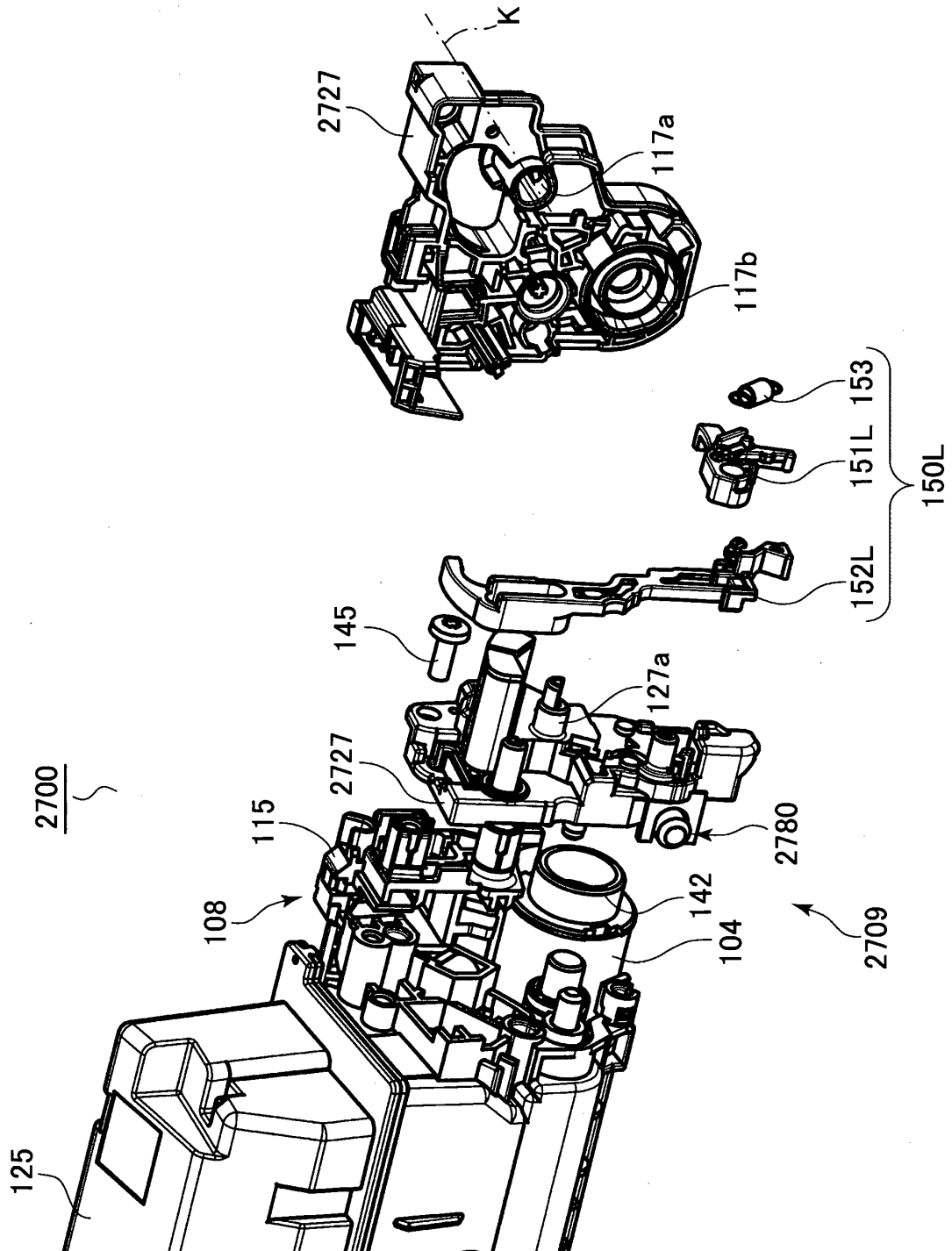
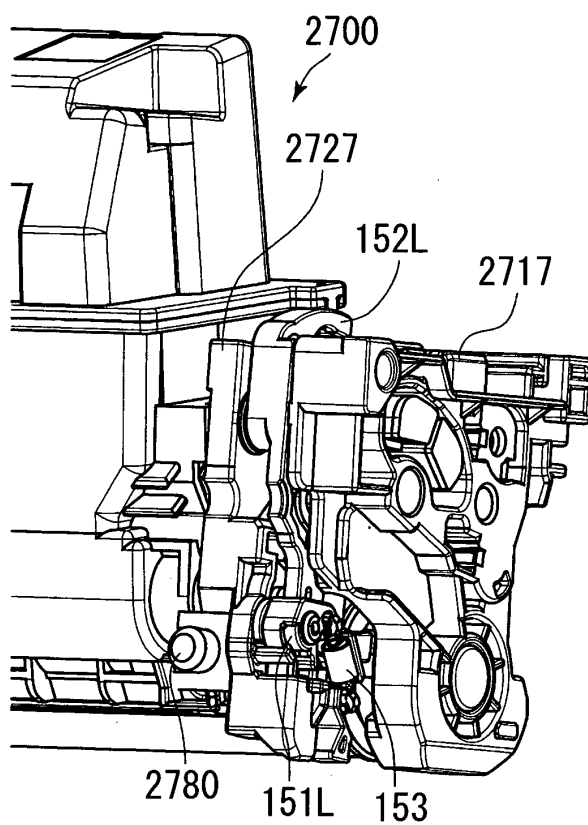


図248

(a)



(b)

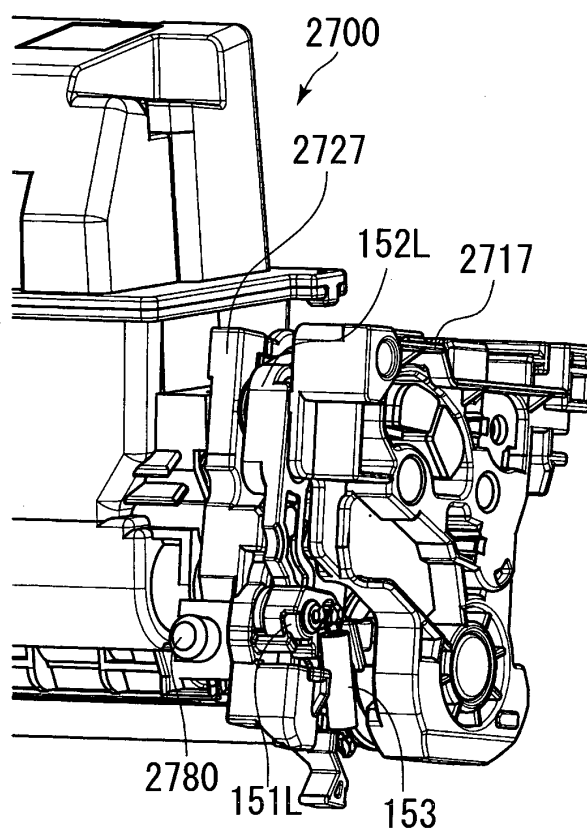
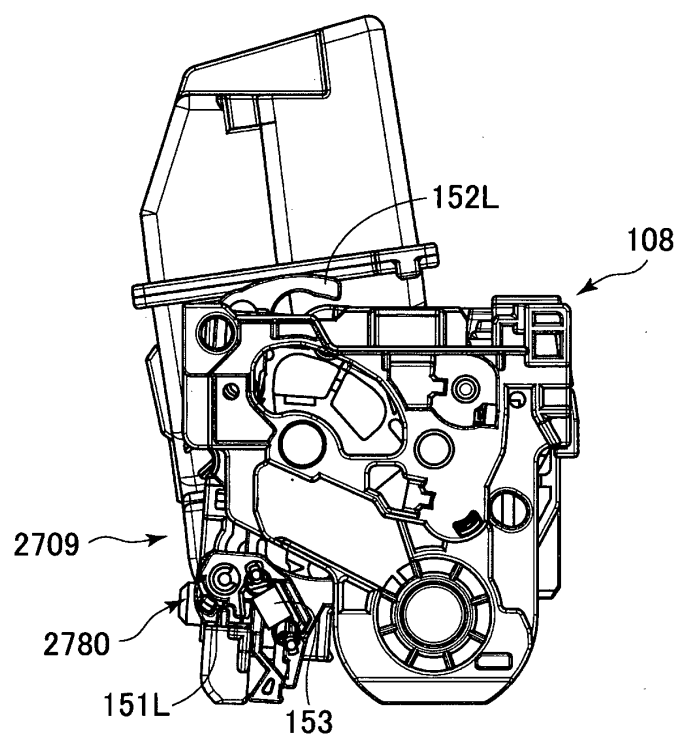


図249

(a)



(b)

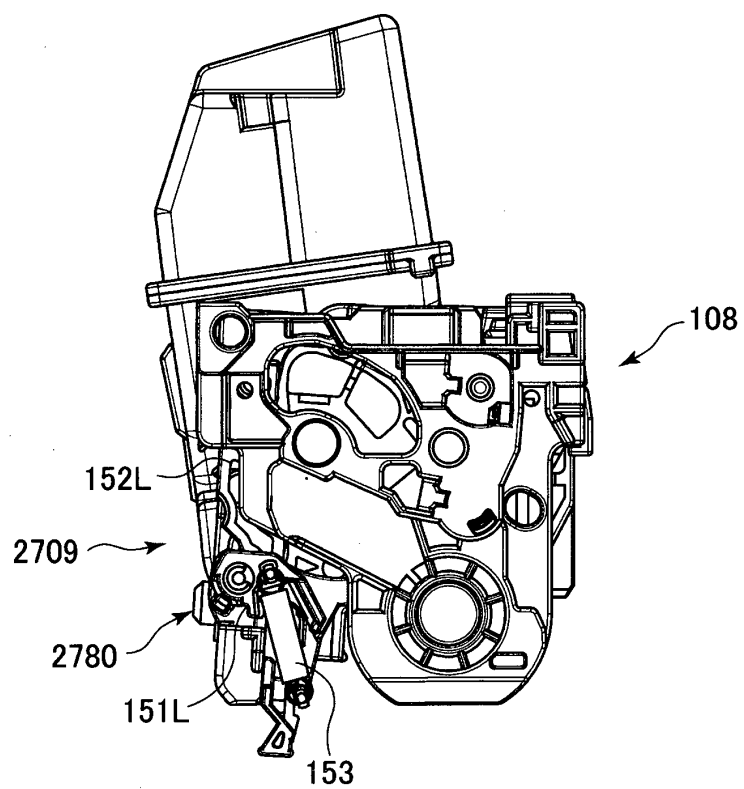


図250

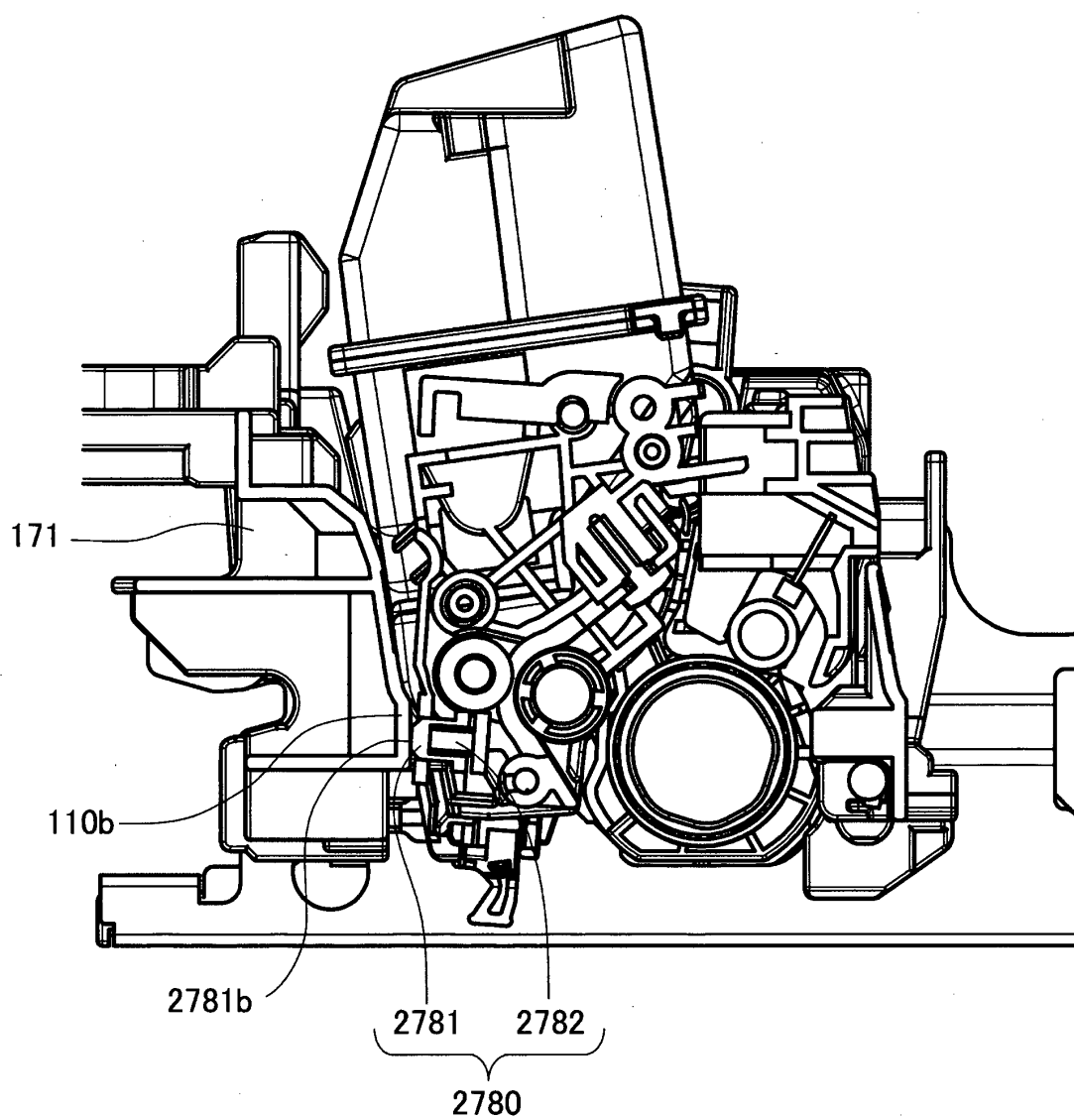


図251

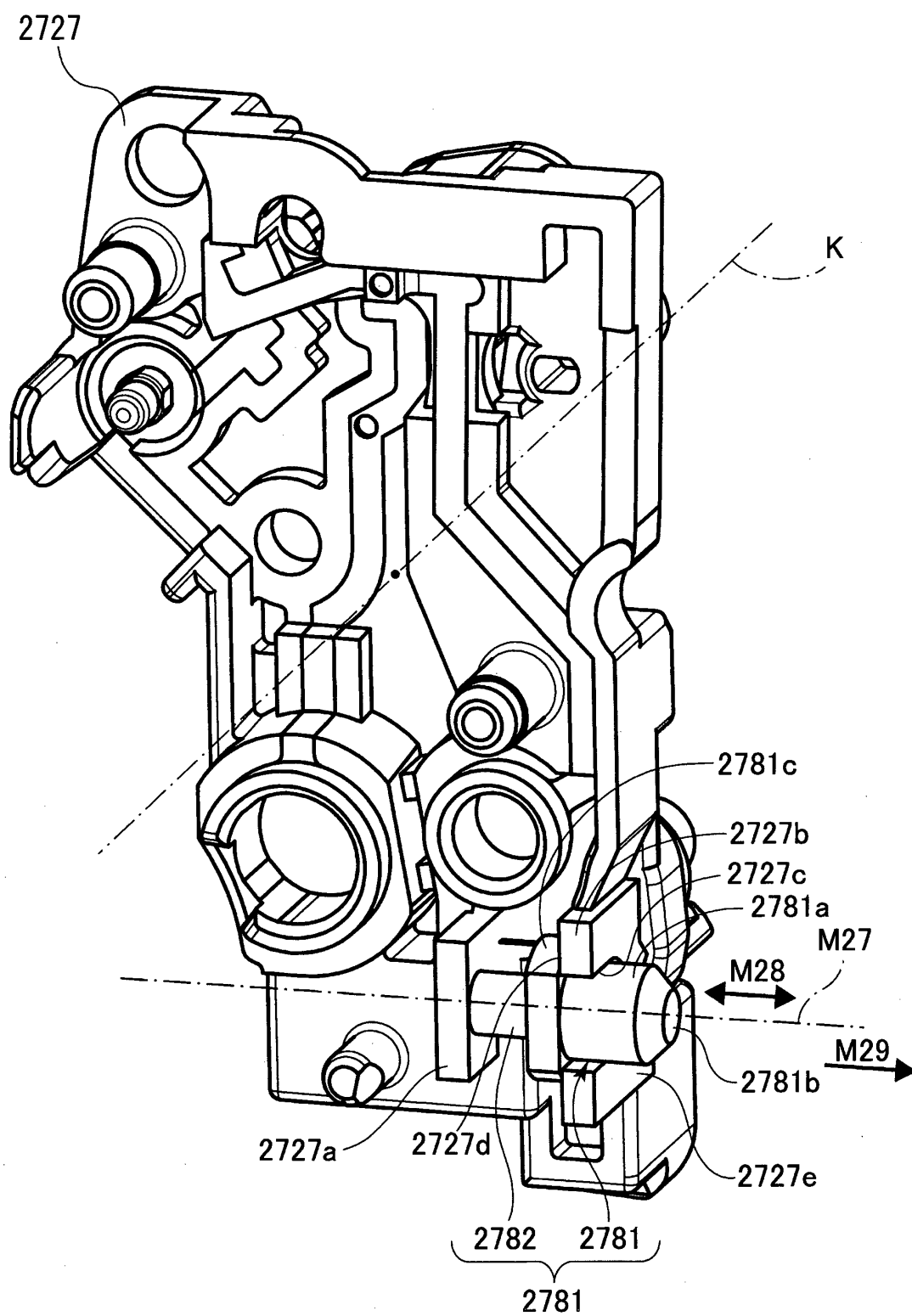


図252

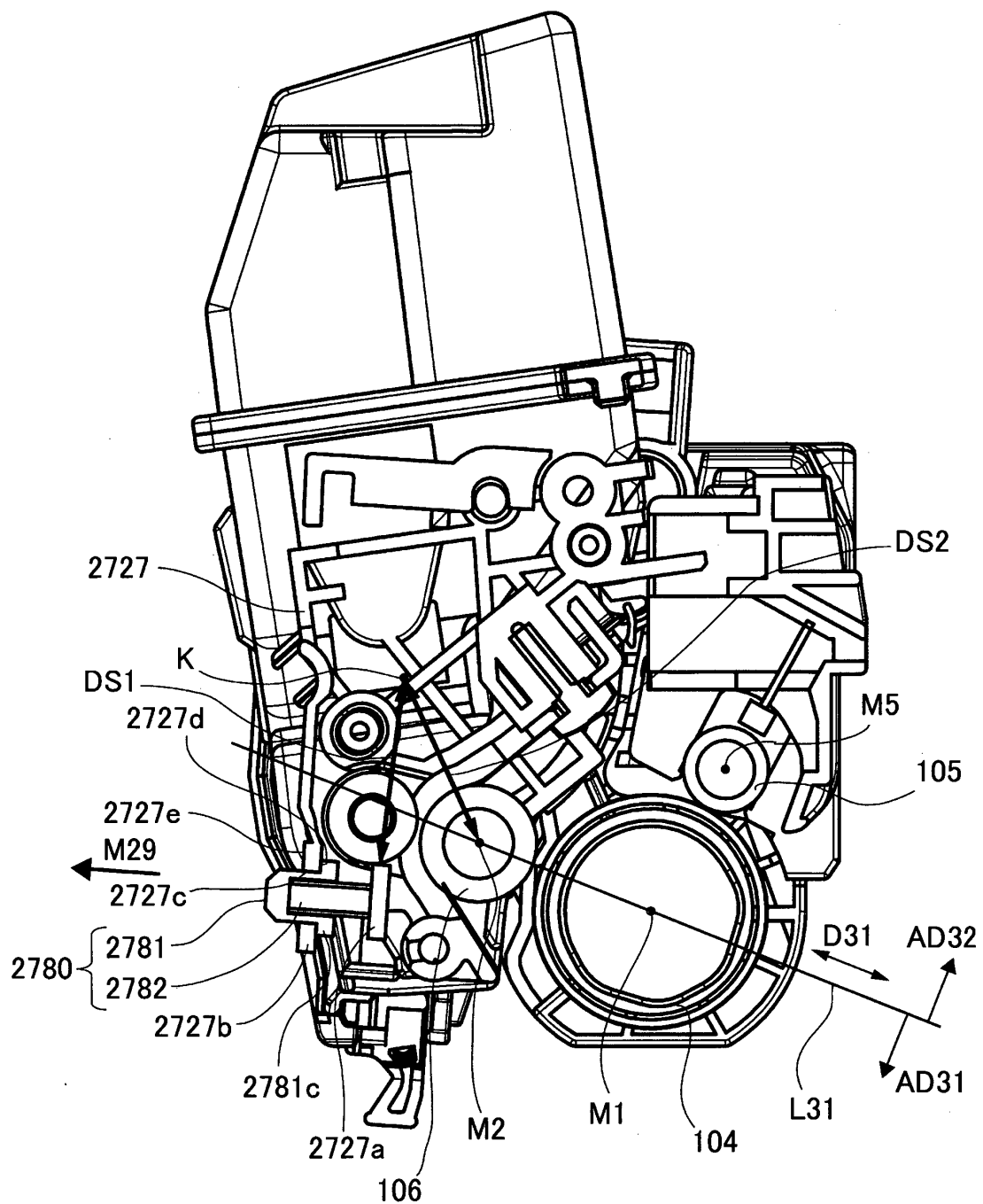


図253

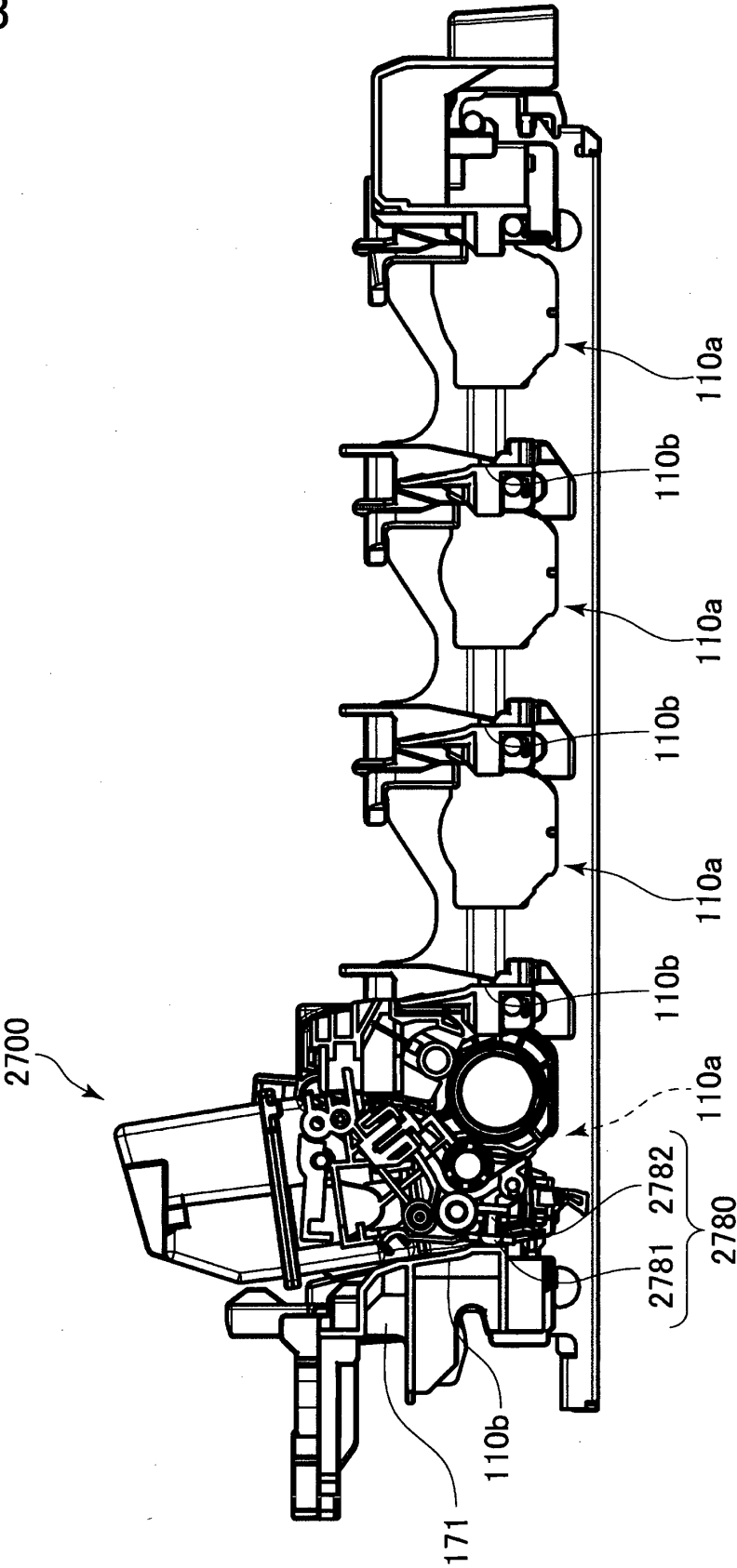


図254

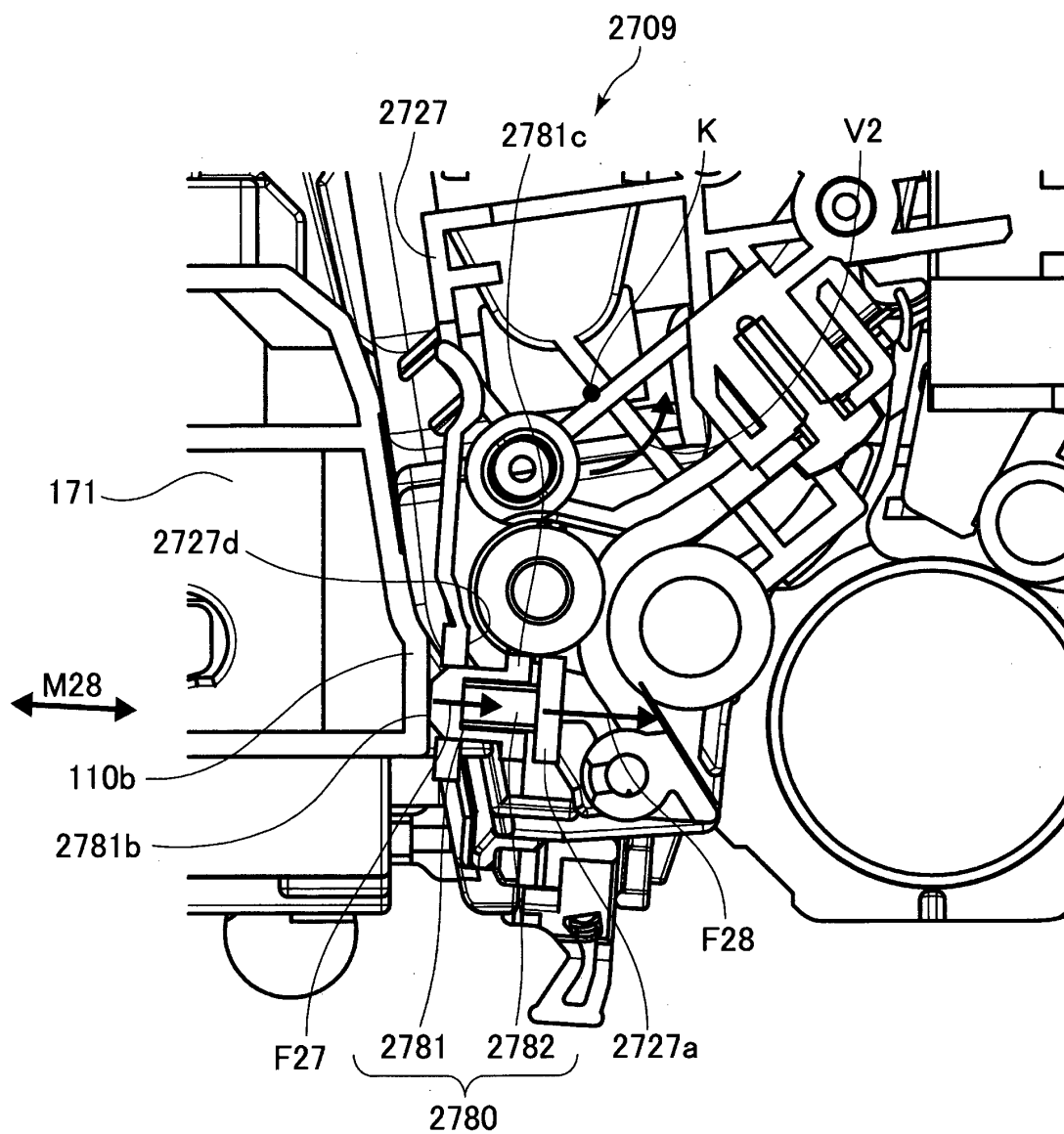


図255

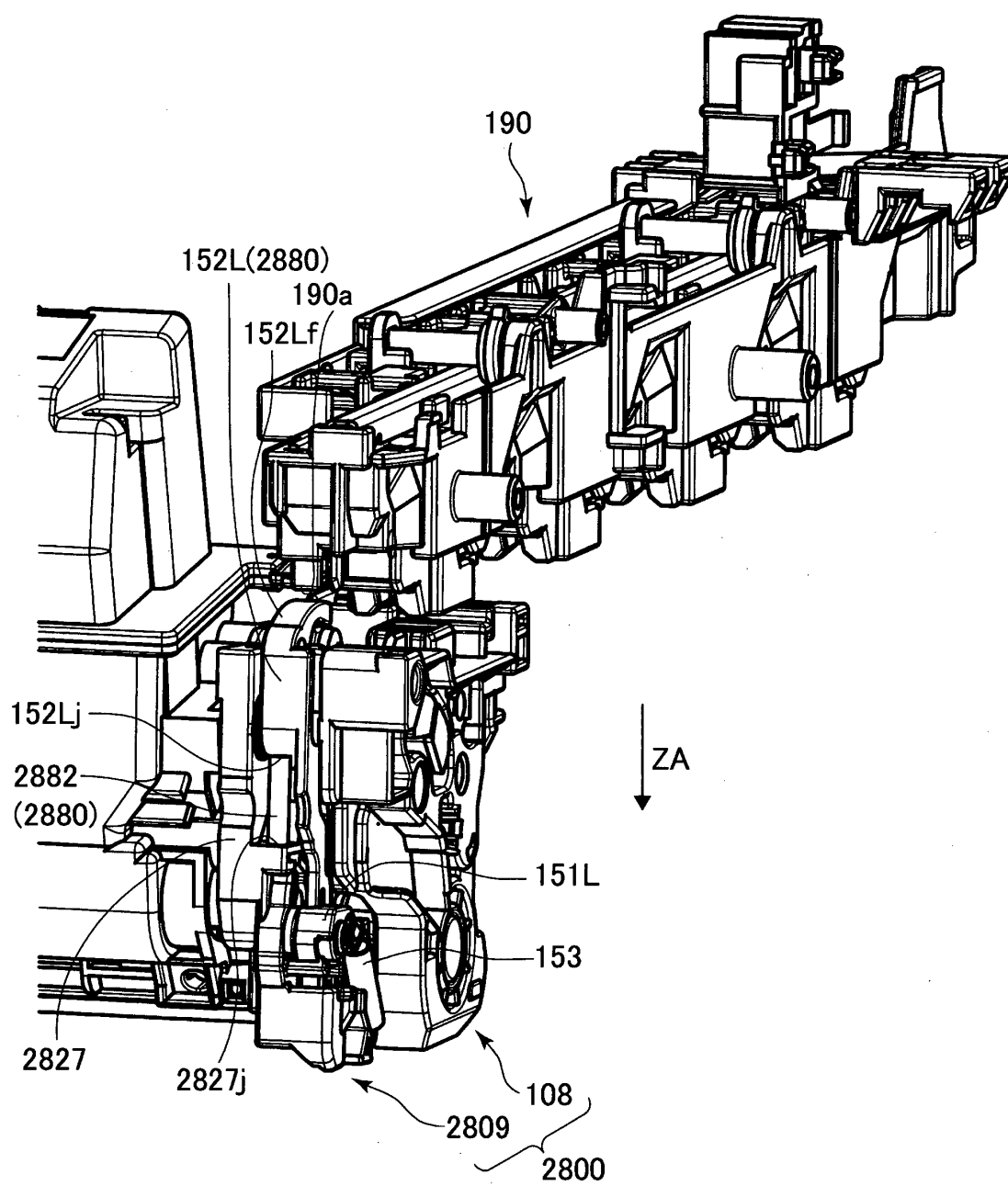


図 256

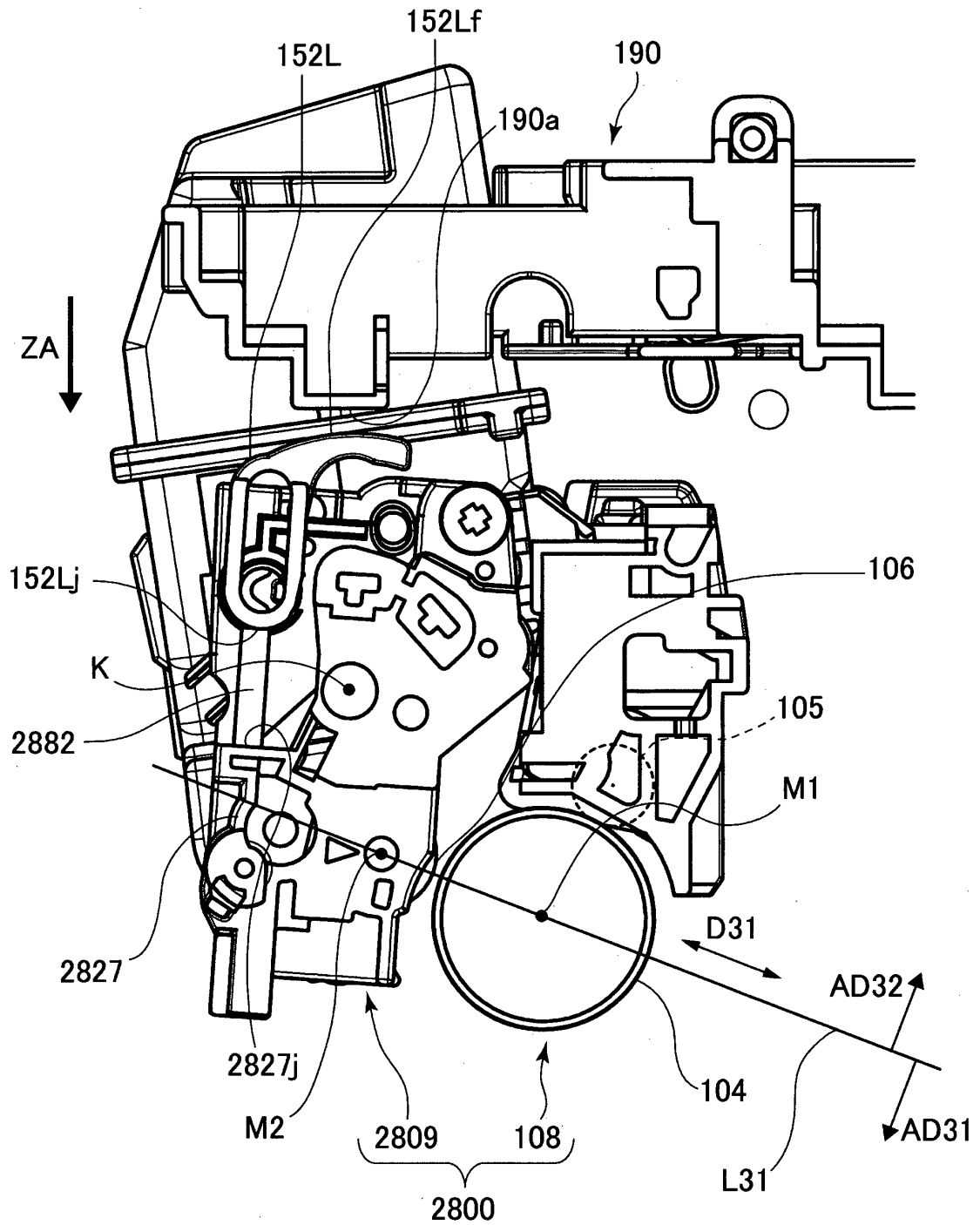


図257

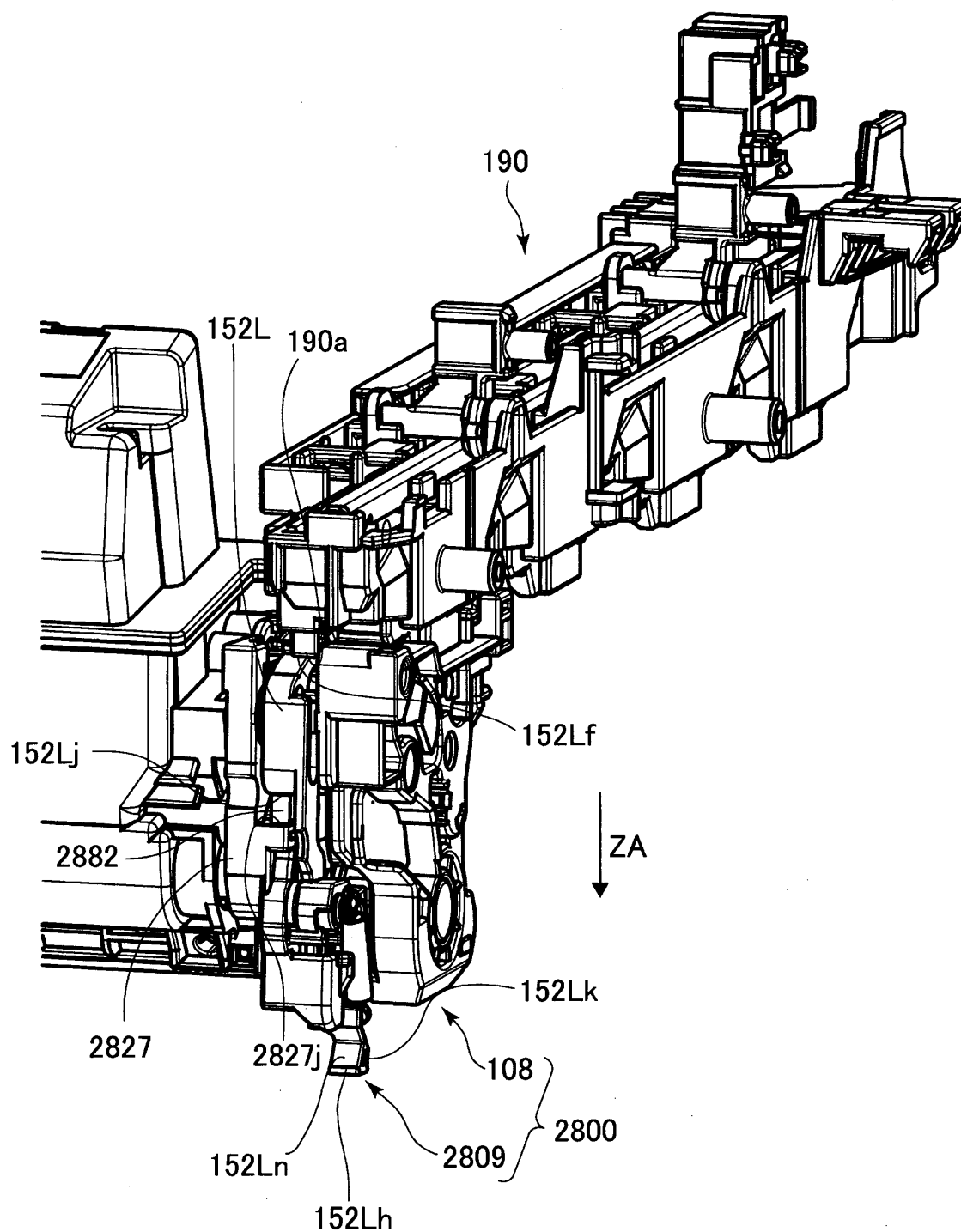


図258

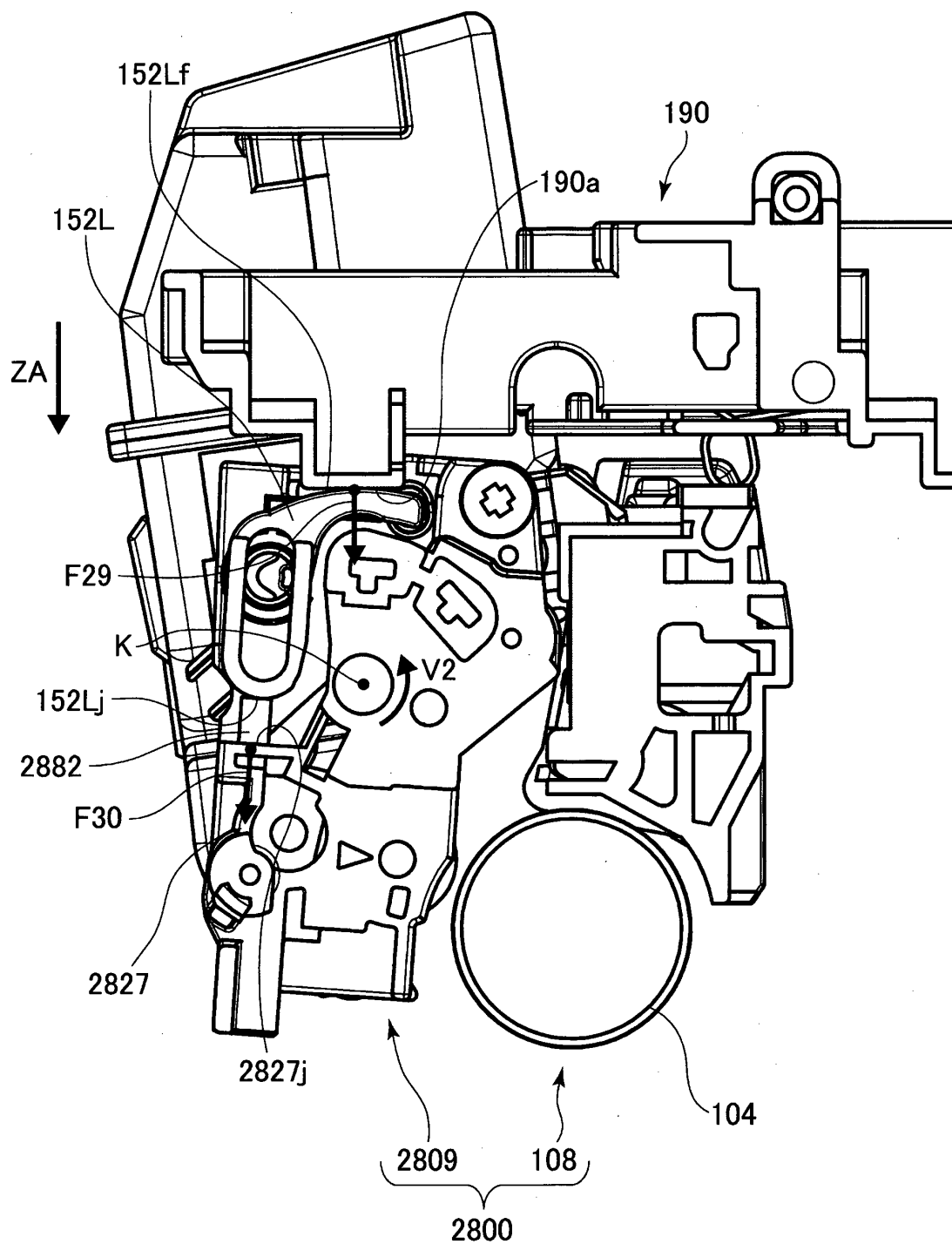


図259

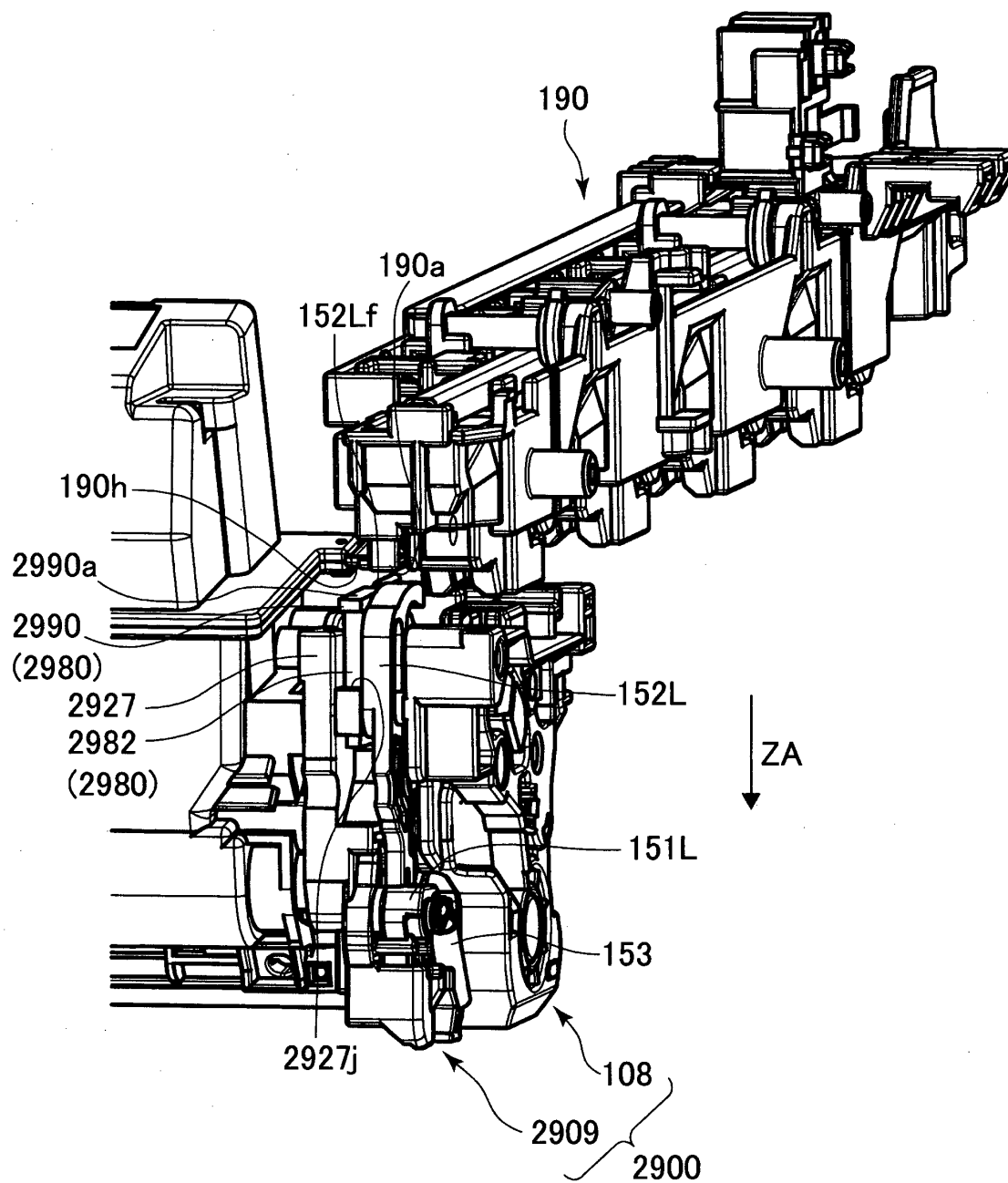


図260

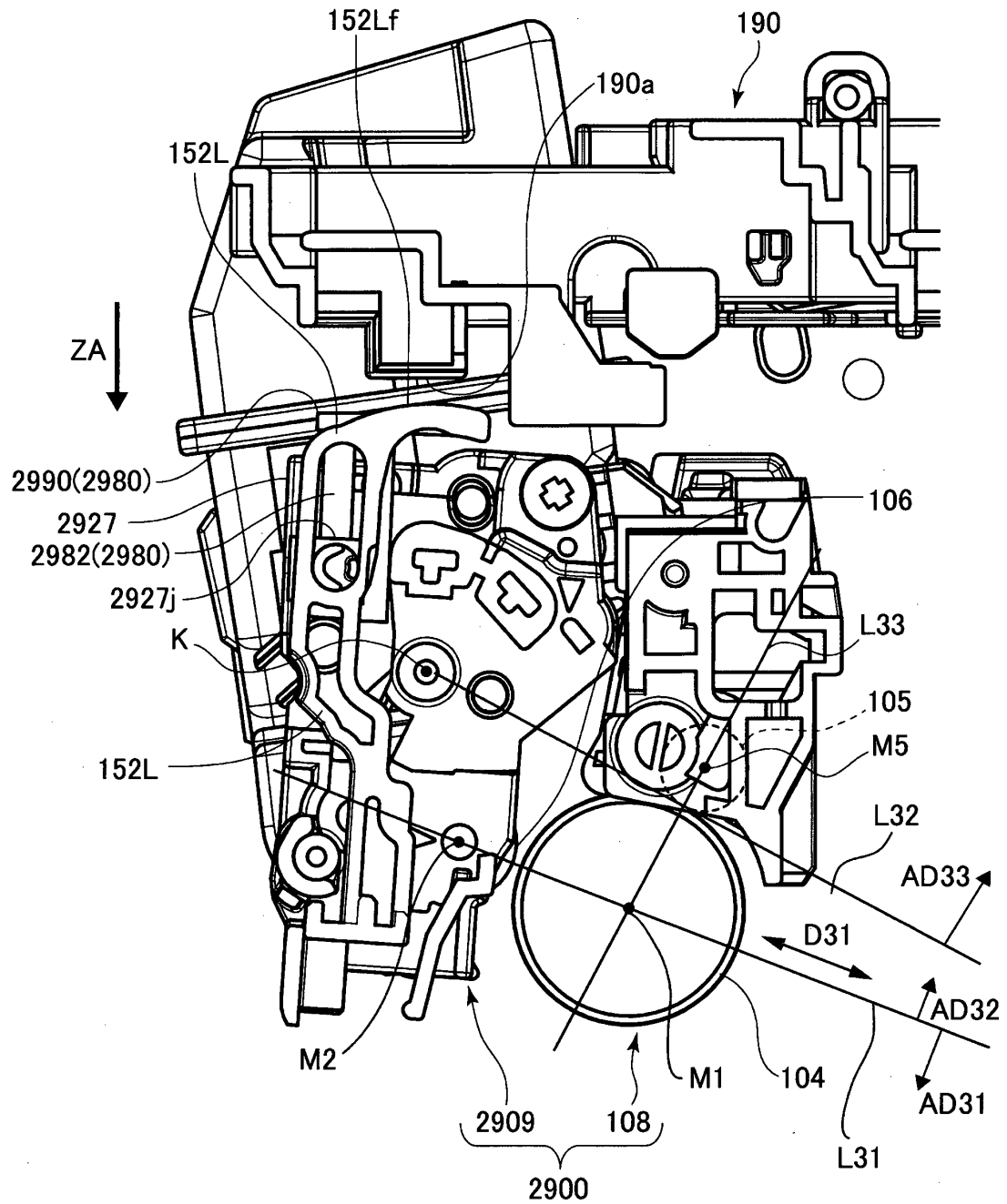


図 261

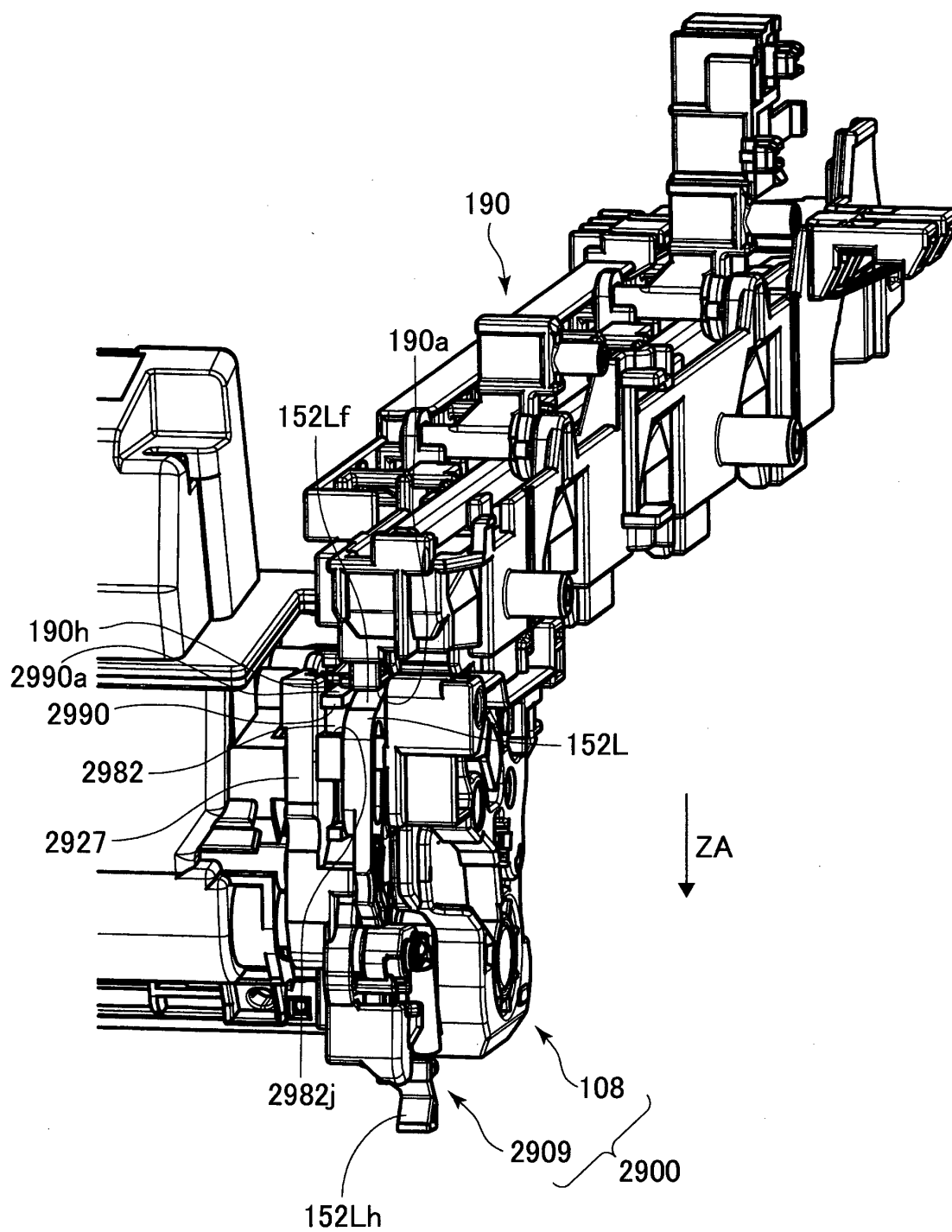


図262

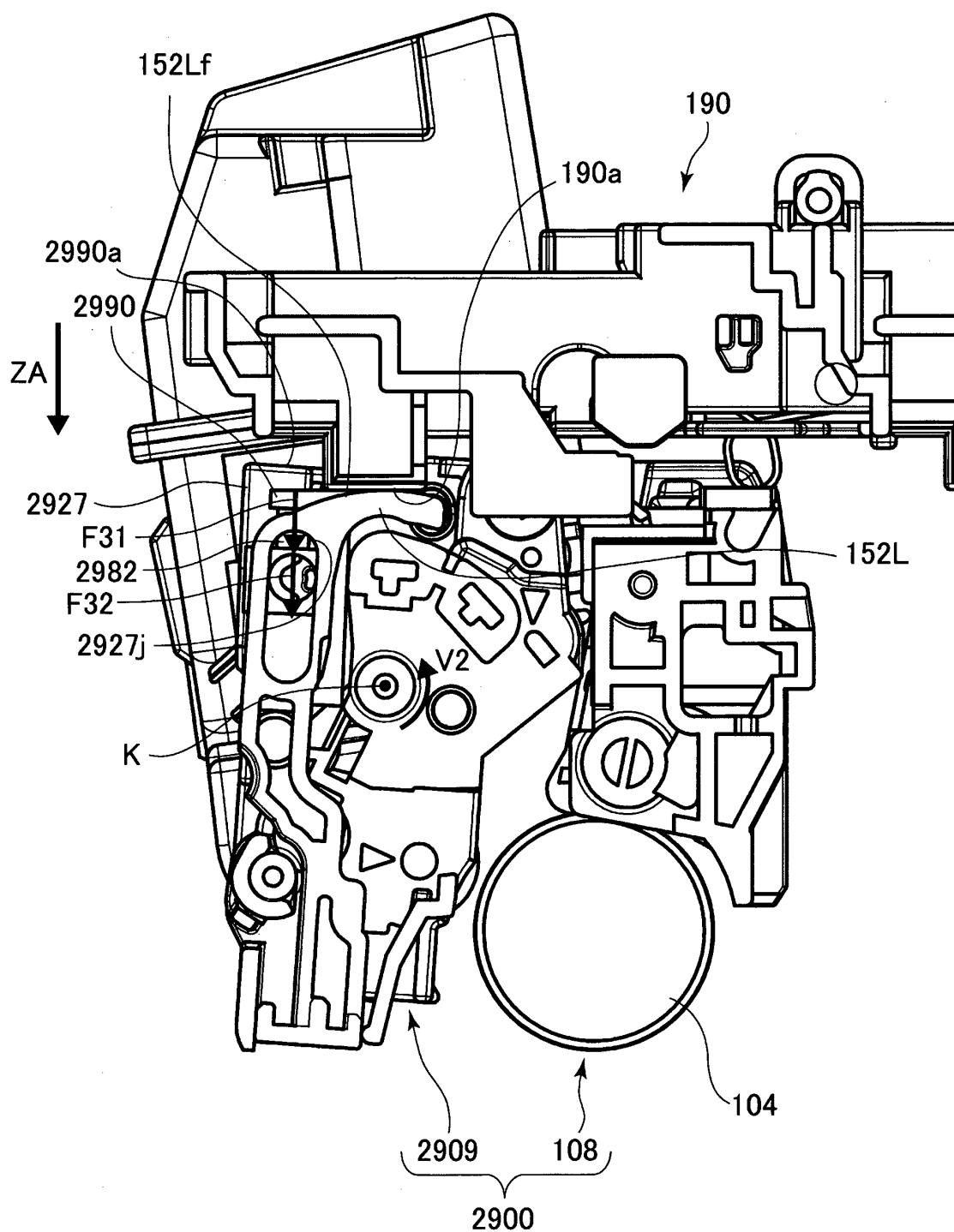
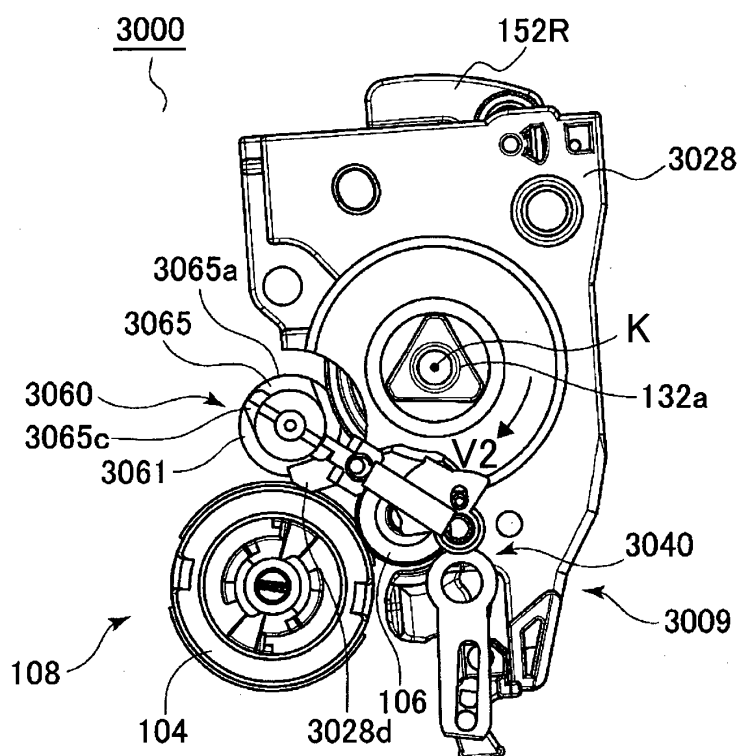


図 263

(a)



(b)

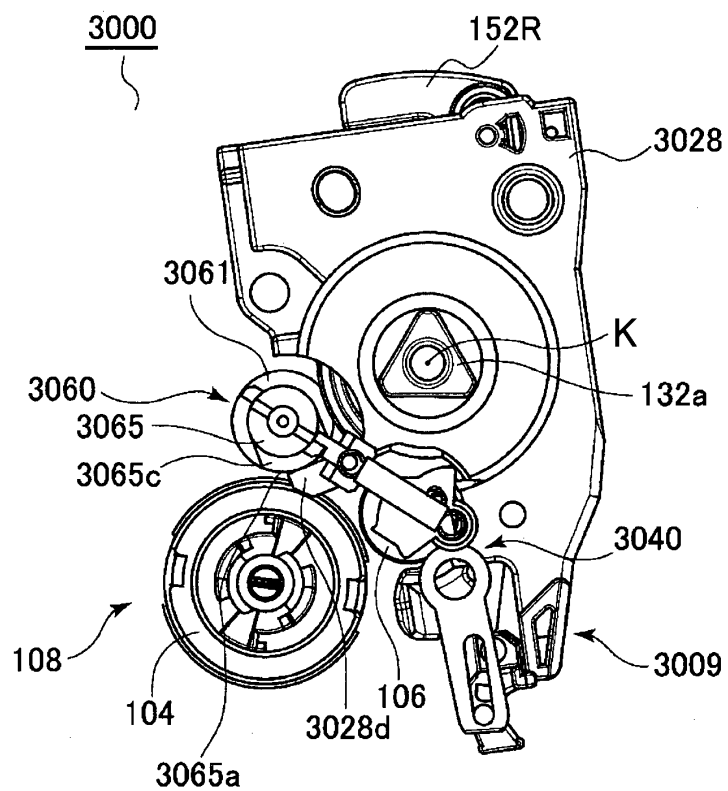


図264

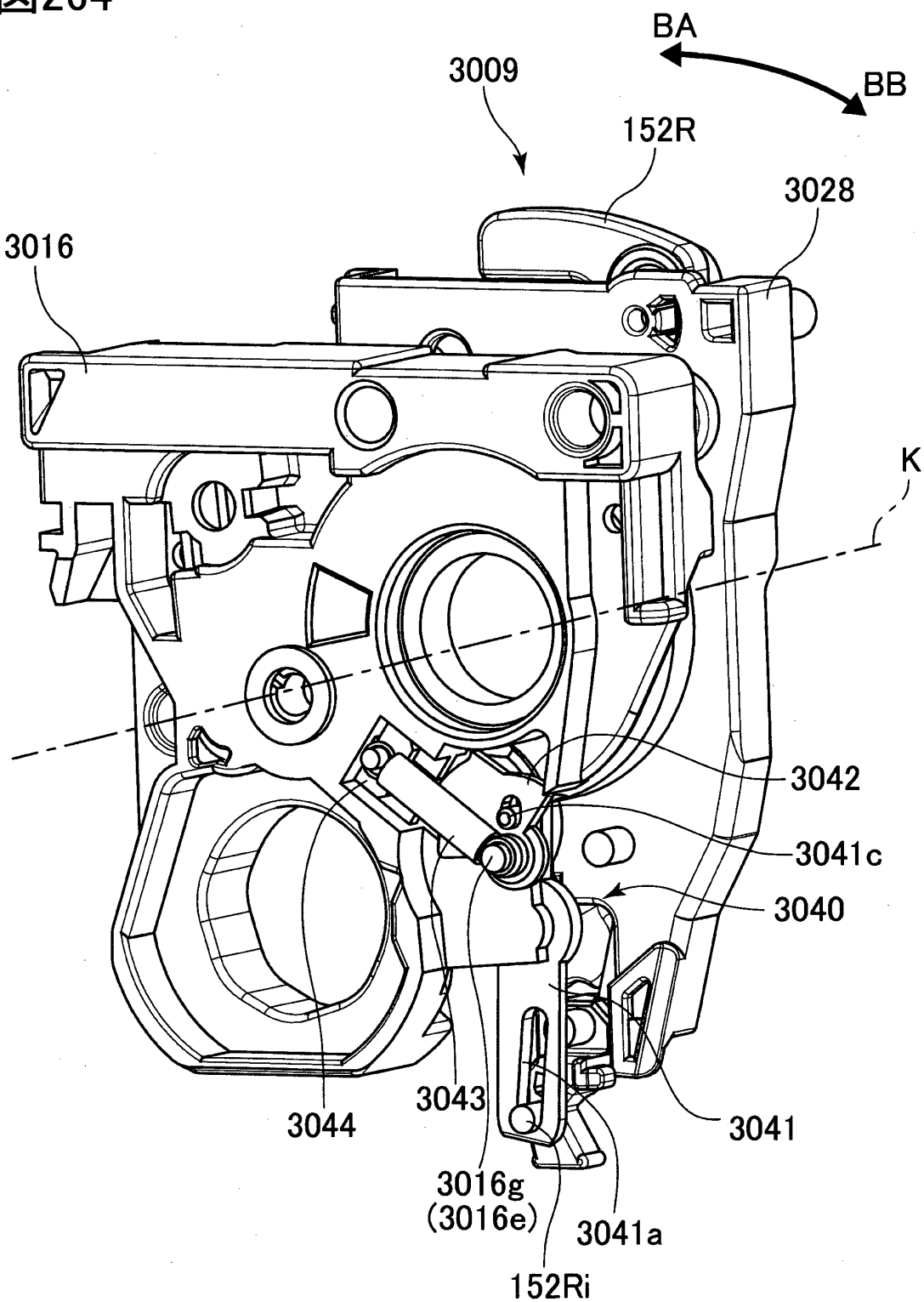


図265

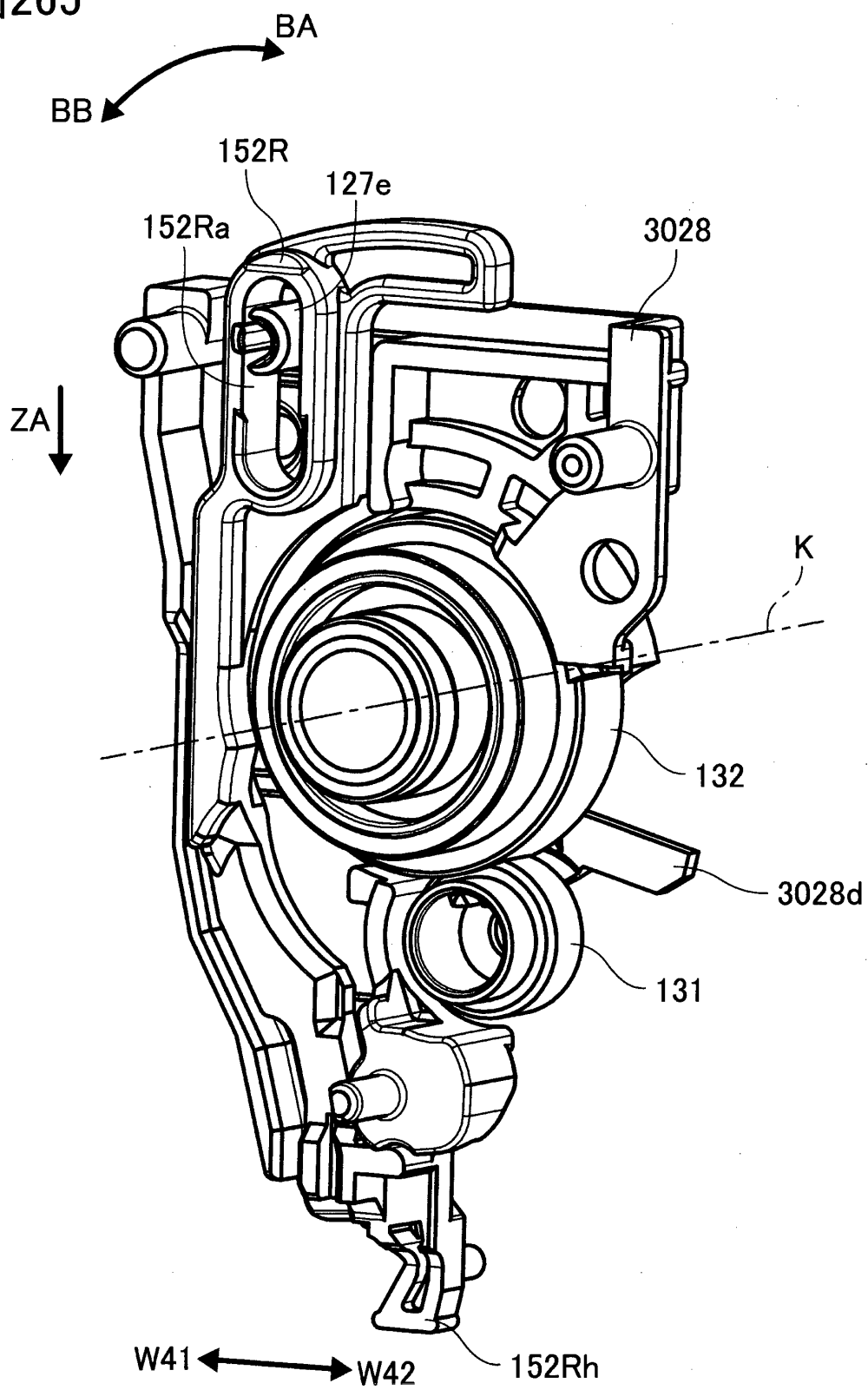


図 266

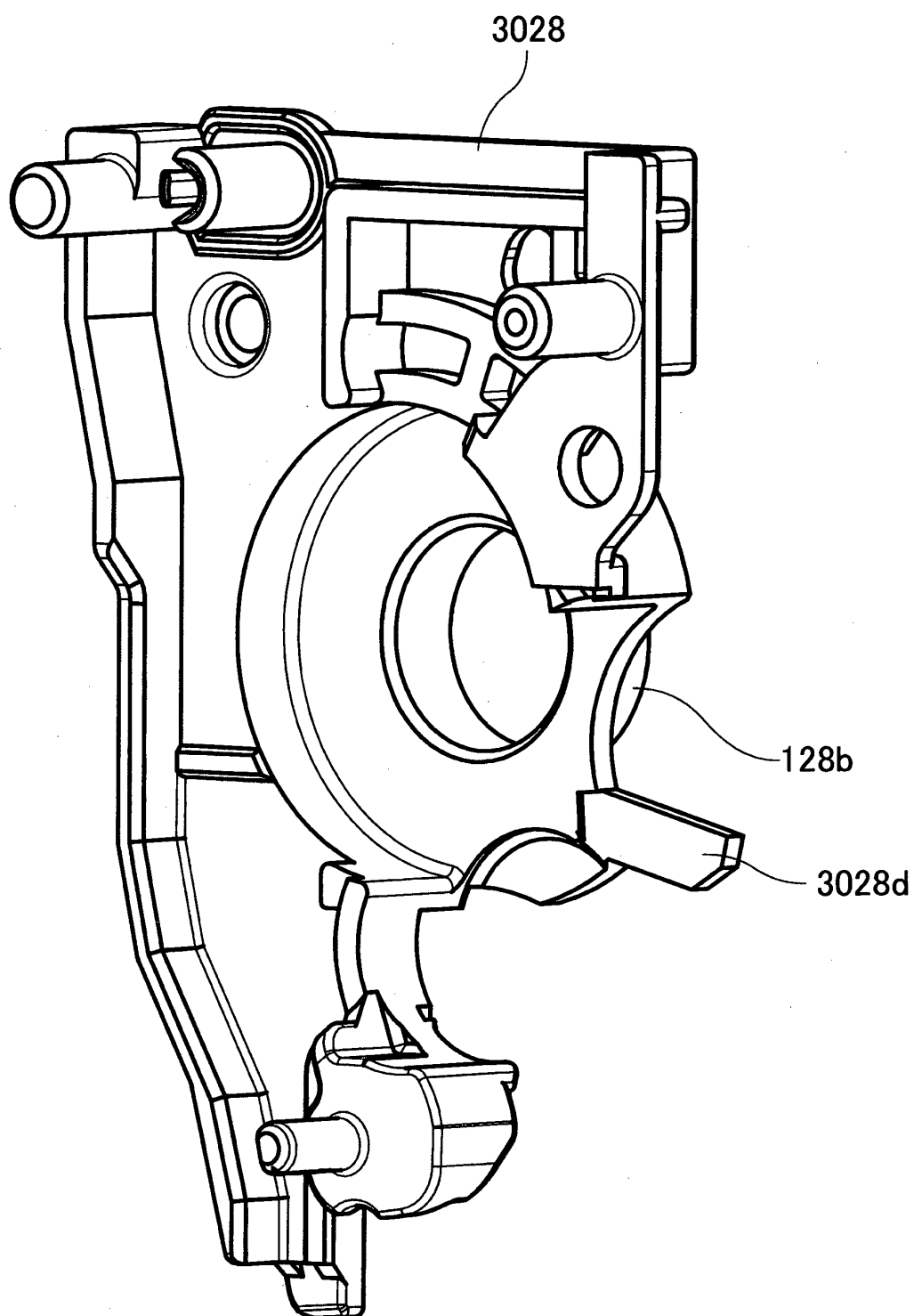


図267

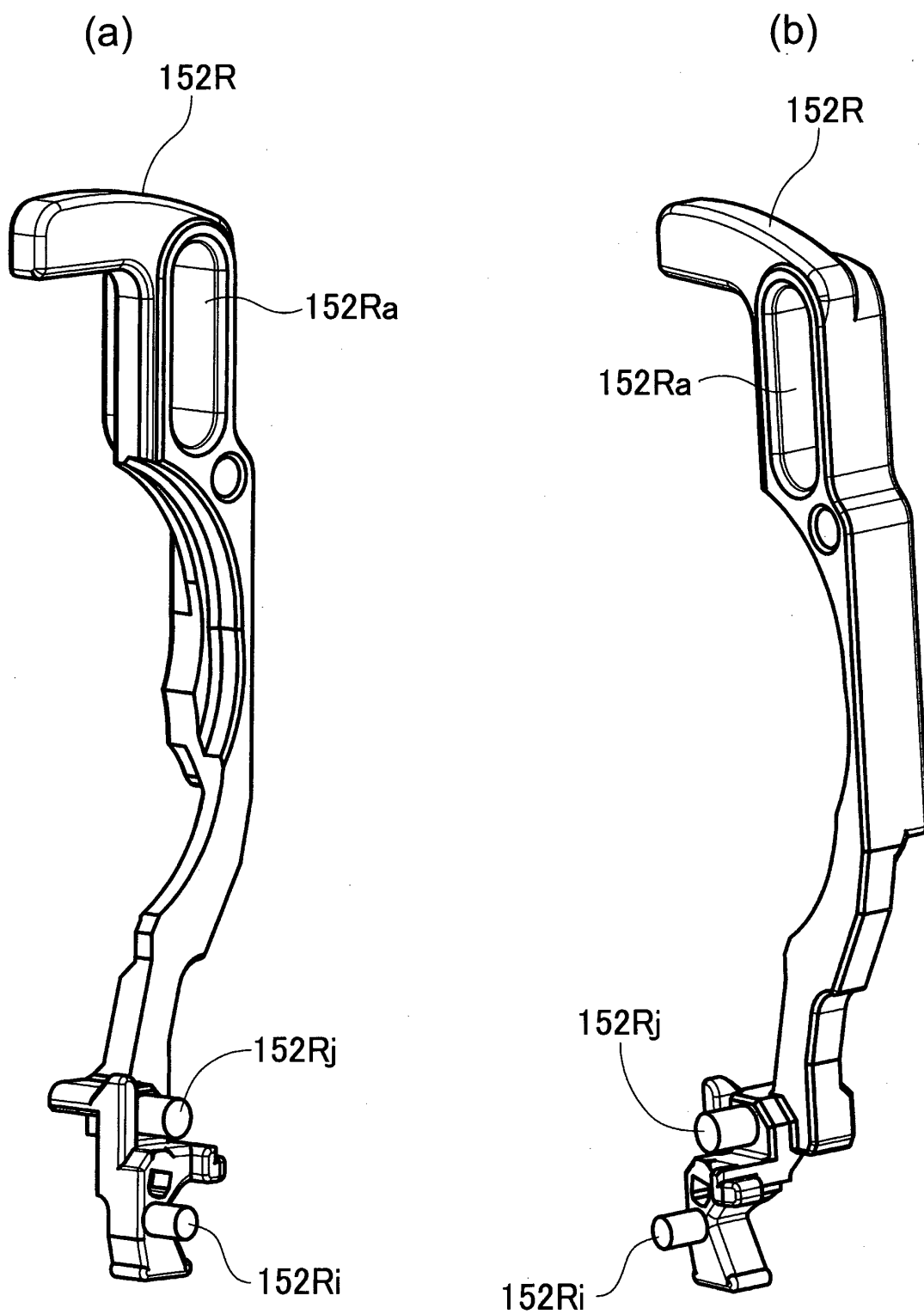


図268

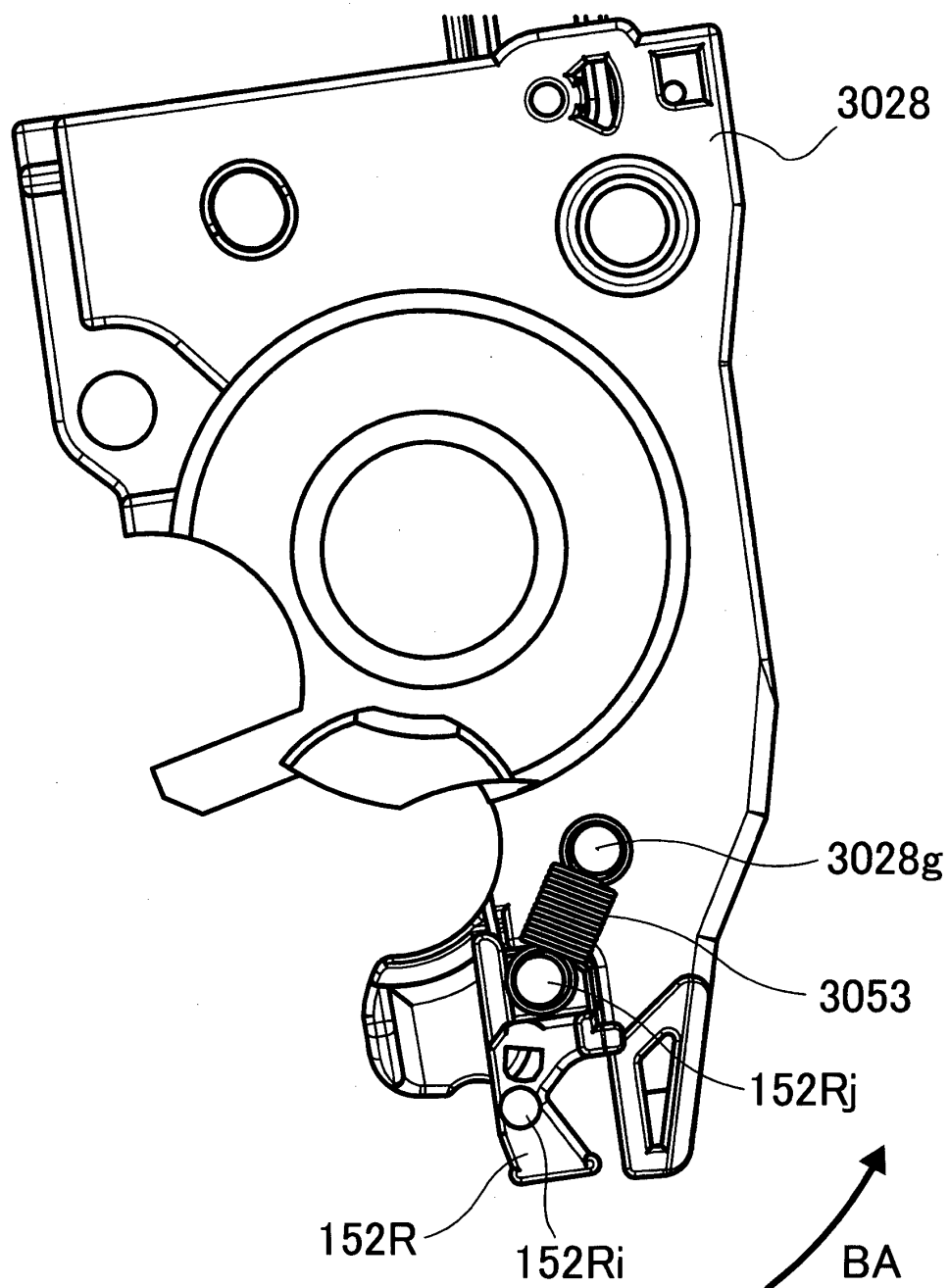


図269

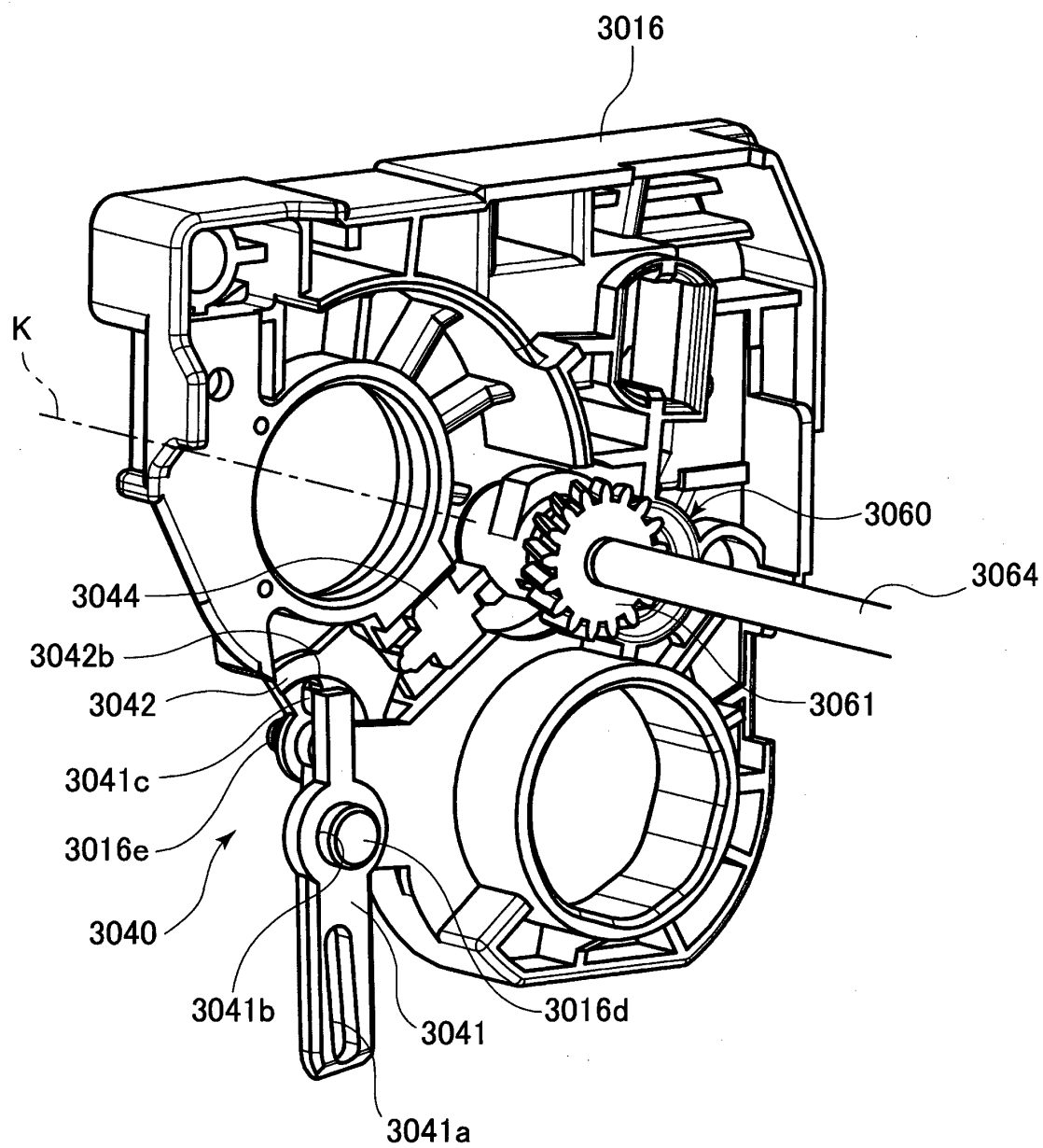


図270

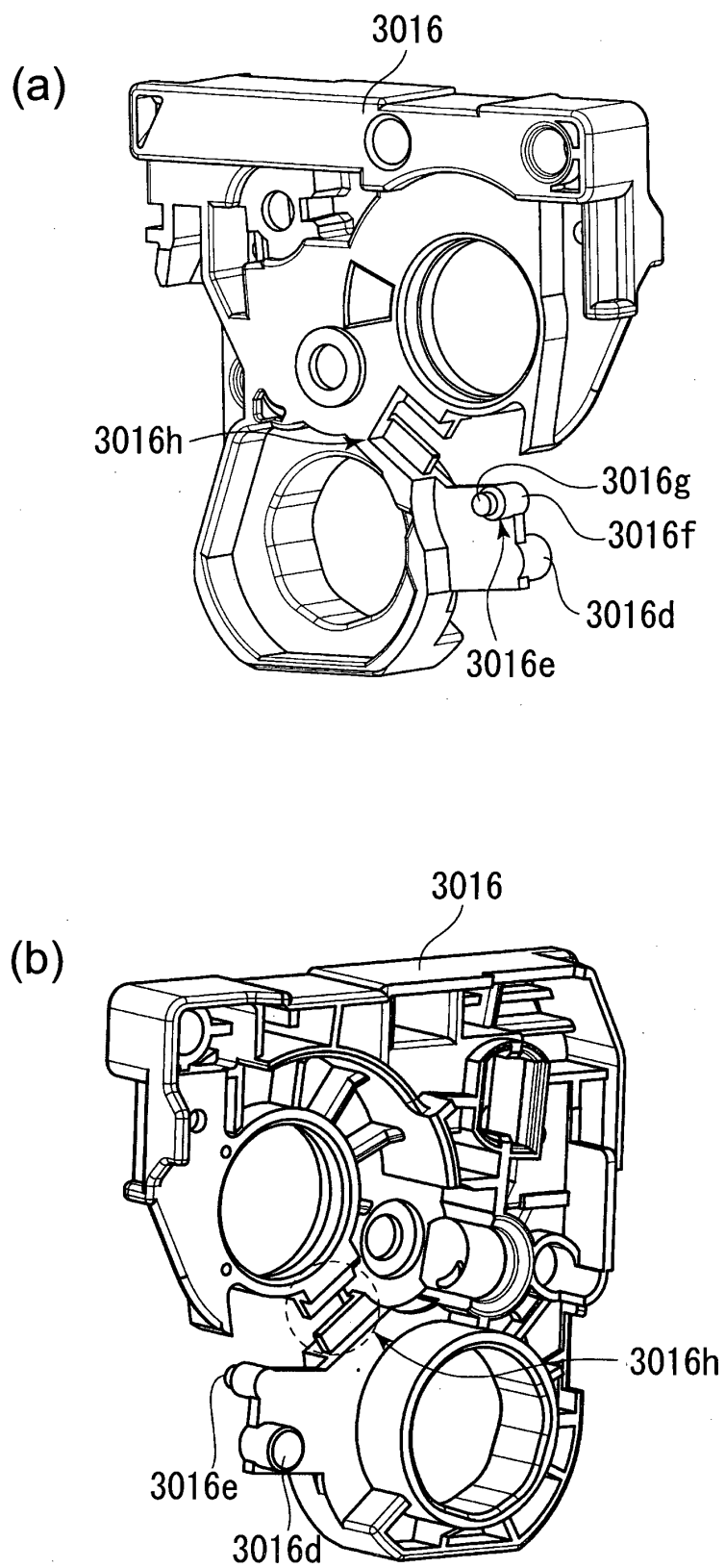


図271

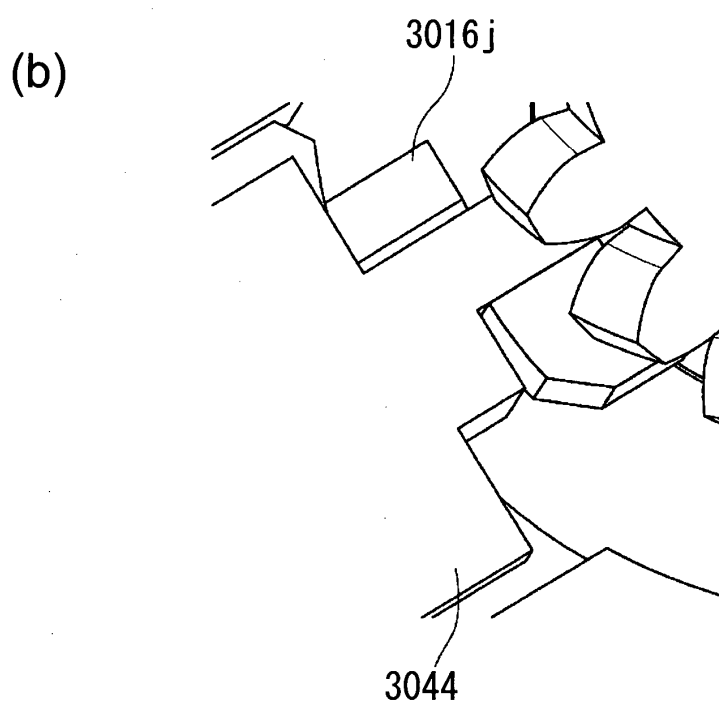
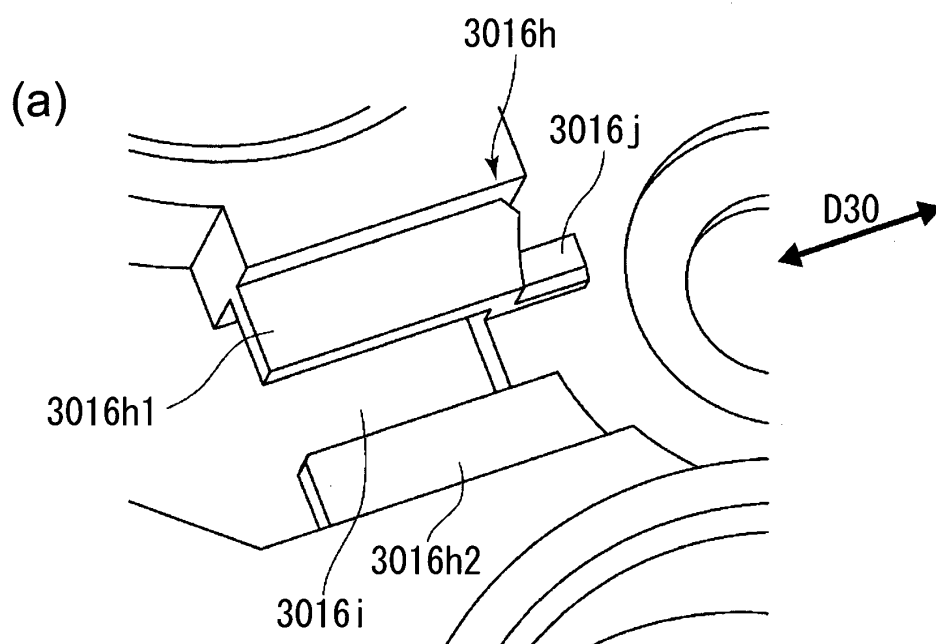


図272

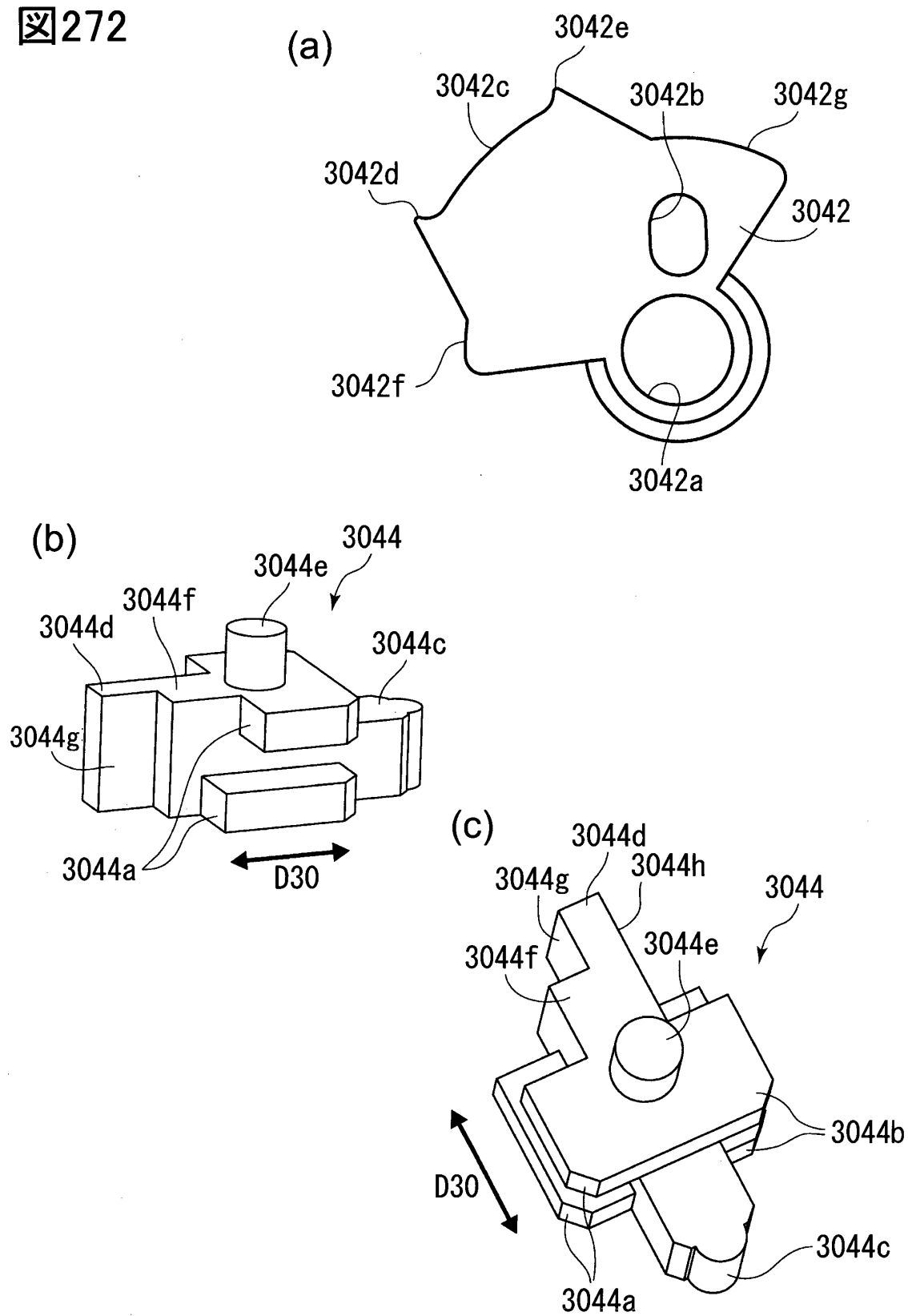


図273

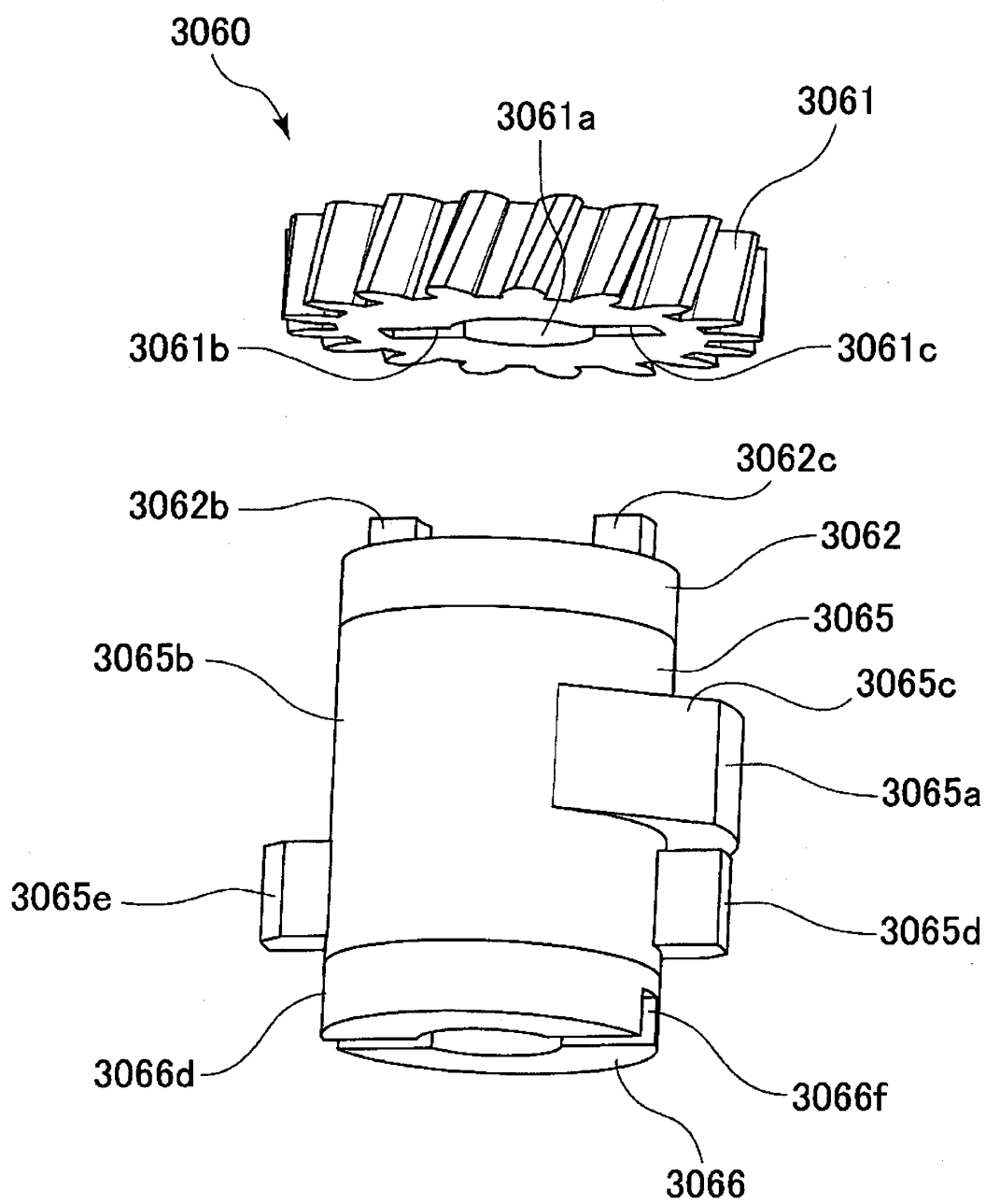


図274

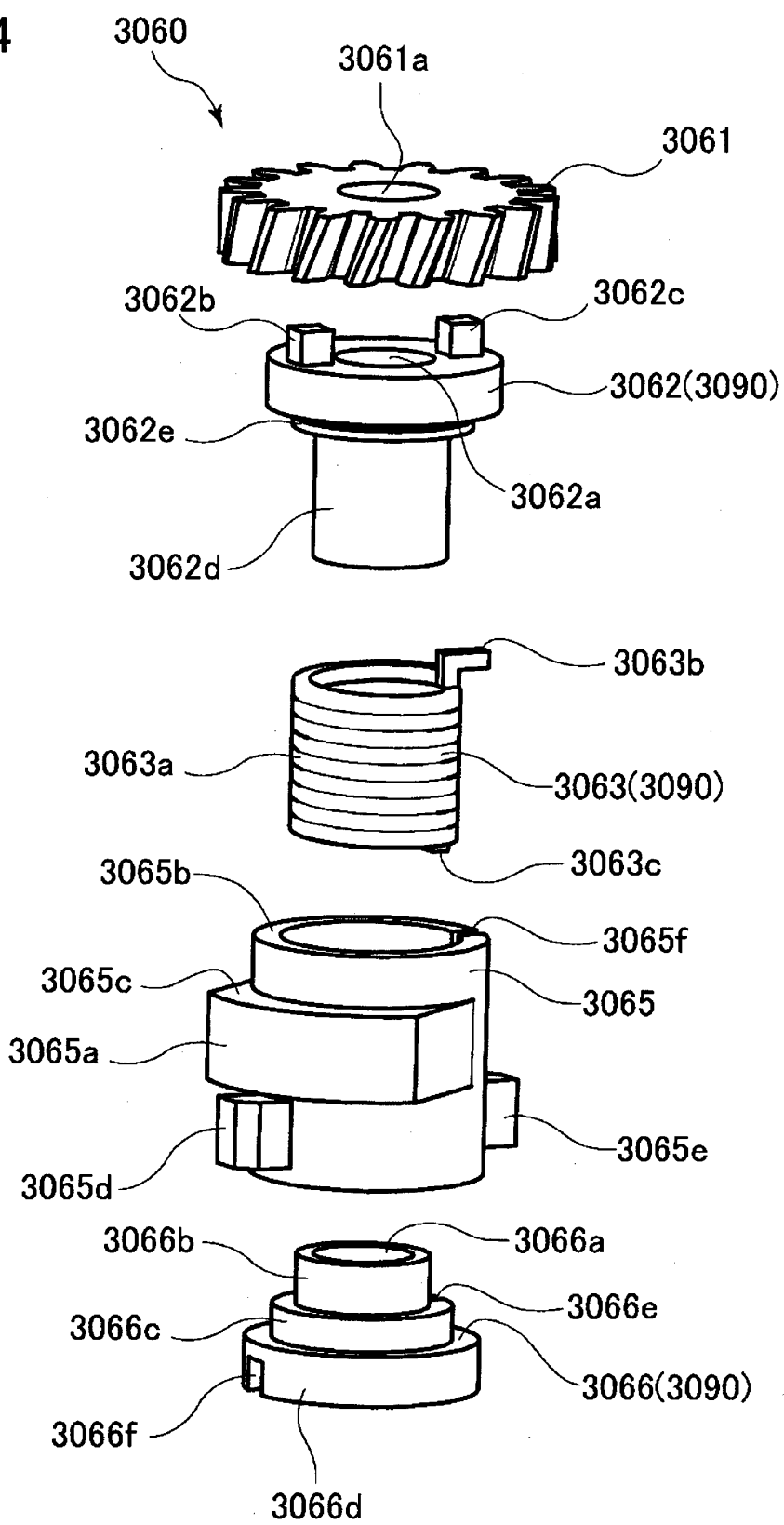


図275

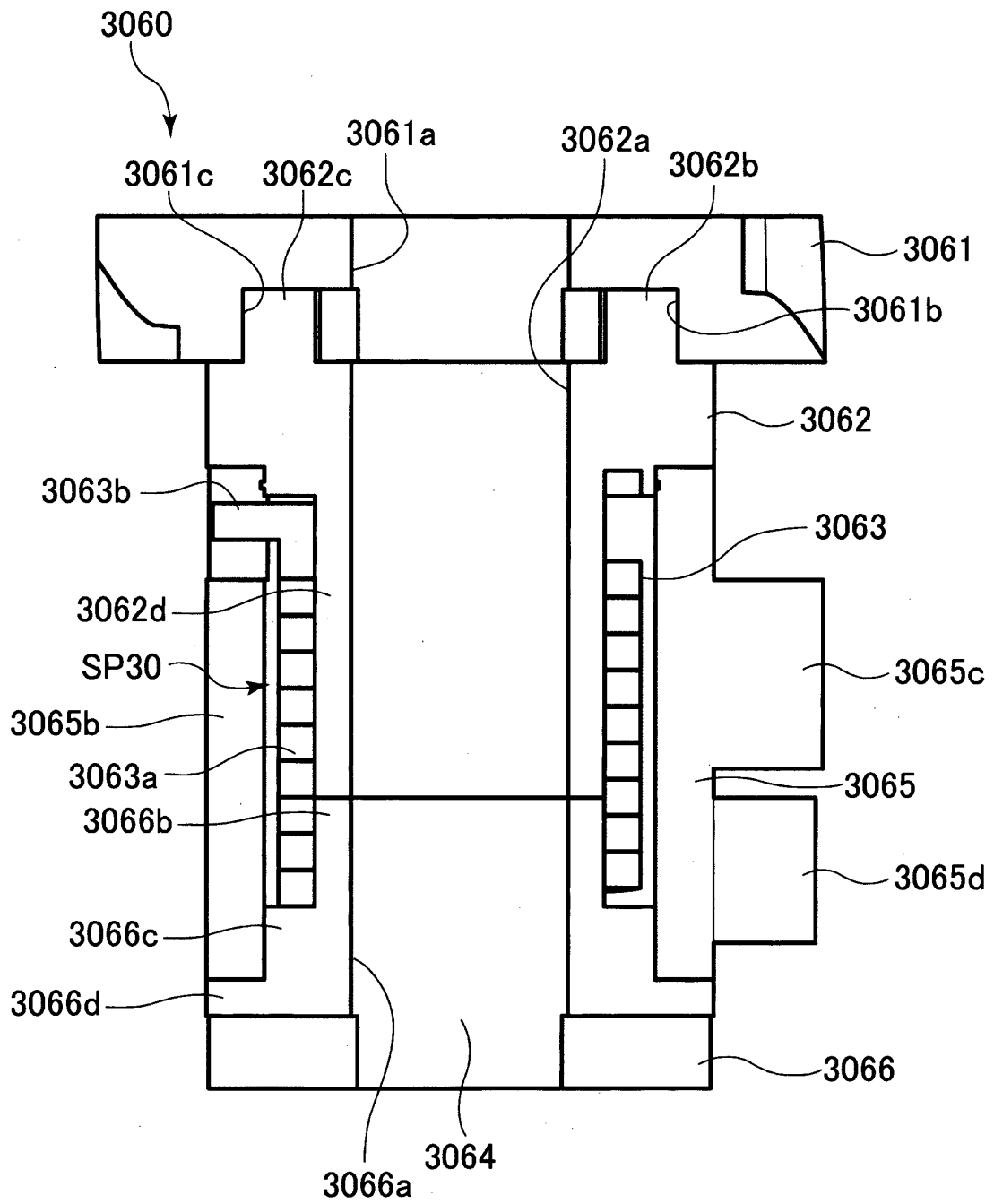


図276

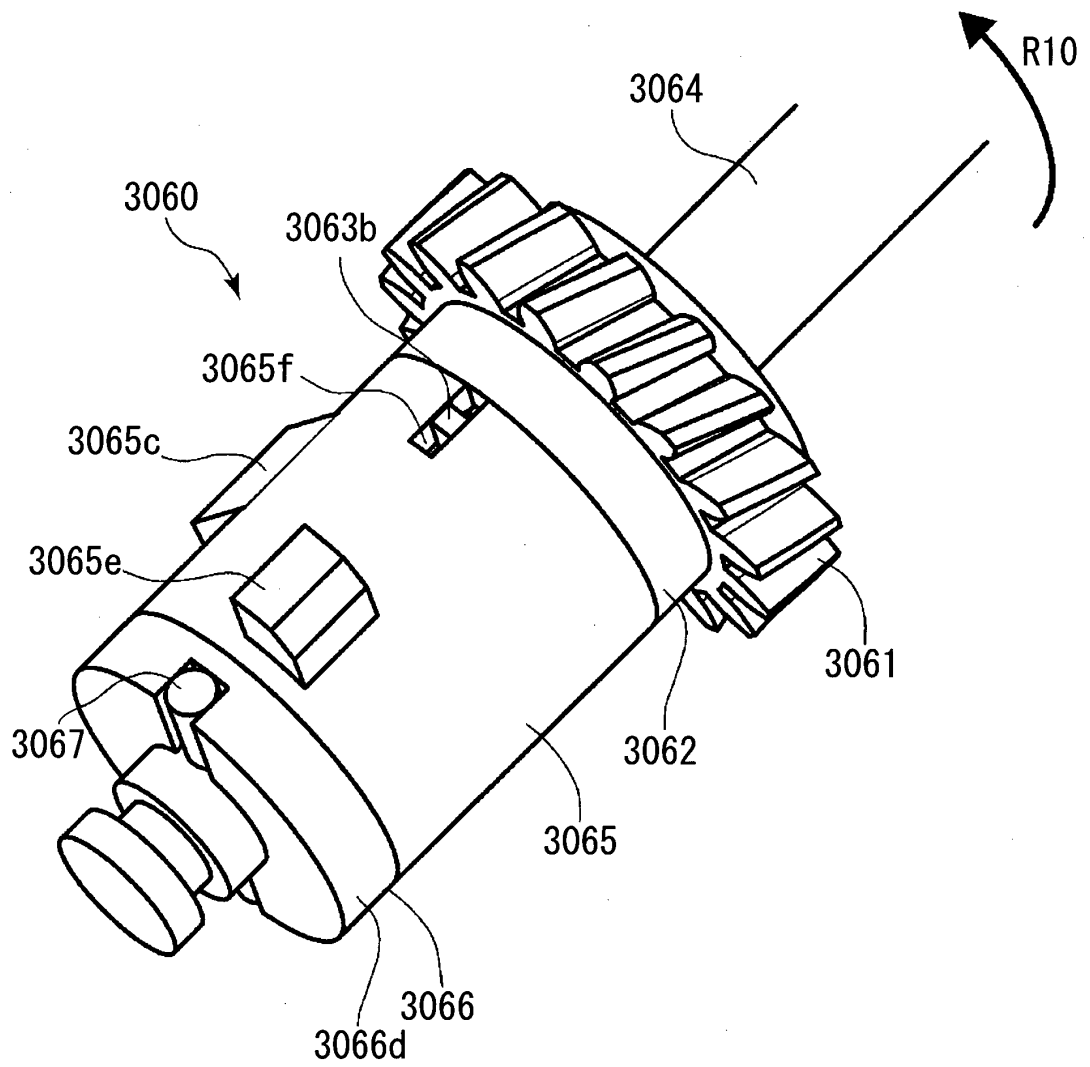


図277

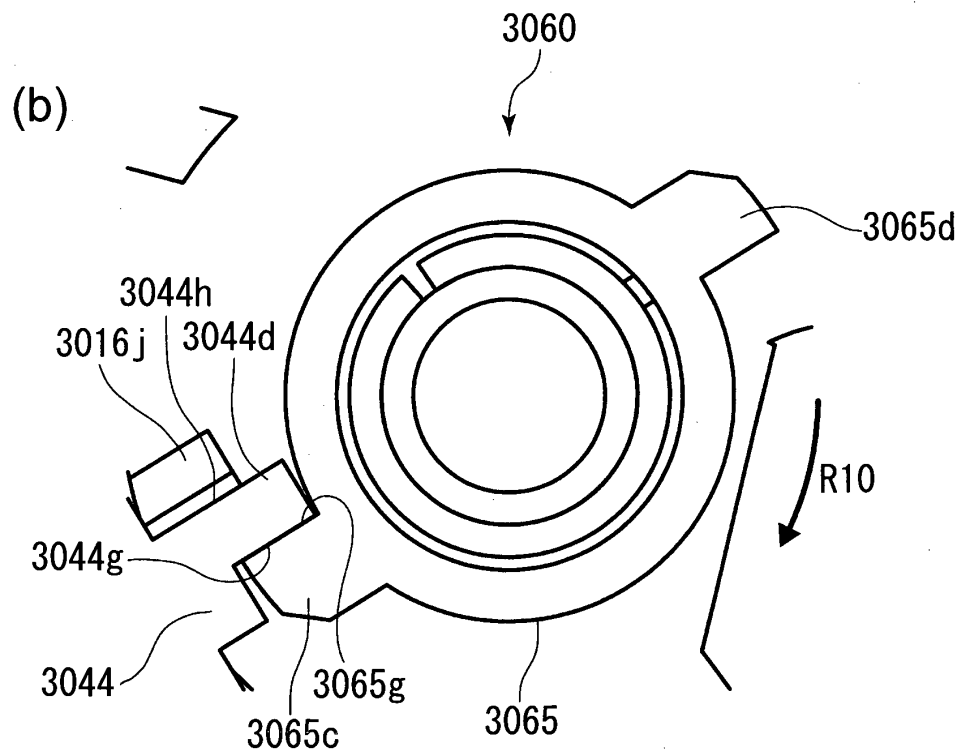
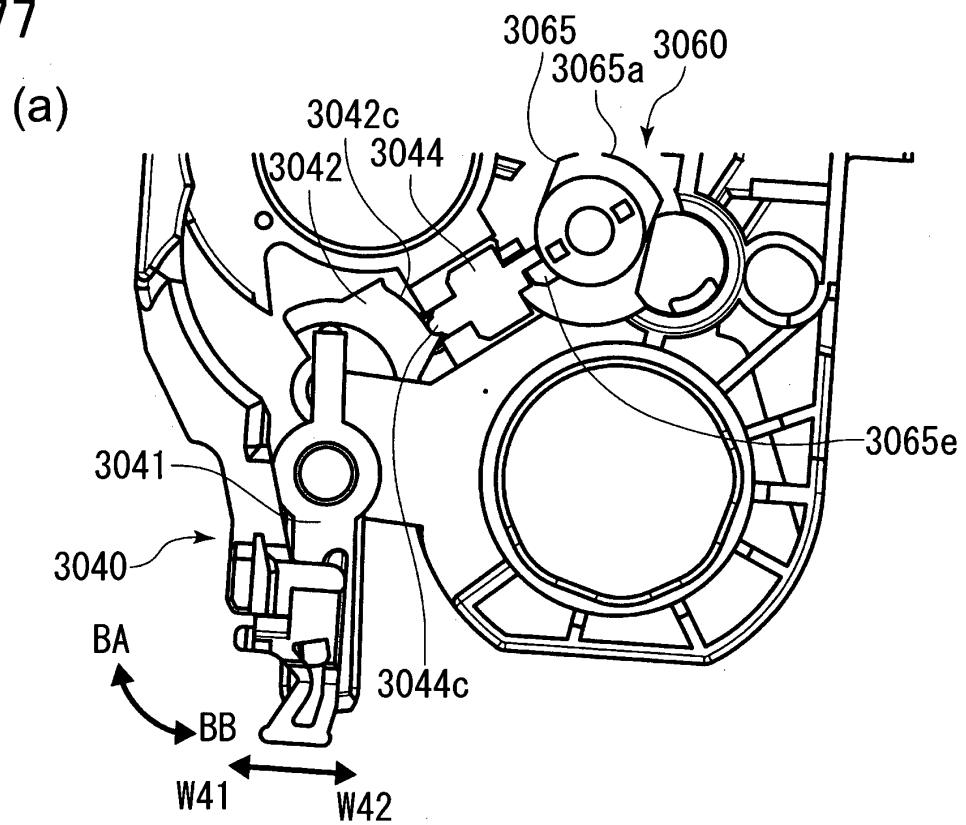


图278

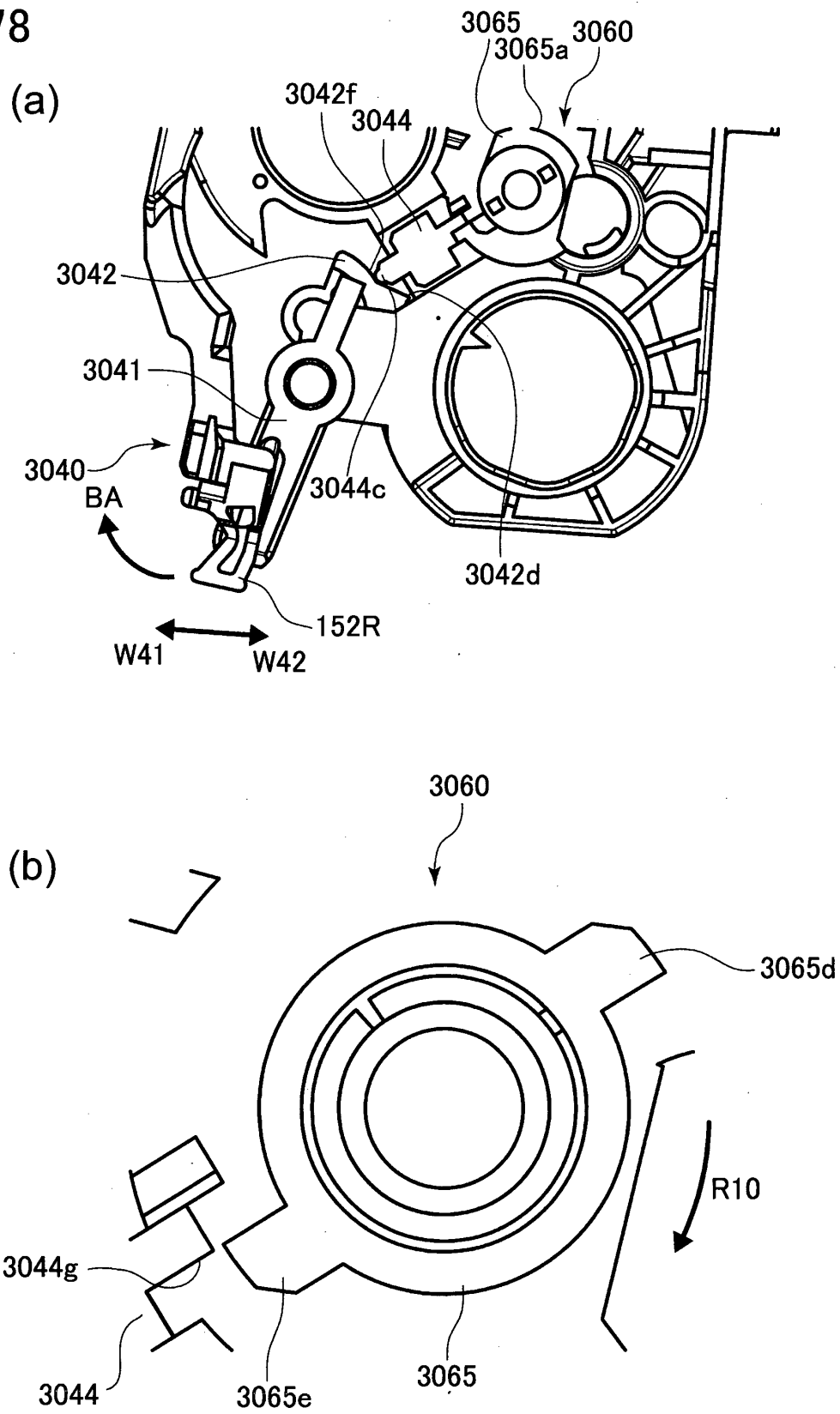


図279

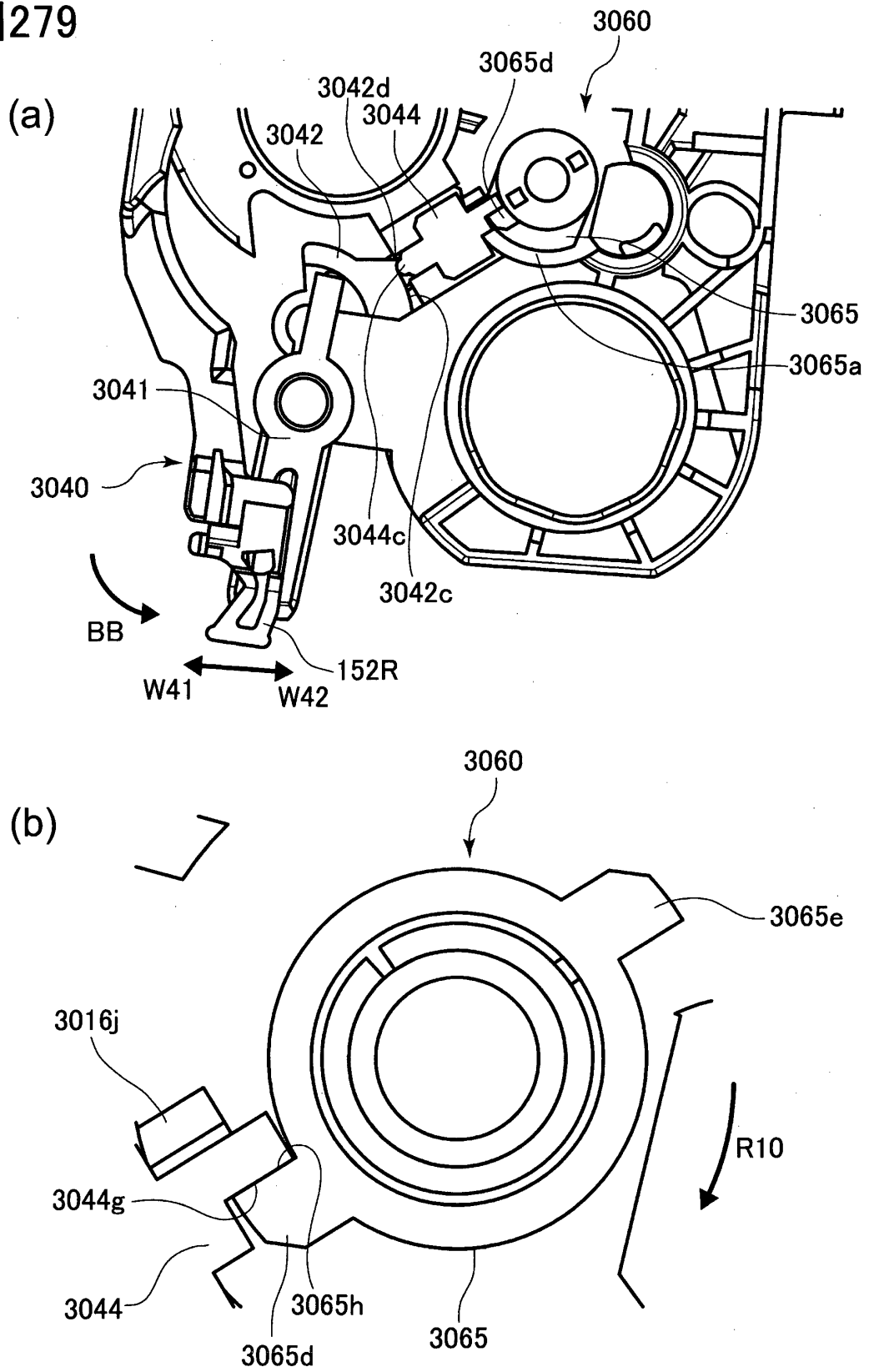


図280

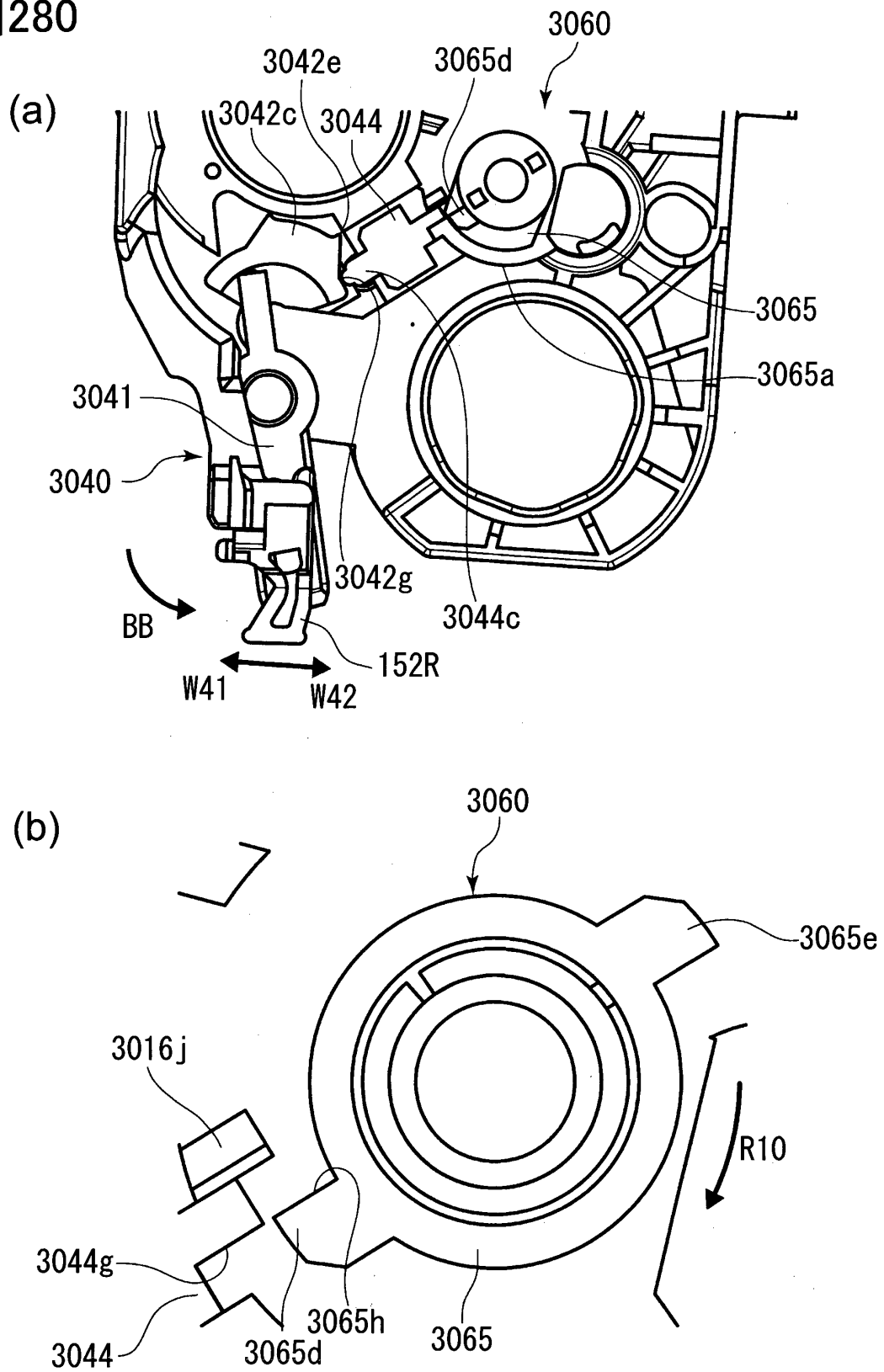


図281

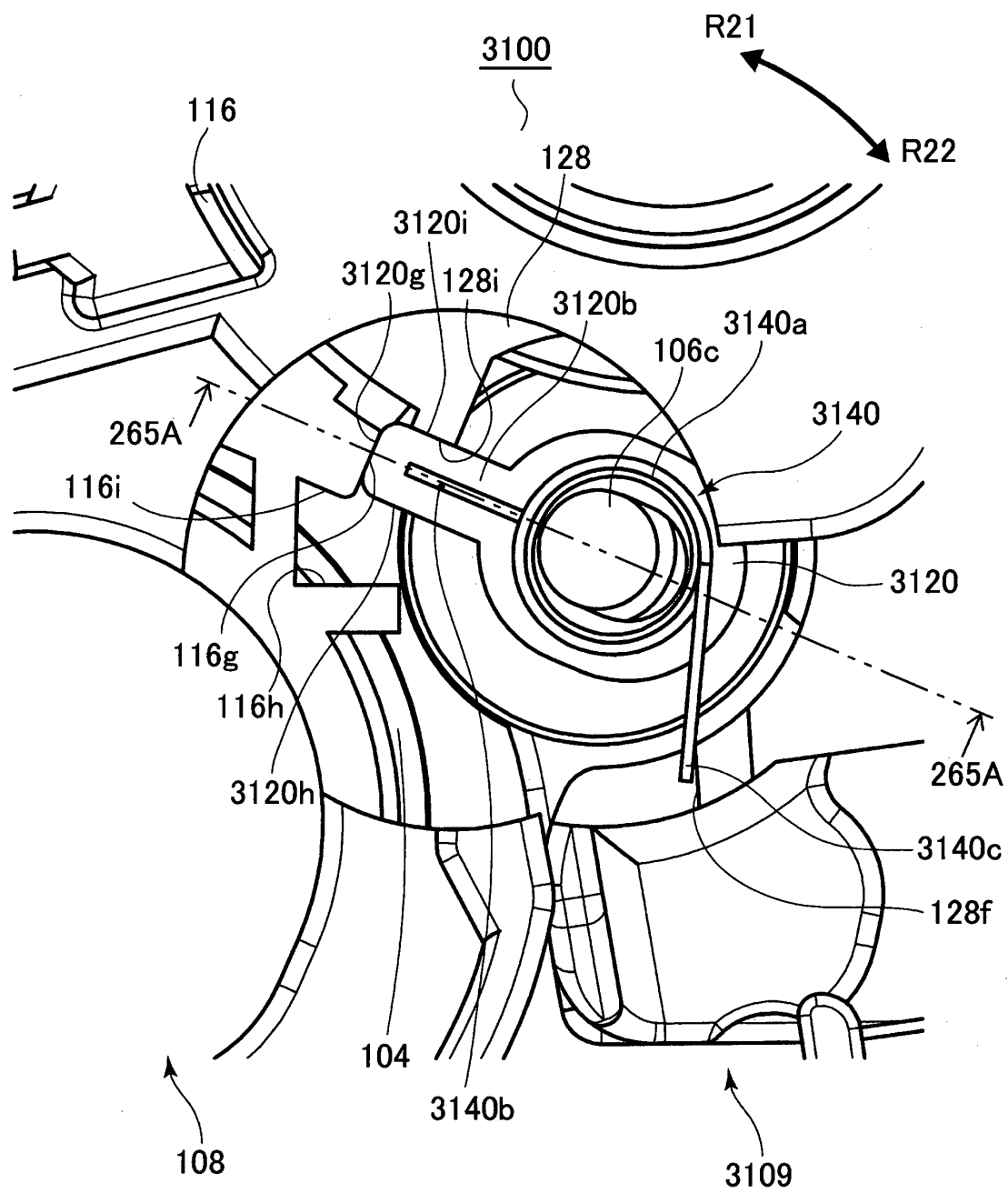


図282

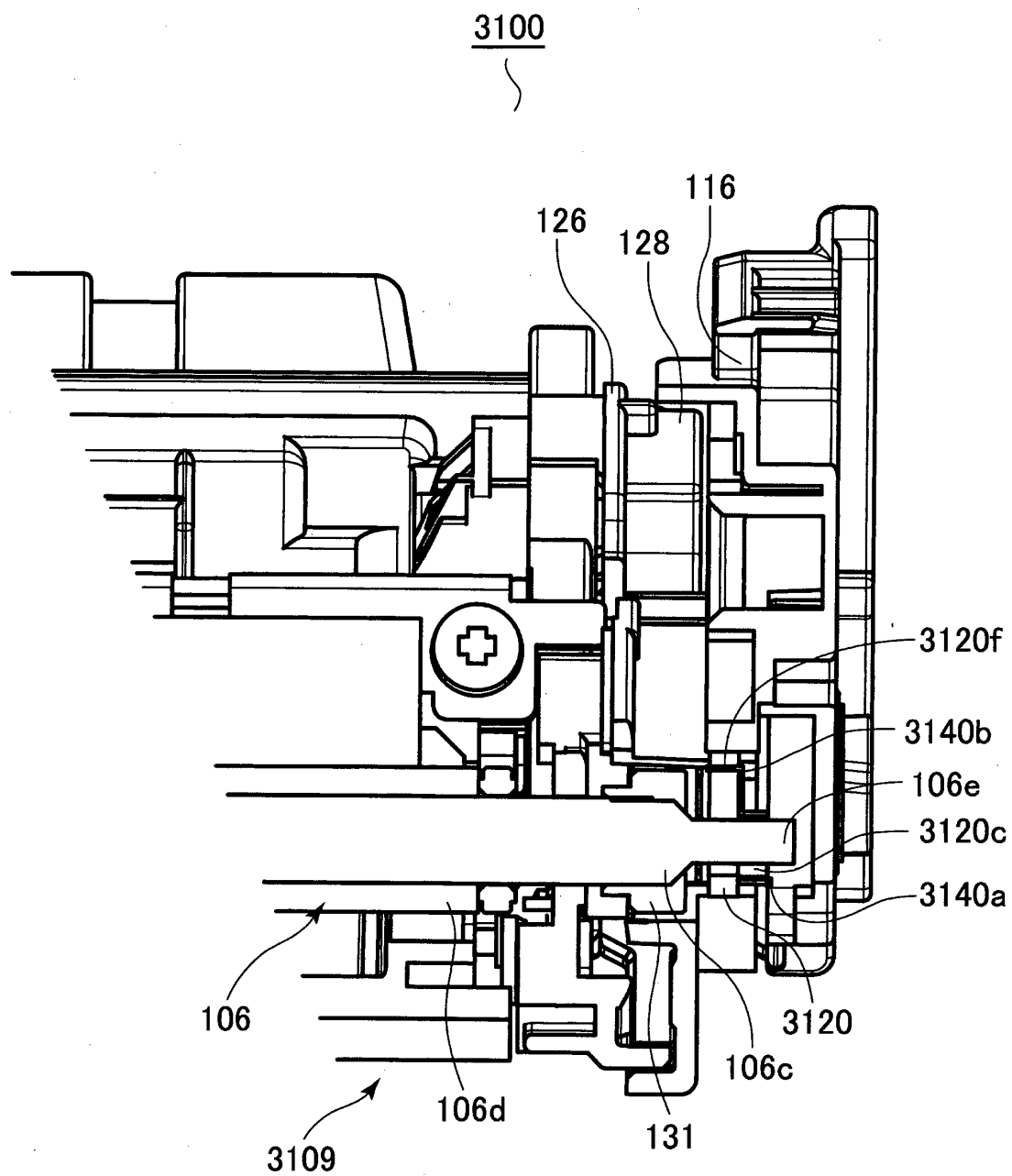


図 283

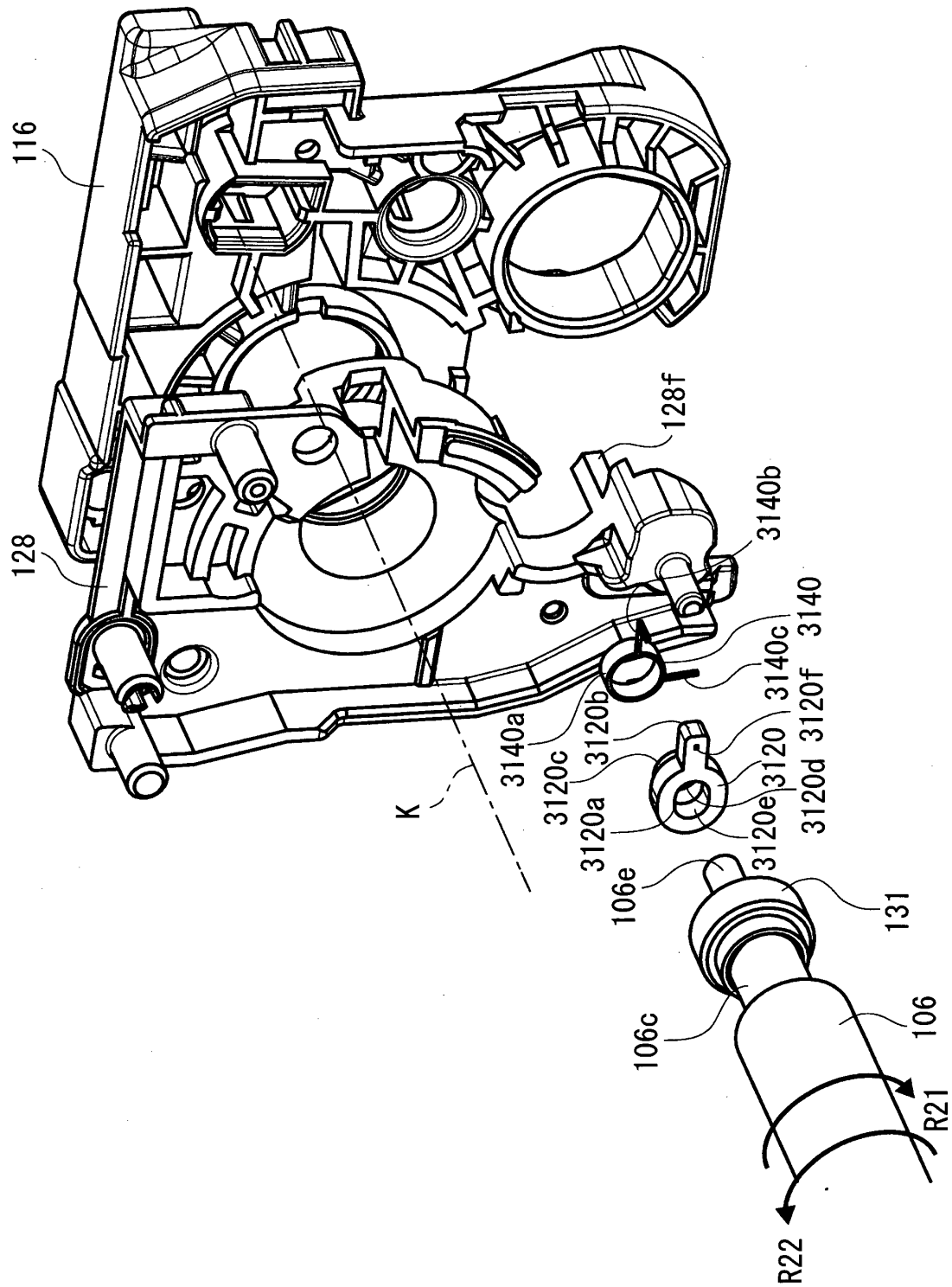


図284

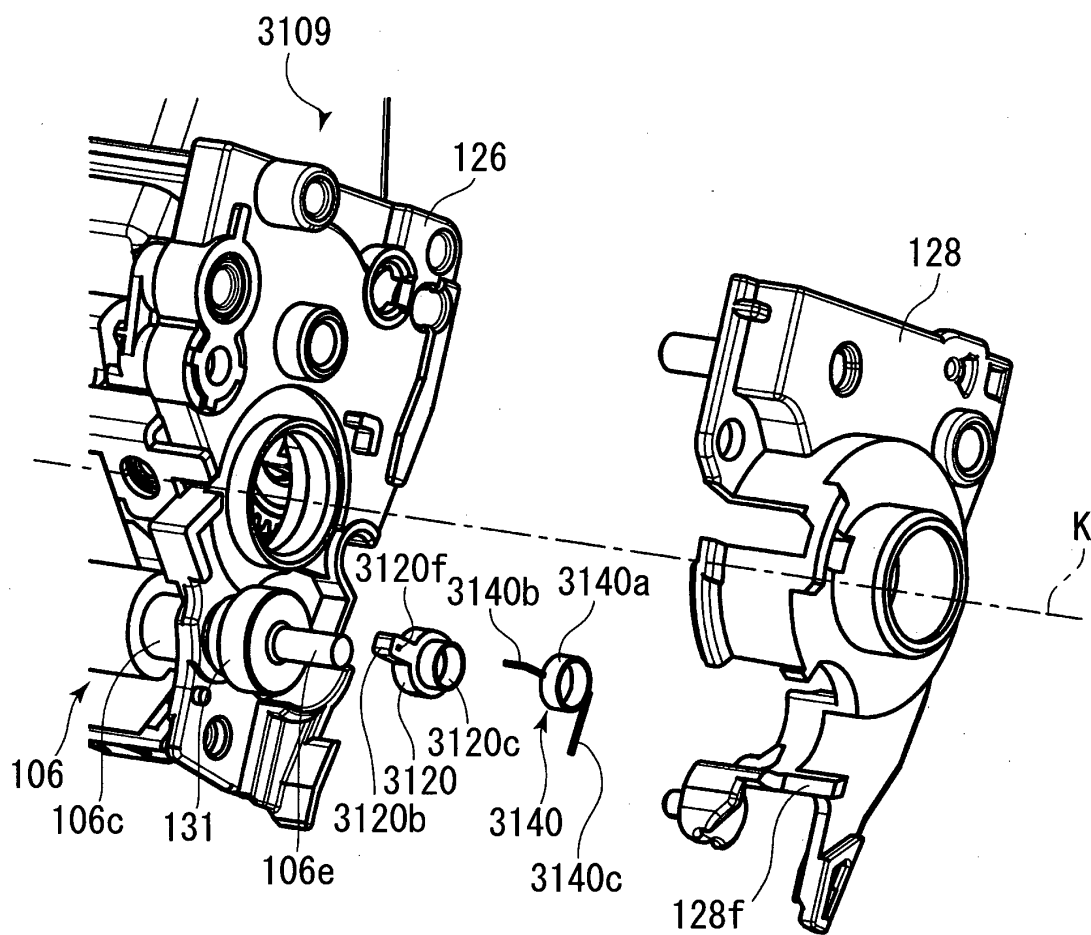
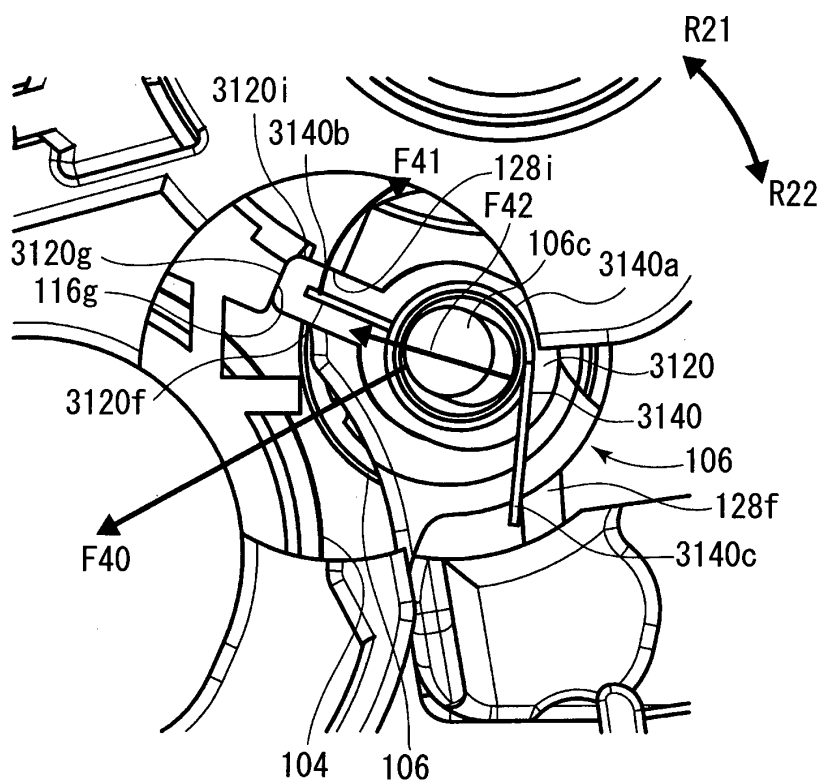


図285

(a)



(b)

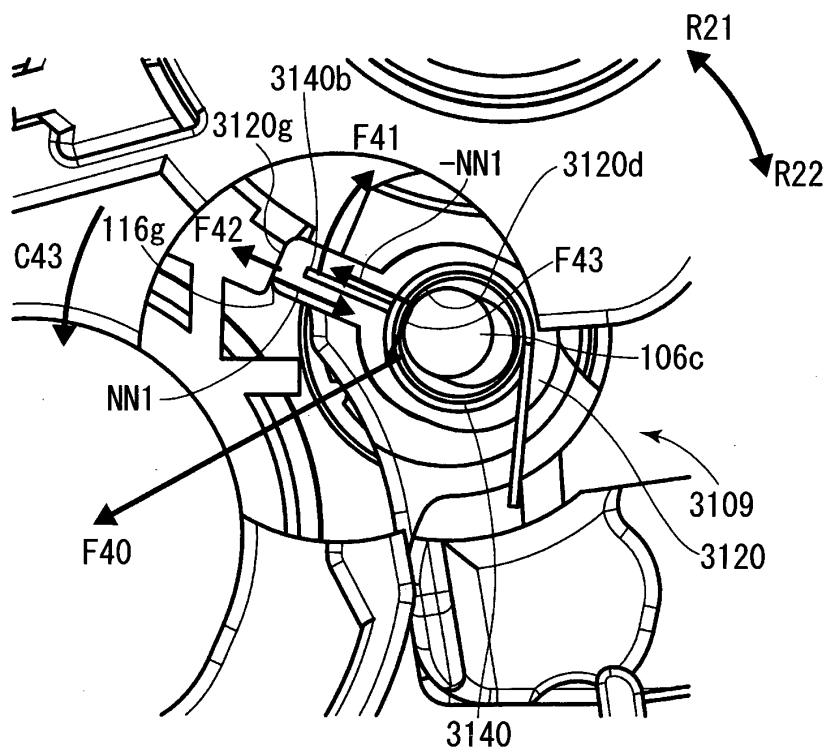


図286

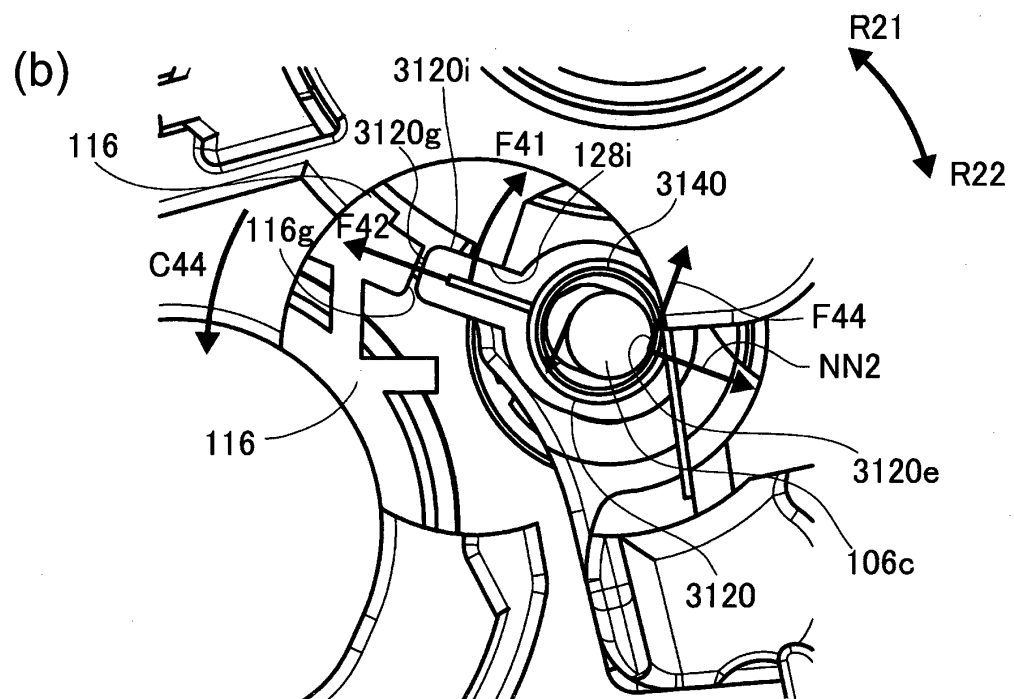
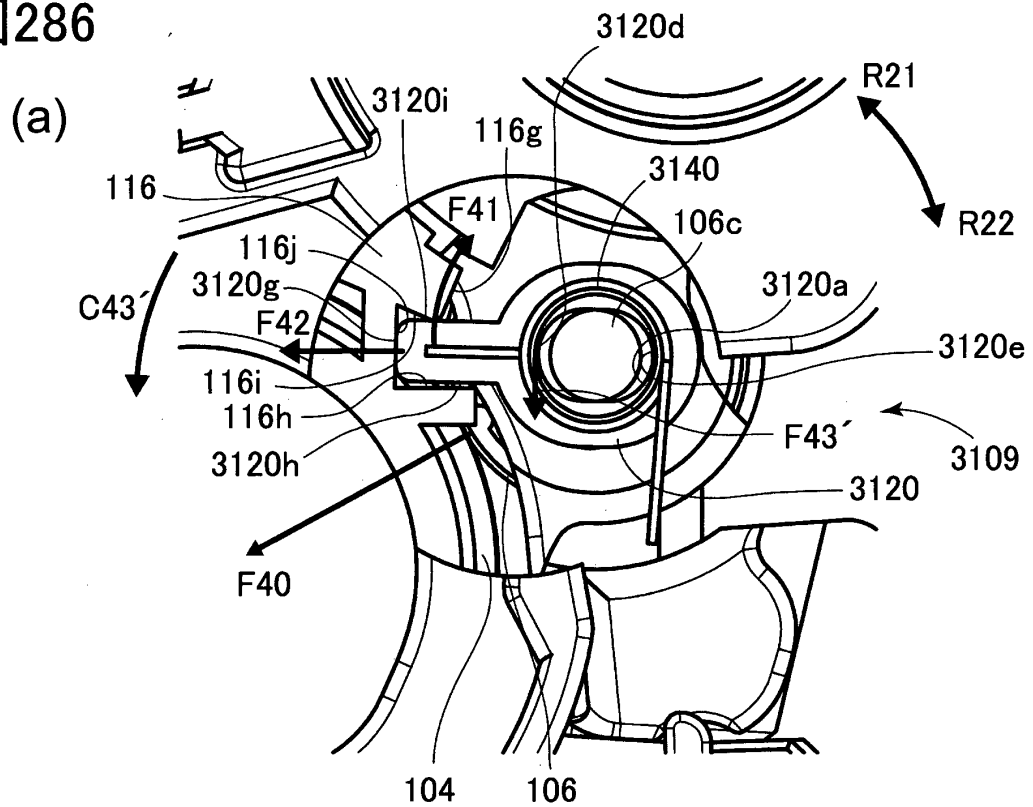


図287

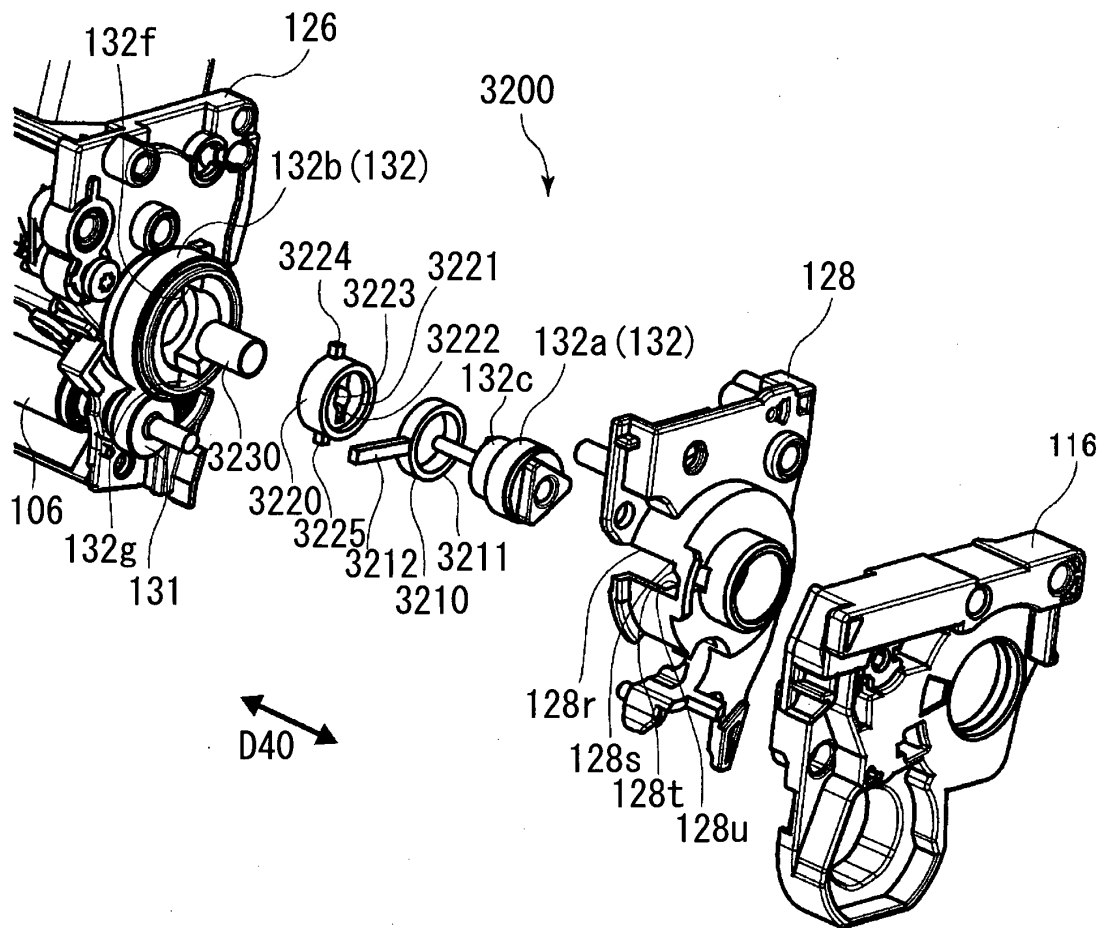


図 288

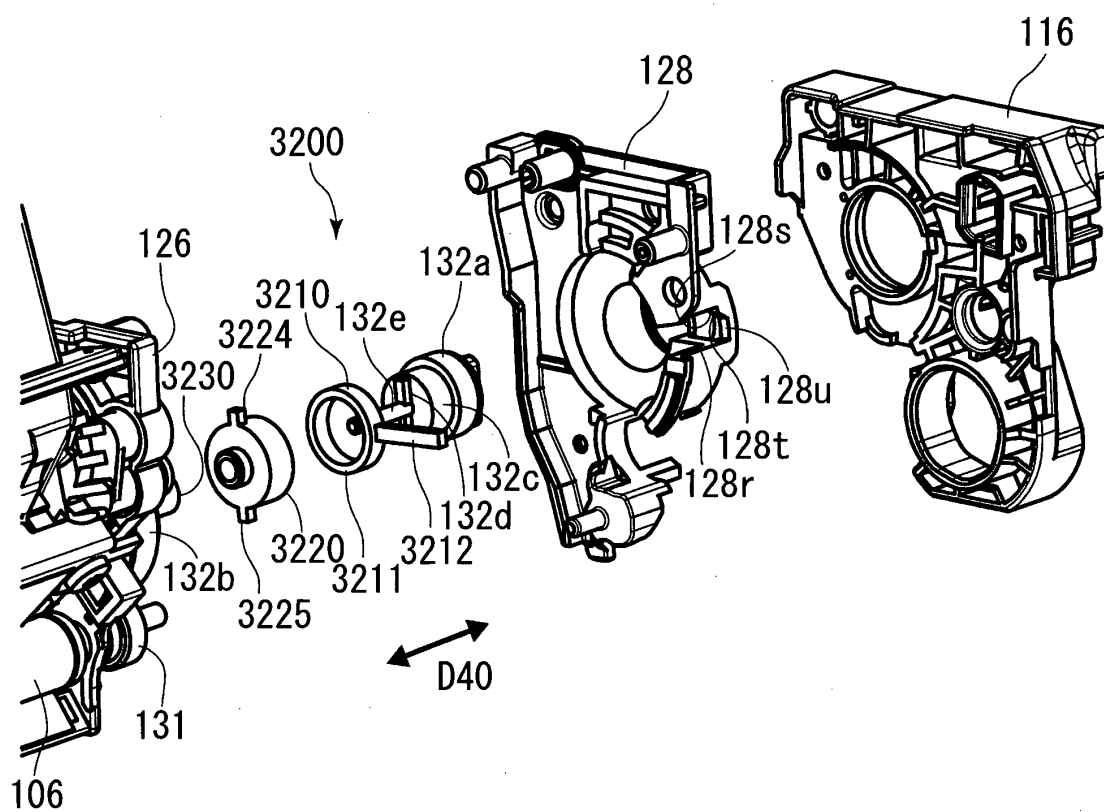


図289

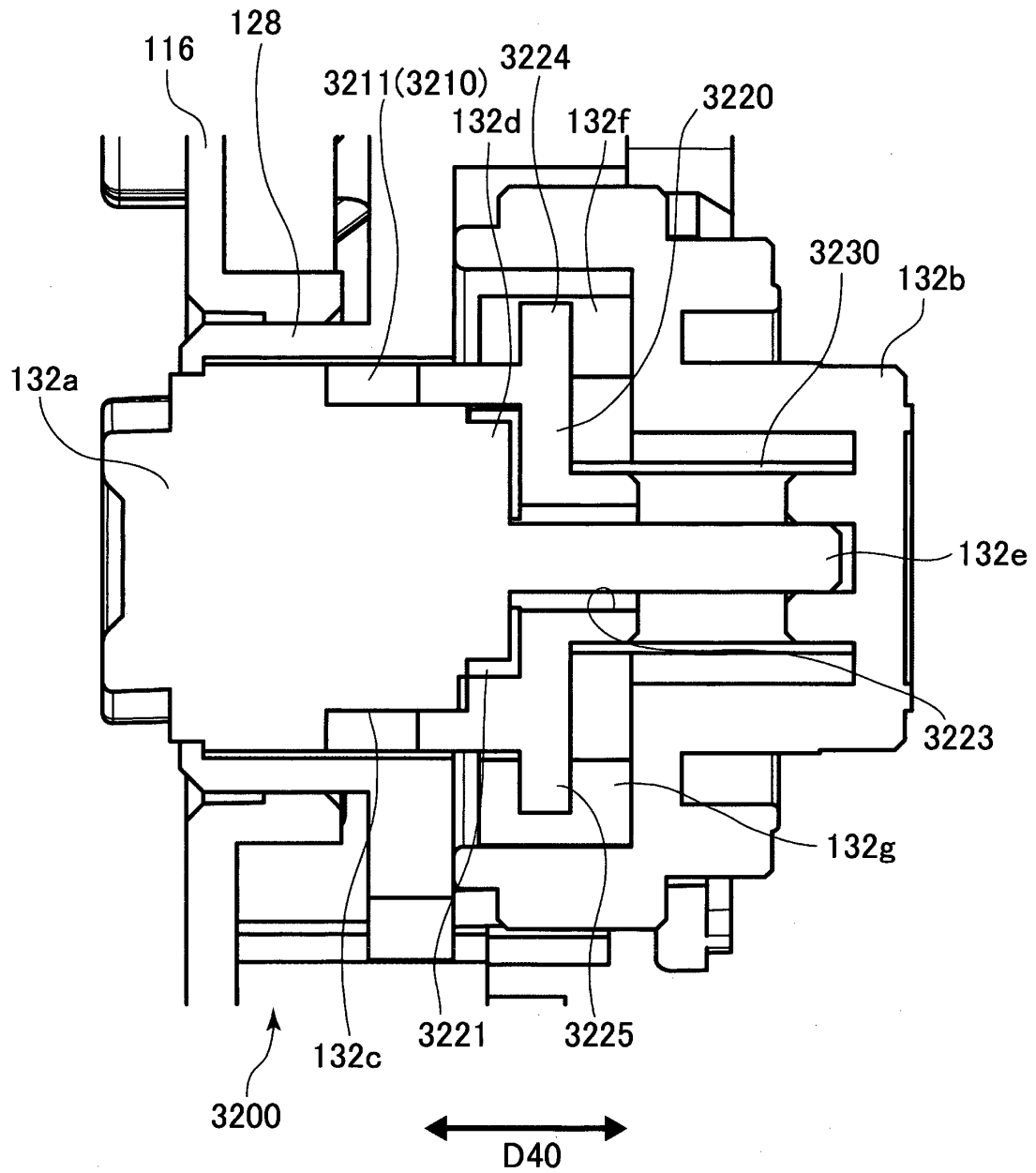
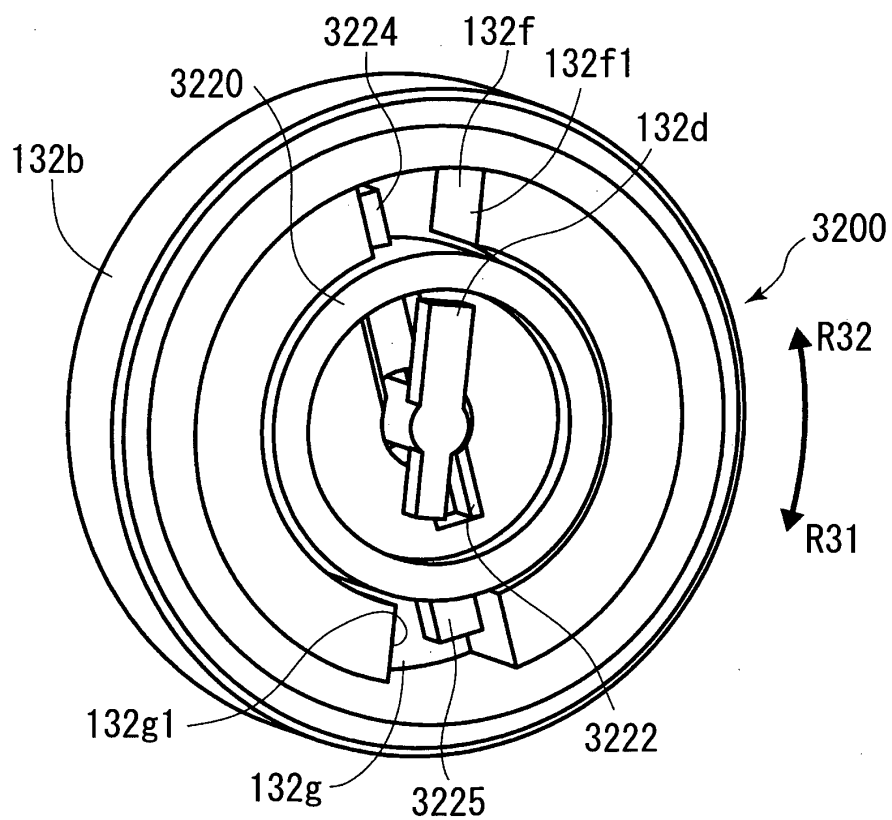


図 290

(a)



(b)

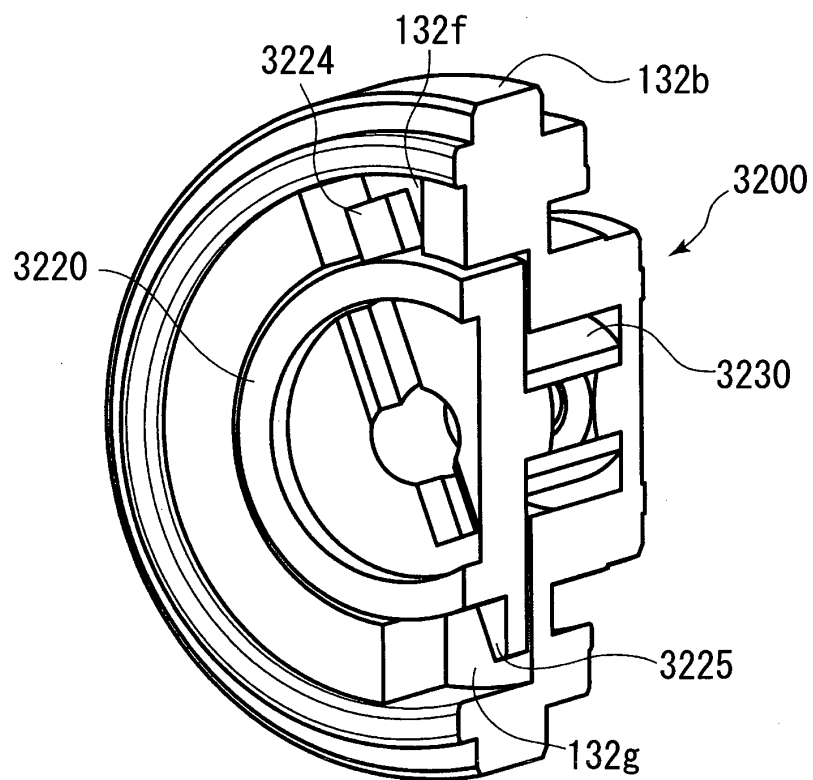


図291

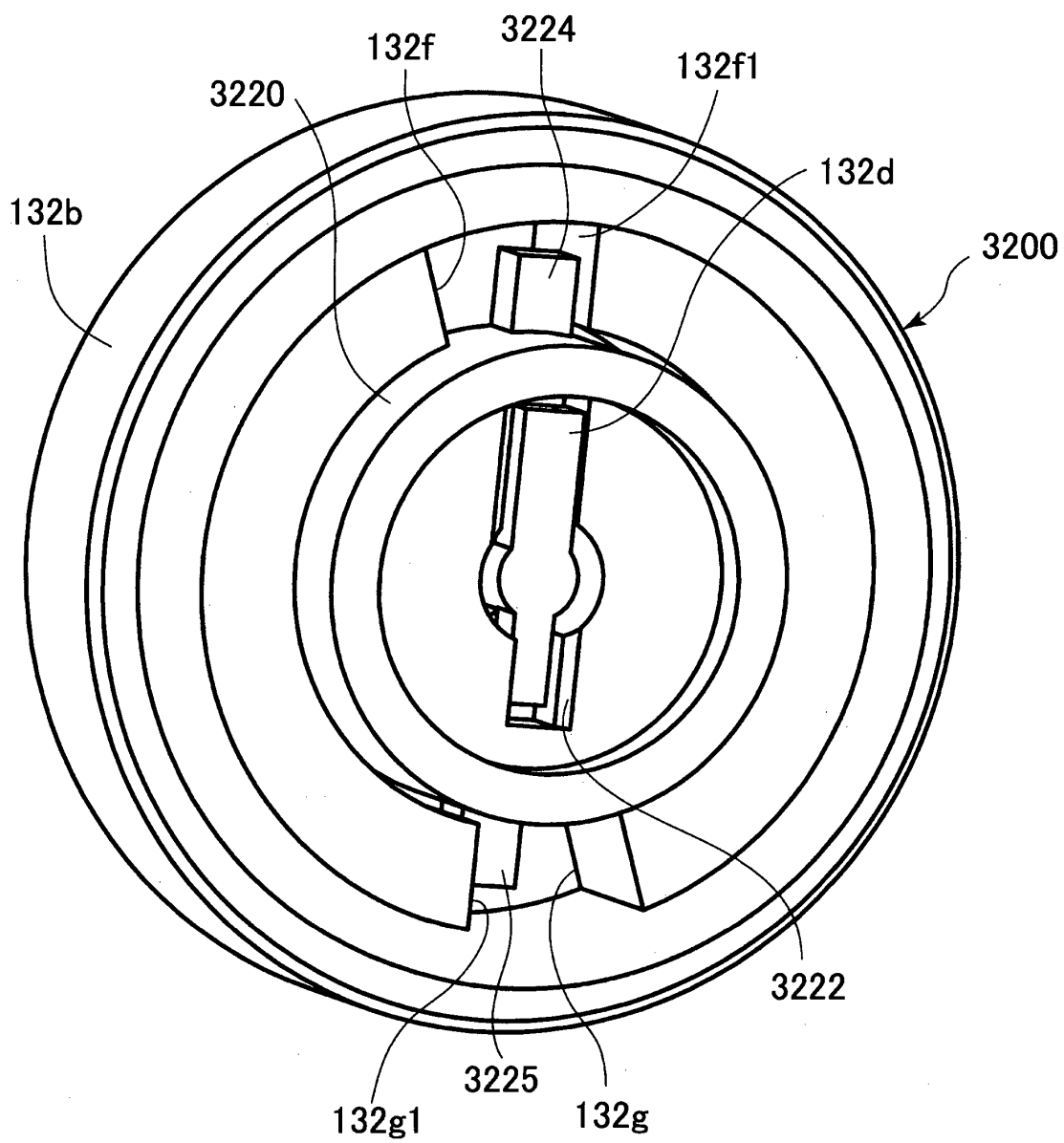


図292

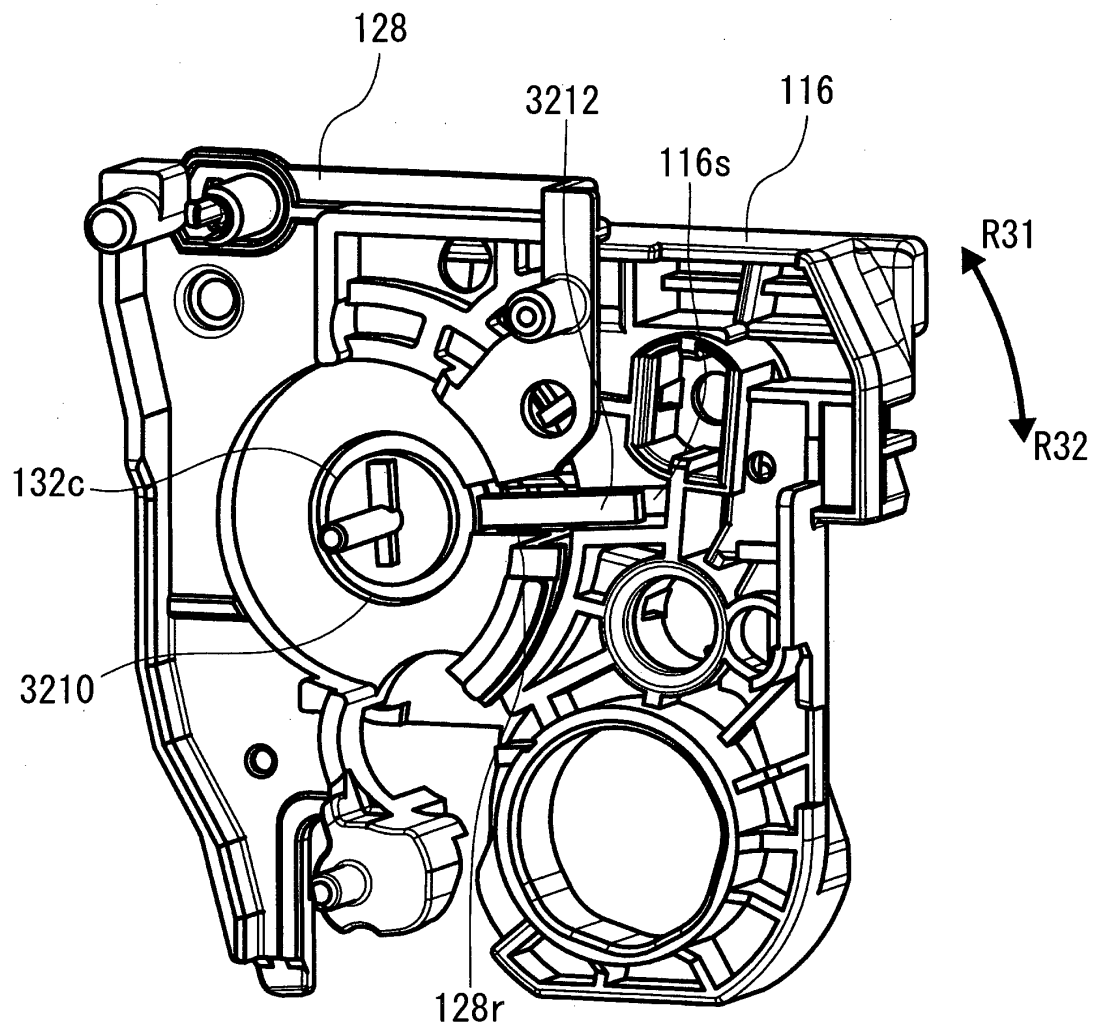


図293

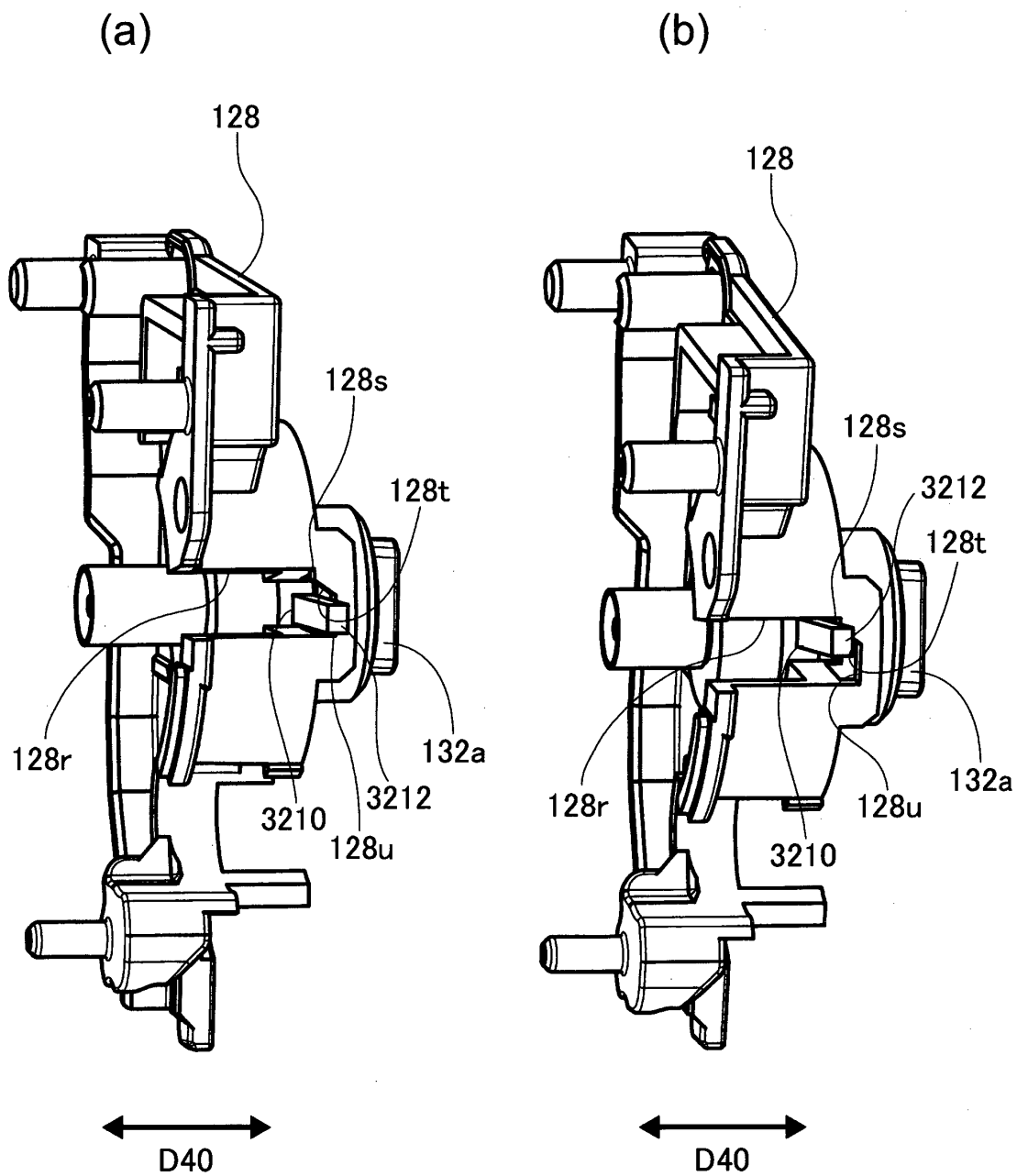


図294

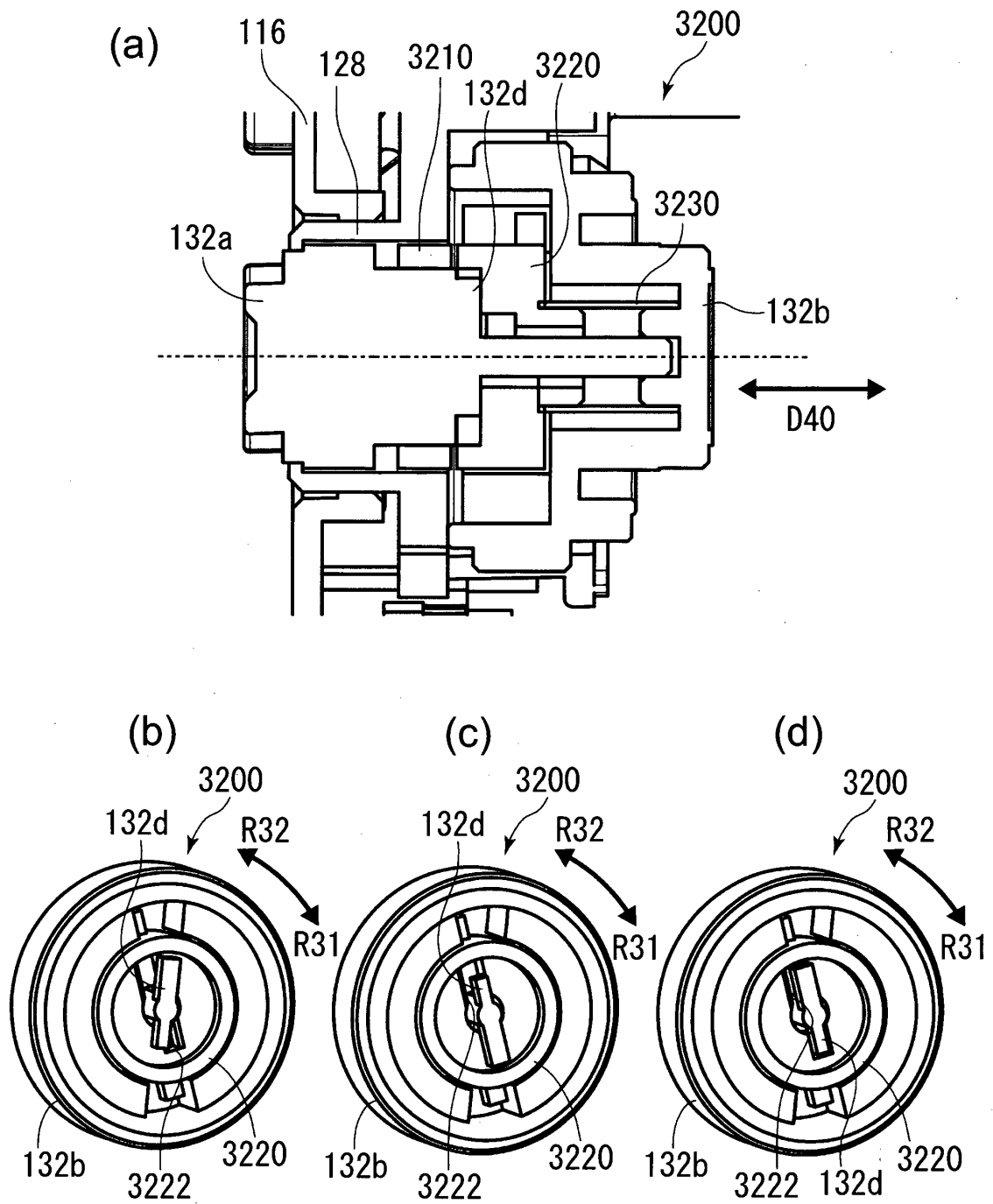


図295

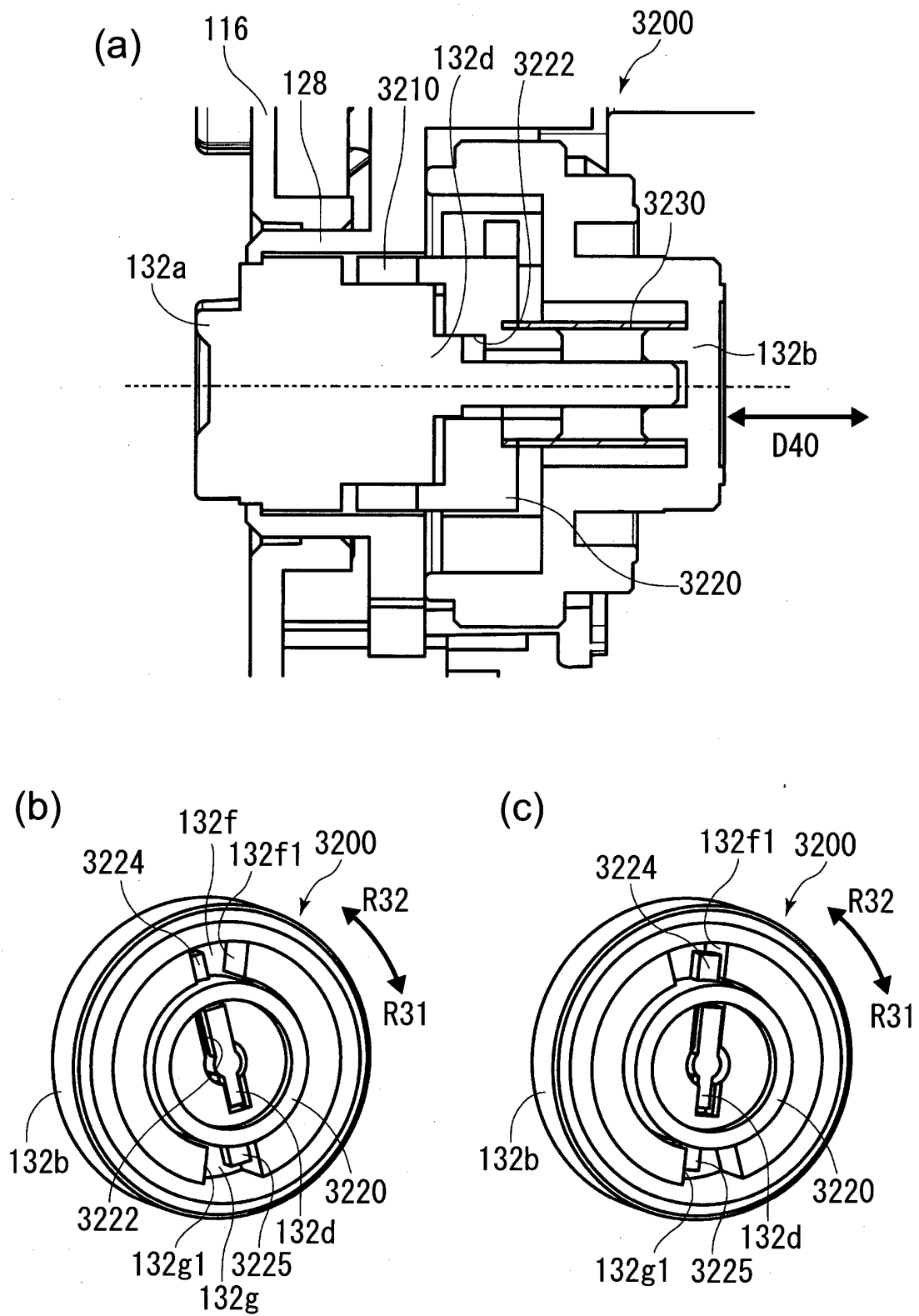


図296

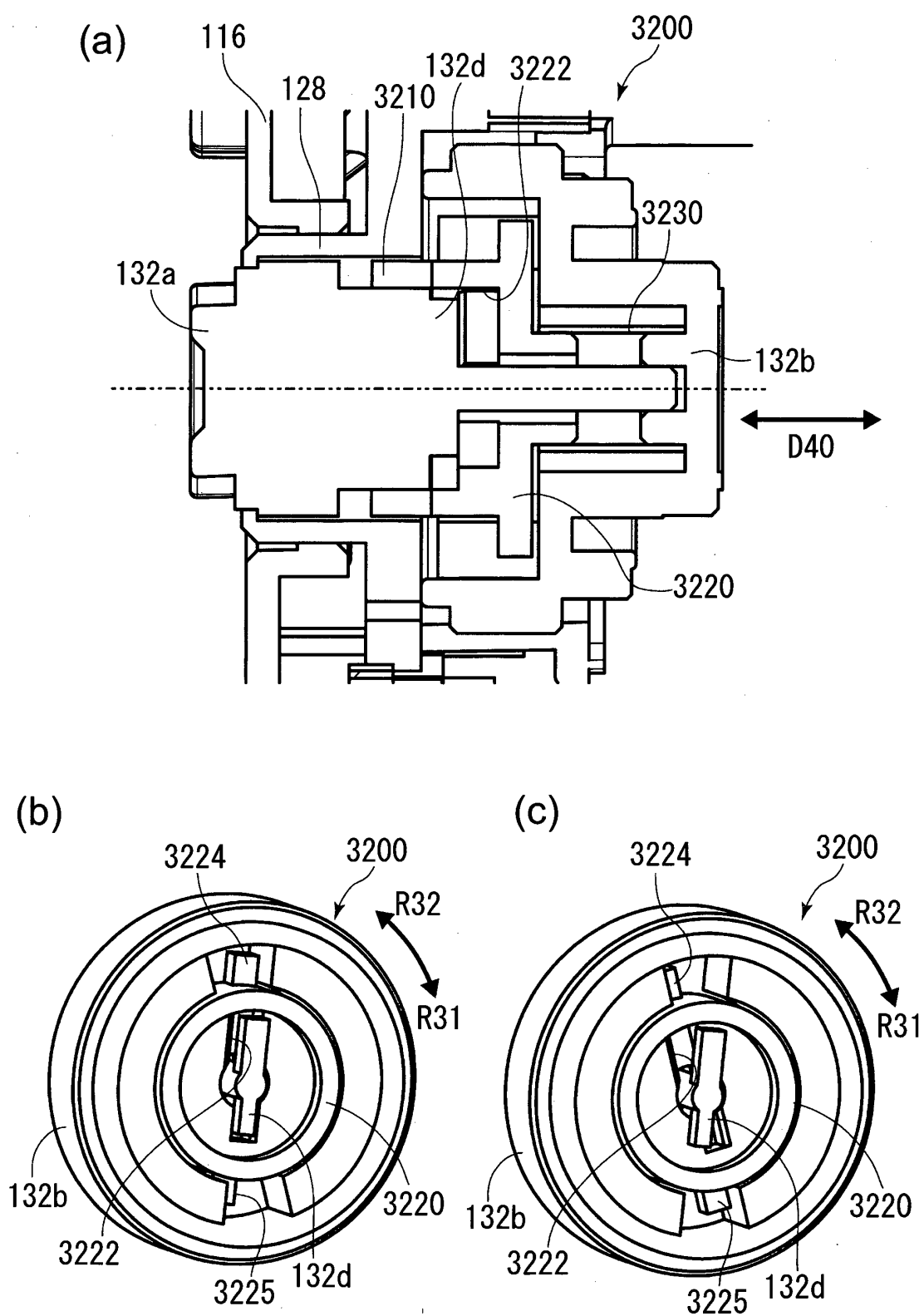


図297

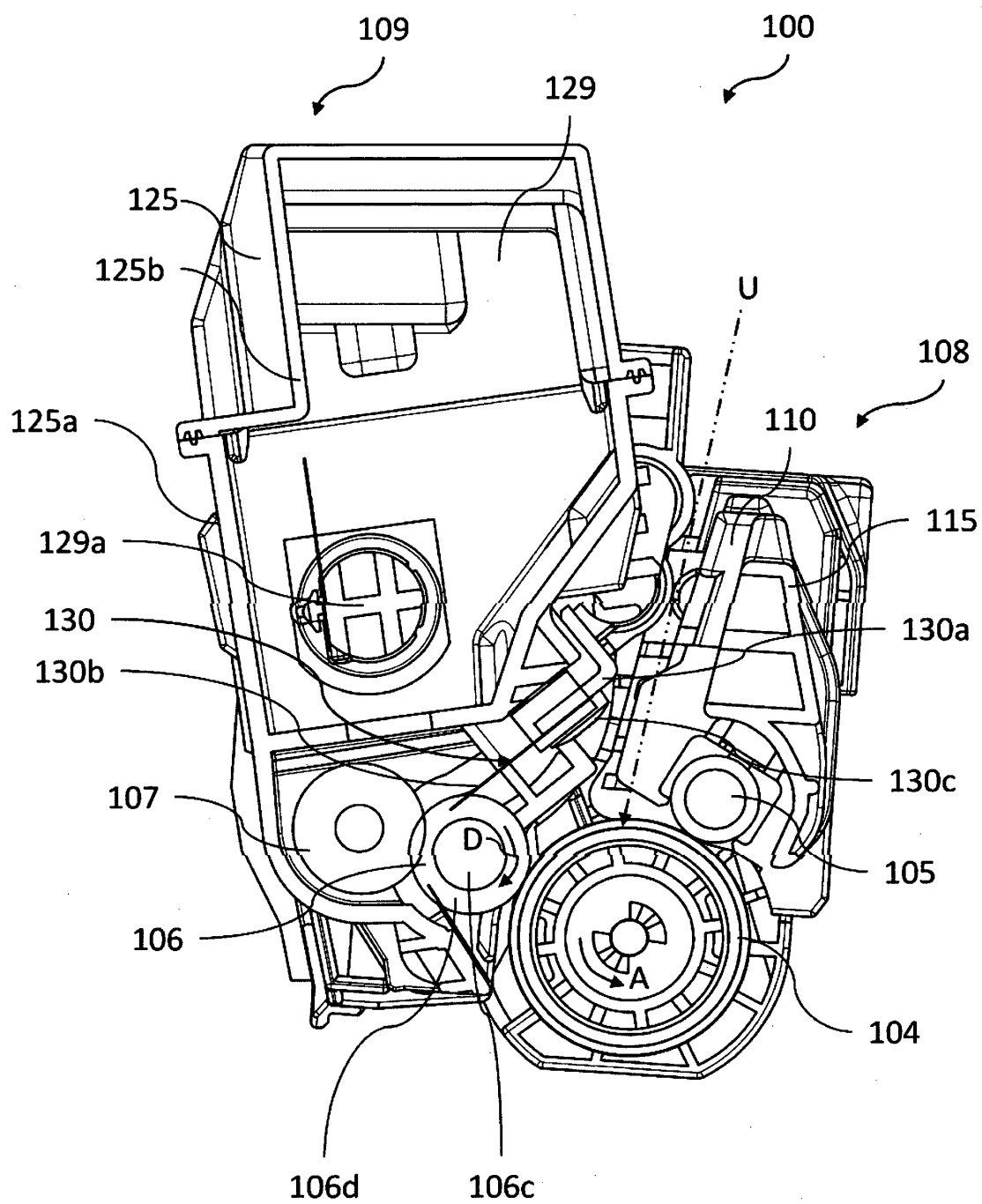


図298

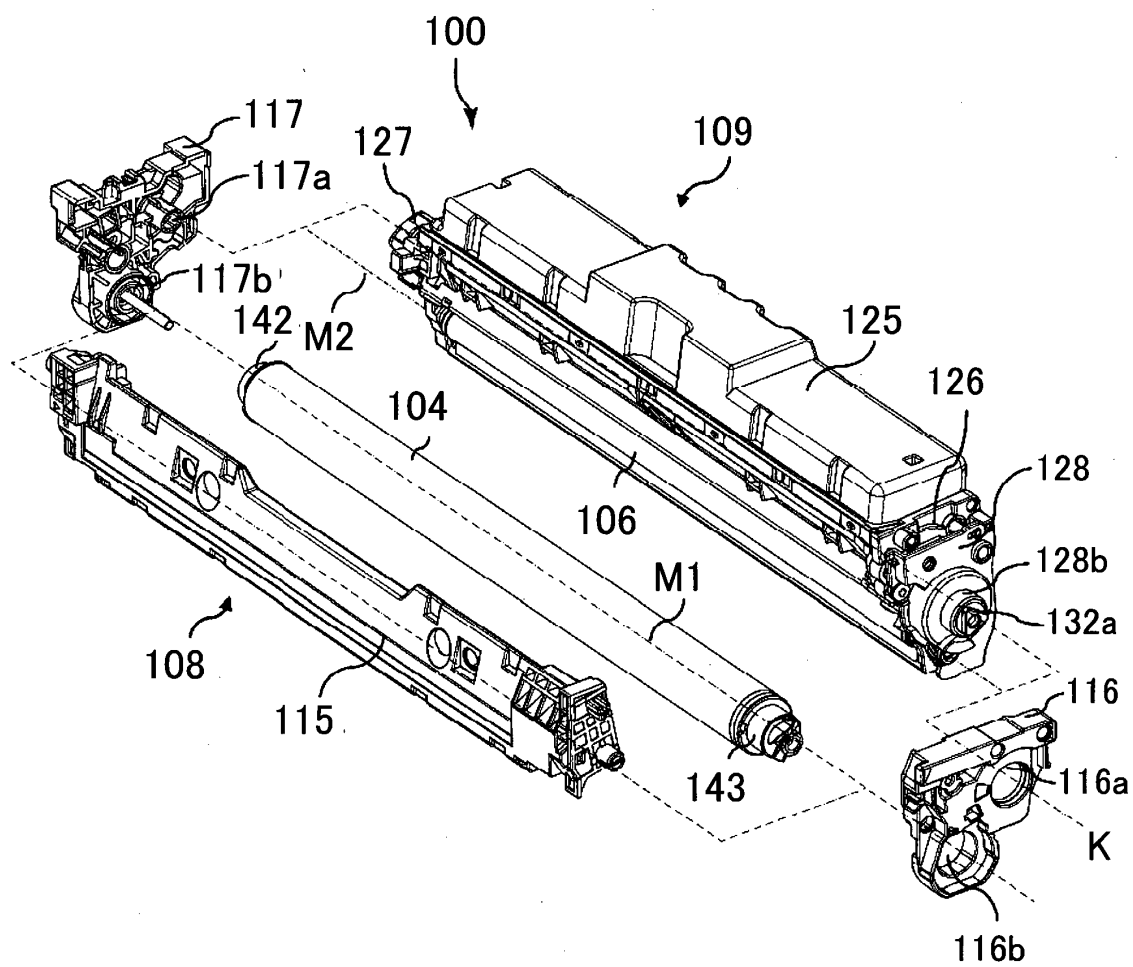


図299

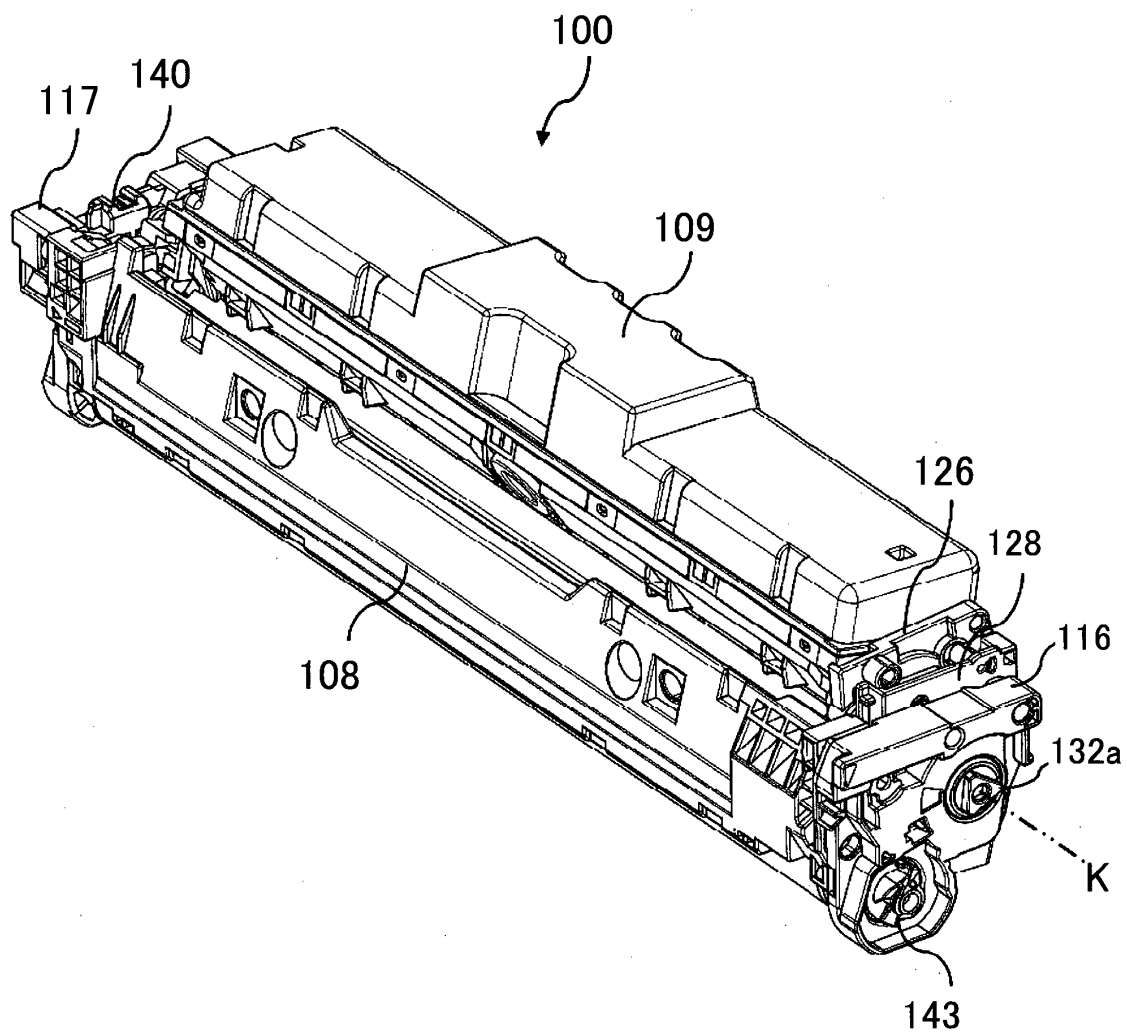
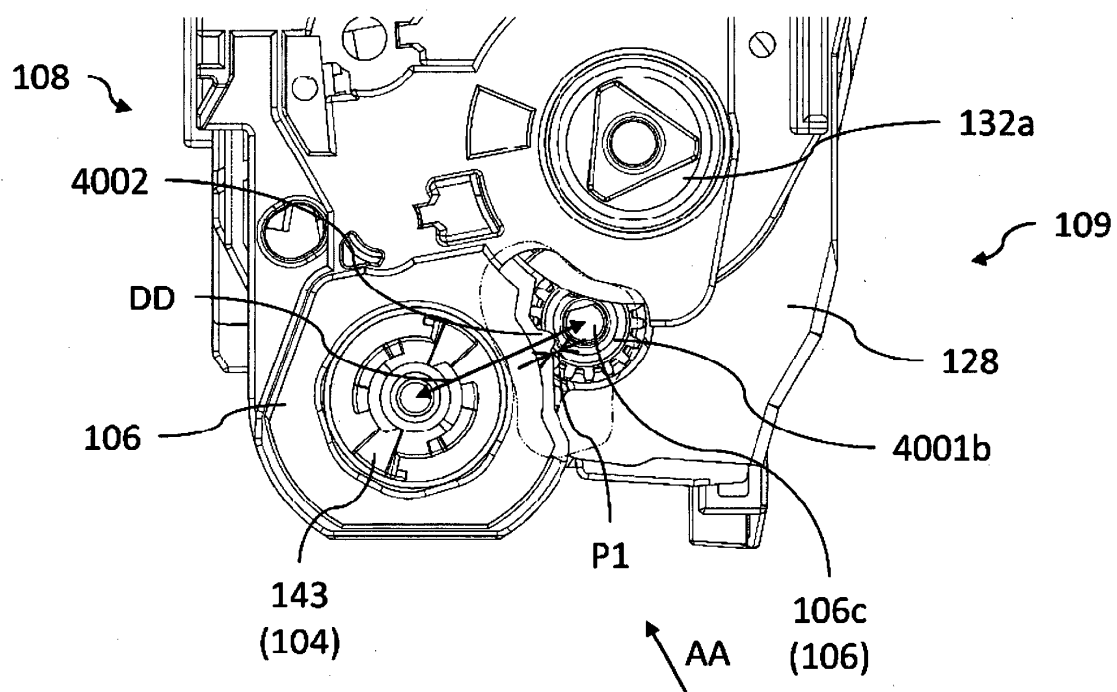


図300

(a)



(b)

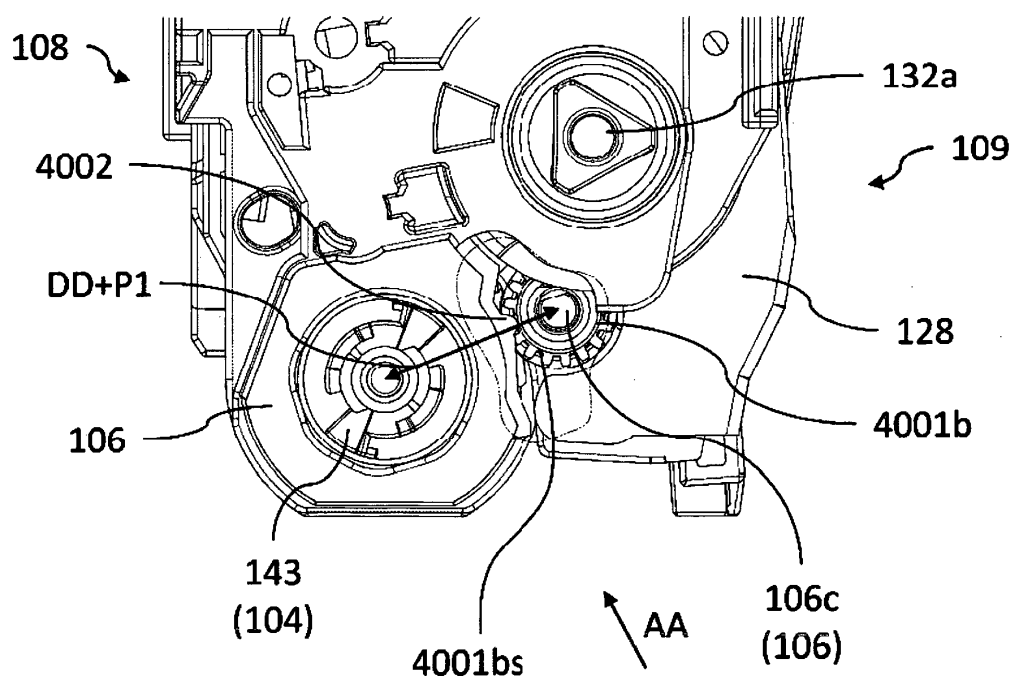
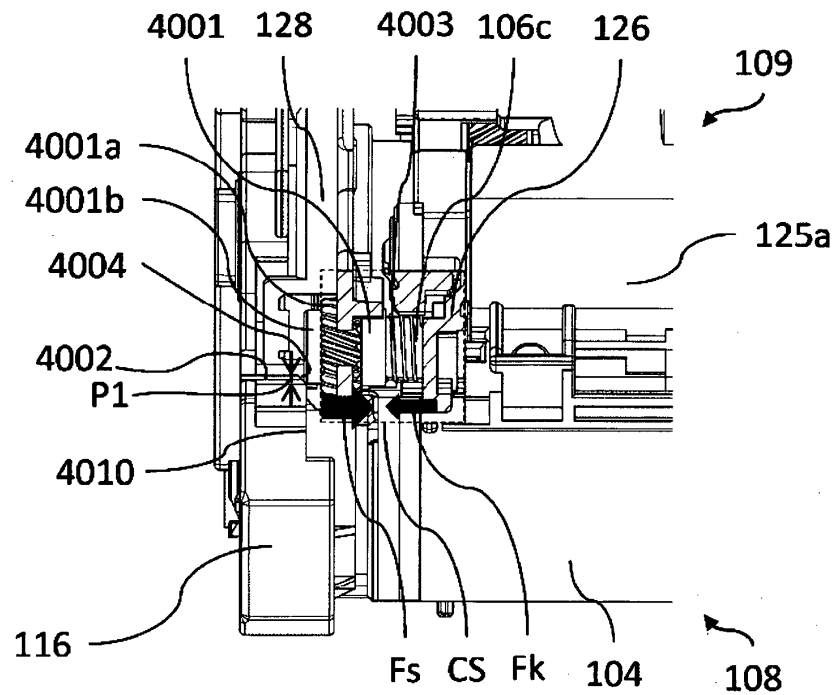


図301

(a)



(b)

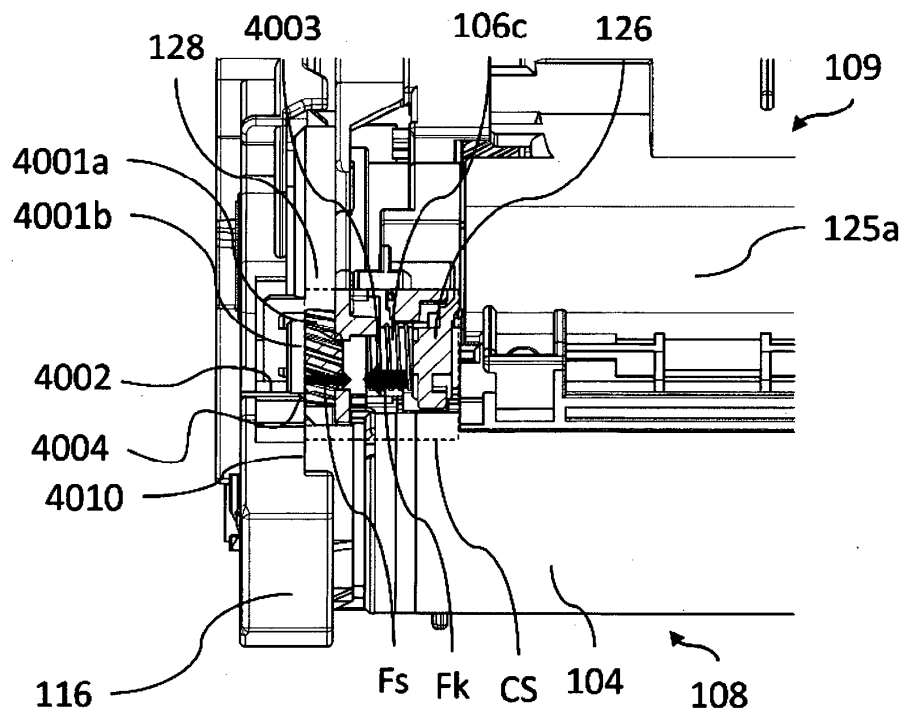


図302

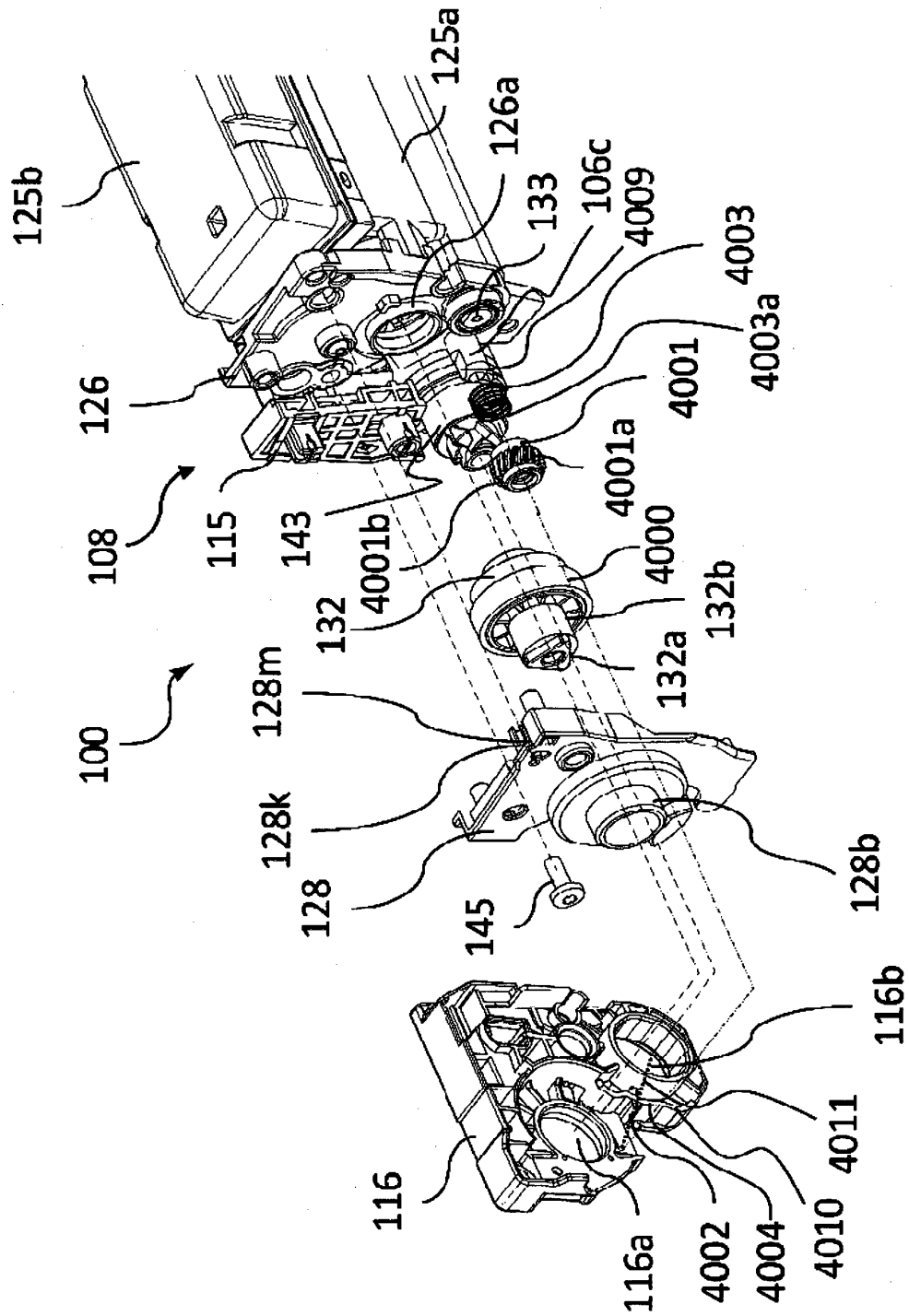
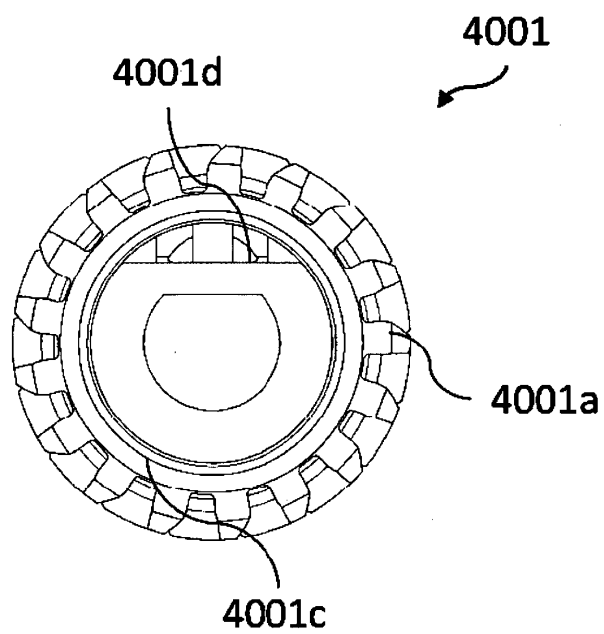


図303

(a)



(b)

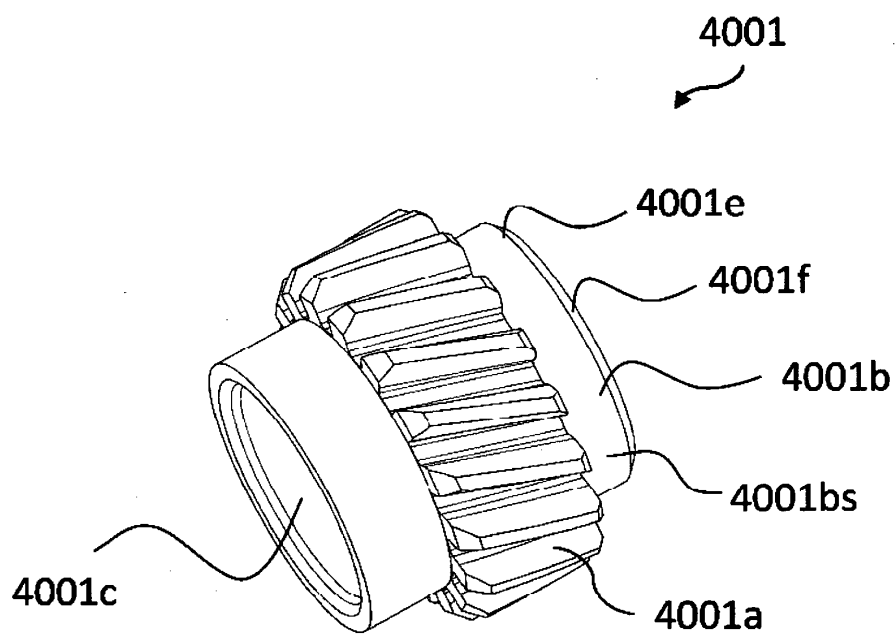
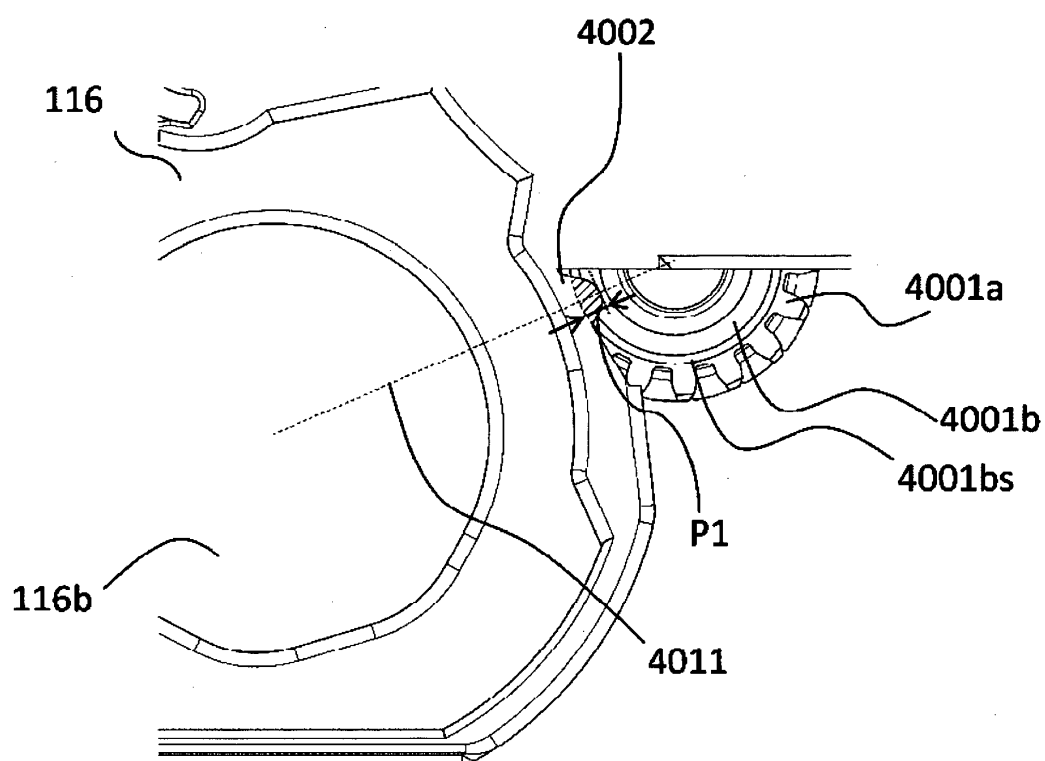


図304

(a)



(b)

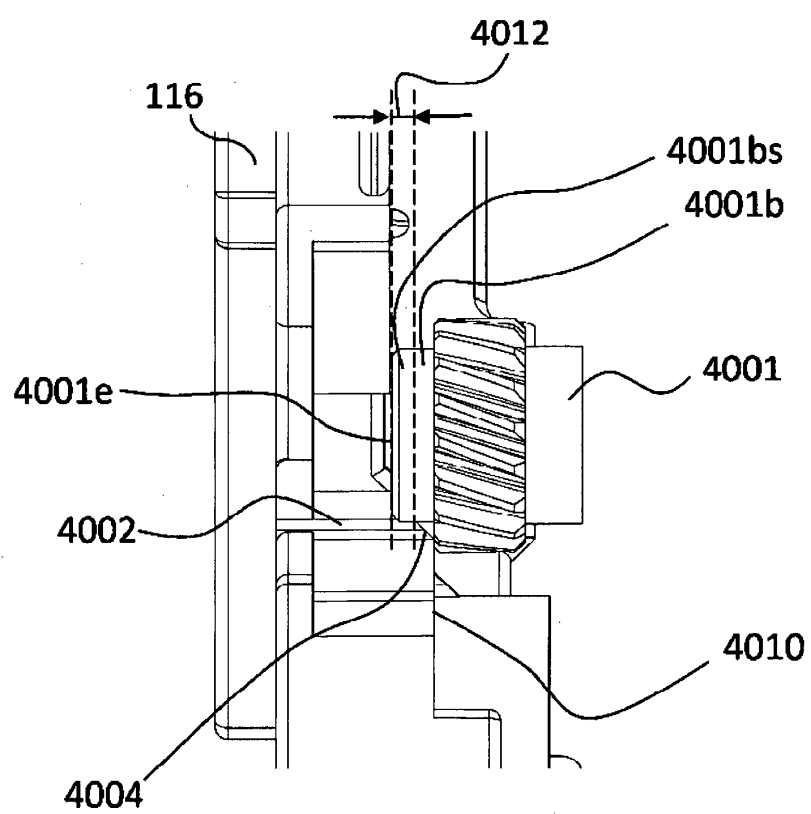
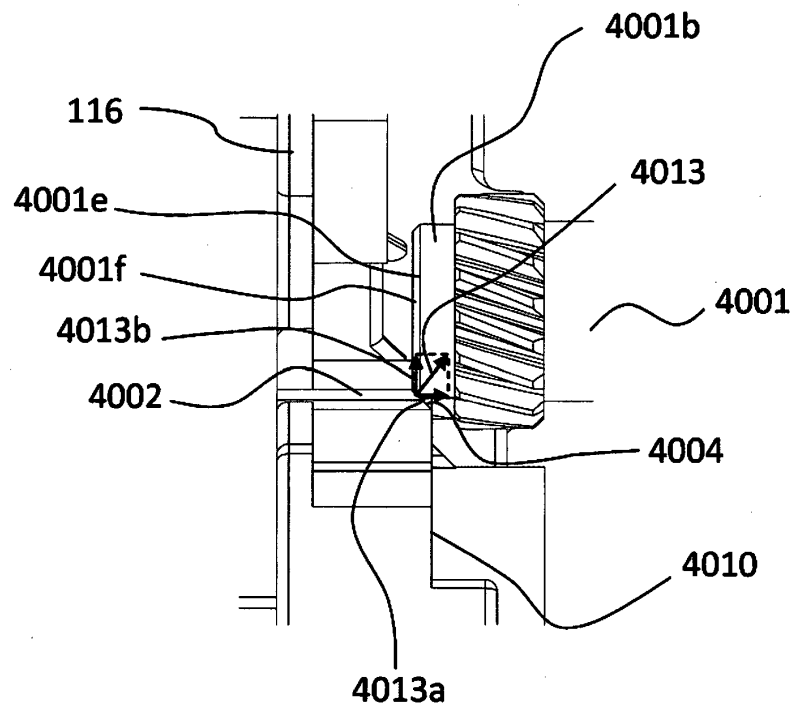


図305

(a)



(b)

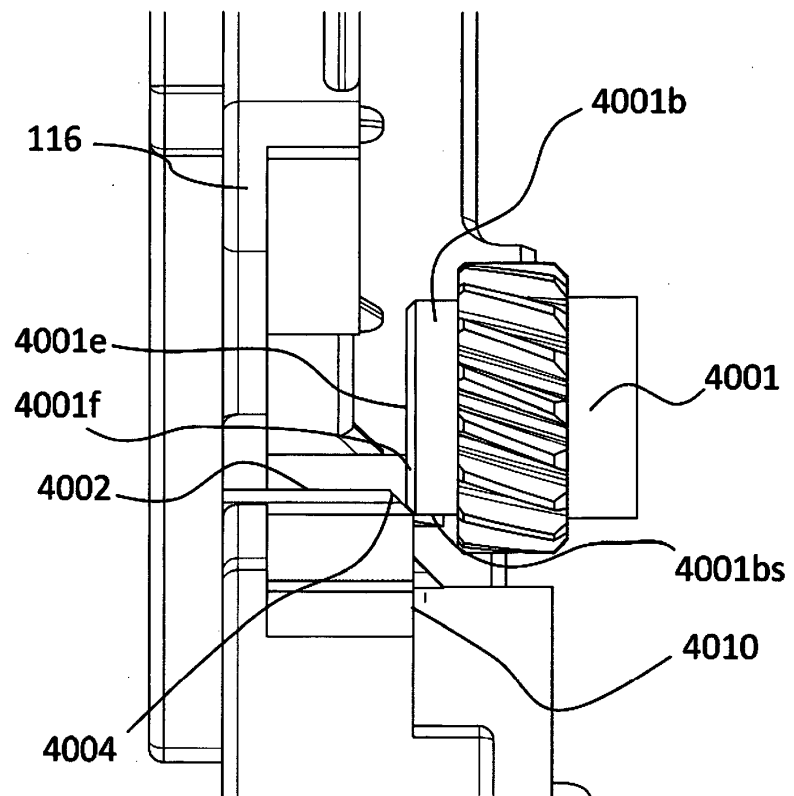
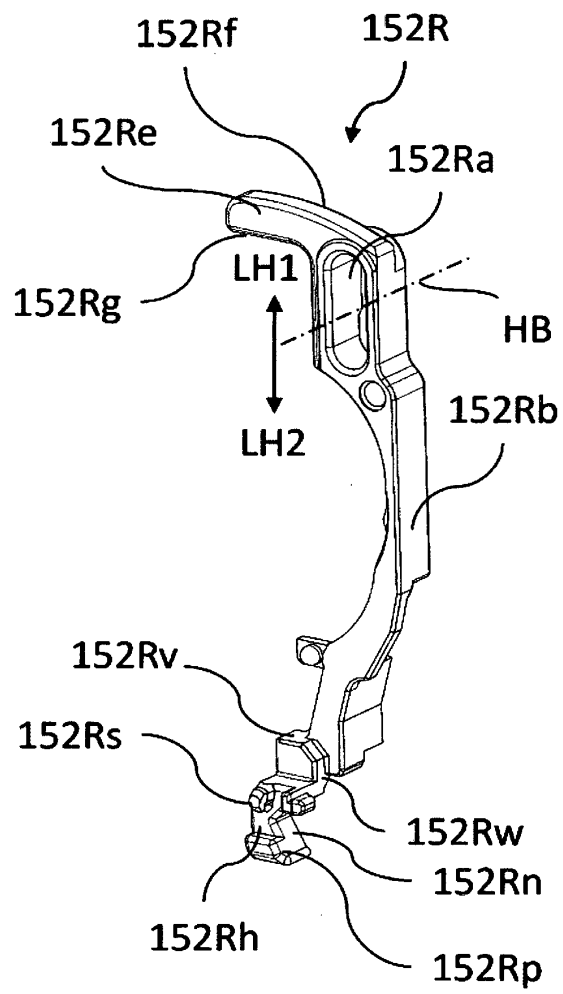


図306

(a)



(b)

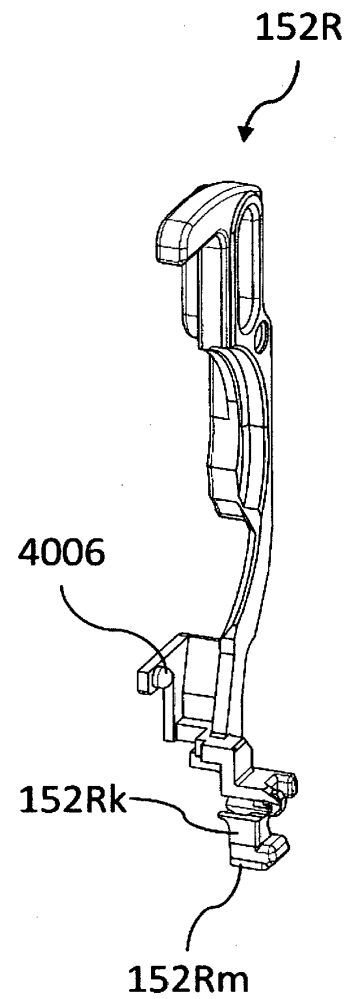
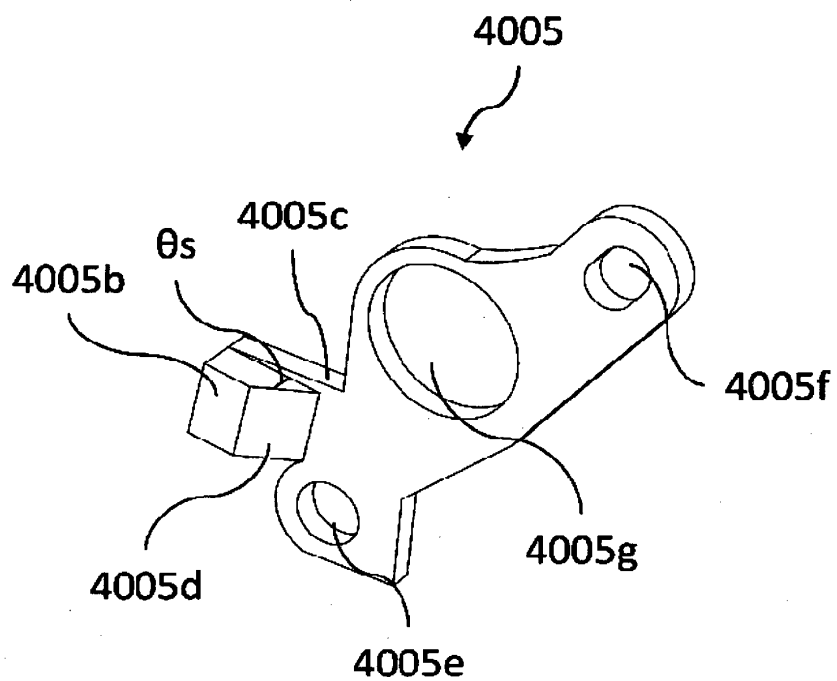


図307

(a)



(b)

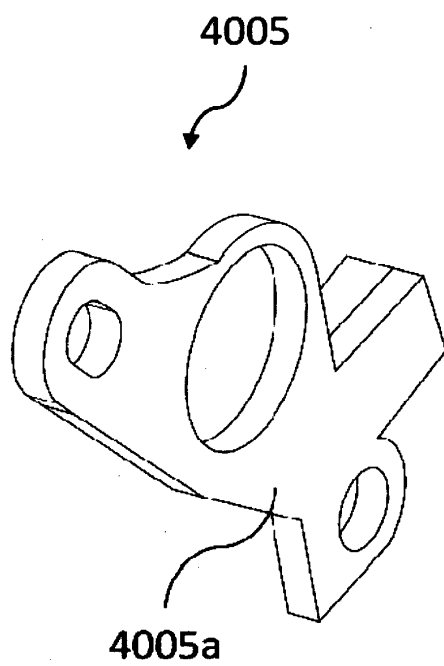


図308

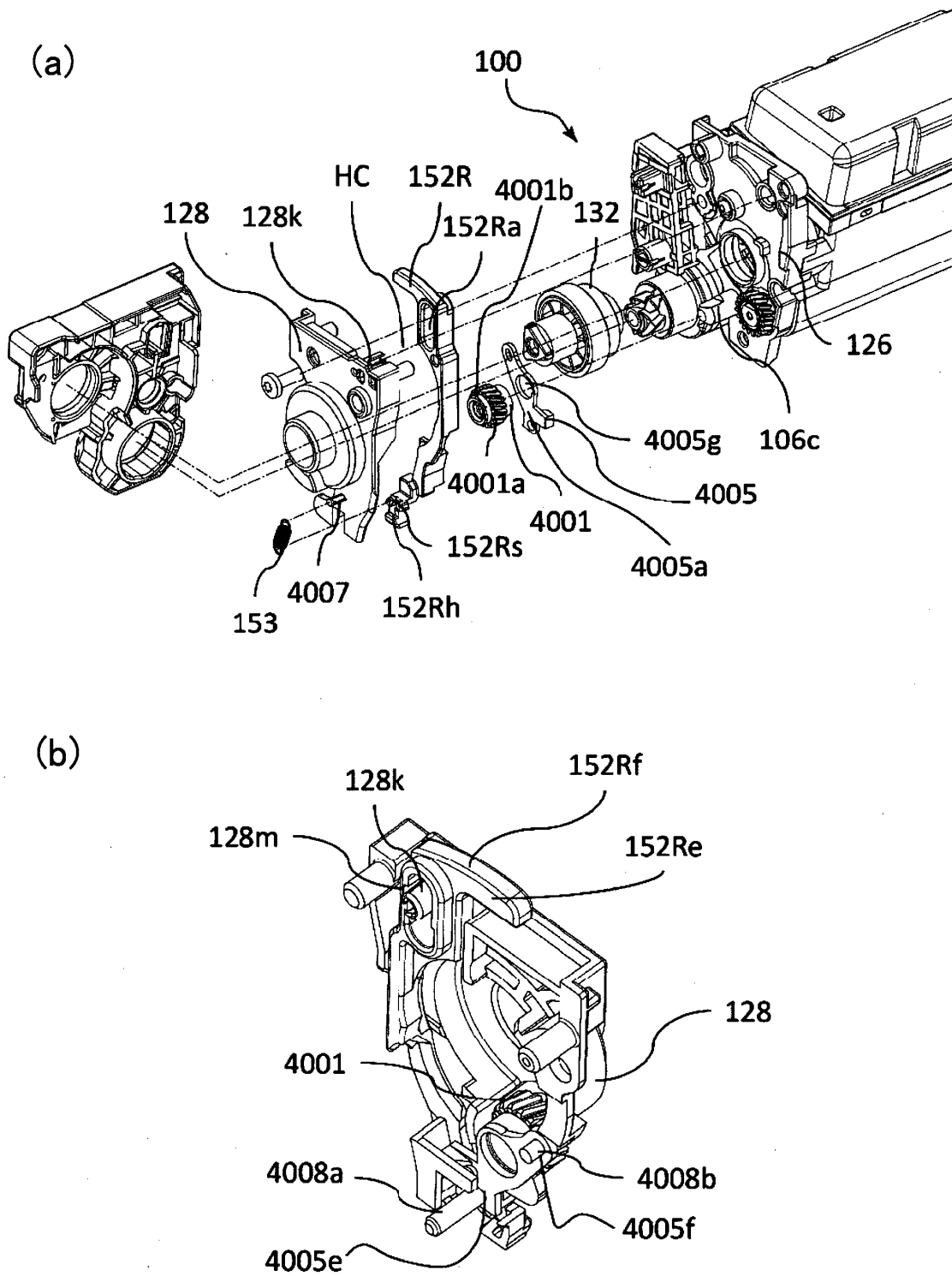


図309

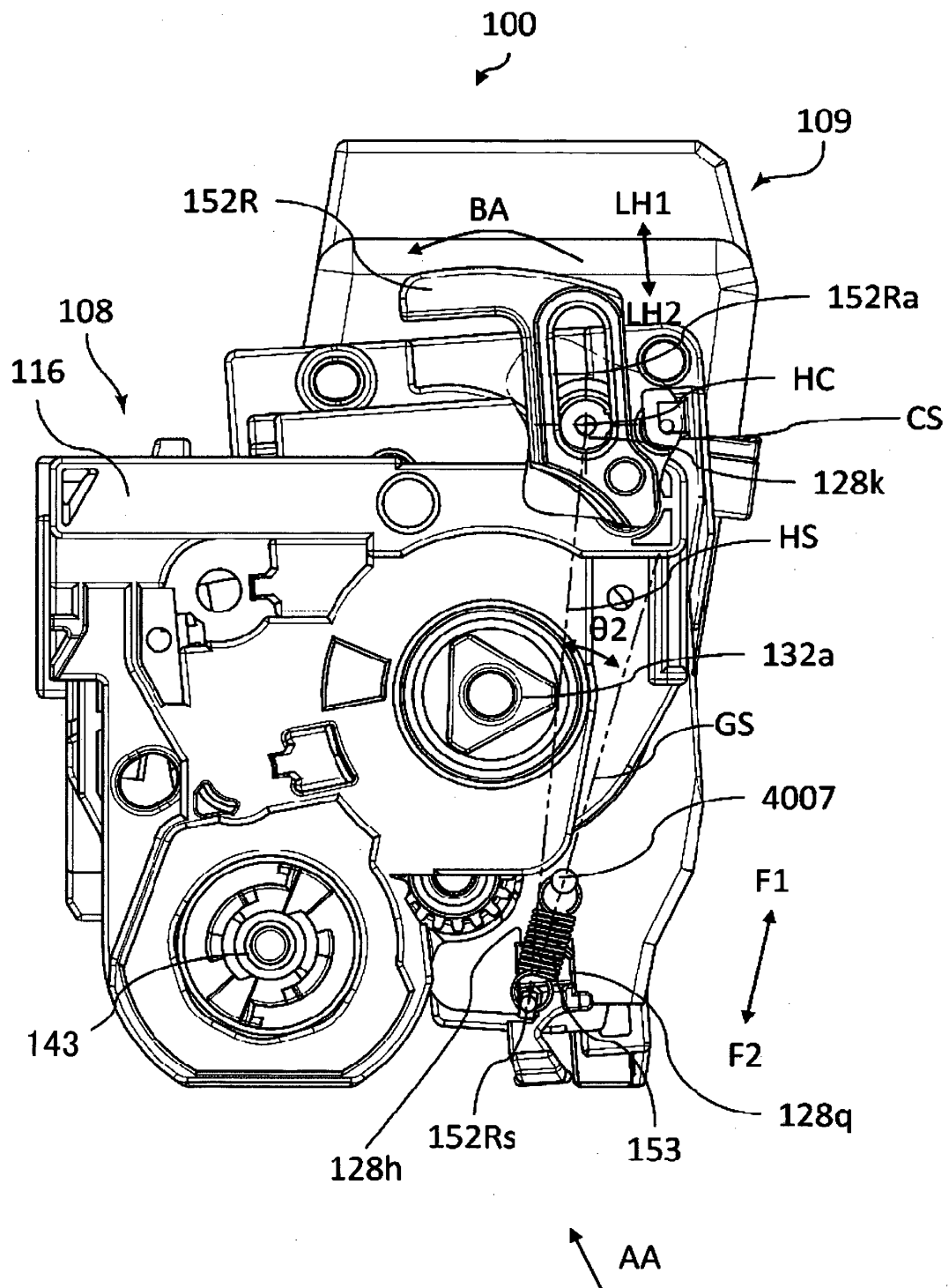


図310

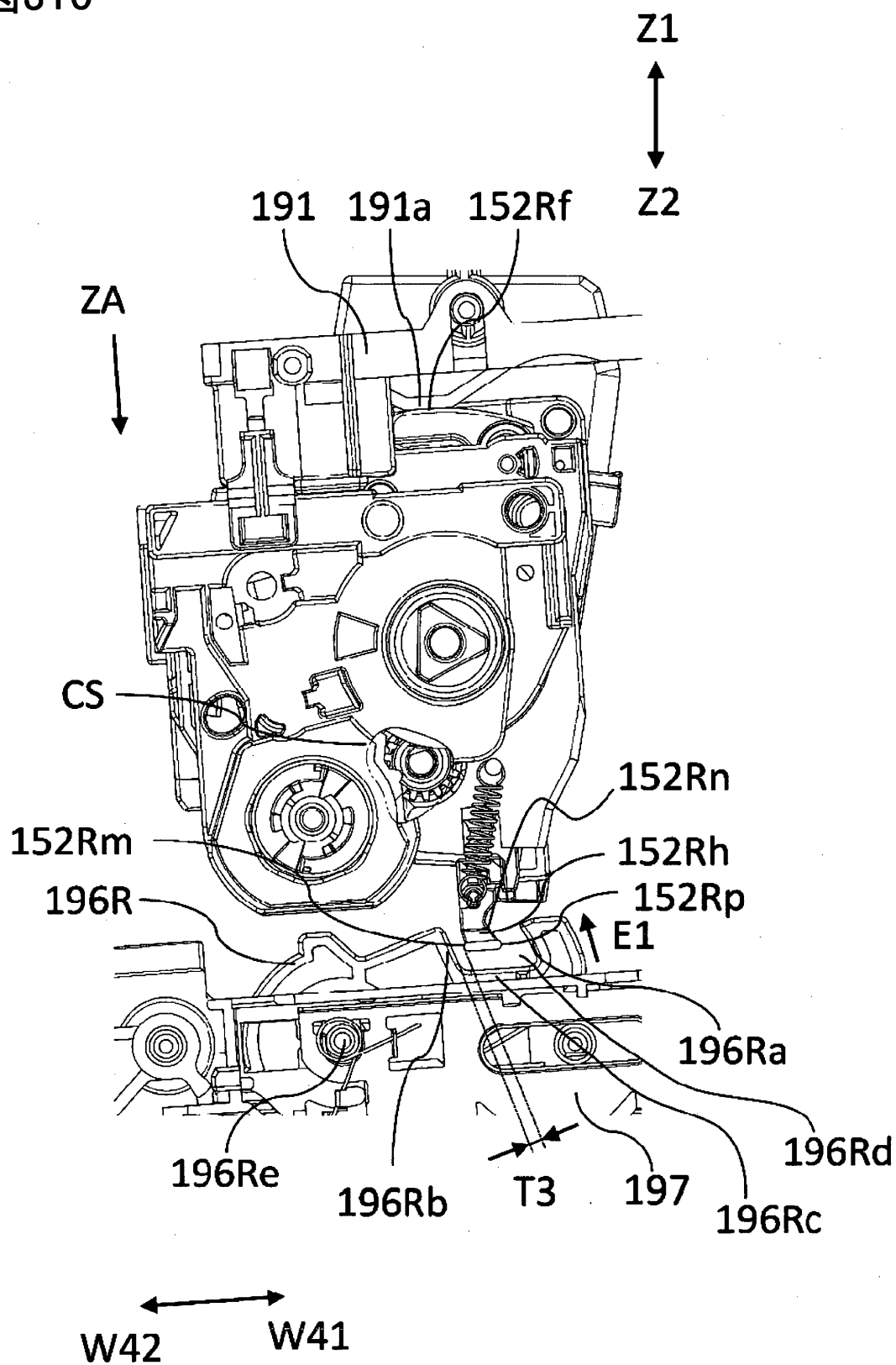


図311

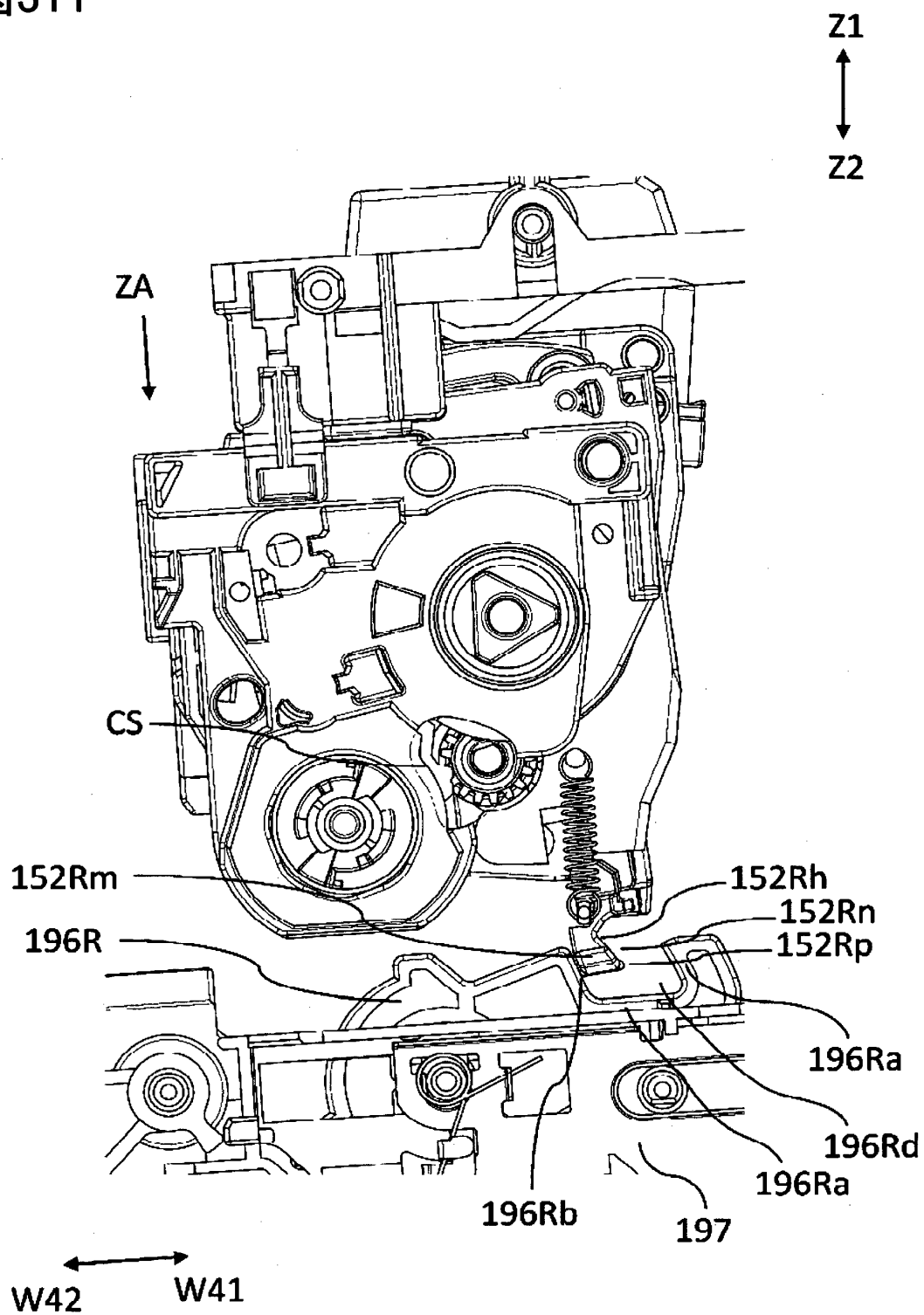


図312

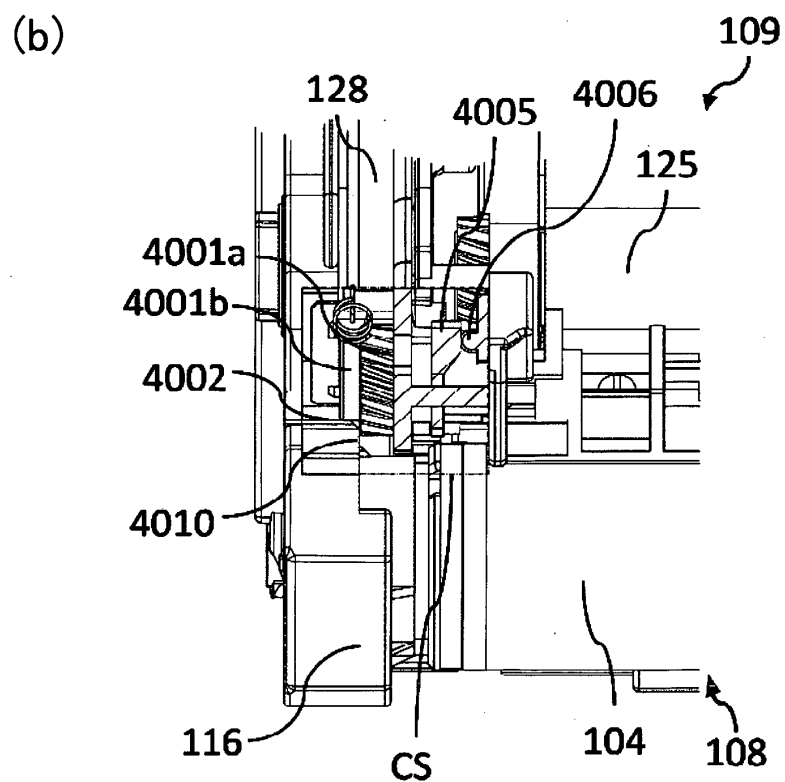
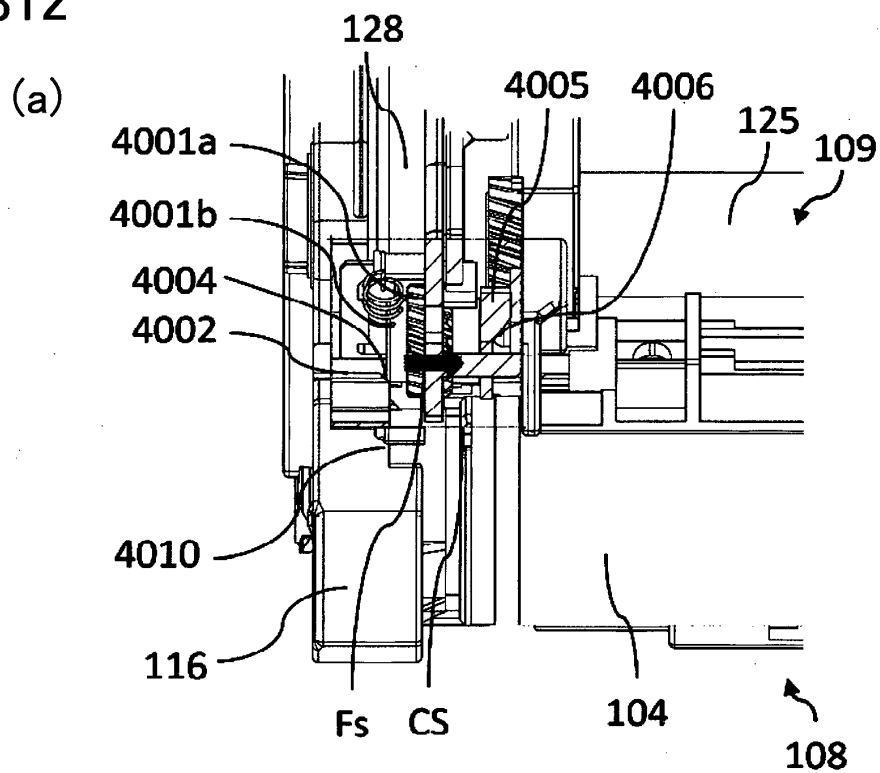
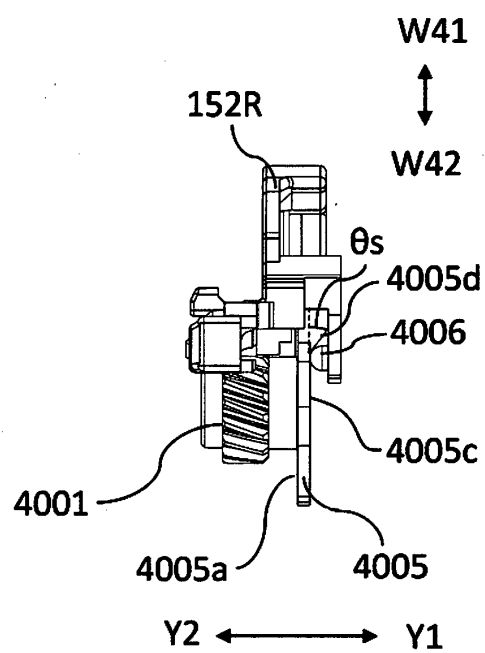


図313

(a)



(b)

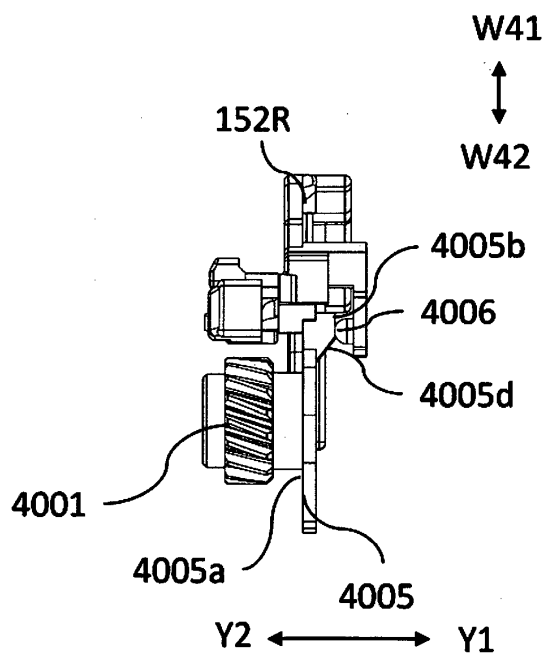


図315

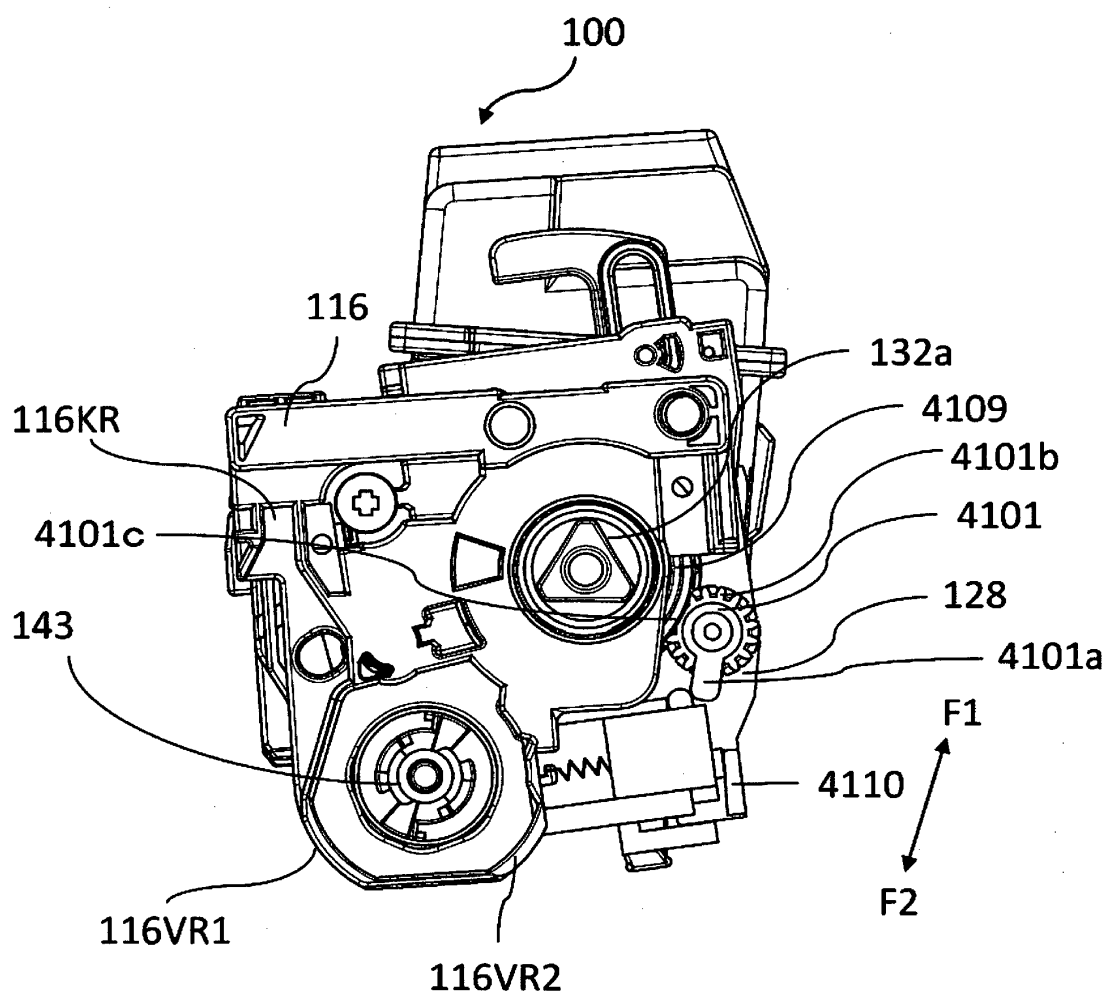


図317

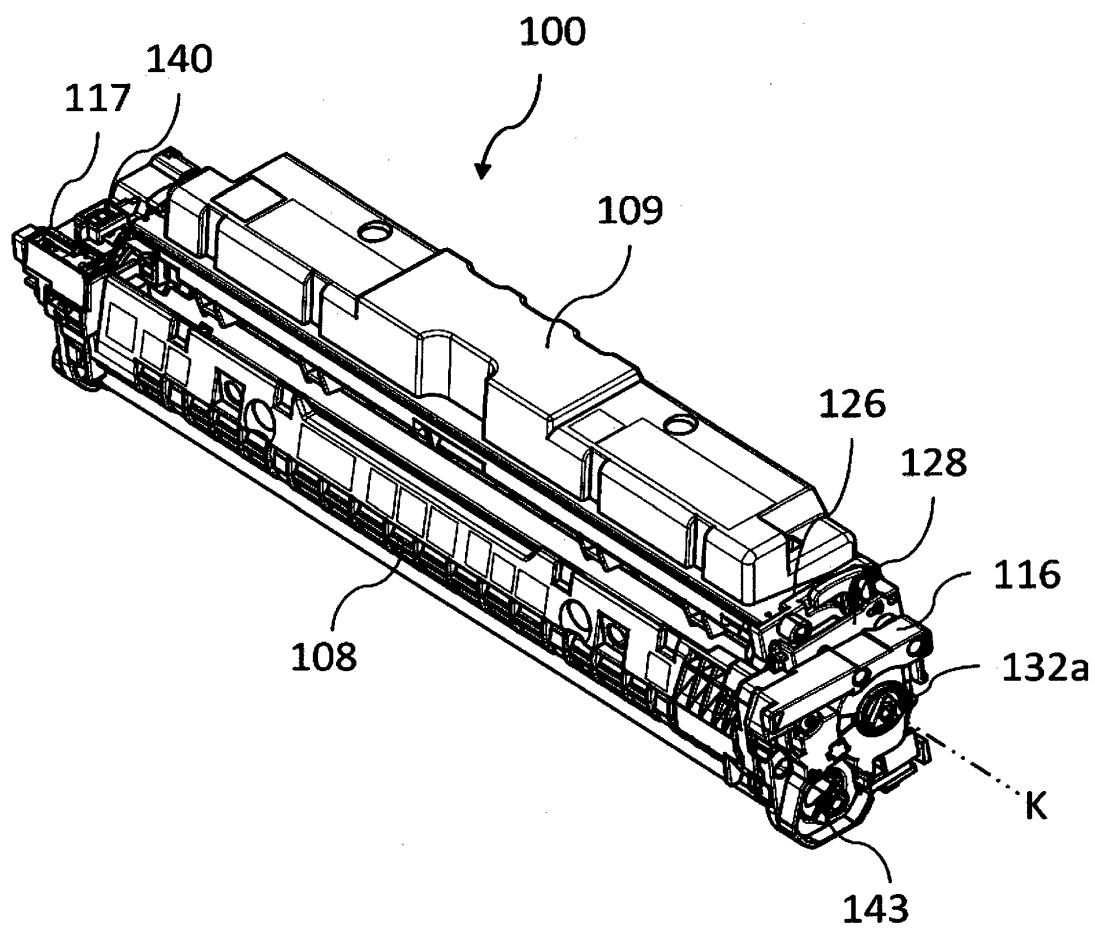
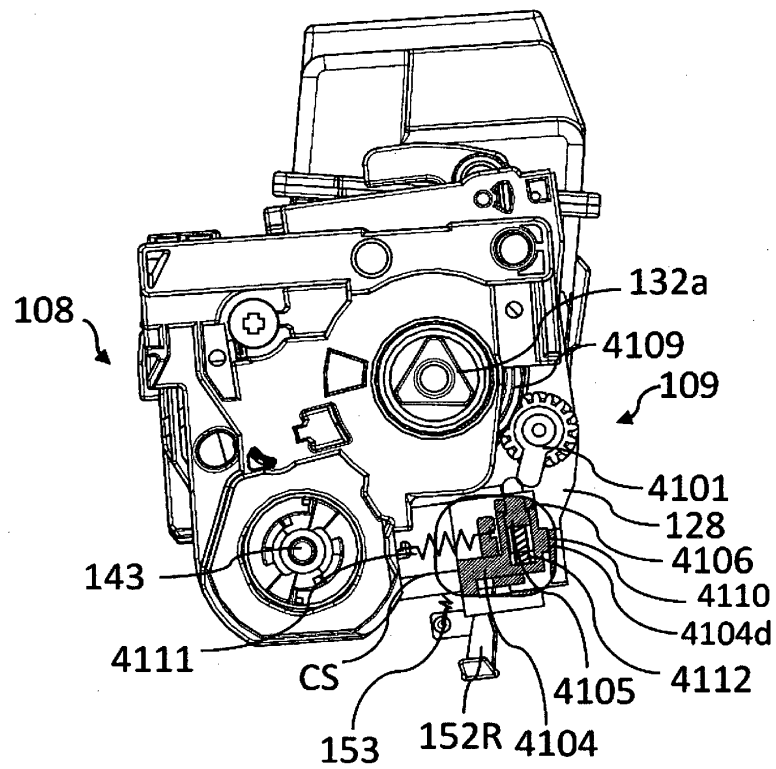


図318

(a)



(b)

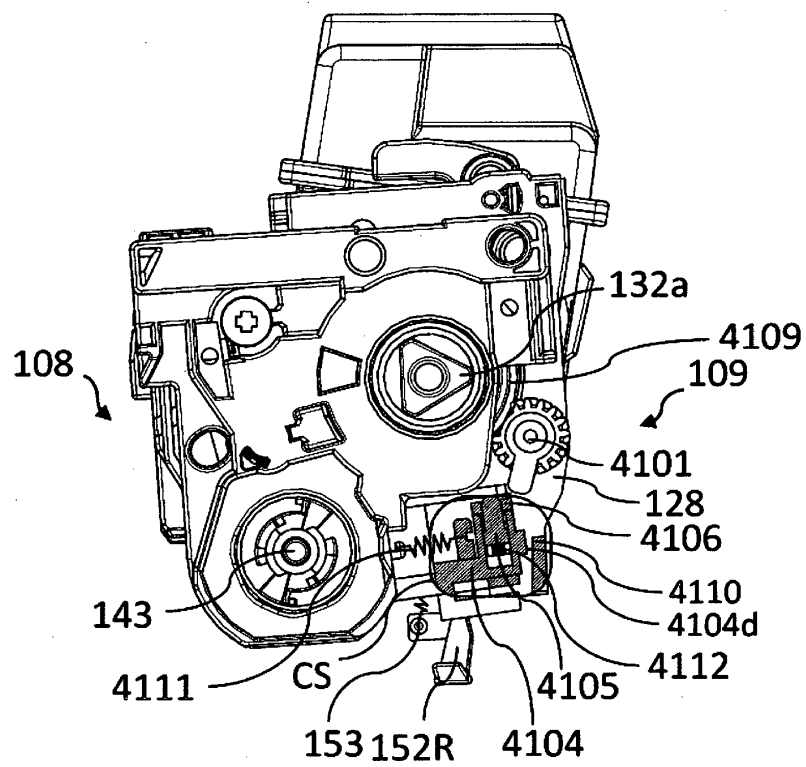
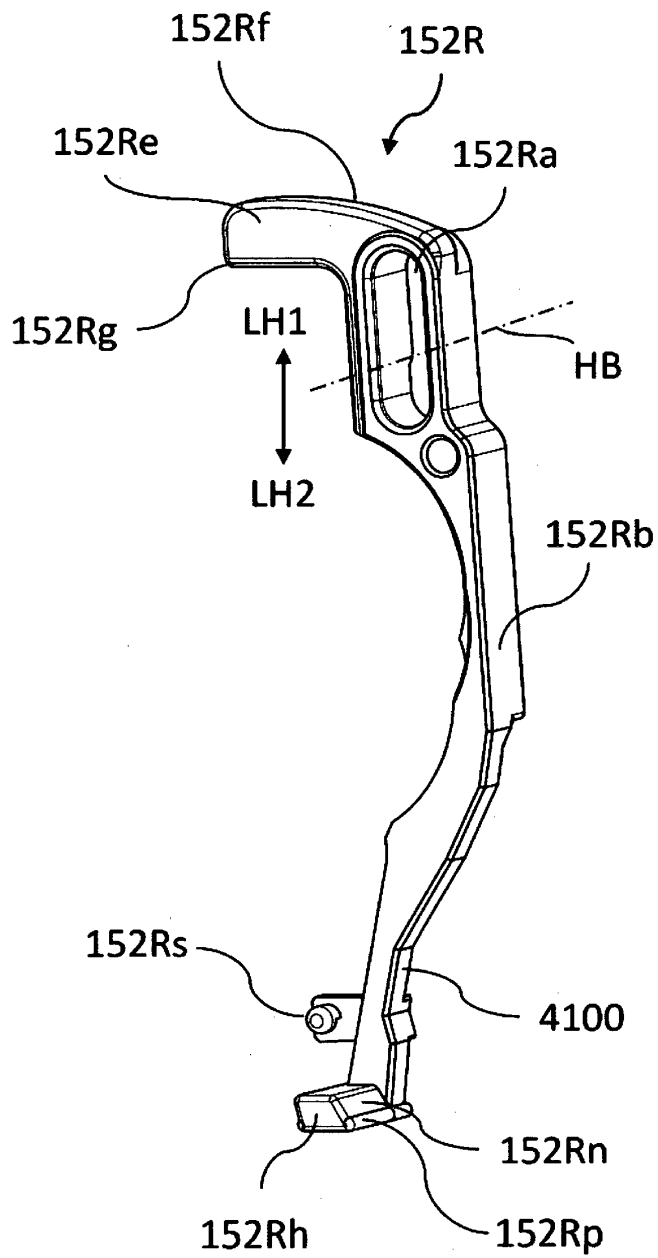


図319

(a)



(b)

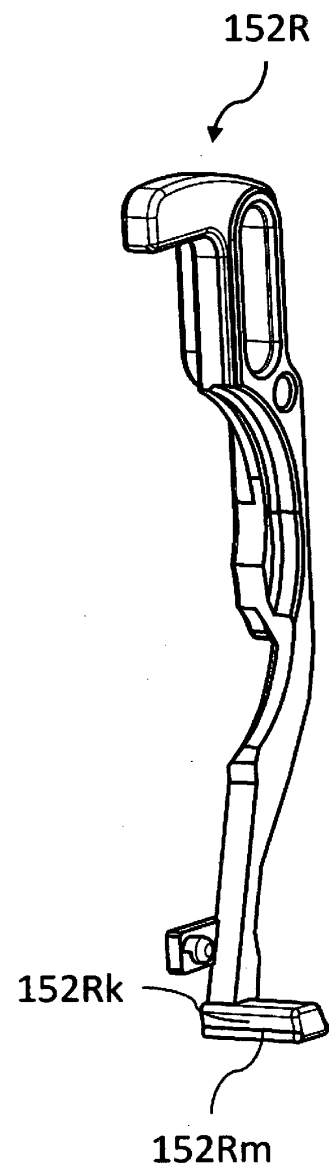


図321

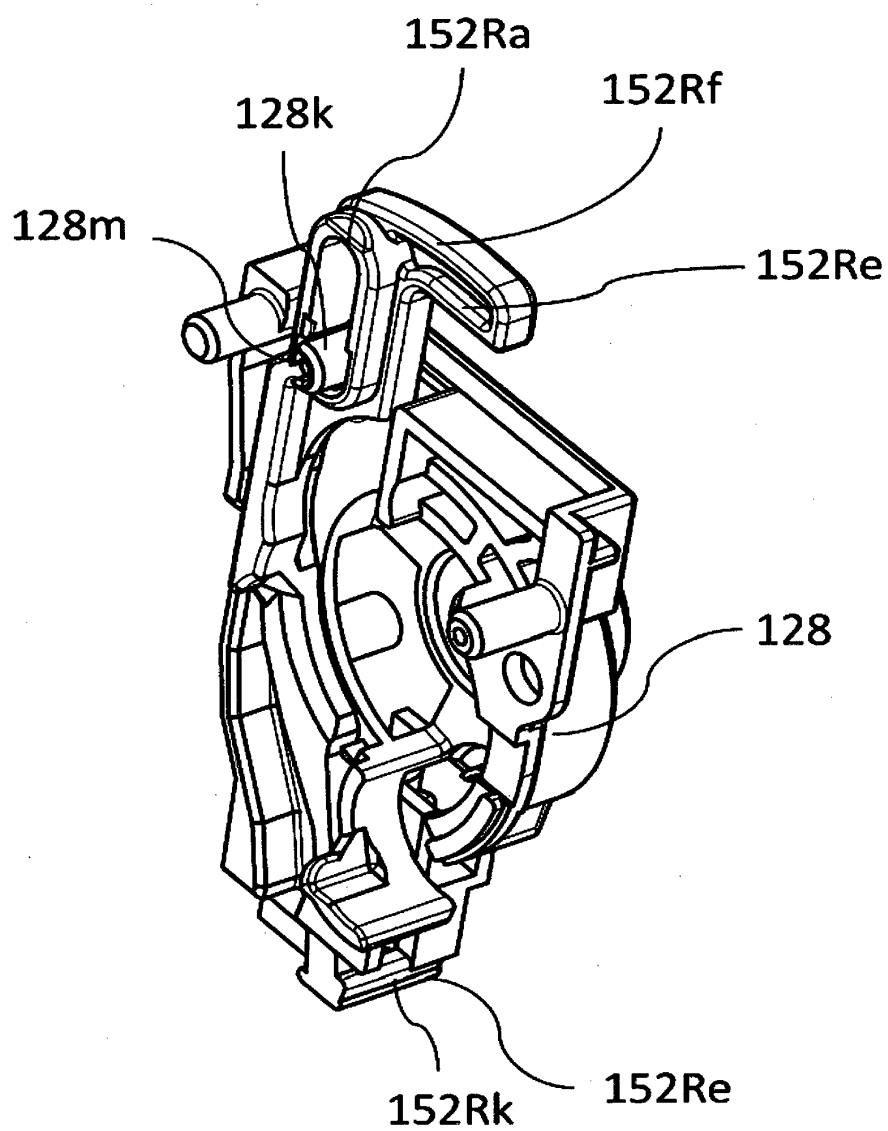


Figure 322 is an exploded perspective view of a mechanical assembly. The assembly includes a main housing (125) with a top surface (125b) and a side surface (125a). A component (108) is shown being inserted into the housing, with a pin (115) passing through it. A bracket (126) is also shown, with a pin (126a) passing through it. A component (132) is shown being inserted into the housing, with a pin (132a) passing through it. A component (131) is shown being inserted into the housing, with a pin (131b) passing through it. A component (133) is shown being inserted into the housing, with a pin (133a) passing through it. A component (106c) is shown being inserted into the housing, with a pin (106c) passing through it. A component (116) is shown being inserted into the housing, with a pin (116a) passing through it. A component (116b) is shown being inserted into the housing, with a pin (116b) passing through it. A component (4103) is shown being inserted into the housing, with a pin (4103) passing through it. A component (4101b) is shown being inserted into the housing, with a pin (4101b) passing through it. A component (4101a) is shown being inserted into the housing, with a pin (4101a) passing through it. A component (4102) is shown being inserted into the housing, with a pin (4102) passing through it. A component (4101) is shown being inserted into the housing, with a pin (4101) passing through it. A component (153) is shown being inserted into the housing, with a pin (153) passing through it. A component (152Rh) is shown being inserted into the housing, with a pin (152Rh) passing through it. A component (152Rs) is shown being inserted into the housing, with a pin (152Rs) passing through it. A component (152Ra) is shown being inserted into the housing, with a pin (152Ra) passing through it. A component (152R) is shown being inserted into the housing, with a pin (152R) passing through it. A component (128k) is shown being inserted into the housing, with a pin (128k) passing through it. A component (128) is shown being inserted into the housing, with a pin (128) passing through it. A component (145) is shown being inserted into the housing, with a pin (145) passing through it. A component (HC) is shown being inserted into the housing, with a pin (HC) passing through it.

图 323

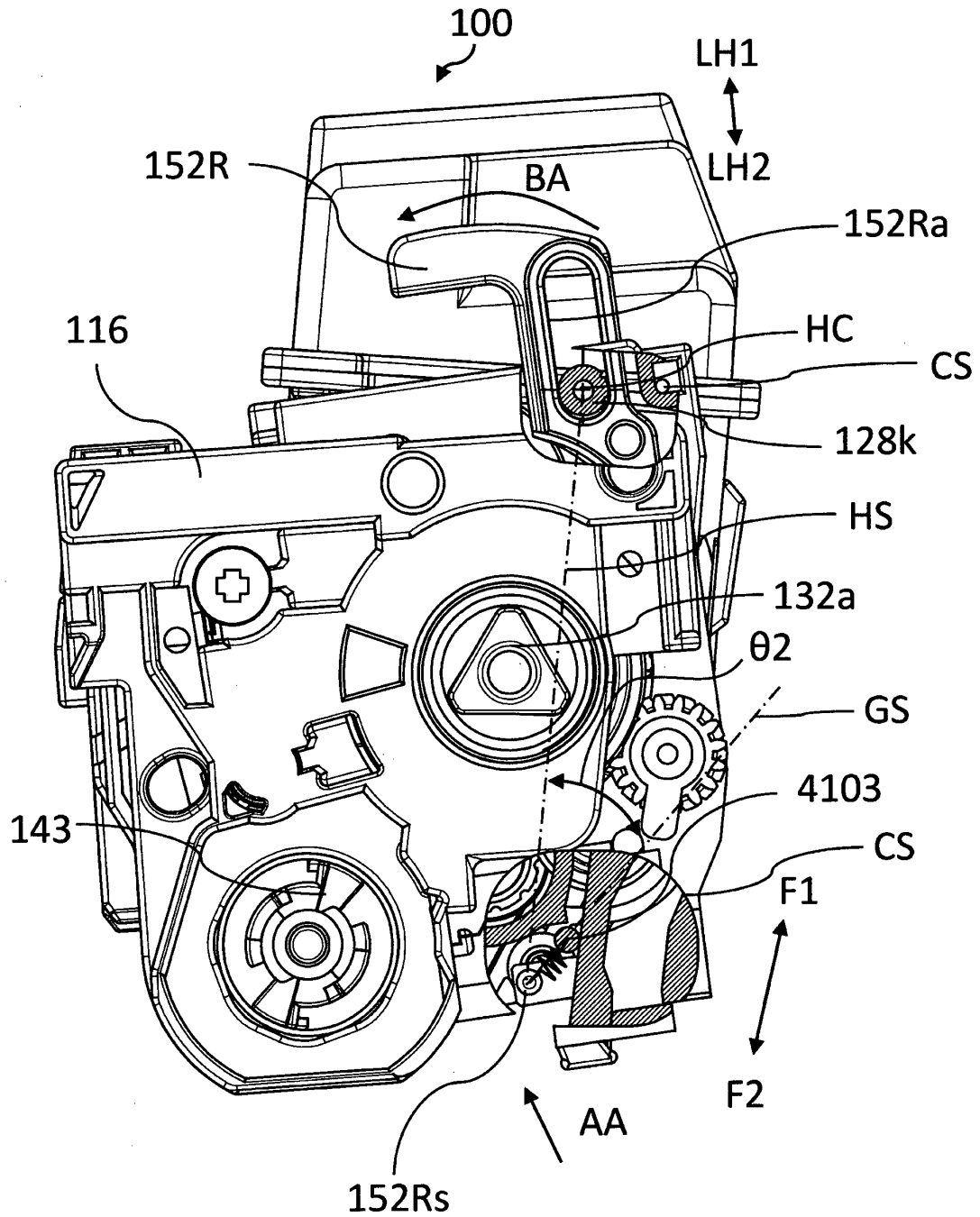


図324

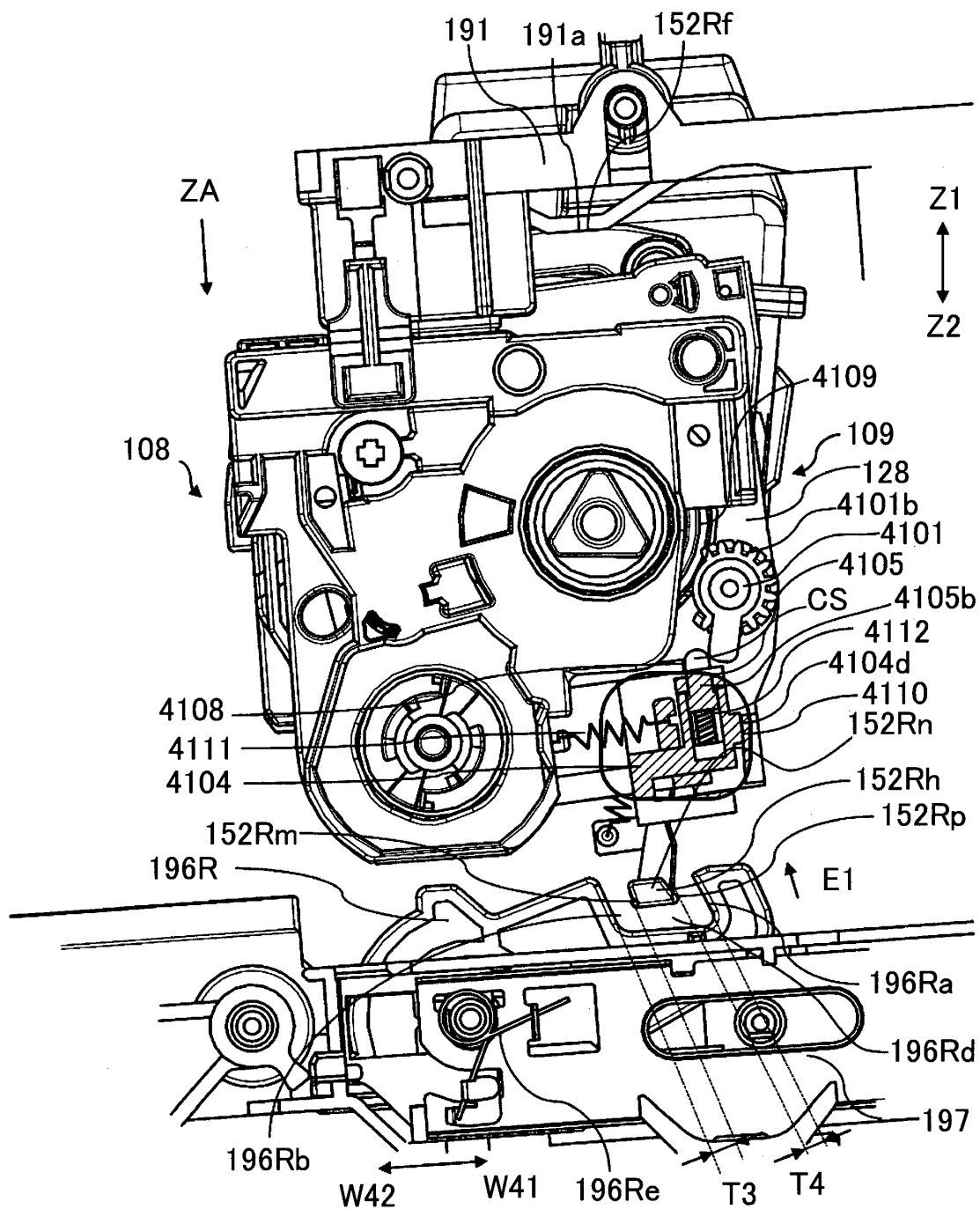


図325

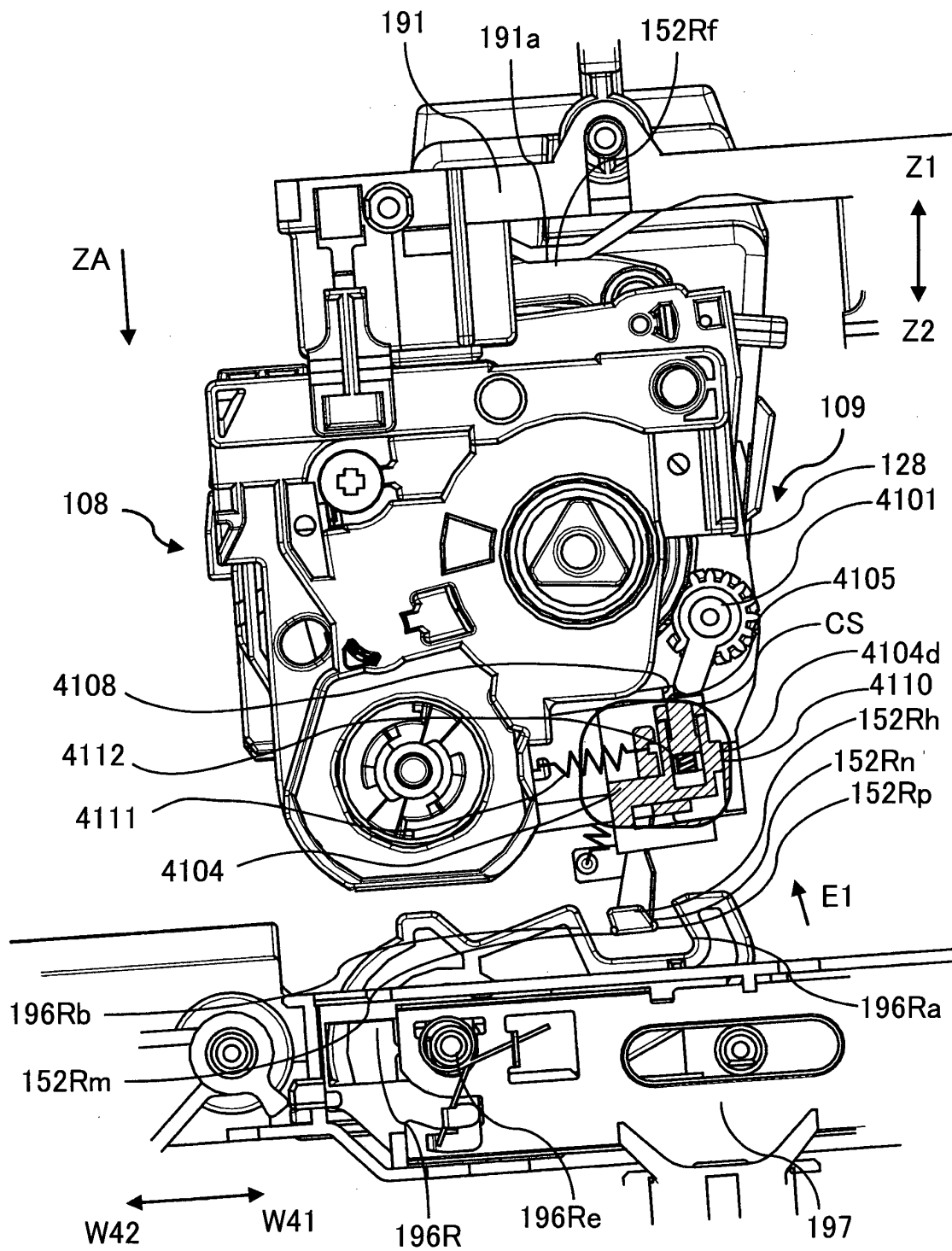


図326

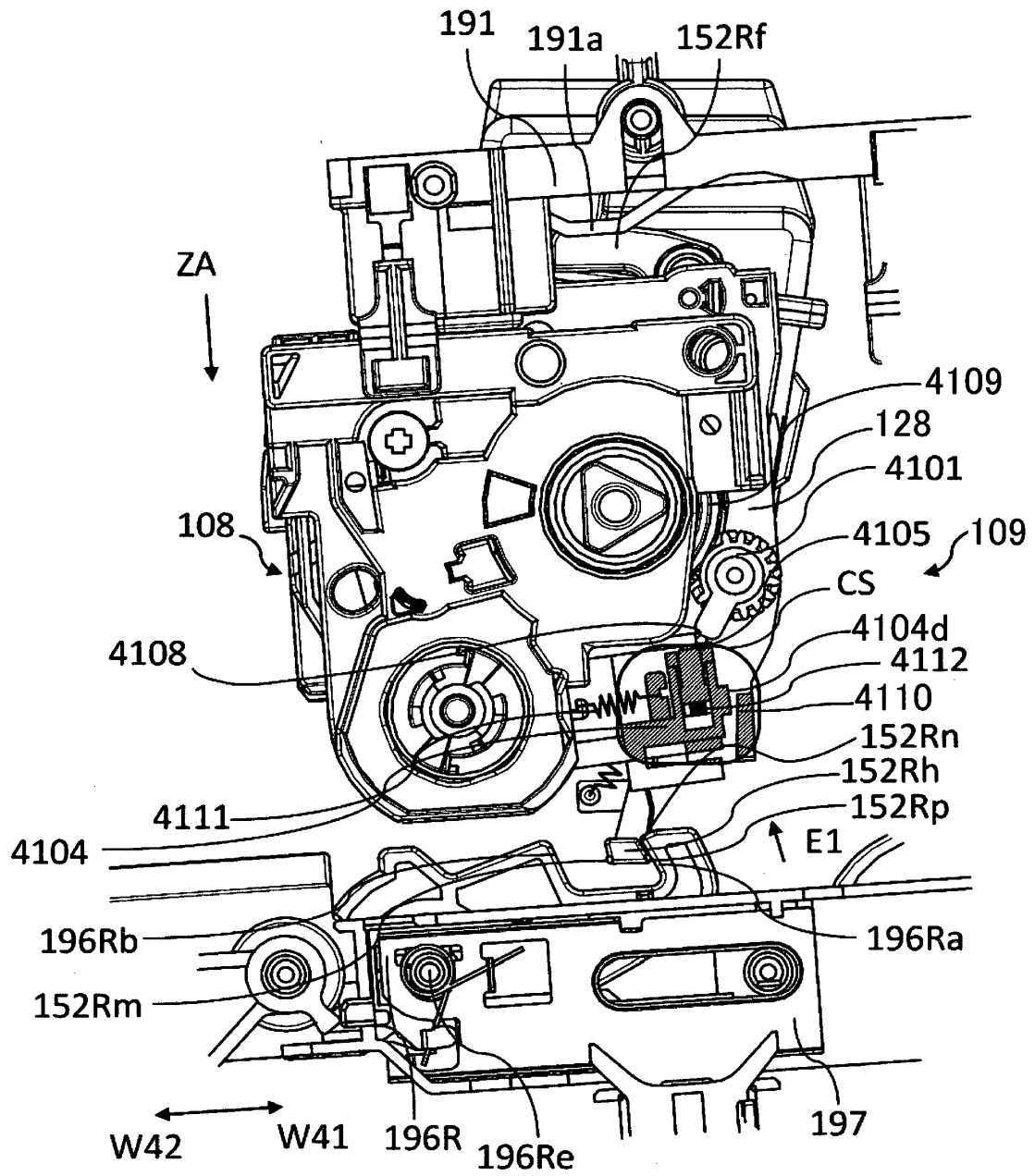


図327

