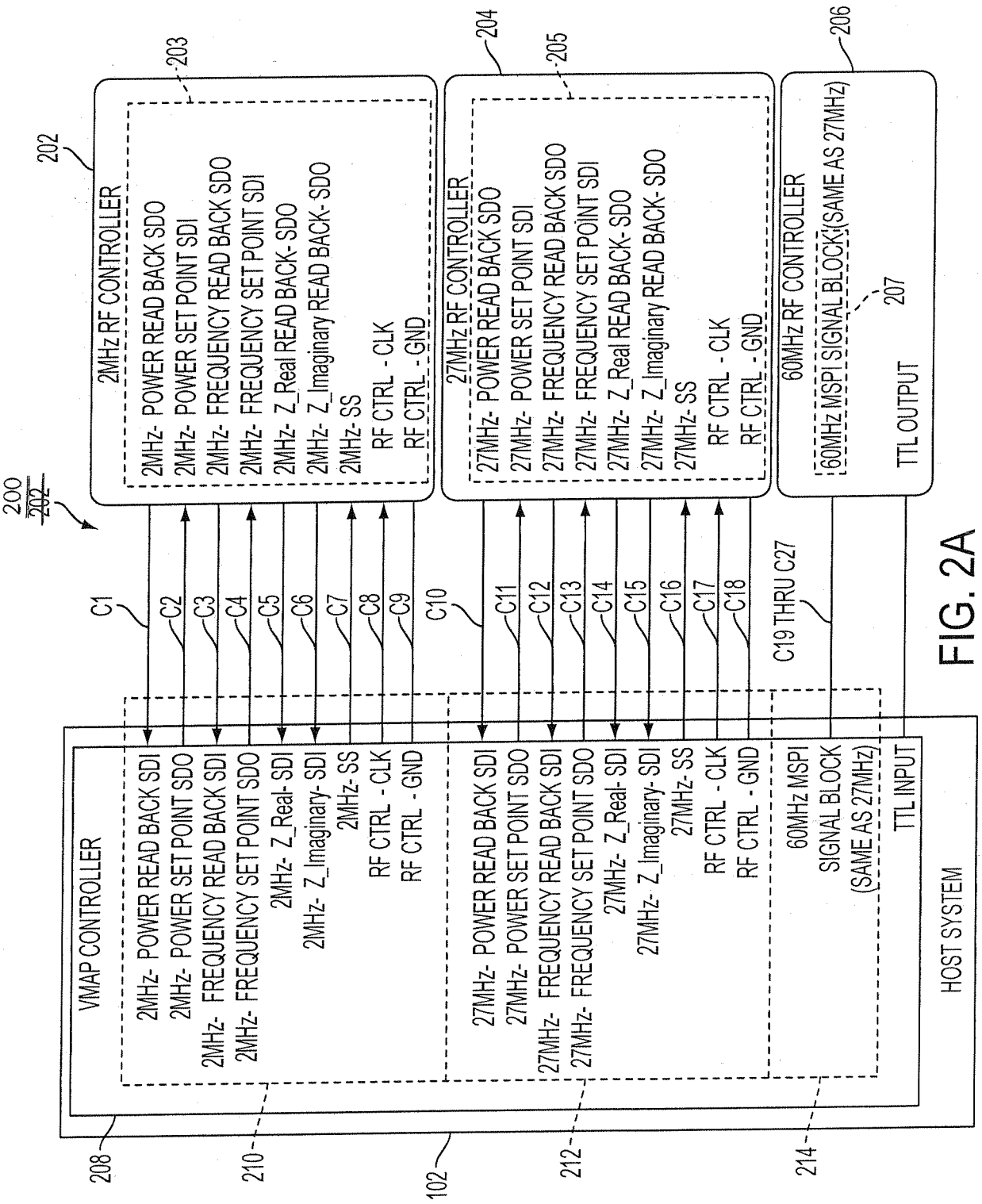

200
202
2MHz RF CONTROLLER
203
2MHz- POWER READ BACK SDO
2MHz- POWER SET POINT SDI
2MHz- FREQUENCY READ BACK SDO
2MHz- FREQUENCY SET POINT SDI
2MHz- Z_Real READ BACK- SDO
2MHz- Z_Imaginary READ BACK- SDO
2MHz- SS
RF CTRL - CLK
RF CTRL - GND
27MHz RF CONTROLLER
204
205
27MHz- POWER READ BACK SDO
27MHz- POWER SET POINT SDI
27MHz- FREQUENCY READ BACK SDO
27MHz- FREQUENCY SET POINT SDI
27MHz- Z_Real READ BACK- SDO
27MHz- Z_Imaginary READ BACK- SDO
27MHz- SS
RF CTRL - CLK
RF CTRL - GND
60MHz RF CONTROLLER
206
60MHz MSPI SIGNAL BLOCK (SAME AS 27MHz)
207
TTL OUTPUT
C1
C2
C3
C4
C5
C6
C7
C8
C9
C10
C11
C12
C13
C14
C15
C16
C17
C18
C19 THRU C27
VMAP CONTROLLER
2MHz- POWER READ BACK SDI
2MHz- POWER SET POINT SDO
2MHz- FREQUENCY READ BACK SDI
2MHz- FREQUENCY SET POINT SDO
2MHz- Z_Real- SDI
2MHz- Z_Imaginary- SDI
2MHz- SS
RF CTRL - CLK
RF CTRL - GND
27MHz- POWER READ BACK SDI
27MHz- POWER SET POINT SDO
27MHz- FREQUENCY READ BACK SDI
27MHz- FREQUENCY SET POINT SDO
27MHz- Z_Real- SDI
27MHz- Z_Imaginary- SDI
27MHz- SS
RF CTRL - CLK
RF CTRL - GND
60MHz MSPI SIGNAL BLOCK (SAME AS 27MHz)
TTL INPUT
HOST SYSTEM
208
210
102
212
214

FIG. 2A

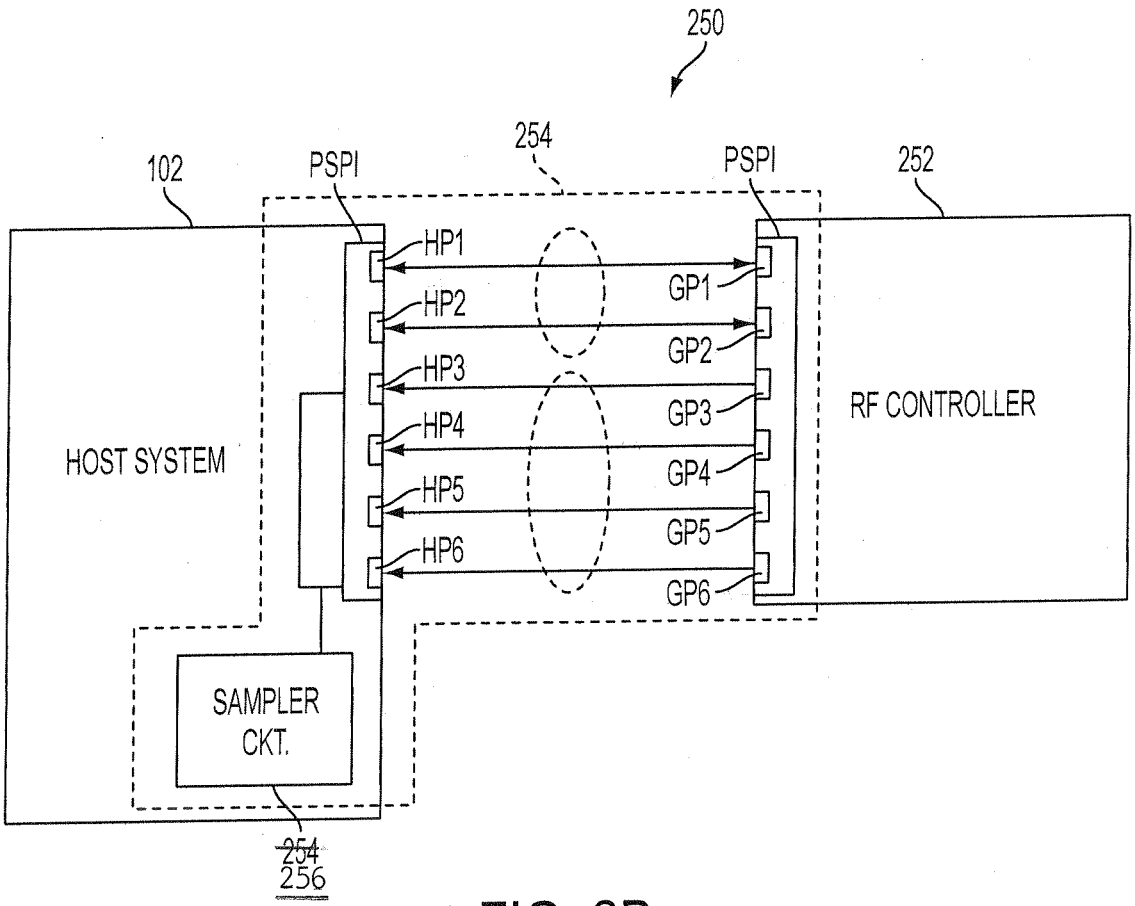

250
254
102
PSPI
PSPI
252
HP1
HP2
HP3
HP4
HP5
HP6
GP1
GP2
GP3
GP4
GP5
GP6
HOST SYSTEM
RF CONTROLLER
SAMPLER CKT.
~~254~~
256

FIG. 2B