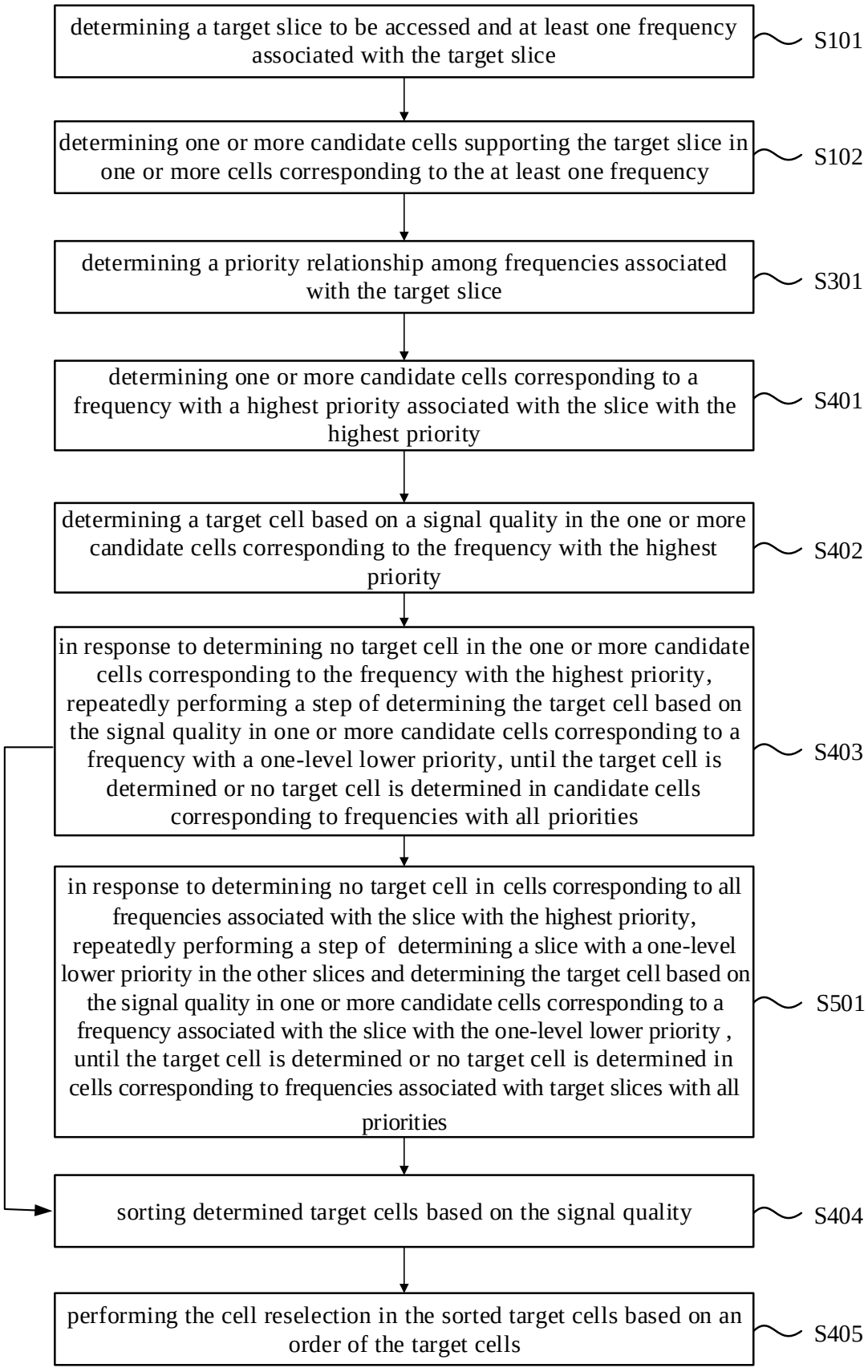
determining a target slice to be accessed and at least one frequency associated with the target slice
S101
determining one or more candidate cells supporting the target slice in one or more cells corresponding to the at least one frequency
S102
determining a priority relationship among frequencies associated with the target slice
S301
determining one or more candidate cells corresponding to a frequency with a highest priority associated with the slice with the highest priority
S401
determining a target cell based on a signal quality in the one or more candidate cells corresponding to the frequency with the highest priority
S402
in response to determining no target cell in the one or more candidate cells corresponding to the frequency with the highest priority, repeatedly performing a step of determining the target cell based on the signal quality in one or more candidate cells corresponding to a frequency with a one-level lower priority, until the target cell is determined or no target cell is determined in candidate cells corresponding to frequencies with all priorities
S403
in response to determining no target cell in cells corresponding to all frequencies associated with the slice with the highest priority, repeatedly performing a step of determining a slice with a one-level lower priority in the other slices and determining the target cell based on the signal quality in one or more candidate cells corresponding to a frequency associated with the slice with the one-level lower priority , until the target cell is determined or no target cell is determined in cells corresponding to frequencies associated with target slices with all priorities
S501
sorting determined target cells based on the signal quality
S404
performing the cell reselection in the sorted target cells based on an order of the target cells
S405
FIG. 5

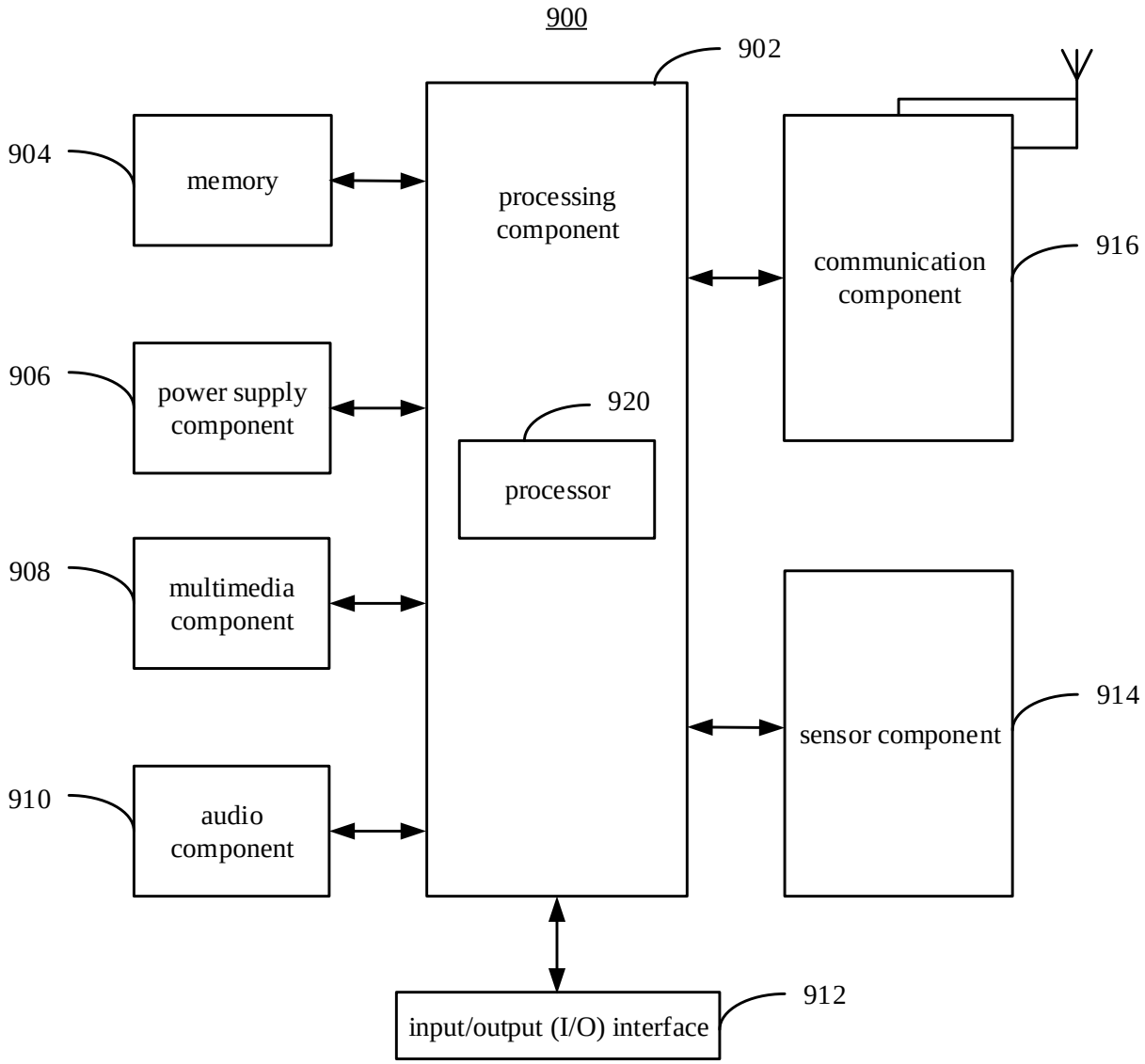
900
902
904
memory
processing component
916
communication component
906
power supply component
920
processor
908
multimedia component
914
sensor component
910
audio component
912
input/output (I/O) interface

FIG. 9