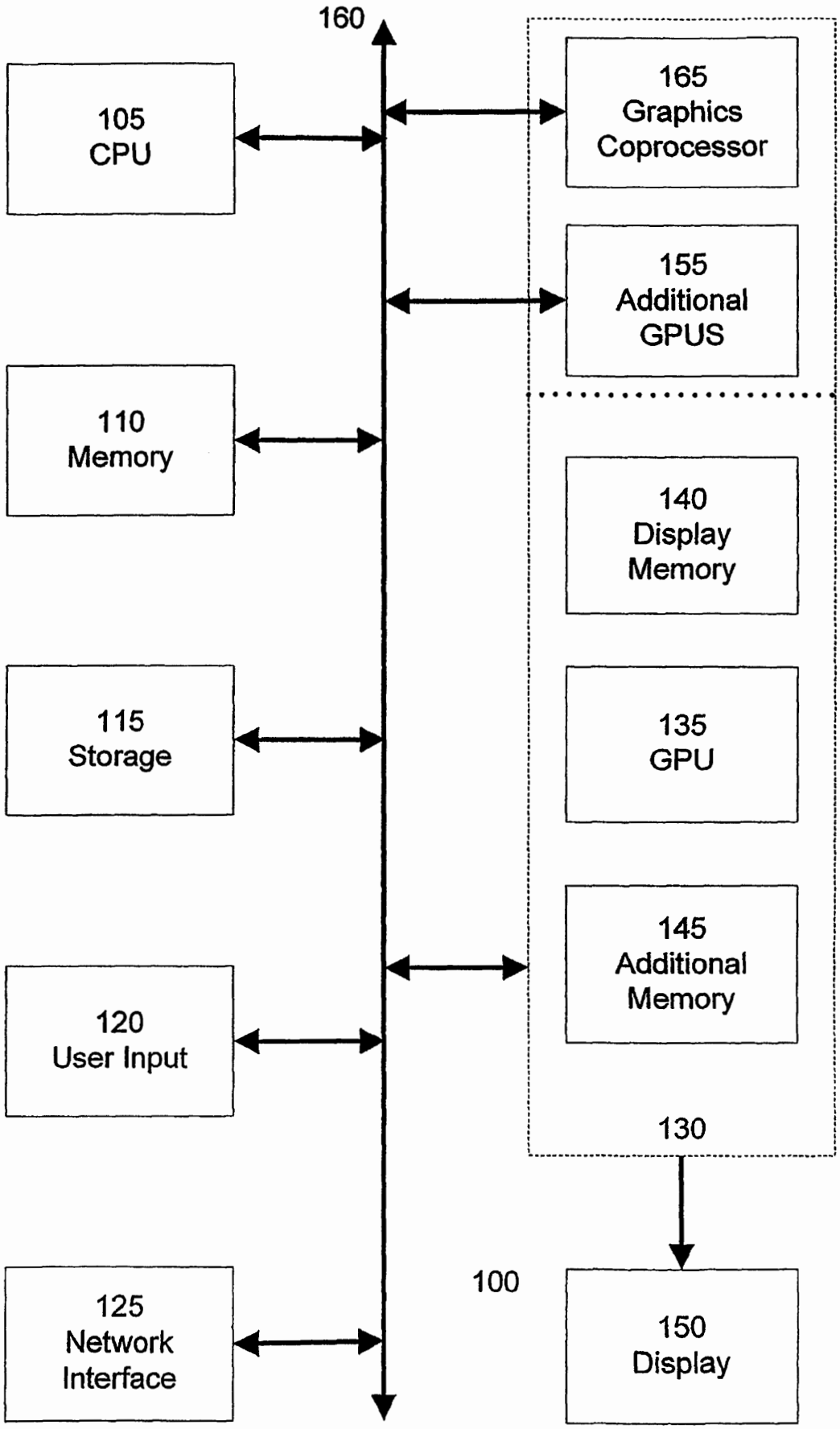

160
105
CPU
165
Graphics
Coprocessor
155
Additional
GPUS
110
Memory
140
Display
Memory
115
Storage
135
GPU
145
Additional
Memory
120
User Input
130
100
125
Network
Interface
150
Display

FIG. 1

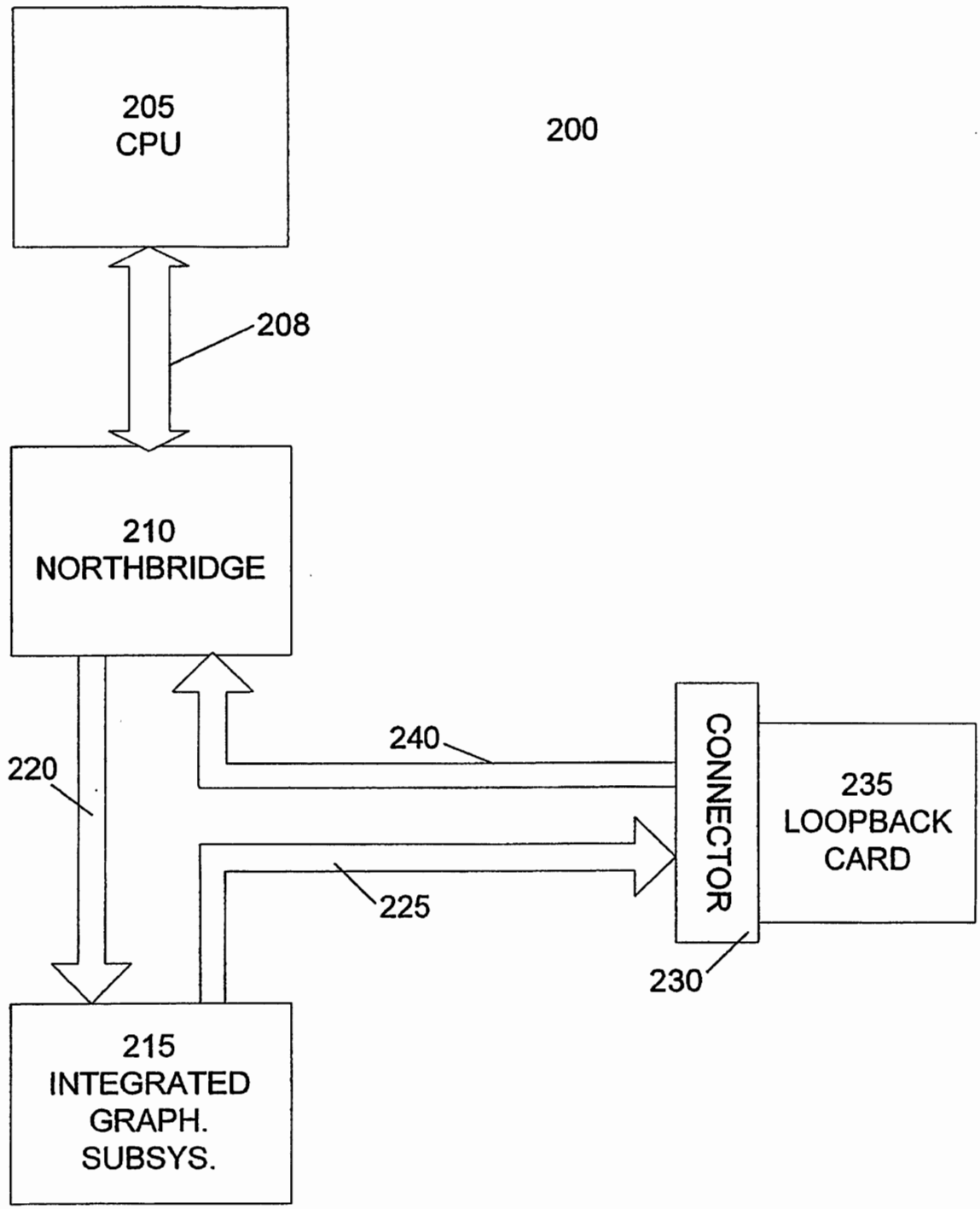

205
CPU
200
208
210
NORTHBRIDGE
220
240
CONNECTOR
235
LOOPBACK
CARD
225
230
215
INTEGRATED
GRAPH.
SUBSYS.

FIG. 2A

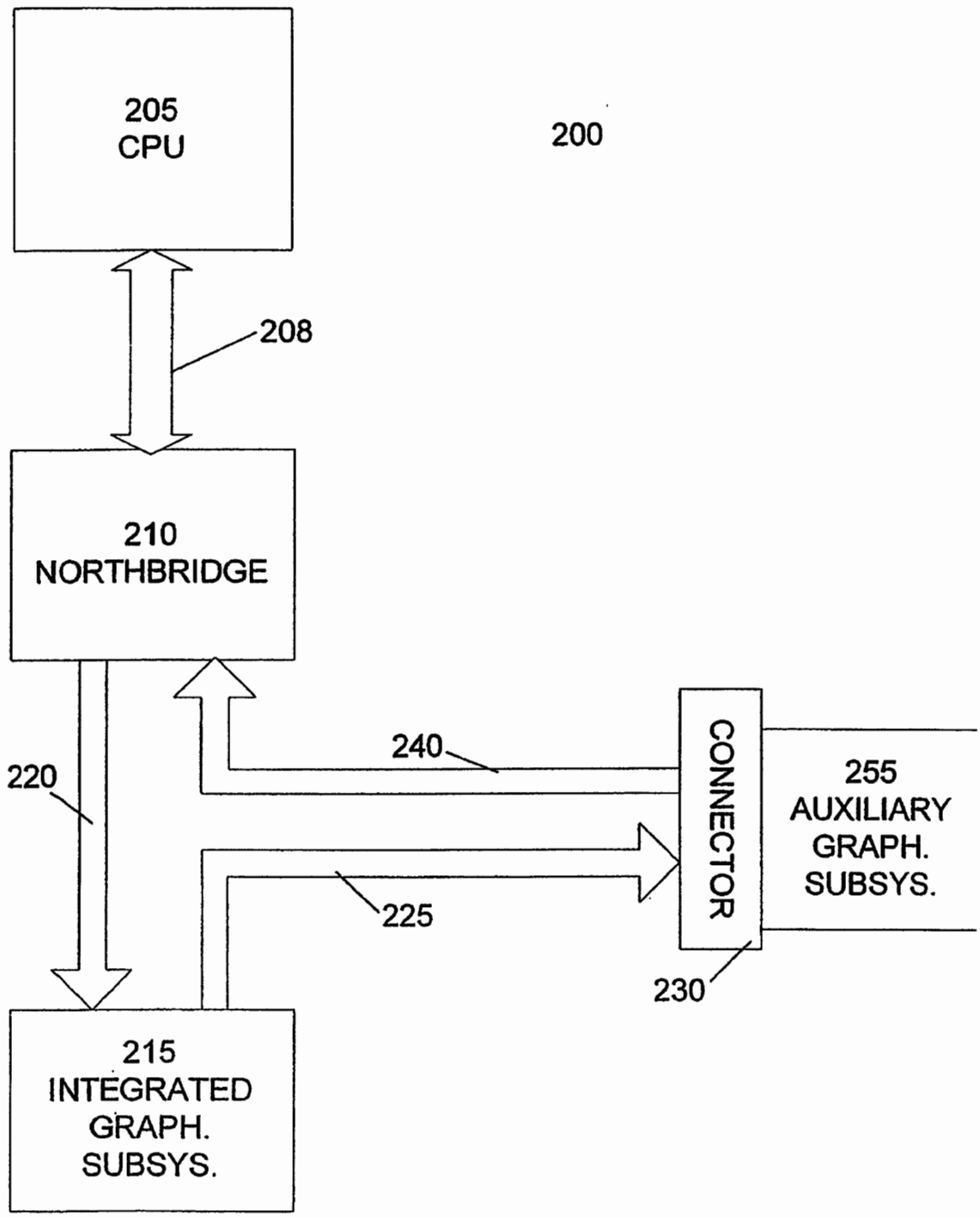

205
CPU
200
208
210
NORTHBRIDGE
220
240
CONNECTOR
255
AUXILIARY
GRAPH.
SUBSYS.
225
230
215
INTEGRATED
GRAPH.
SUBSYS.

FIG. 2B

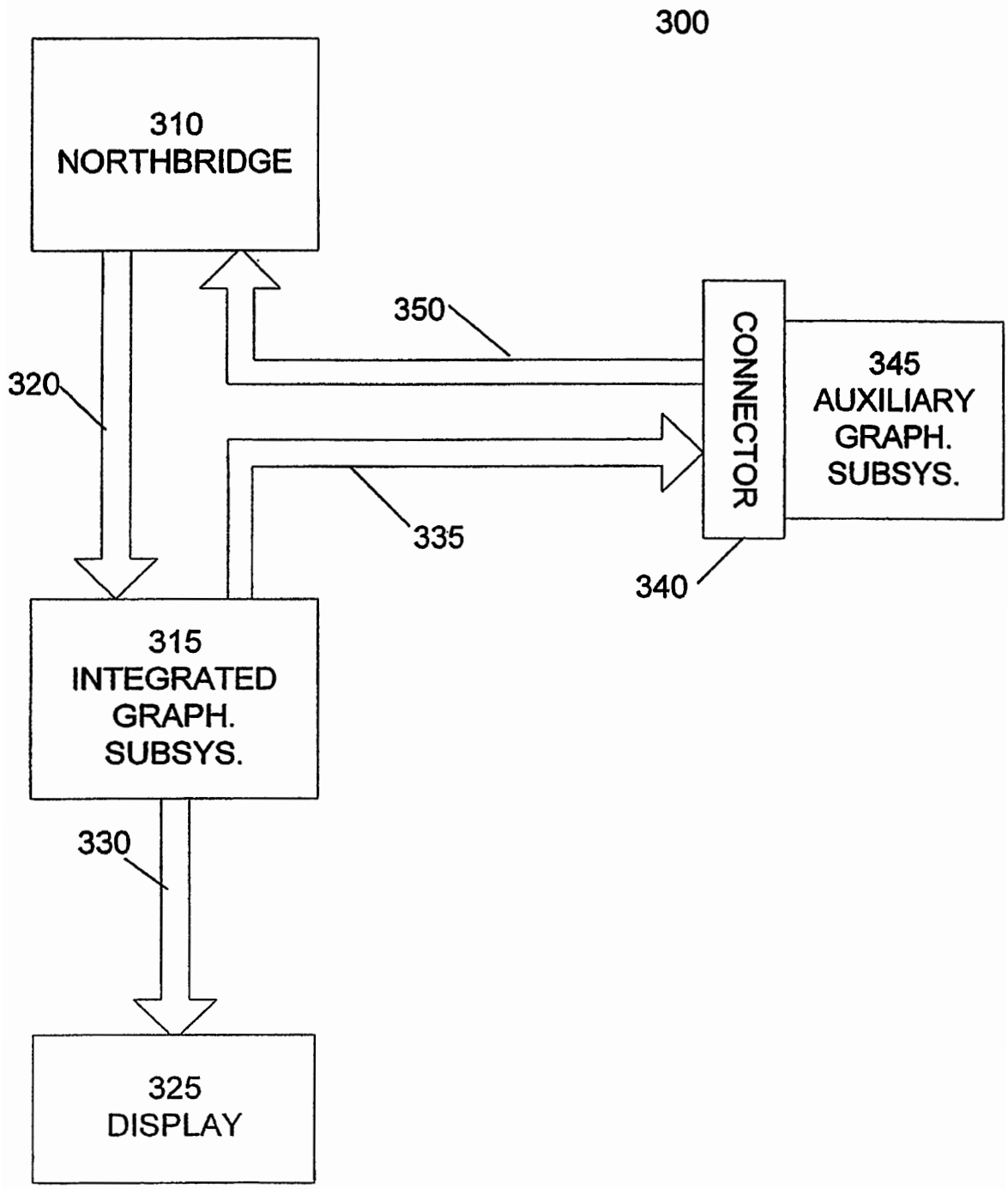

300
310
NORTHBRIDGE
350
320
CONNECTOR
345
AUXILIARY
GRAPH.
SUBSYS.
335
340
315
INTEGRATED
GRAPH.
SUBSYS.
330
325
DISPLAY

FIG. 3

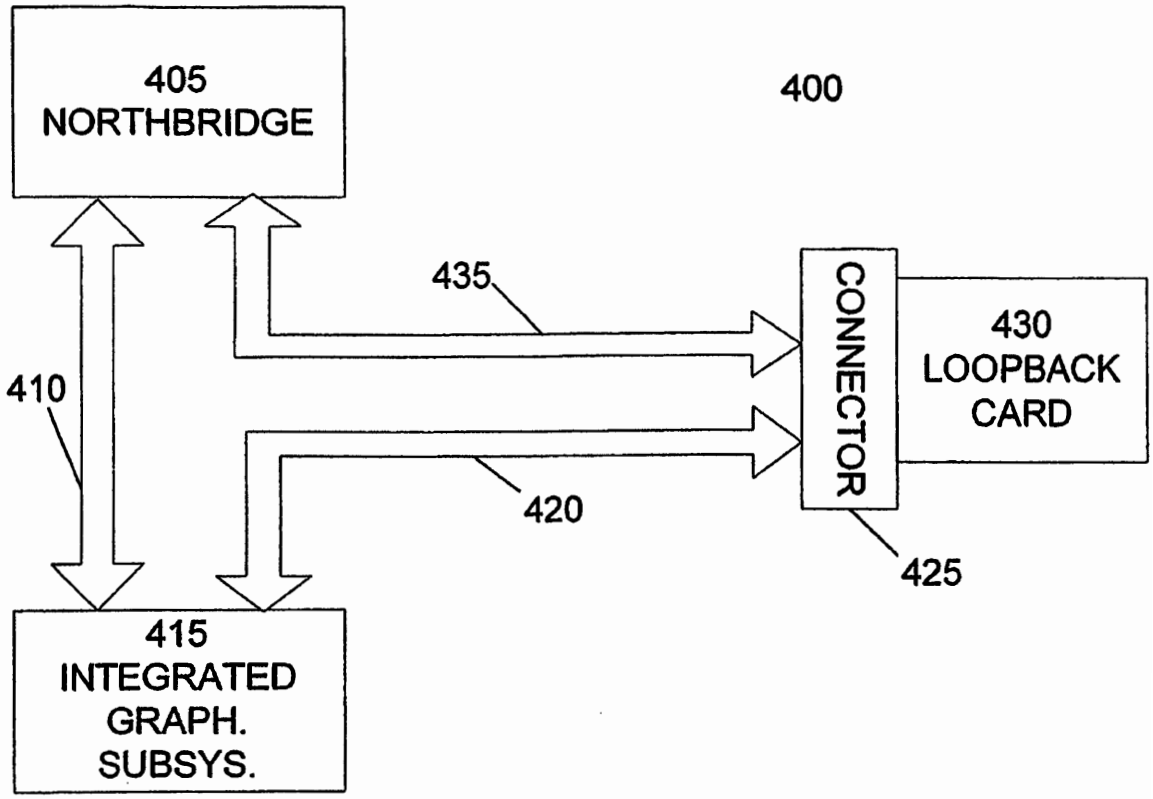

405
NORTHBRIDGE
400
435
CONNECTOR
430
LOOPBACK
CARD
410
420
425
415
INTEGRATED
GRAPH.
SUBSYS.

FIG. 4A

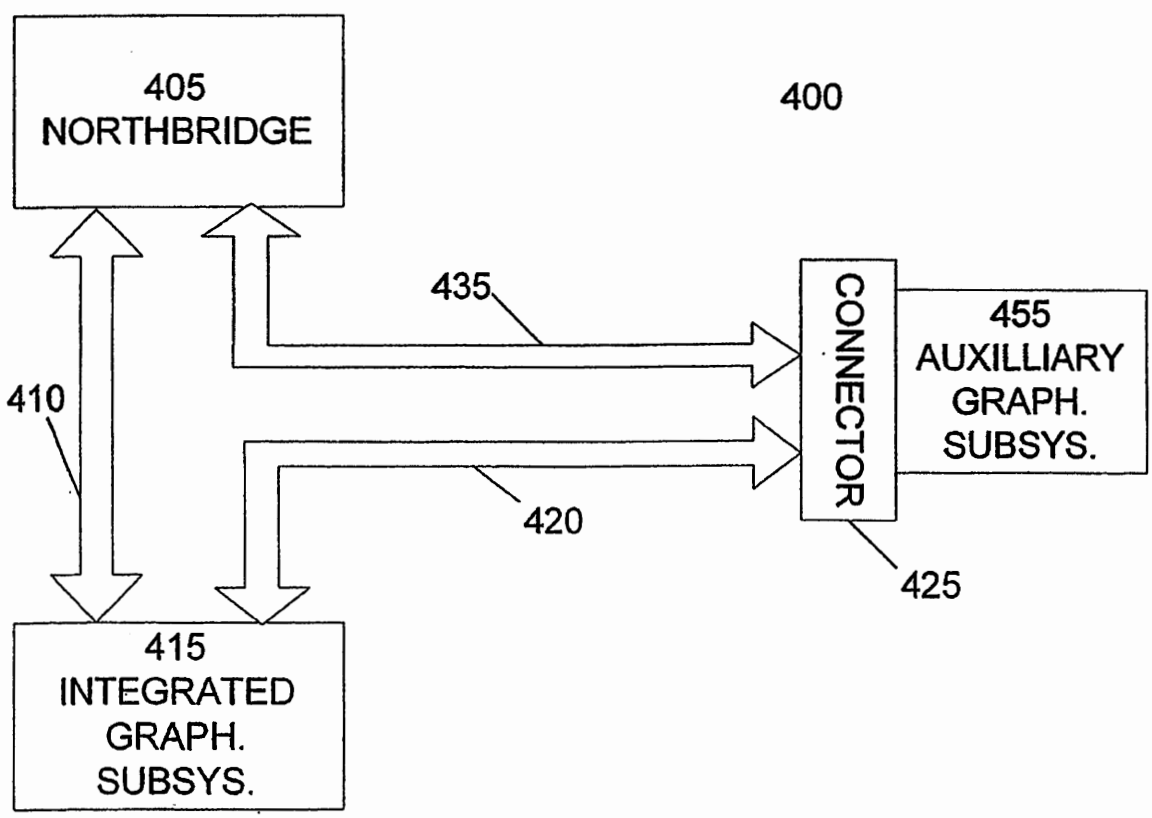

405
NORTHBRIDGE
400
435
CONNECTOR
455
AUXILLIARY
GRAPH.
SUBSYS.
410
420
425
415
INTEGRATED
GRAPH.
SUBSYS.

FIG. 4B