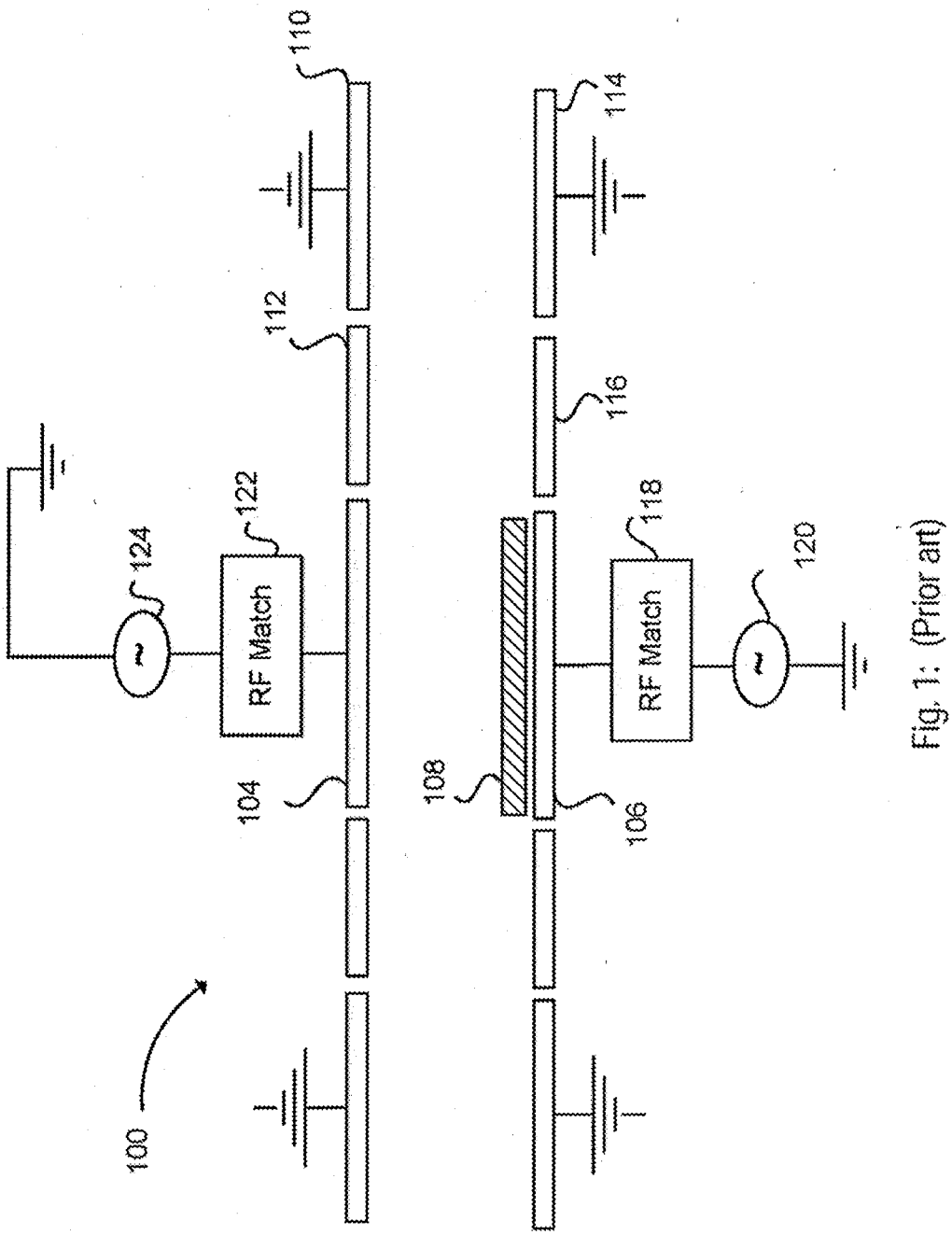


Fig. 1: (Prior art)

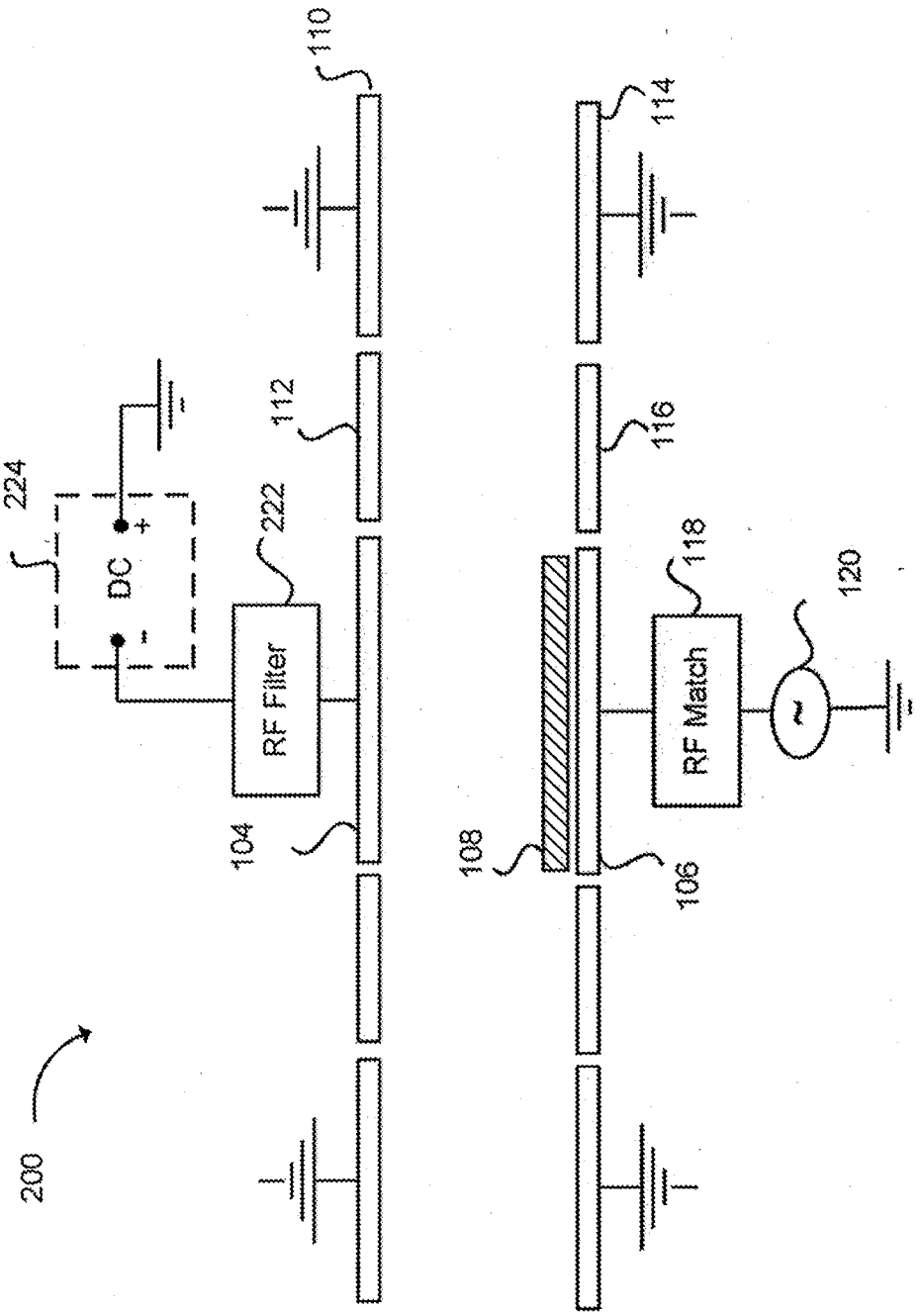


Fig. 2: (Prior art)

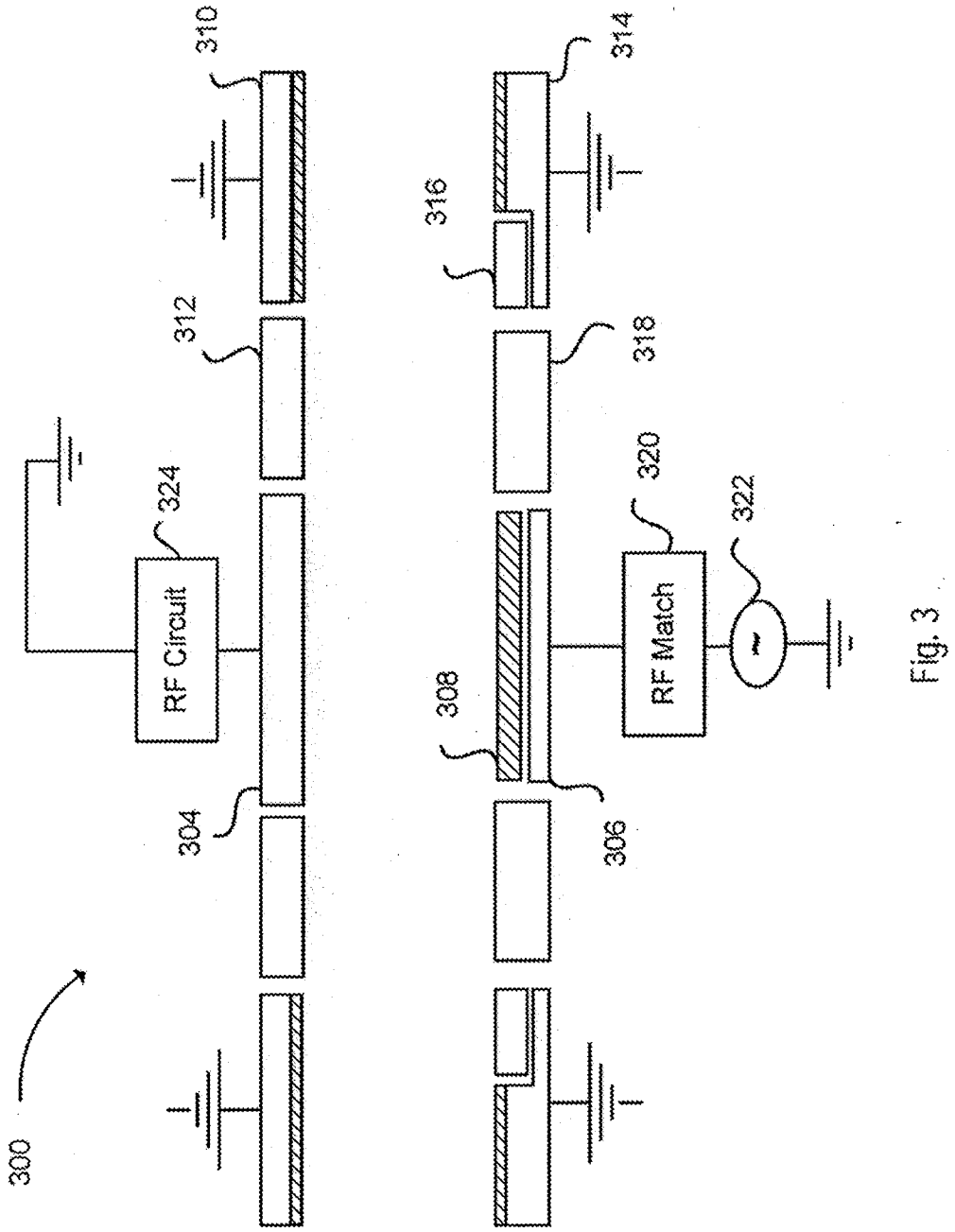

300
RF Circuit
324
304
312
310
308
306
316
318
314
320
RF Match
322

Fig. 3

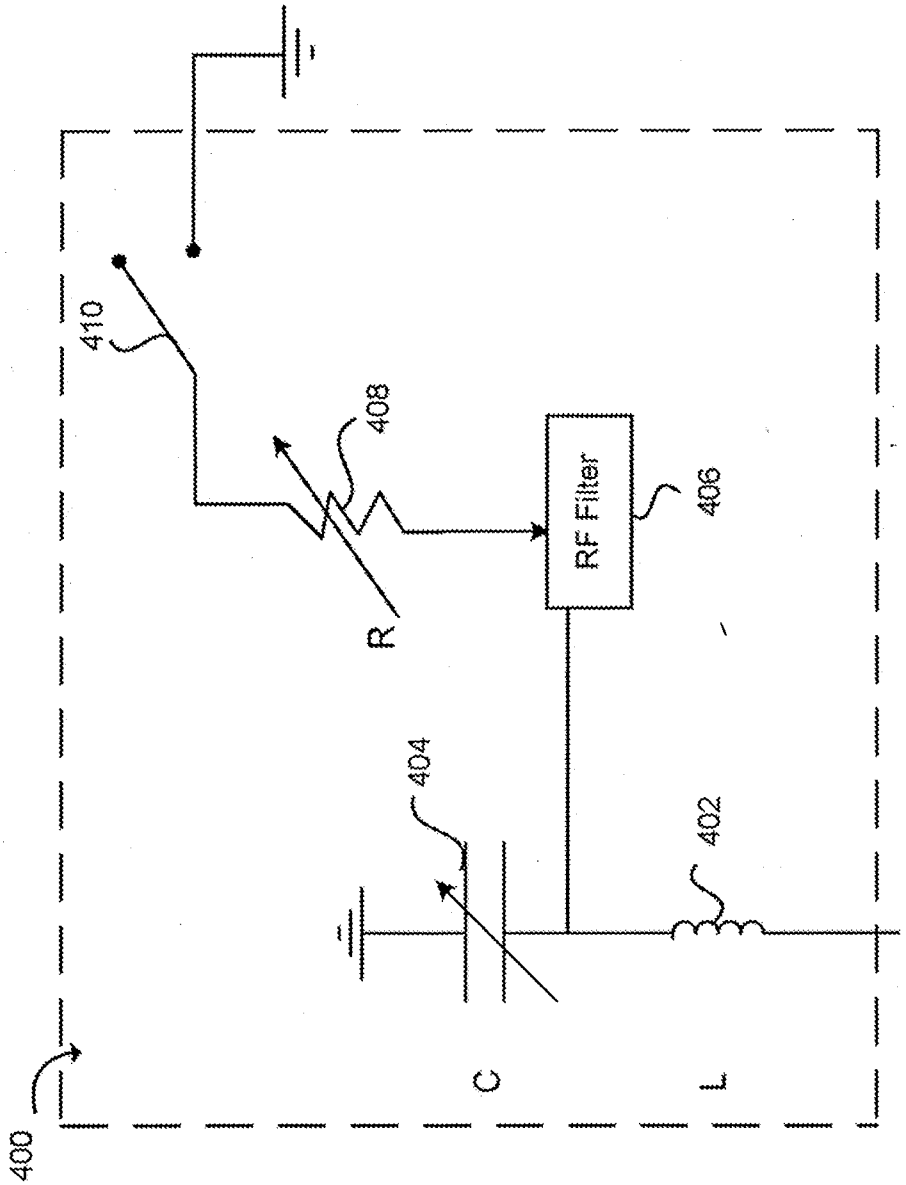


Fig. 4

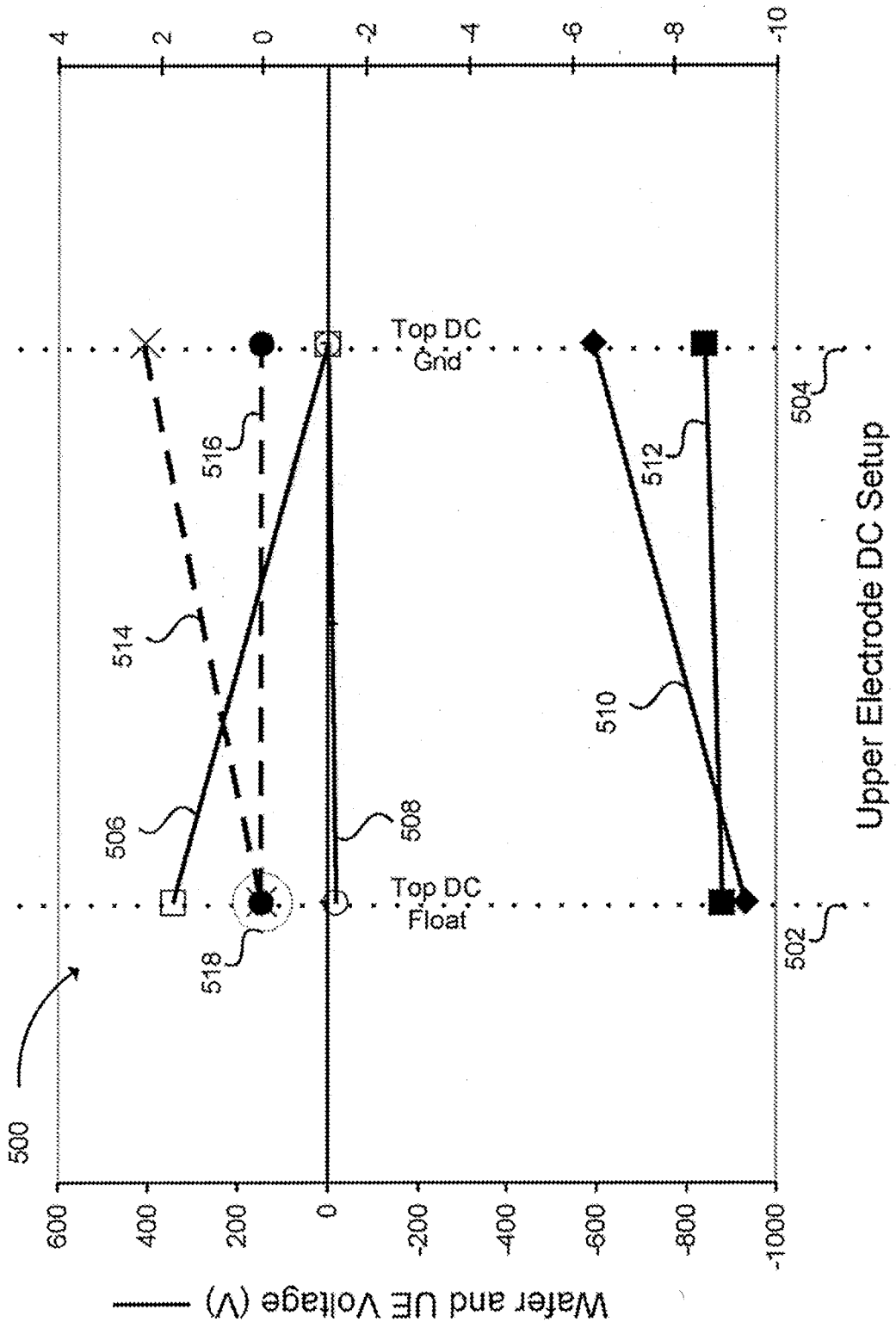

Wafer and UE Voltage (V)
UE DC Current (A)
Upper Electrode DC Setup
Top DC Gnd
Top DC Float
500
502
504
506
508
510
512
514
516
518

Fig. 5