Fig.1

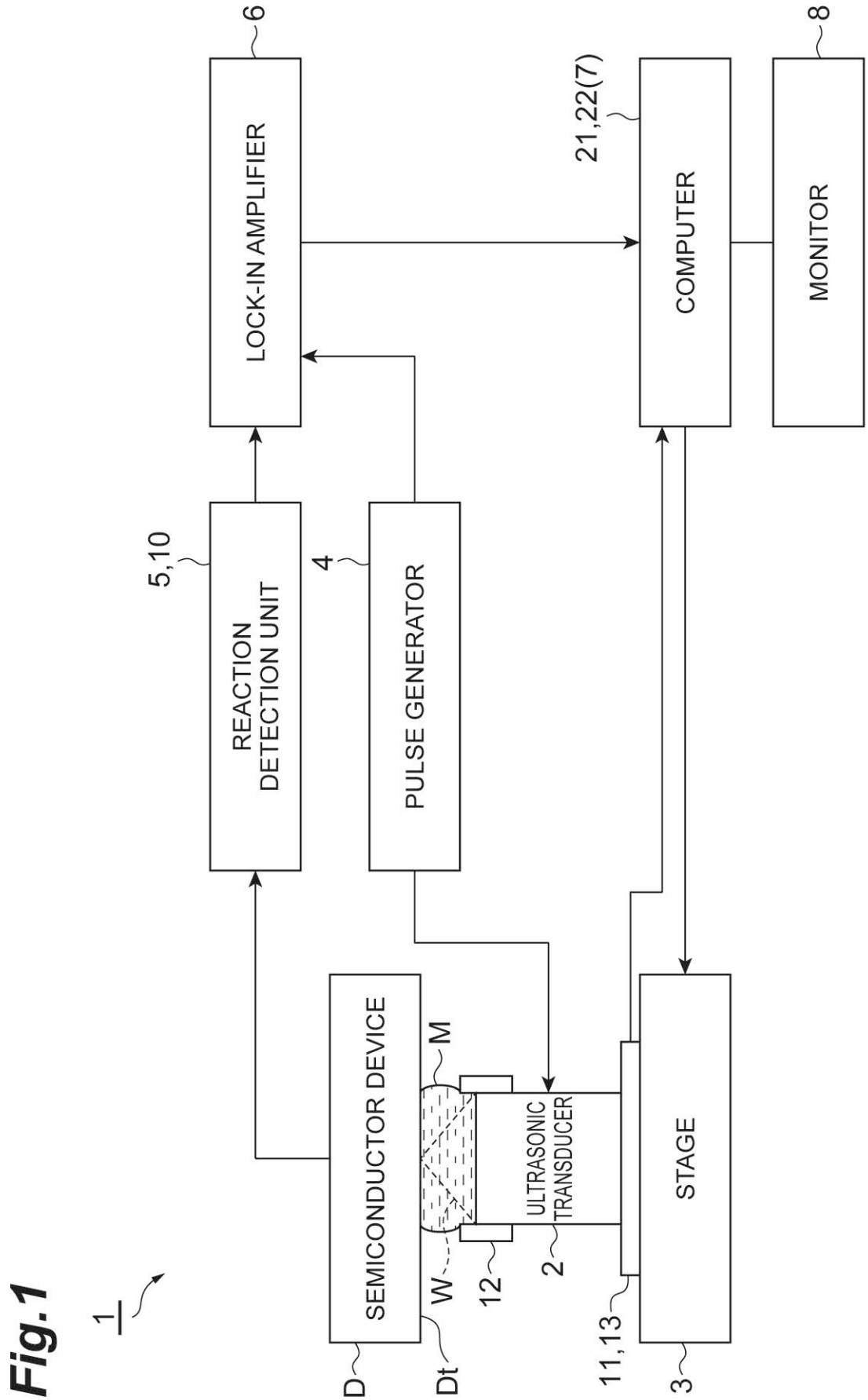

1
6
LOCK-IN AMPLIFIER
21,22(7)
COMPUTER
8
MONITOR
5,10
REACTION DETECTION UNIT
4
PULSE GENERATOR
SEMICONDUCTOR DEVICE
M
W
12
2
ULTRASONIC TRANSDUCER
STAGE
D
Dt
11,13
3

Fig.2

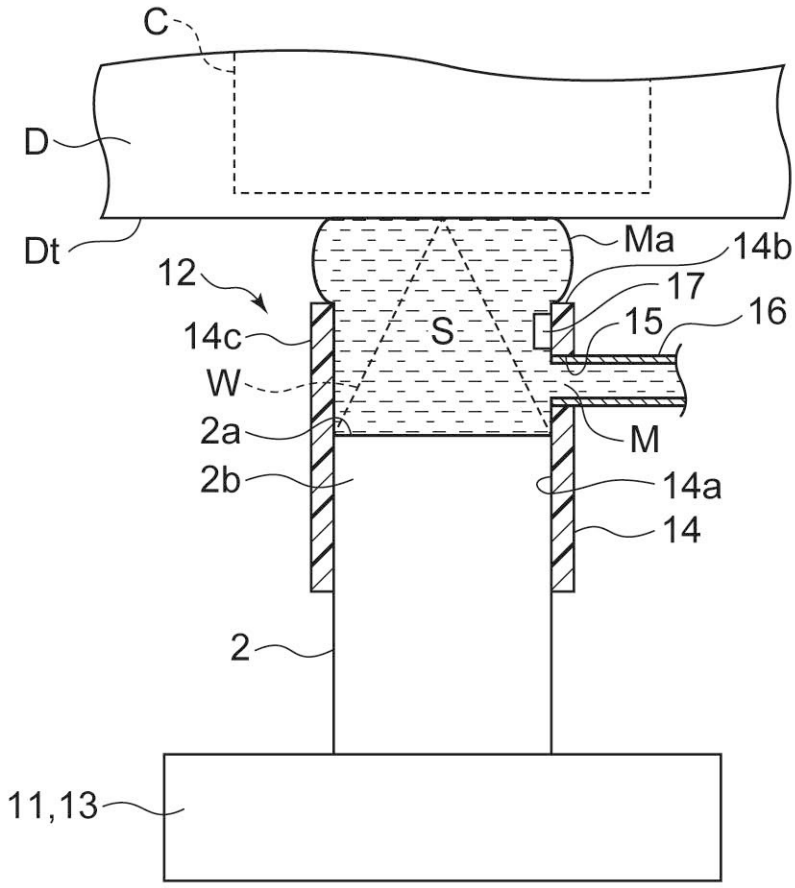
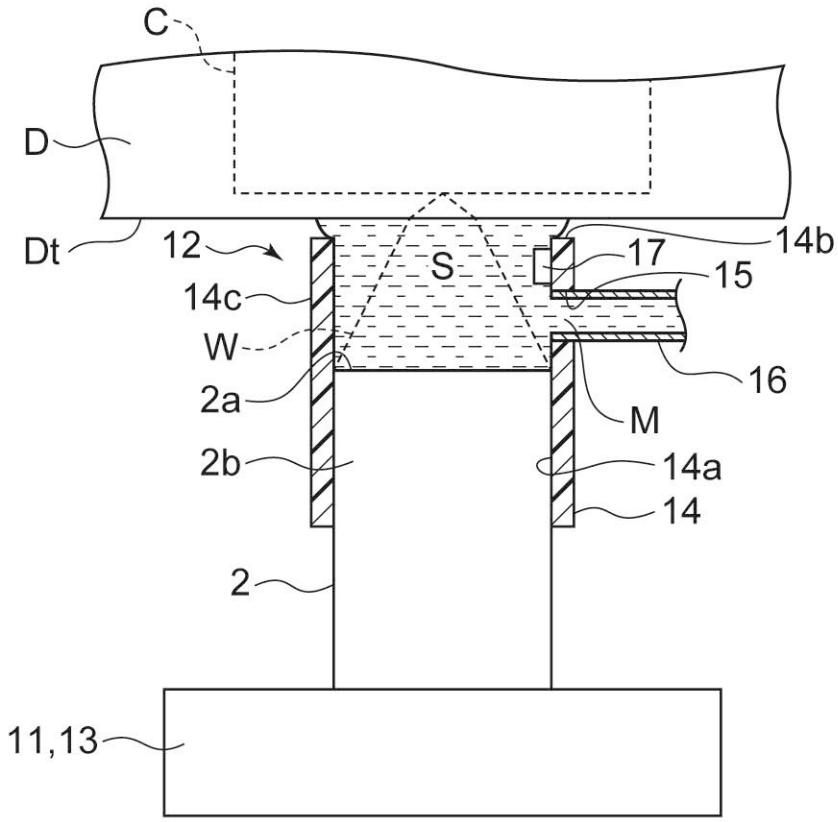


Fig.3



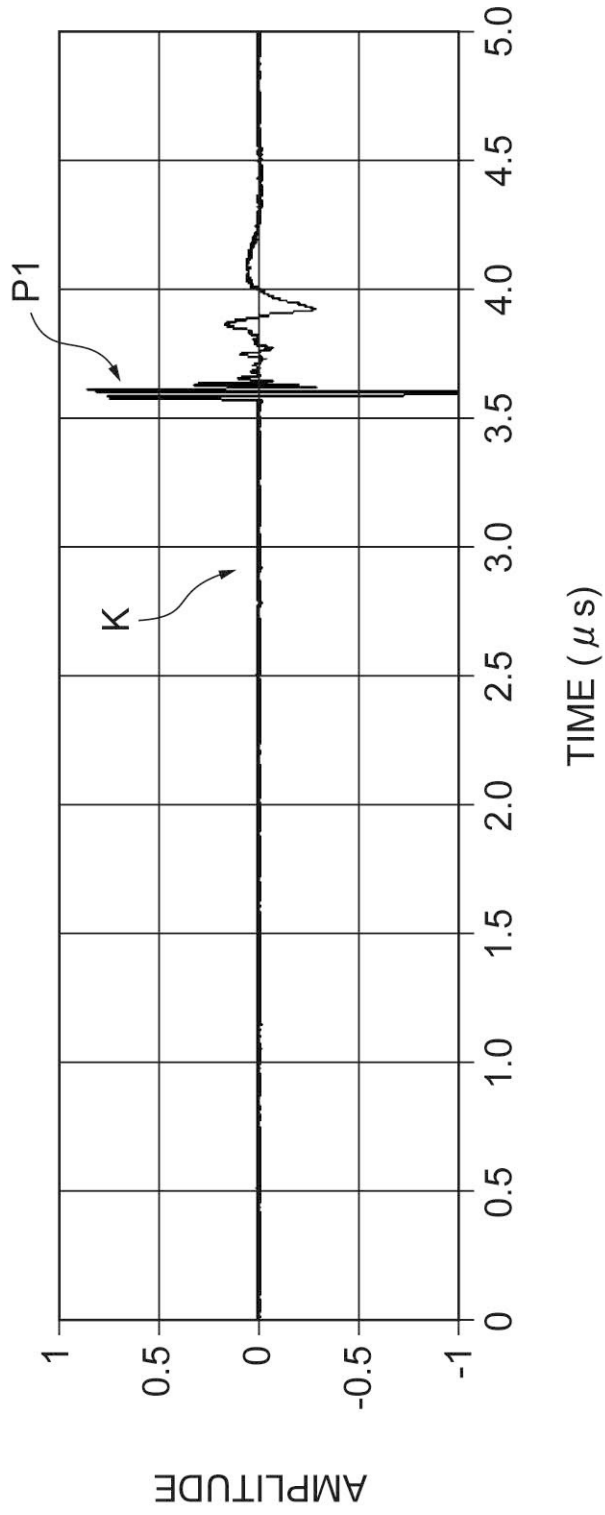


Fig.4

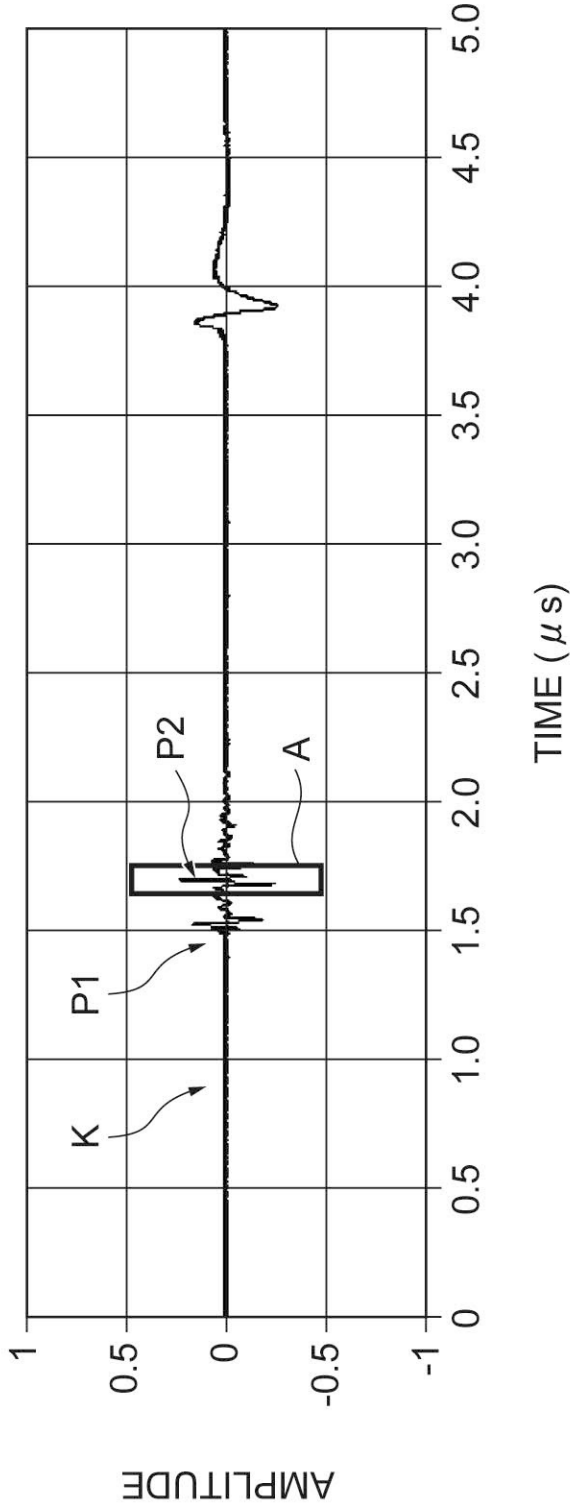


Fig.5

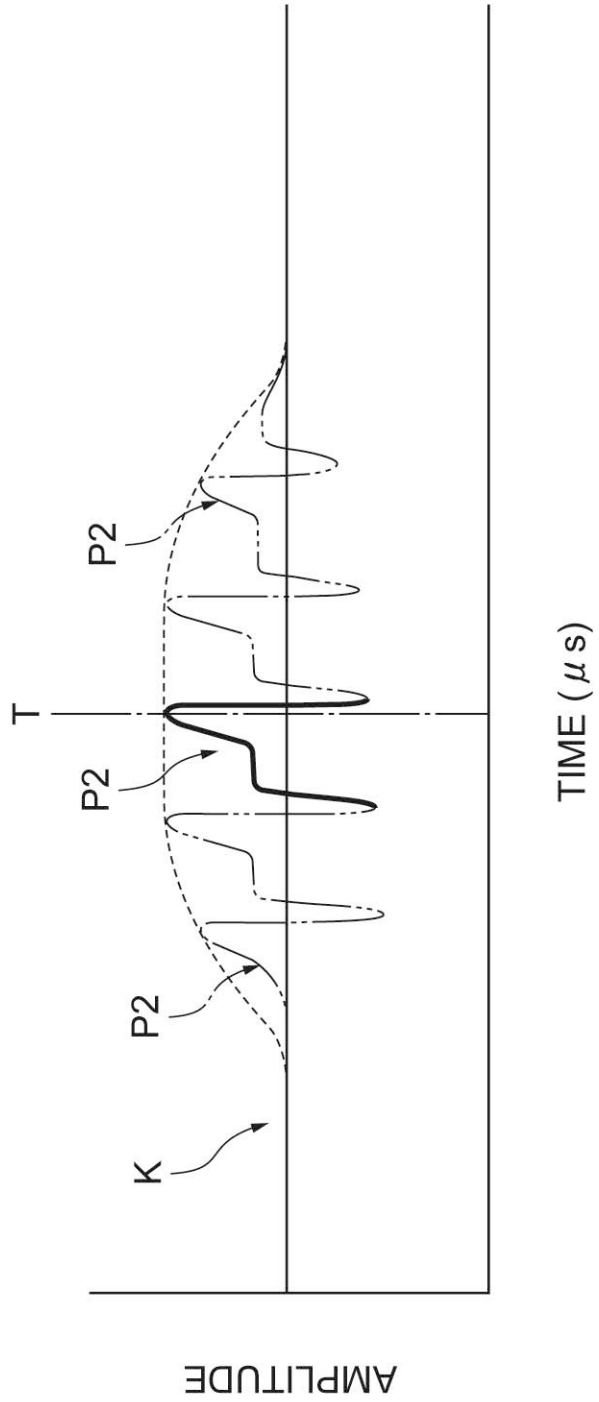


Fig.6

Fig.7

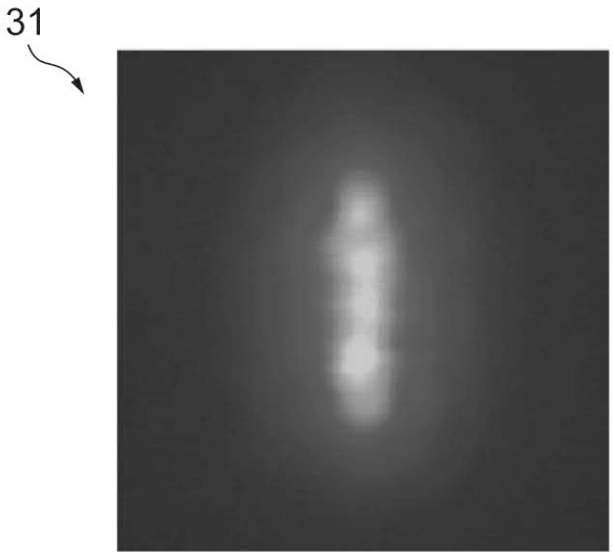


Fig.8

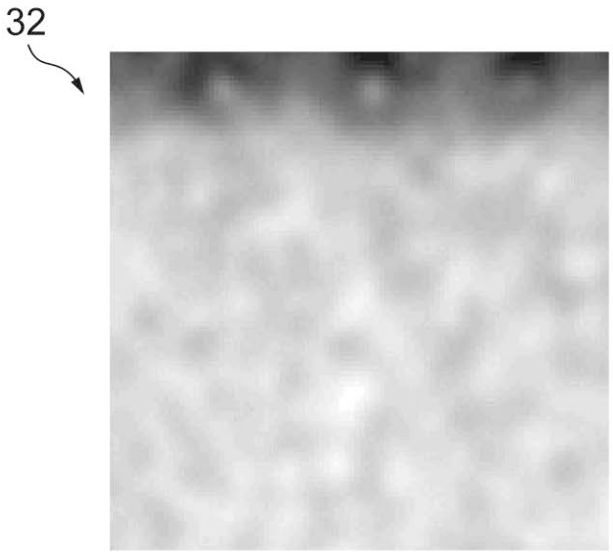


Fig.9

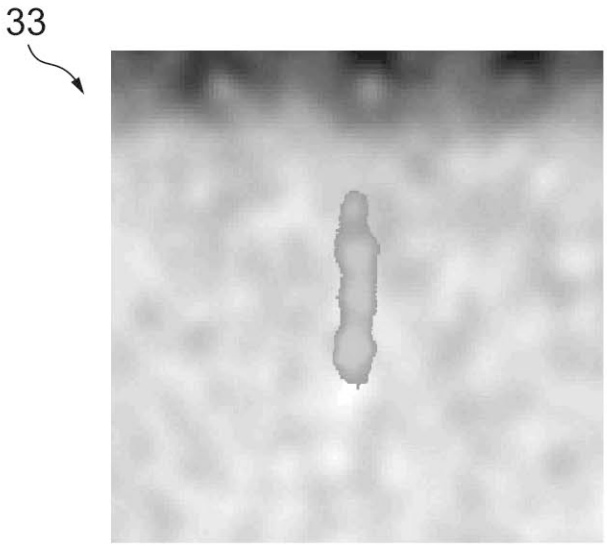
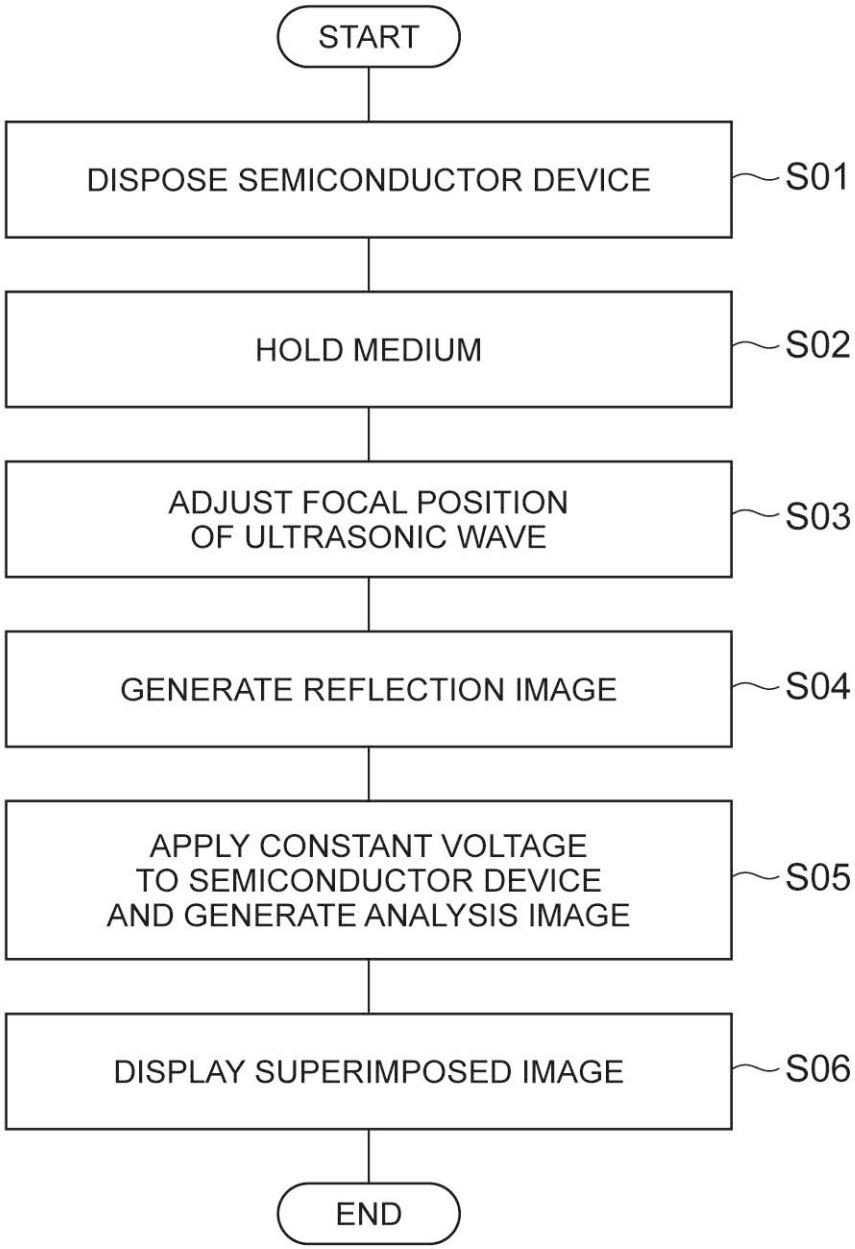


Fig.10


START
DISPOSE SEMICONDUCTOR DEVICE
S01
HOLD MEDIUM
S02
ADJUST FOCAL POSITION OF ULTRASONIC WAVE
S03
GENERATE REFLECTION IMAGE
S04
APPLY CONSTANT VOLTAGE TO SEMICONDUCTOR DEVICE AND GENERATE ANALYSIS IMAGE
S05
DISPLAY SUPERIMPOSED IMAGE
S06
END