

FIG. 1

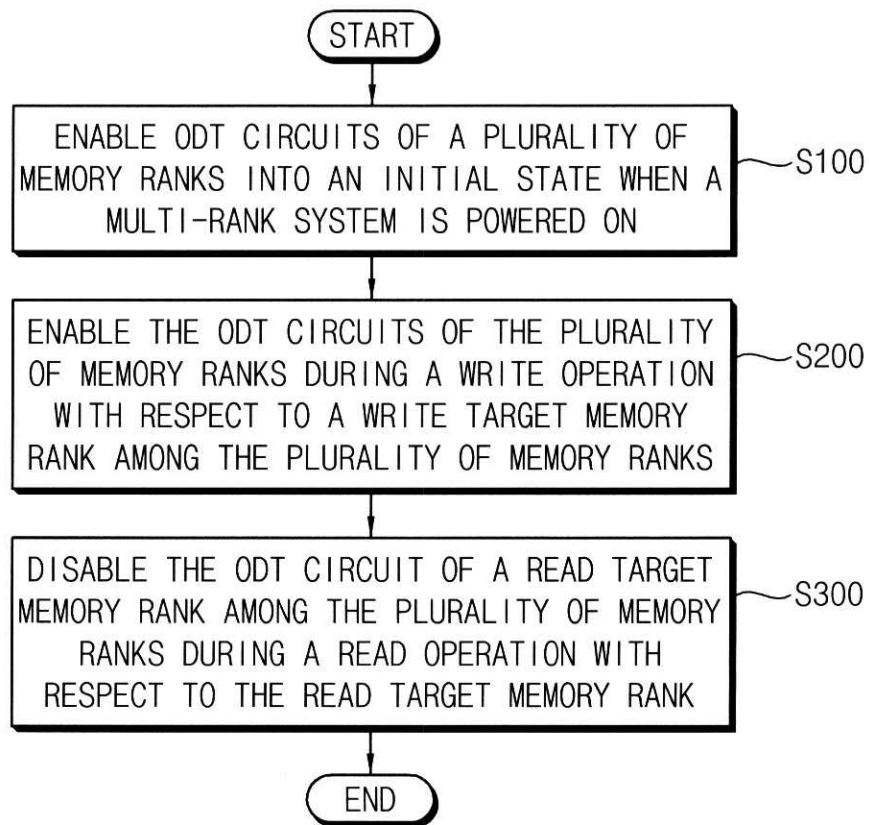


FIG. 2

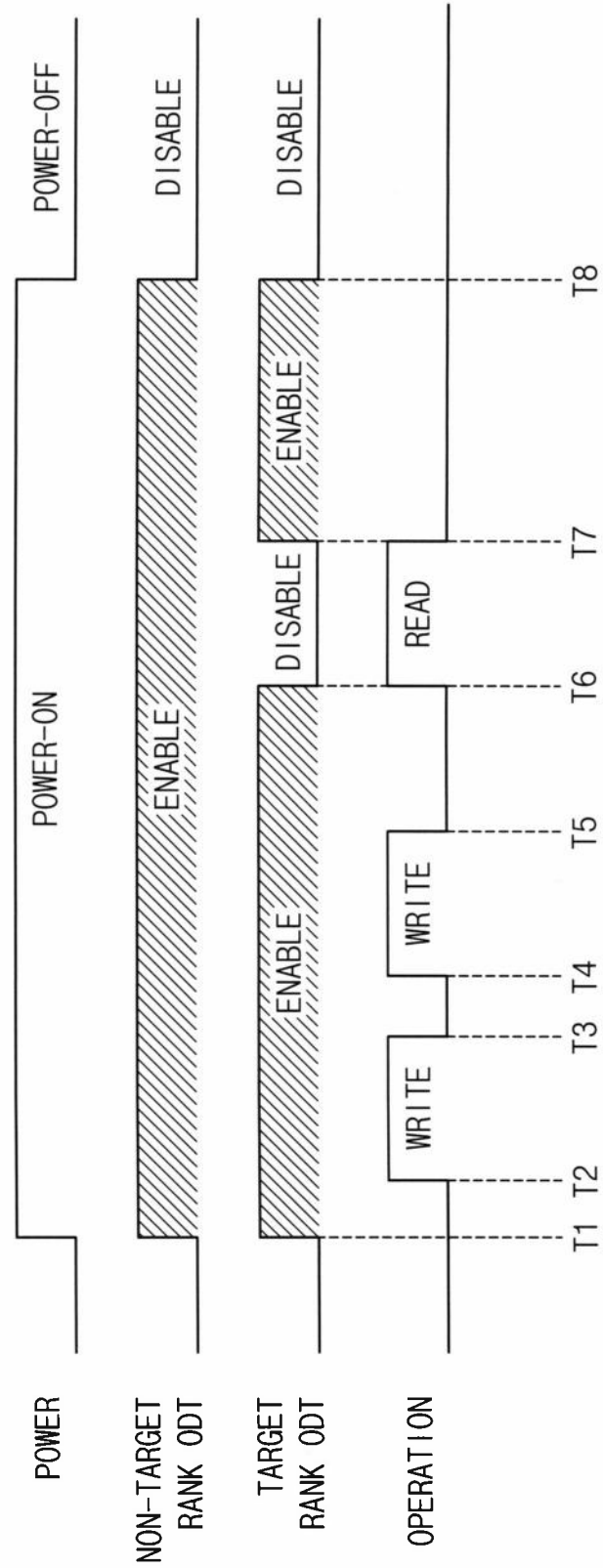


FIG. 3

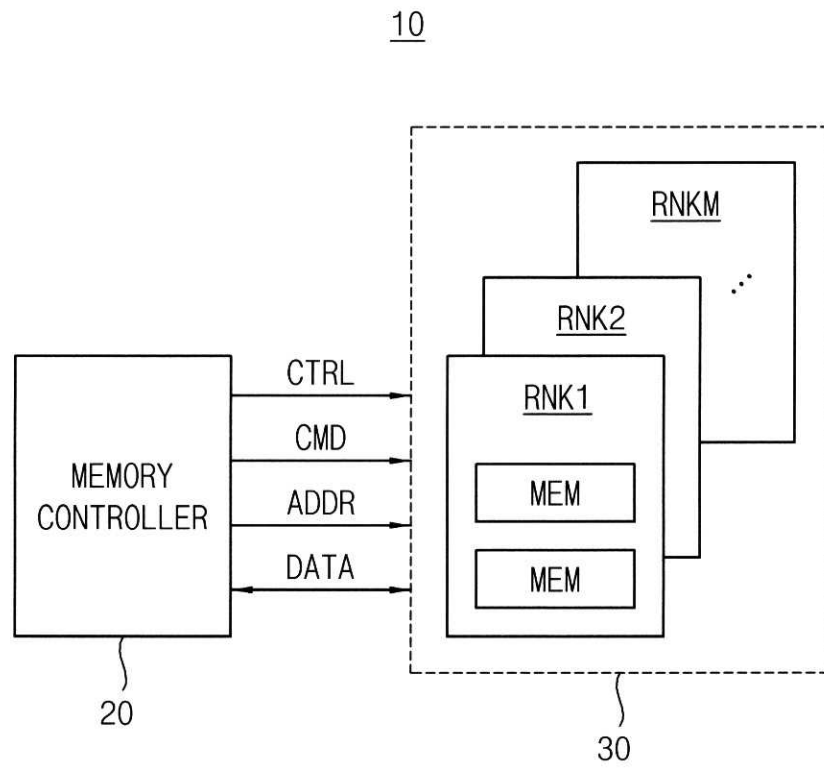


FIG. 4

400

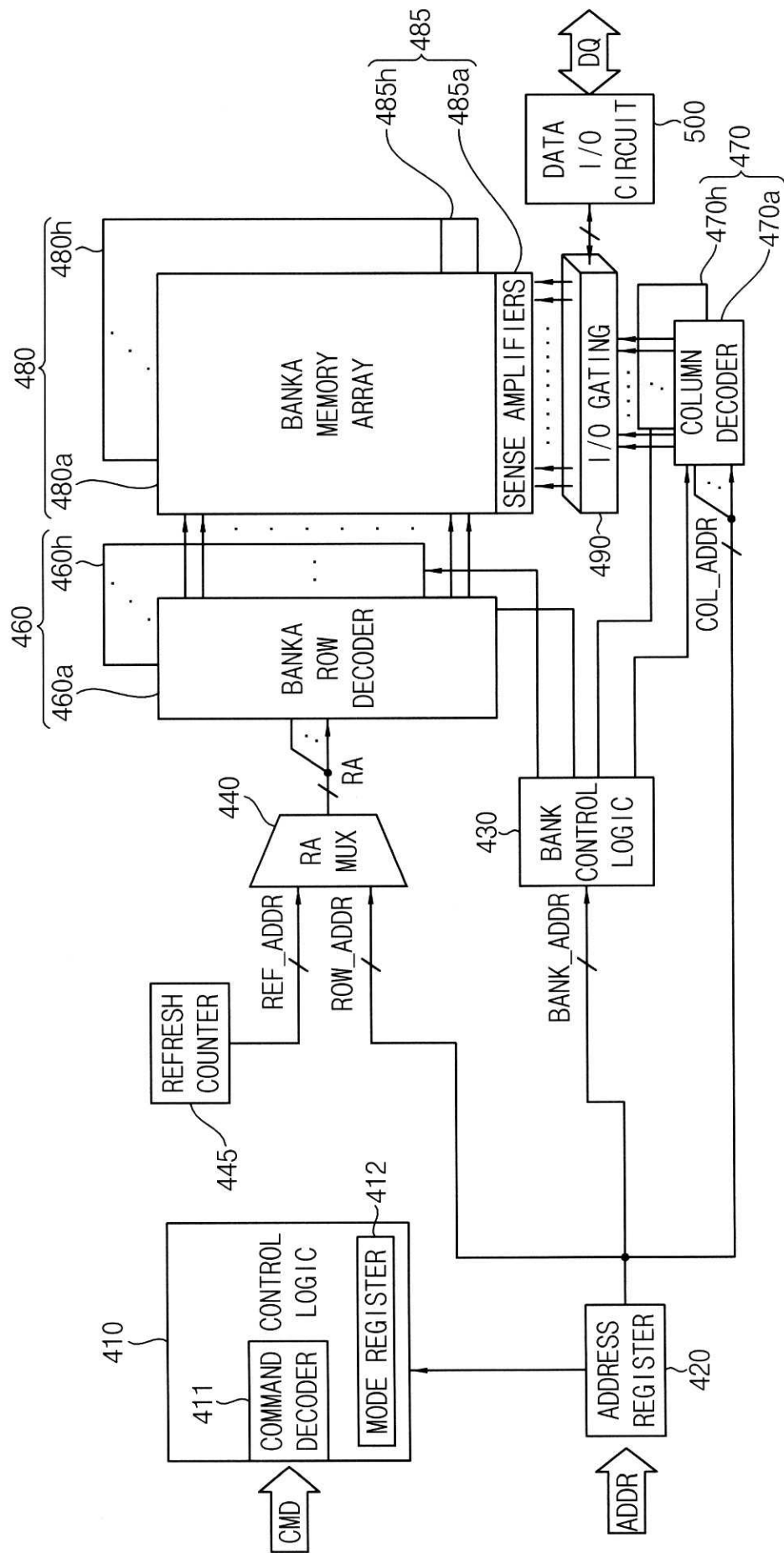


FIG. 5

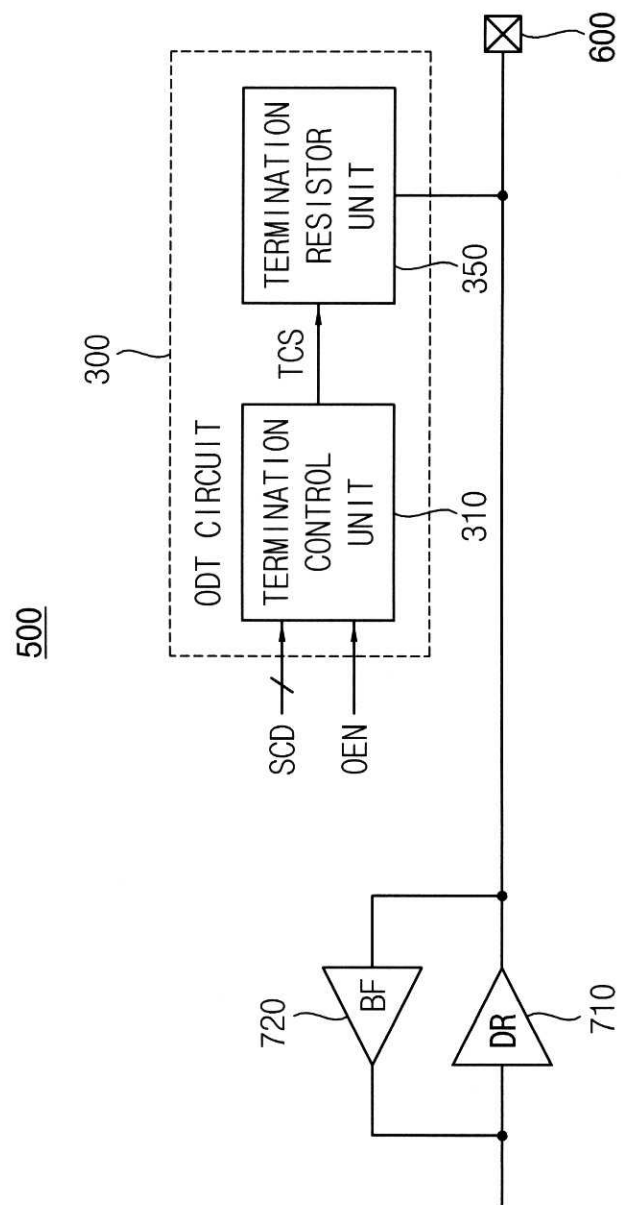


FIG. 6

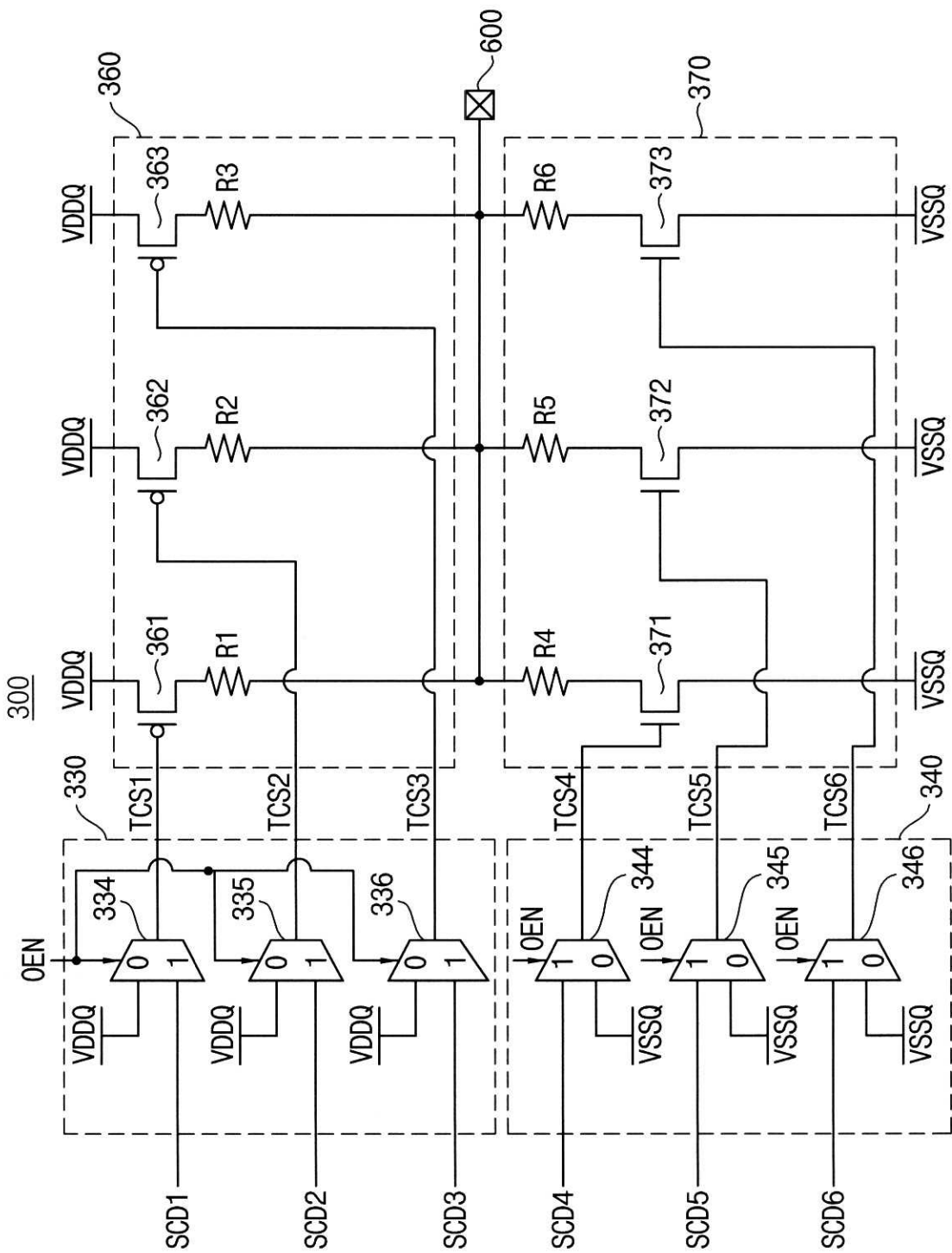


FIG. 7

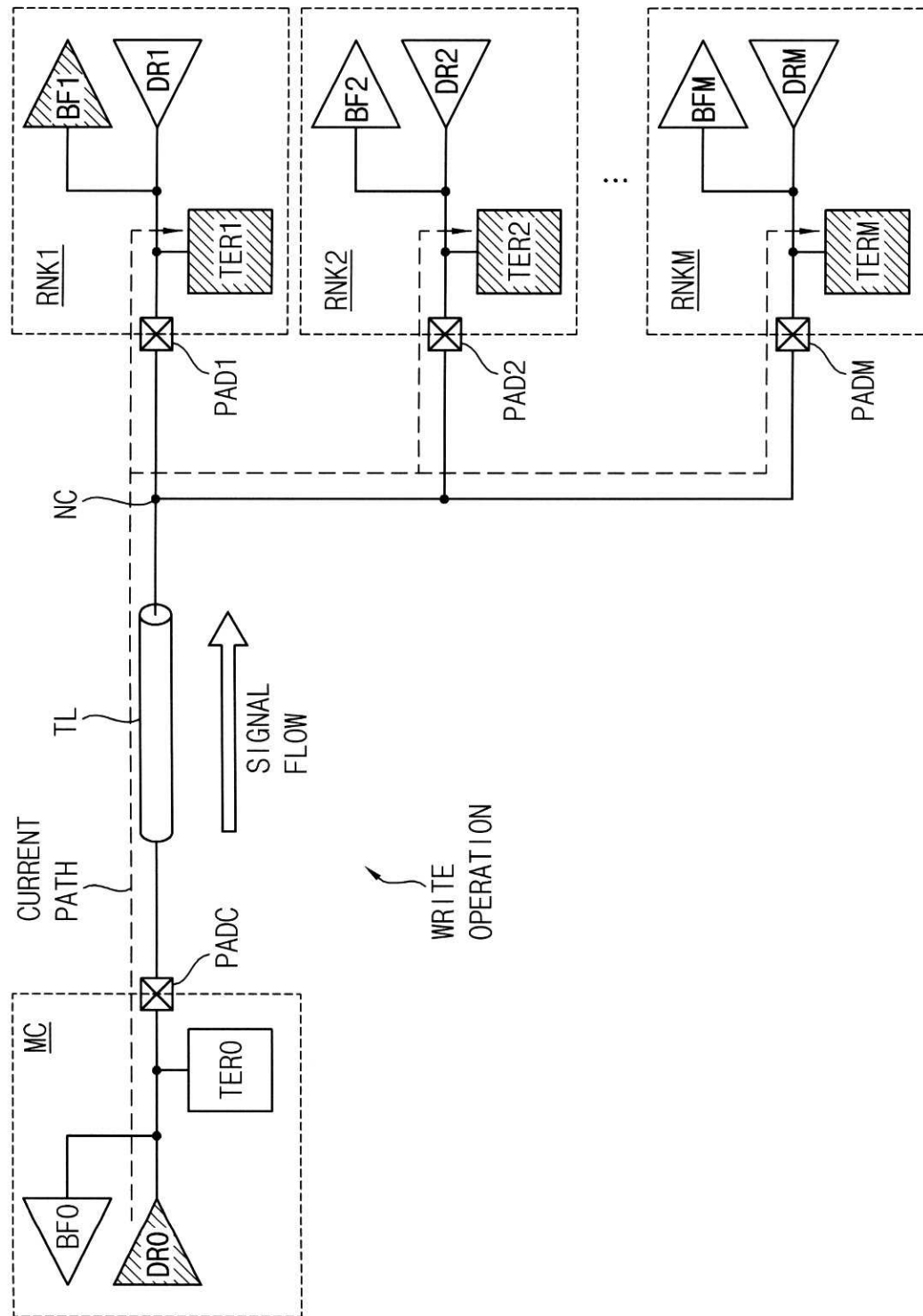


FIG. 8A

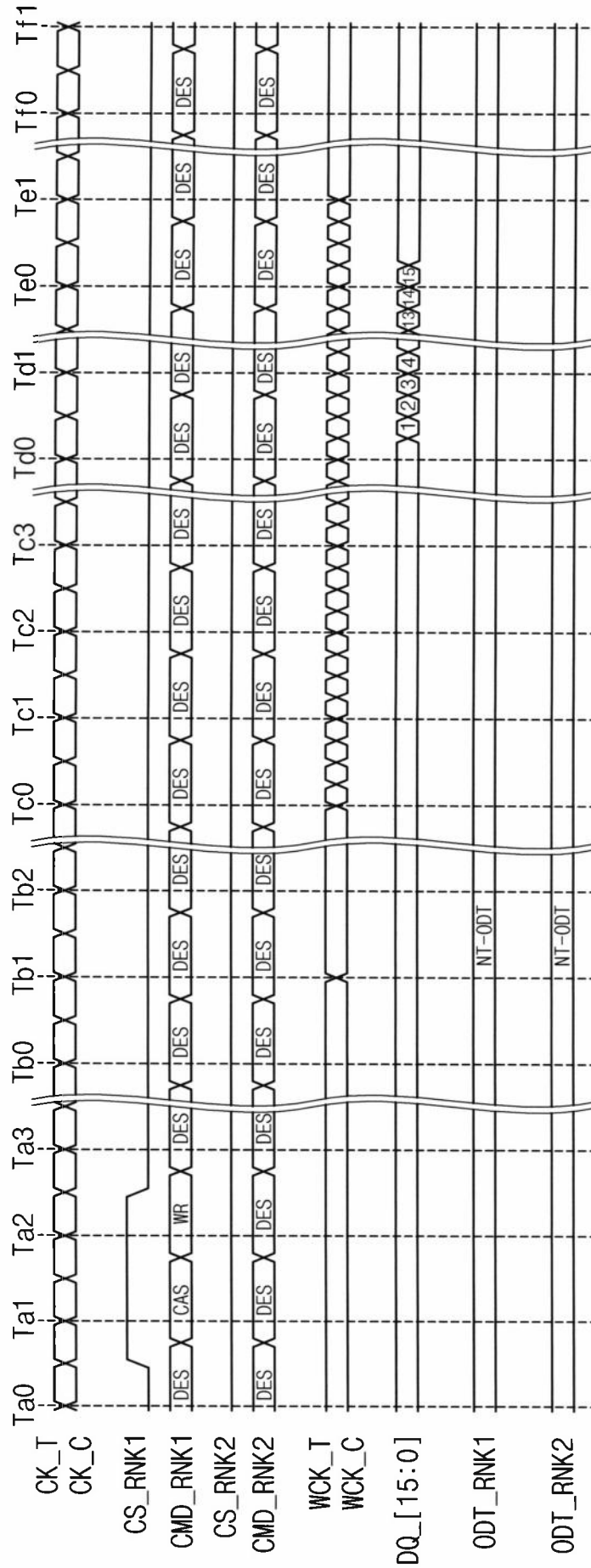


FIG. 8B

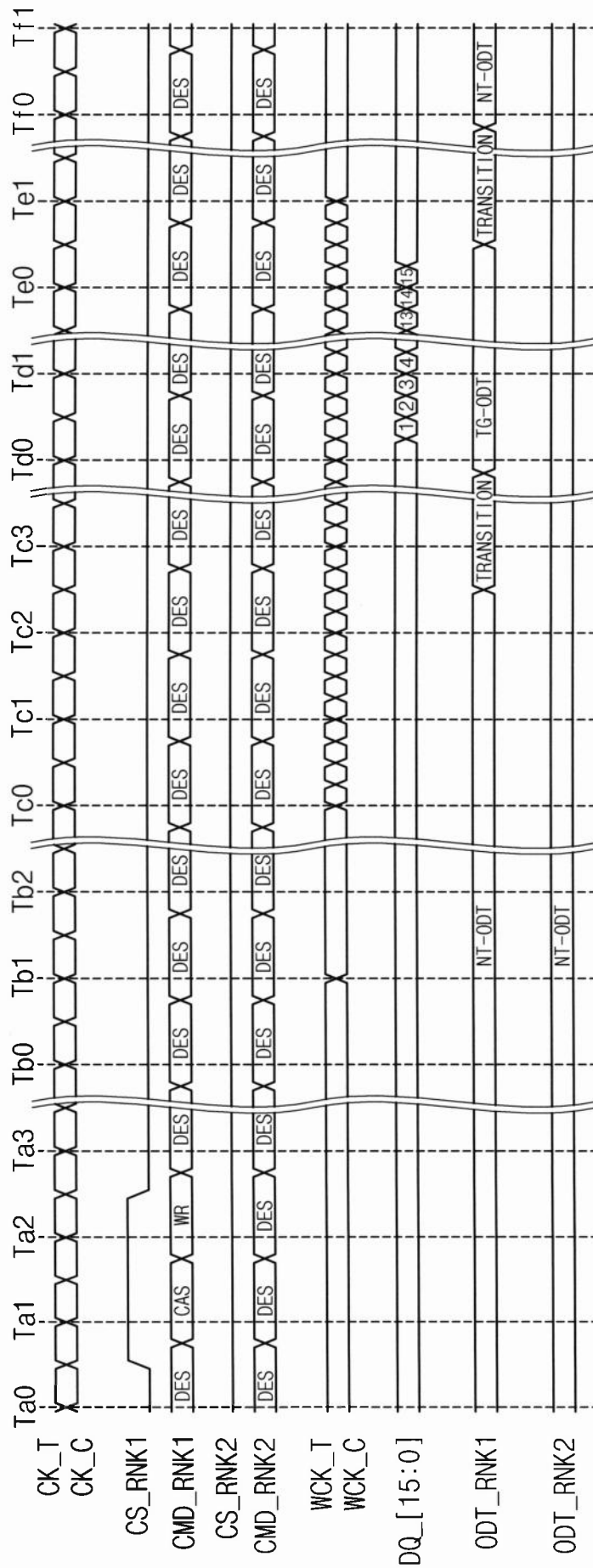


FIG. 9

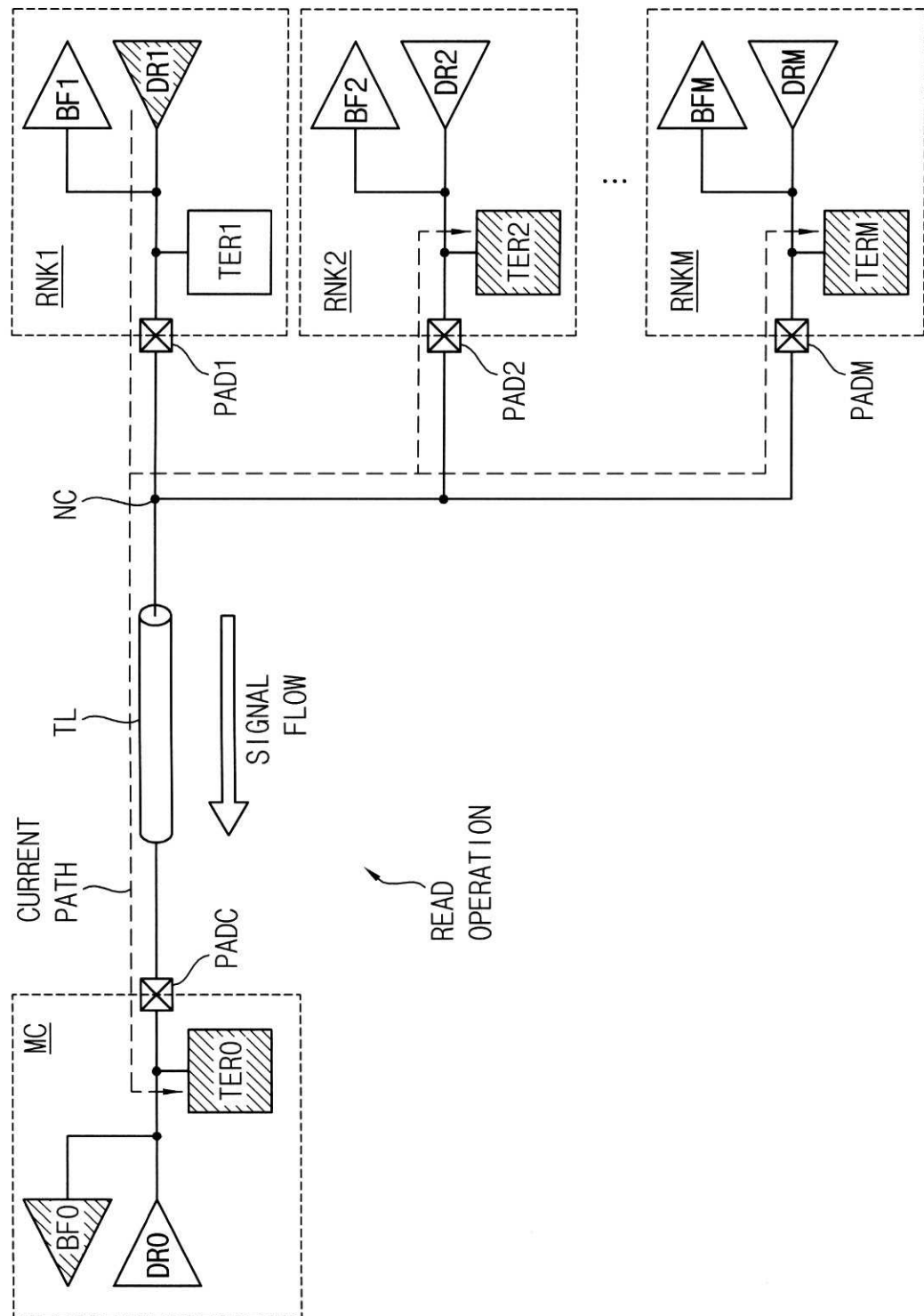


FIG. 10

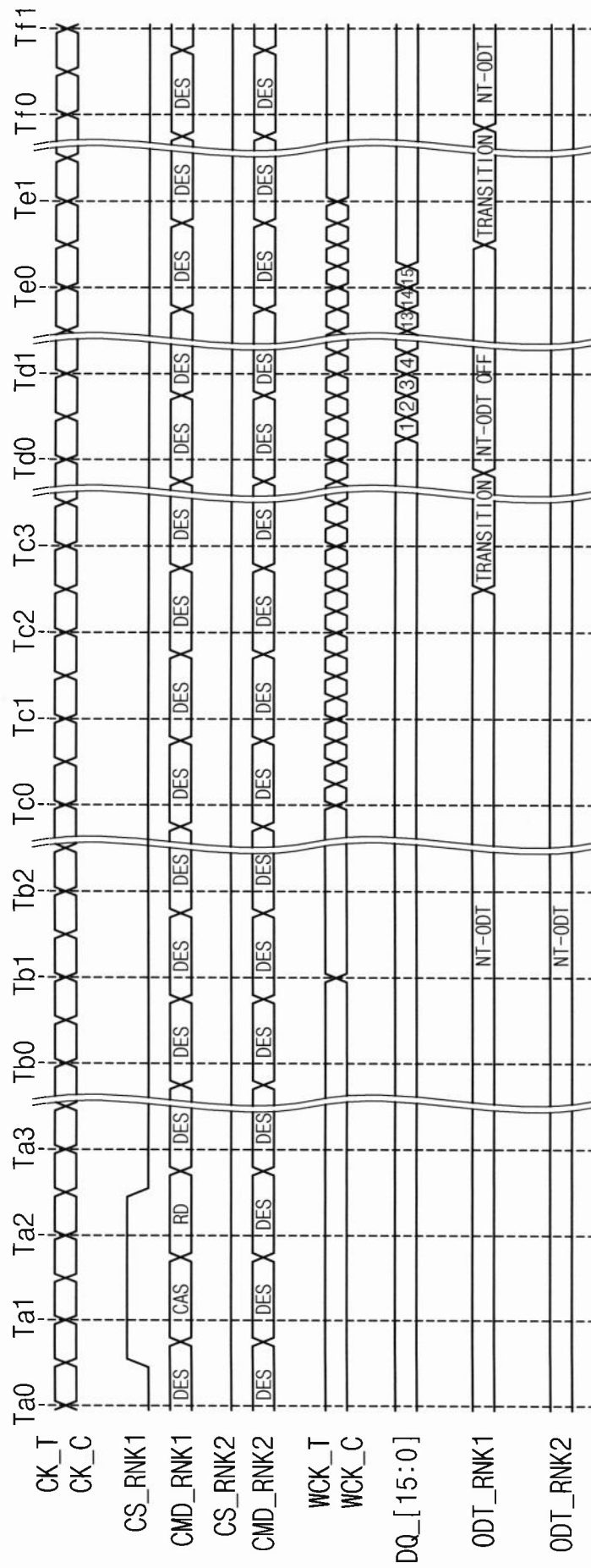


FIG. 11

	RNK_TG	RNK_NT	MC
READ	DISABLED	$M \cdot R_{tt}$	$M \cdot R_{tt}$
WRITE	$M \cdot R_{tt}$	$M \cdot R_{tt}$	DISABLED

FIG. 12

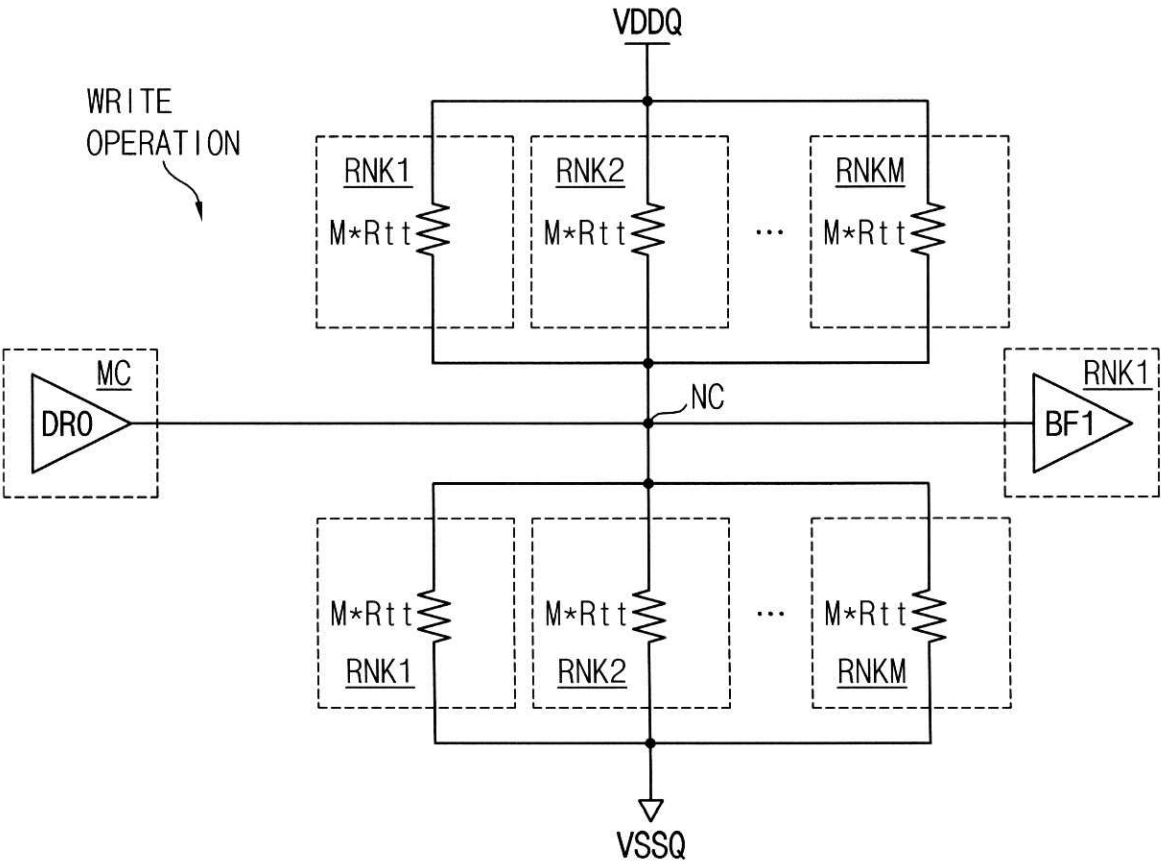


FIG. 13

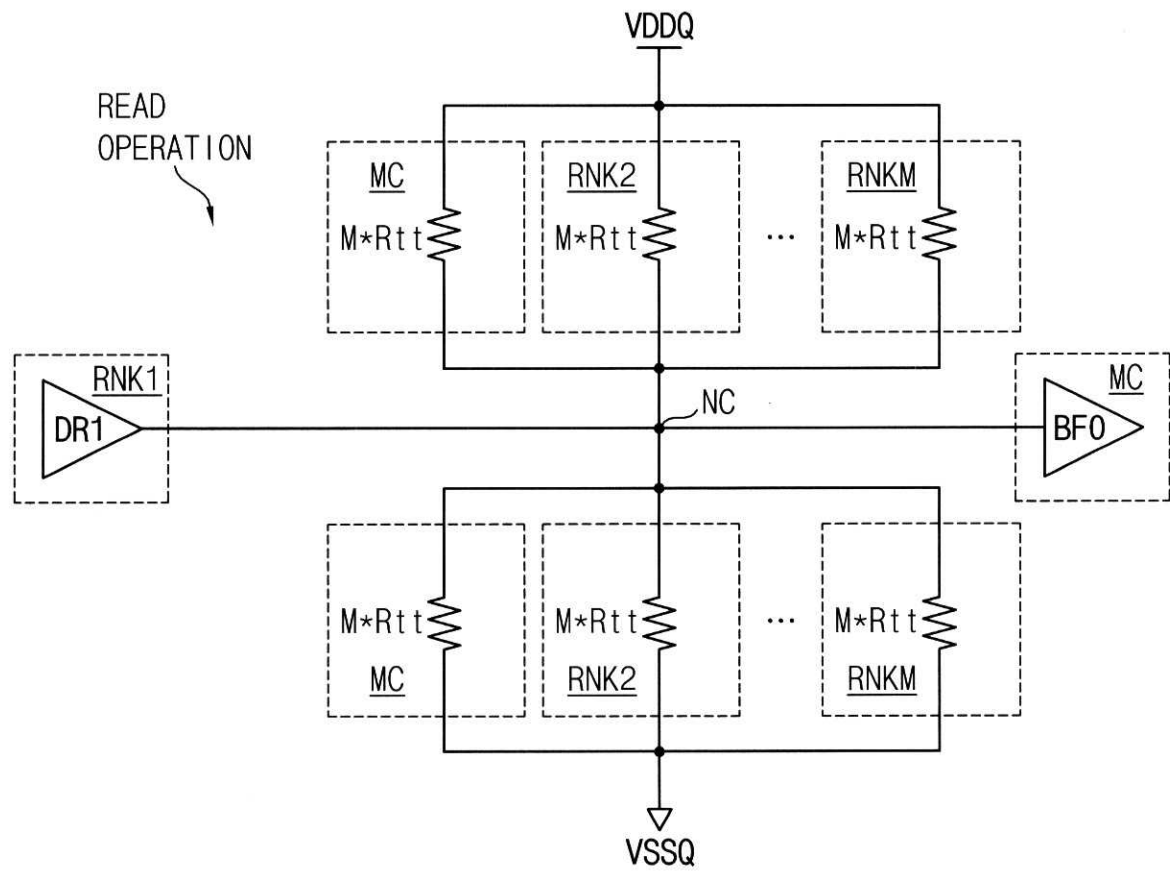


FIG. 14A

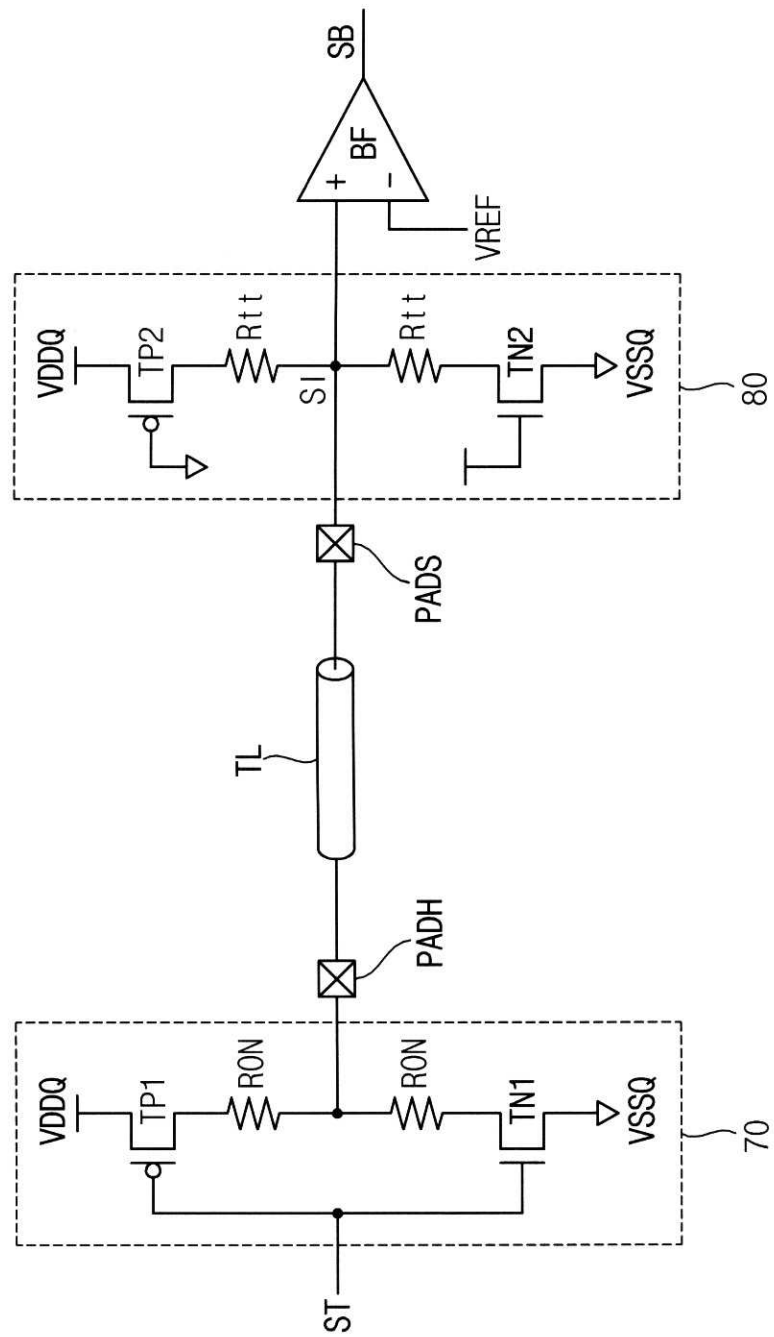


FIG. 14B

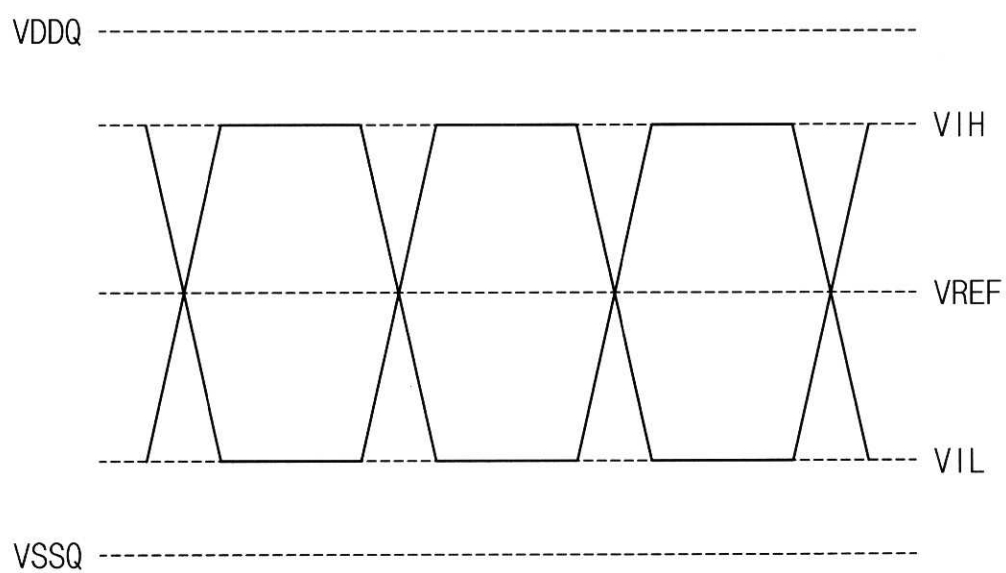


FIG. 15A

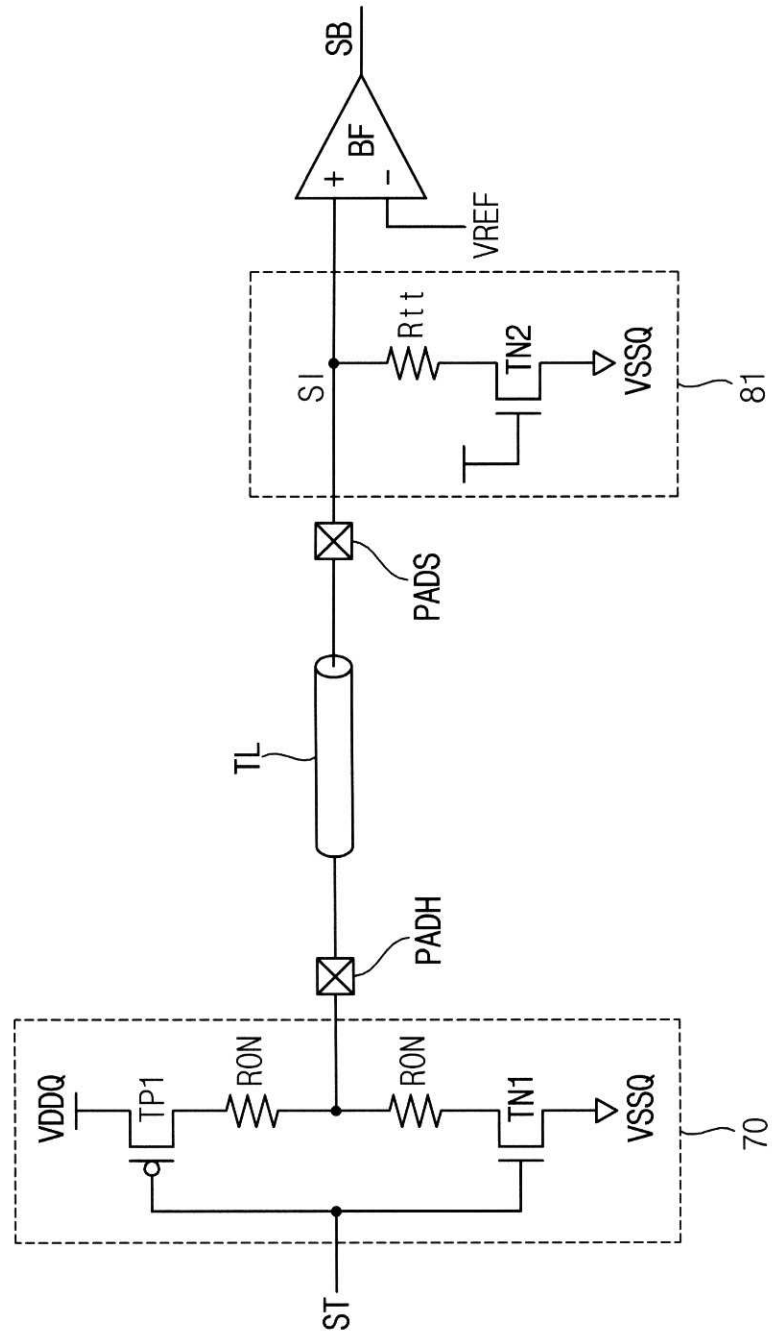


FIG. 15B

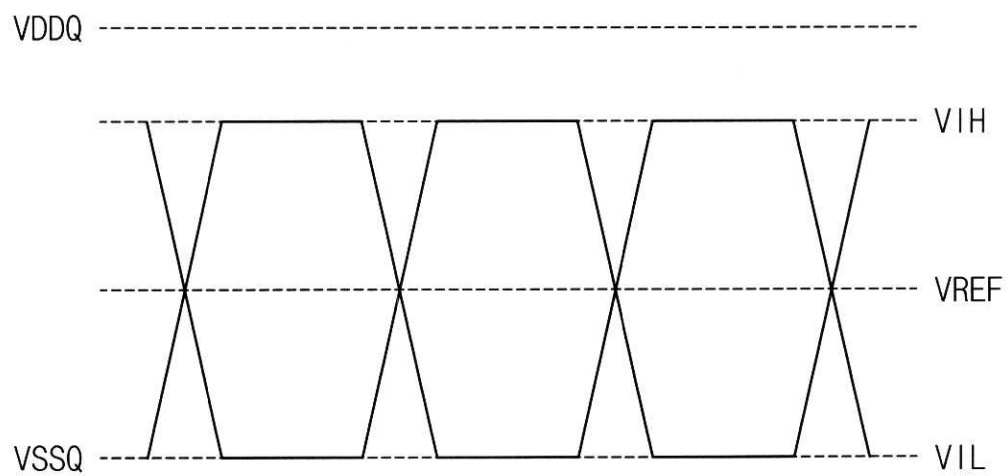


FIG. 16B

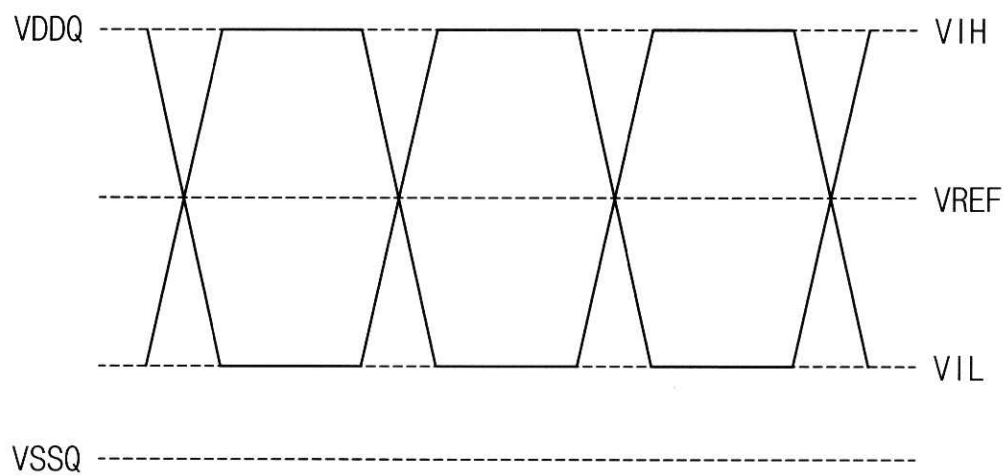


FIG. 17

	RNK_TG	RNK_NT	MC
READ	DISABLED	$M \cdot R_{tt}$	$M \cdot R_{tt}$
WRITE	$M \cdot R_{tt} + R_{tg}$	$M \cdot R_{tt}$	DISABLED

FIG. 18

CMD	CA0	CA1	CA2	CA3	CA4	CA5	CA6
CAS	L	L	H	EDC_EN	WS_WR	WS_RD	WS_FAST
	DC0	DC1	DC2	DC3	NT0->RFU	NT1->RFU	BL

FIG. 19A

MRSET	OP7	OP6	OP5	OP4	OP3	OP2	OP1	OP0
	RFU					ODT		

FIG. 19B

MRSET	OP7	OP6	OP5	OP4	OP3	OP2	OP1	OP0
	RFU		TG-ODT			NT-ODT		

FIG. 20

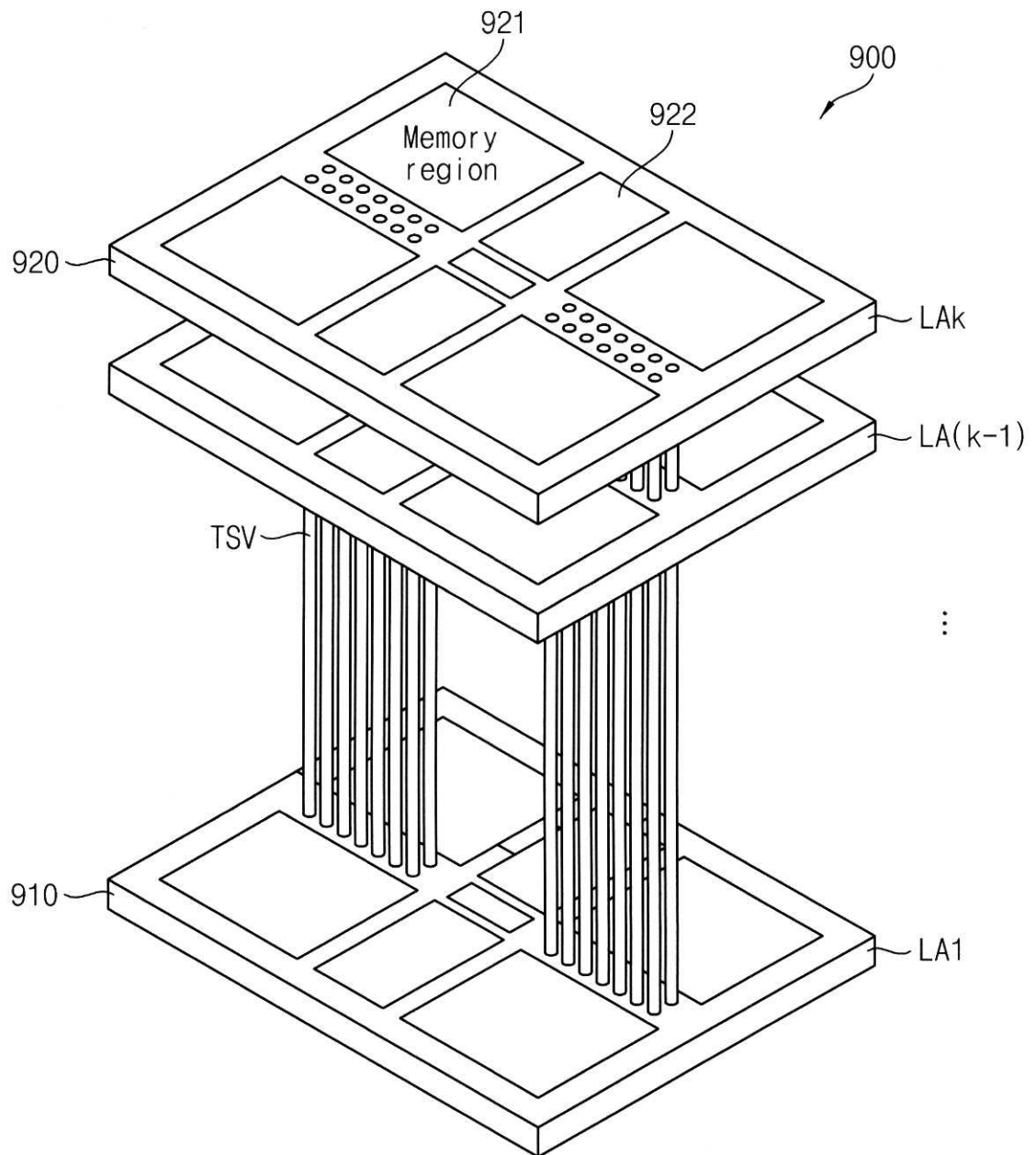


FIG. 21

