

ABSTRACT OF THE DISCLOSURE

SEMICONDUCTOR DEVICE AND ELECTRONIC DEVICE

In a logic circuit including transistors with the same conductivity, a reduction in output voltage is prevented with use of at least three transistors and a capacitor.

- 5 With use of an oxide semiconductor in a semiconductor layer of the transistor, a logic circuit with high output voltage and high withstand voltage is achieved. With use of the logic circuit, a semiconductor device with high output voltage and high withstand voltage is achieved.

Fig. 1A