

ABSTRACT

The disclosure relates to a semiconductor apparatus and a semiconductor system and an operating method of the semiconductor system. A semiconductor system includes a first semiconductor apparatus and a second semiconductor apparatus. The first semiconductor apparatus provides the second semiconductor apparatus with a chip enable signal and a command address signal set, and the second semiconductor apparatus performs an internal operation based on the chip enable signal and the command address signal set. The first semiconductor apparatus provides the second semiconductor apparatus with a selection chip enable command, and the second semiconductor apparatus transmits data to the first semiconductor apparatus or receives the data from the first memory device after receiving the selection chip enable command.

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FIG. 3A