WHAT IS CLAIMED IS

1. An electronic device, comprising:

a substrate;

a plurality of transversal signal lines, disposed on the substrate;

a first insulating layer and a first conducting structure penetrating the first insulating layer, disposed on the substrate;

a first vertical signal line, disposed on the substrate, intersected with the transversal signal lines;

a second vertical signal line, disposed on the substrate, intersected with the transversal signal lines, wherein the second vertical signal line is connected to one of the transversal signal lines through the first conducting structure; and

a first shielding vertical line, disposed on the substrate, wherein an orthogonal projection of the first shielding vertical line on the substrate is located between an orthogonal projection of the first vertical signal line on the substrate and an orthogonal projection of the second vertical signal line on the substrate, wherein the transversal signal lines extend along a first direction, each of the first vertical signal line, the second vertical signal line and the first shielding vertical line extends along a second direction, and the first direction and the second direction are intersected with each other; and

a common electrode line disposed on the substrate, wherein the common electrode line is located between the adjacent two of the transversal signal lines and extends along the first direction, and the first shielding vertical line electrically connects the common electrode line, and wherein the common electrode line includes a first line and a second line, the first line and the second line are located between two directly adjacent transversal signal lines, and each of the first line and the second line extends along the first direction.

2. The electronic device according to claim 1, further comprising a plurality of pixel structures disposed on the substrate, wherein one of the pixel structures is surrounded by adjacent two of the transversal signal lines and the second vertical signal line and comprises a pixel electrode, wherein the pixel electrode is overlapped with the first vertical signal line or the second vertical signal line in a direction perpendicular to the substrate.

3. The electronic device according to claim 2, wherein the pixel electrode is overlapped with the first shielding vertical line.

4. The electronic device according to claim 2, wherein the pixel electrode traverses the second vertical signal line, the pixel electrode has a central trunk portion, and the second vertical signal line is overlapped with the central trunk portion.

5. The electronic device according to claim 1, wherein the first shielding vertical line is a transparent wire.

6. The electronic device according to claim 1, wherein the common electrode line is intersected with the first vertical signal line and the second vertical signal line.

7. The electronic device according to claim 1, wherein the first shielding vertical line and the common electrode line are directly stacked on each other.

8. The electronic device according to claim 1, wherein the first insulating layer is disposed between the first shielding vertical line and the common electrode line, and another first conducting structure electrically connects the first shielding vertical line and the common electrode line.

9. The electronic device according to claim 1, further comprising a plurality of pixel structures disposed on the substrate, wherein one of the pixel structures is located between adjacent two of the transversal signal lines and comprises a pixel electrode, wherein a film layer of the first shielding vertical line is

located between a film layer of the common electrode line and a film layer of the pixel electrode.

10. The electronic device according to claim 1, further comprising a plurality of pixel structures disposed on the substrate, wherein one of the pixel structures is located between adjacent two of the transversal signal lines and comprises a pixel electrode, wherein a film layer of the common electrode line is located between a film layer of the first shielding vertical line and a film layer of the pixel electrode.

11. The electronic device according to claim 1, wherein the first shielding vertical line is overlapped with the first line, the second line, or both the first line and the second line.

12. The electronic device according to claim 1, further comprising a second shielding vertical line, wherein the first vertical signal line is located between the first shielding vertical line and the second shielding vertical line.

13. The electronic device according to claim 12, further comprising a third vertical signal line, wherein the third vertical signal line is located between the first vertical signal line and the second shielding vertical line.

14. The electronic device according to claim 13, further comprising a fourth vertical signal line, wherein orthogonal projections of the first vertical signal line and the third vertical signal line on the substrate are located between the orthogonal projection of the second vertical signal line on the substrate and an orthogonal projection of the fourth vertical signal line on the substrate.

15. The electronic device according to claim 1, further comprising: a shielding electrode, connected to the first shielding vertical line.

16. The electronic device according to claim 1, wherein the first shielding vertical line is completely located between adjacent two of the transversal signal lines.

17. The electronic device according to claim 1, further comprising:

a plurality of pixel structures disposed on the substrate, wherein one of the pixel structures comprises a pixel electrode, wherein the orthogonal projection of the first shielding vertical line on the substrate is outside an orthogonal projection of the pixel electrode on the substrate.

18. The electronic device according to claim 1, further comprising a plurality of pixel structures and a third vertical signal line, wherein the pixel structures are arranged on the substrate in an array, the second vertical signal line is located between the third vertical signal line and the first vertical signal line, and the first vertical signal line, the second vertical signal line, and the third vertical signal line are located between two adjacent rows of the pixel structures.

19. The electronic device according to claim 18, further comprising a second shielding vertical line disposed on the substrate, wherein an orthogonal projection of the second shielding vertical line on the substrate is located between the orthogonal projection of the second vertical signal line on the substrate and an orthogonal projection of the third vertical signal line on the substrate.

20. The electronic device according to claim 1, further comprising a plurality of pixel structures disposed on the substrate, wherein one of the pixel structures is located between adjacent two of the transversal signal lines and comprises a pixel electrode, wherein a film layer of the first shielding vertical line is located below a film layer of the pixel electrode.