METHOD OF AND APPARATUS FOR CONTROLLING SUPPLY OF POWER TO A PERIPHERAL DEVICE IN A COMPUTER SYSTEM

Abstract

A peripheral device (104), such as a data storage device, is employed in a computer system (100) having one or more computers (102). The computer system (100) includes an interface architecture, such as Fibre channel or Serial Storage Architecture (SSA), providing a continuously clocked serial link. The data storage device (104) includes a power supply (112) and data storage components (110), such as disk drives, carried in a housing. One or more data communication interfaces (114), each of which is adapted for coupling with a computer (102) through conductors, are carried on the housing and coupled to the data storage components (110). The data storage components (110) have a substantially powered up state and a substantially powered down state which is entered in response to a power down signal. Detector circuitry is operative to detect a clock signal loss at each one of the data communicaiton interfaces (114) and to produce the power down signal responsive to the detection.

Fig1.

This Sheet Has Been Replaced

