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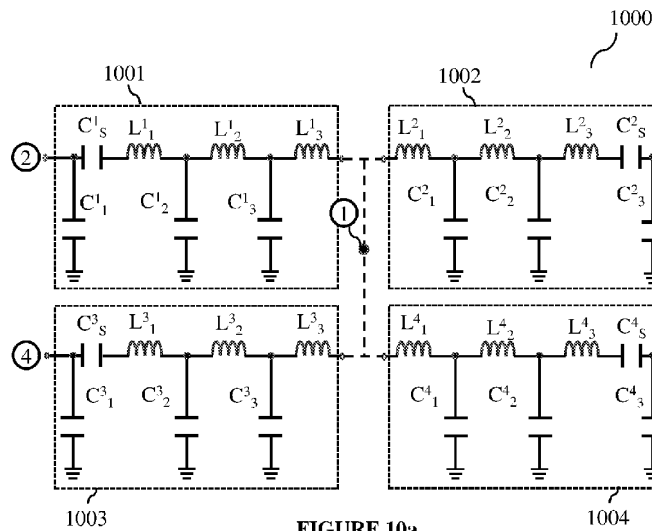
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(54) **Title:** A MULTIPLEXER FOR VERY HIGH FREQUENCY APPLICATIONS



(57) **Abstract:** This document describes a multiplexer for very high frequency applications. The multiplexer comprises two or more arms whereby each arm of the multiplexer comprises a bandpass filter that is implemented using a multi-stage LC ladder circuit that connects an input terminal to a first output terminal, and a capacitor connected in series between a series inductor and a parallel capacitor of a final stage of the first multi-stage LC ladder circuit on a path connecting the input terminal and the first output terminal. The capacitor connected in series at the final stage of the first multi-stage LC ladder circuit has a resonant frequency located within the passband of the bandpass filter.

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