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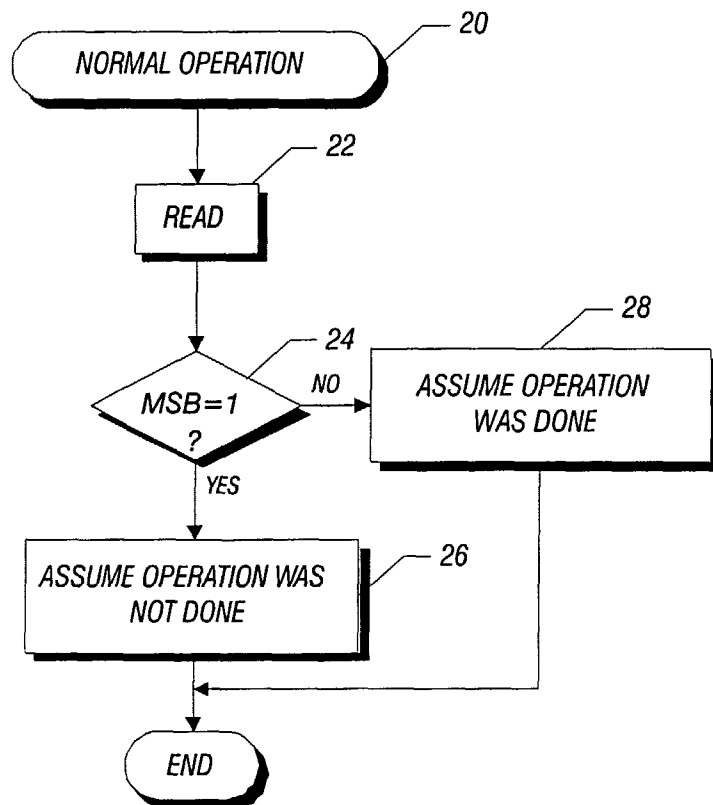
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(54) Title: USING MULTIPLE STATUS BITS PER CELL FOR HANDLING POWER FAILURES DURING WRITE OPERATIONS



(57) Abstract: A multi-level cell memory (30) may include at least two status bits, the status bits may be examined to determine whether or not a write operation was successful after a power loss occurs.

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