CONSTRAINTS FOR VIDEO CODING AND DECODING

CROSS REFERENCE TO RELATED APPLICATIONS

[001] ~~Under the applicable patent law and/or rules pursuant to the Paris Convention, this application is made to timely~~This application is a national phase of International Patent Application No. PCT/CN2021/071008, filed on January 11, 2021, which claims the priority to and benefits of International Patent Application No. PCT/CN2020/071620, filed on January 12, 2020. ~~For all purposes under the law, t~~The entire disclosure of the aforementioned application is incorporated by reference as part of the disclosure of this application.

TECHNICAL FIELD

[002] This document is related to video and image coding and decoding technologies.

BACKGROUND

[003] Digital video accounts for the largest bandwidth use on the internet and other digital communication networks. As the number of connected user devices capable of receiving and displaying video increases, it is expected that the bandwidth demand for digital video usage will continue to grow.

SUMMARY

[004] The disclosed techniques may be used by video or image decoder or encoder embodiments for in which sub-picture based coding or decoding is performed.

[005] In one example aspect a method of video processing is disclosed. The method includes performing a conversion between a block of a video and a bitstream of the video. The bitstream conforms to a formatting rule specifying that a size of a merge estimation region (MER) is indicated in the bitstream. The size of the MER is based on a dimension of a video unit, and the MER comprises a region used for deriving a motion candidate for the conversion.

[006] In another example aspect a method of video processing is disclosed. The method includes performing a conversion between a block of a video and a bitstream of the video in a palette coding mode in which a palette of representative sample values is used for coding the block of video in the bitstream. A maximum number of palette size or palette predictor size used in the palette mode is restricted to m×N, m and N being positive integers.