

FIG. 1

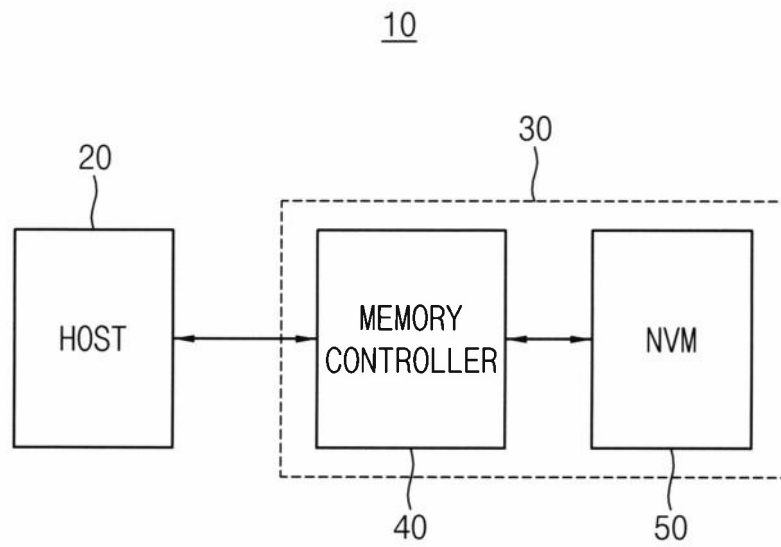


FIG. 2

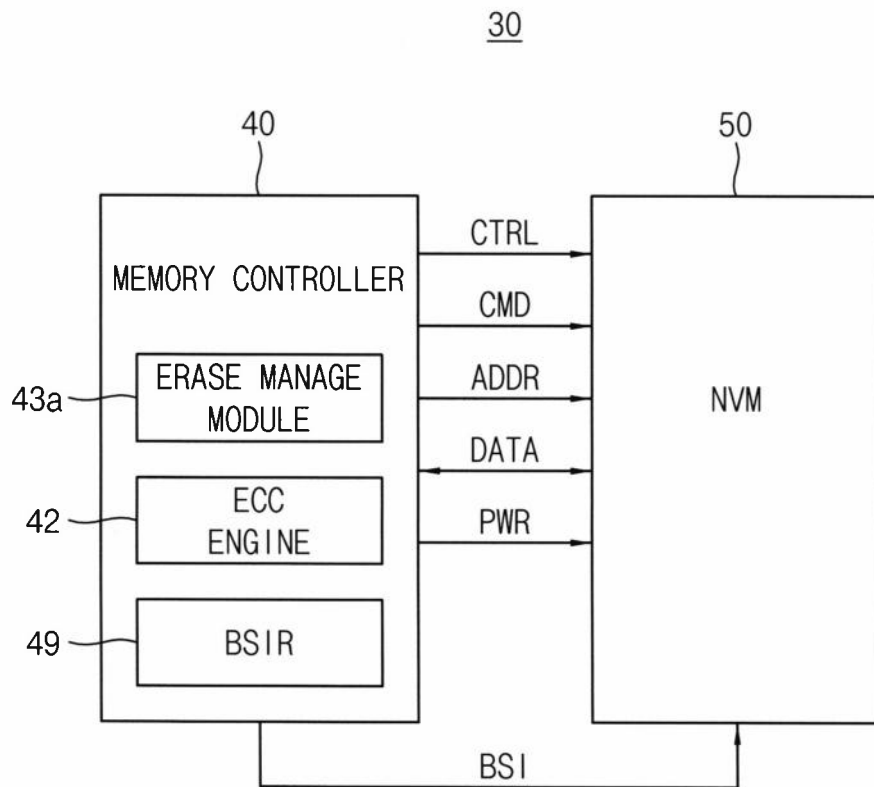


FIG. 3

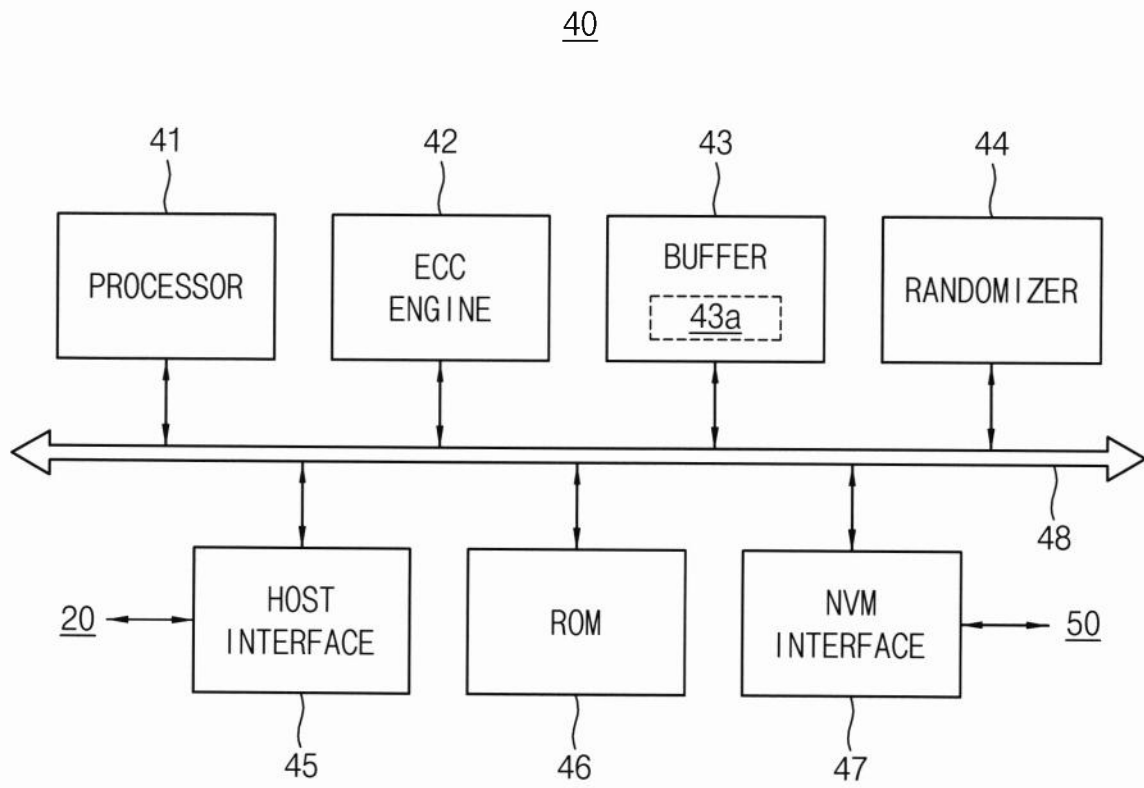


FIG. 4

50

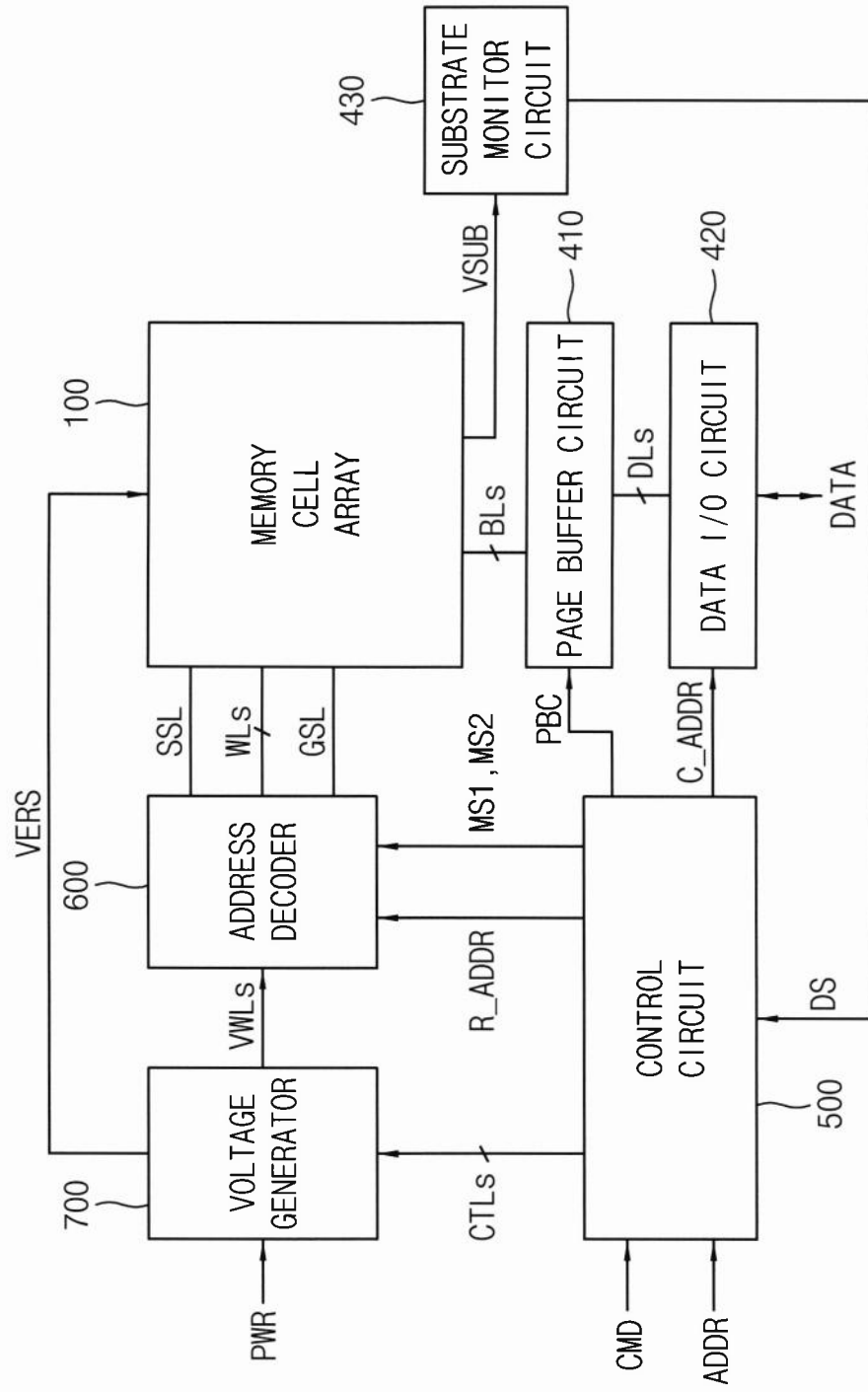


FIG. 5

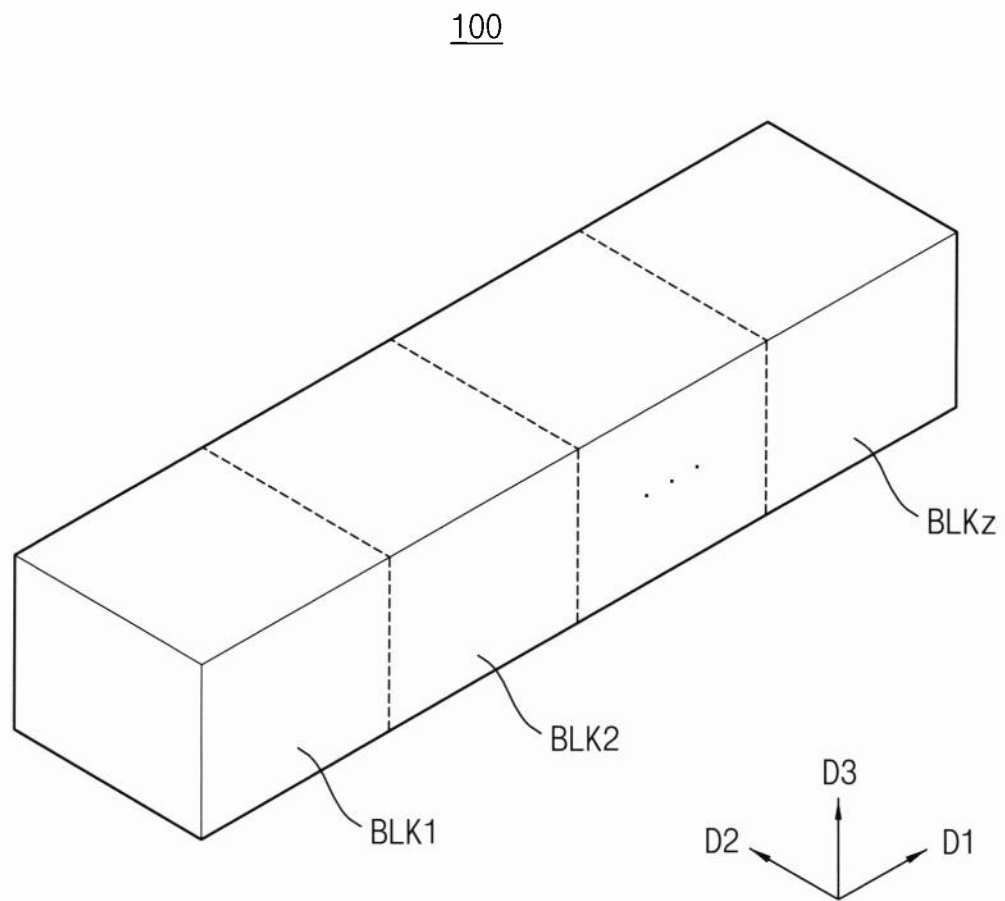


FIG. 6

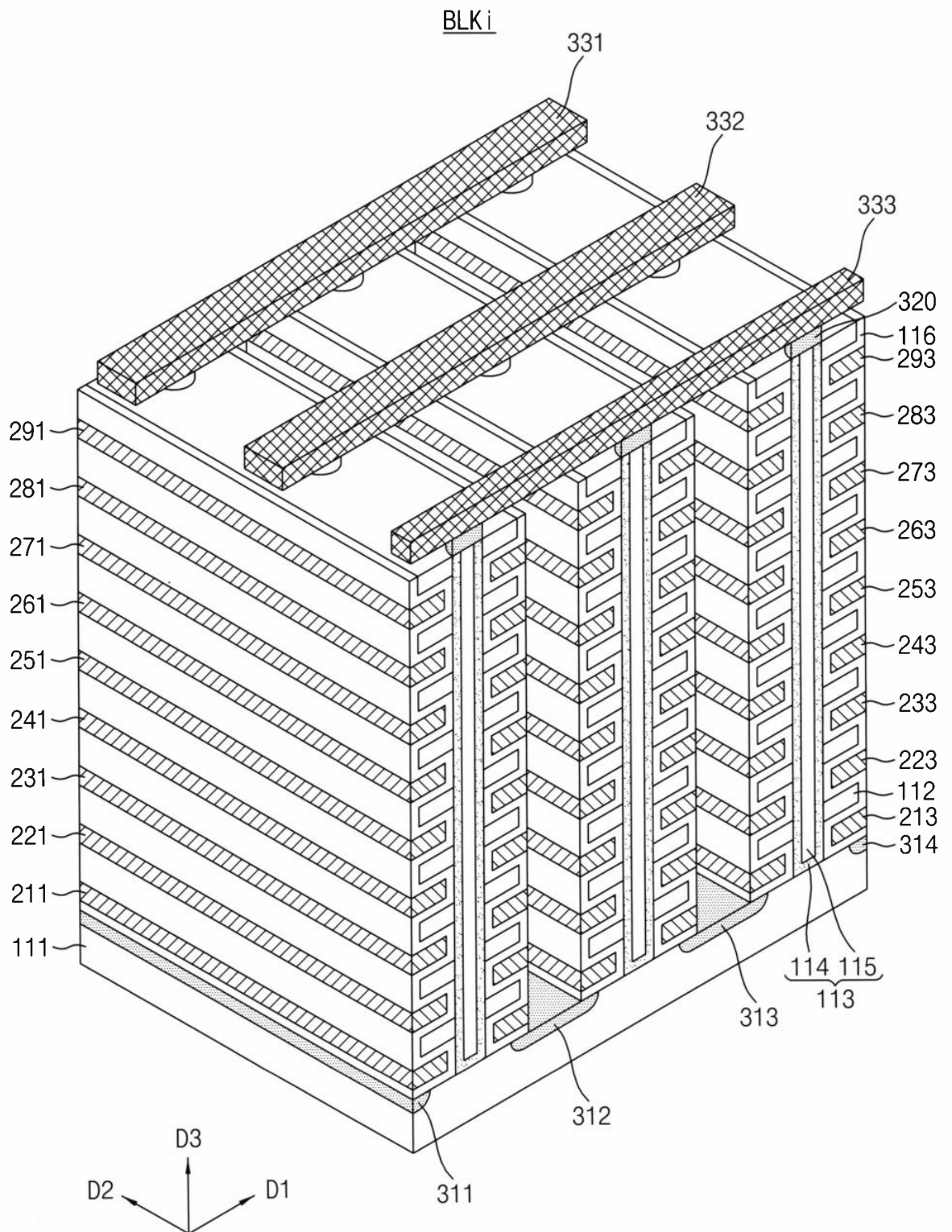


FIG. 7

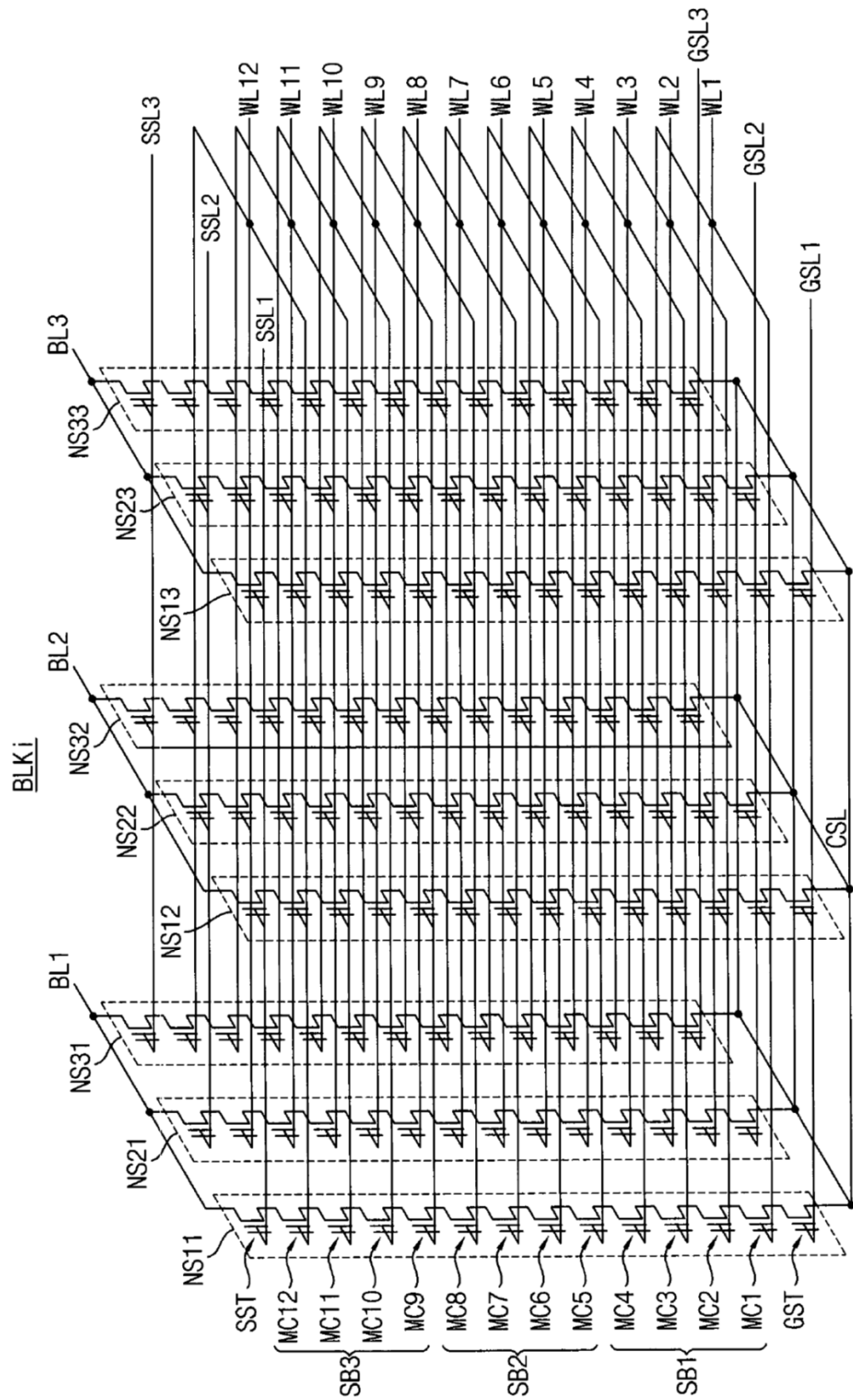


FIG. 8

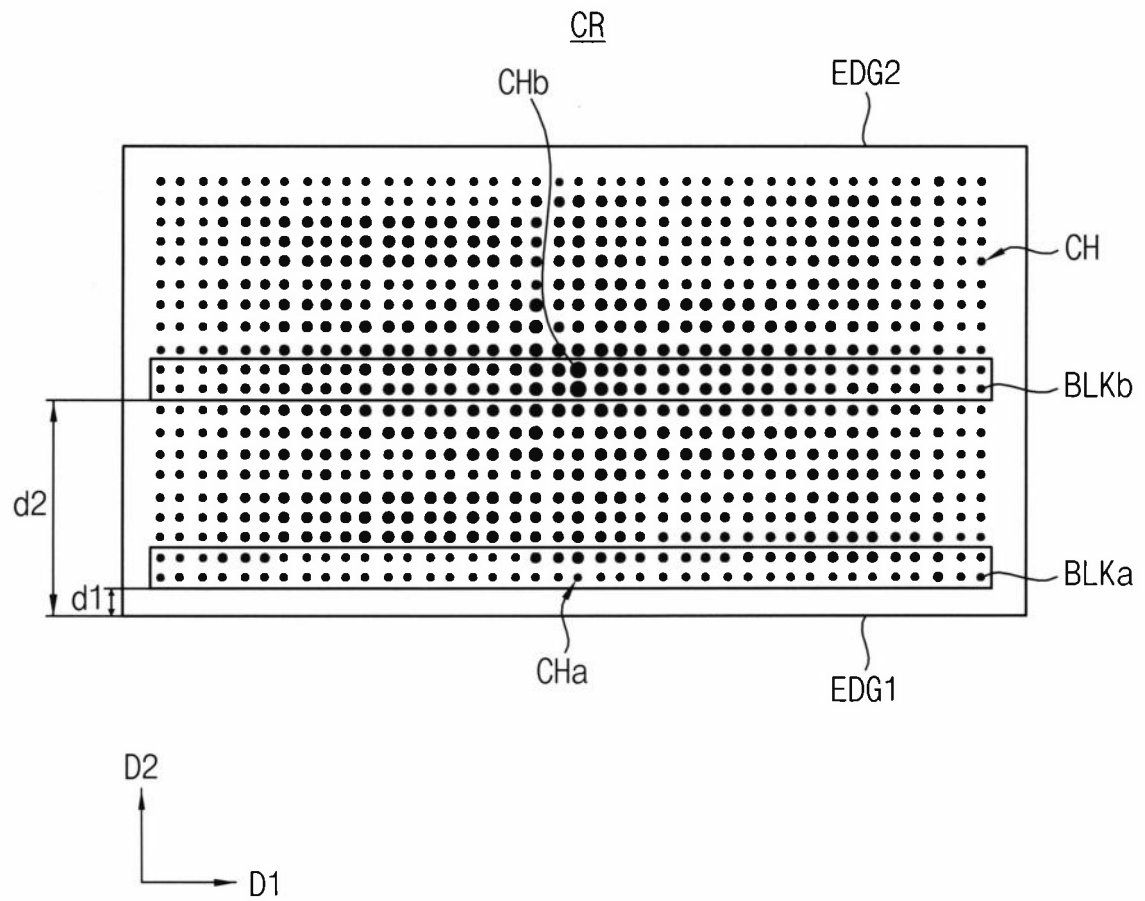


FIG. 9A

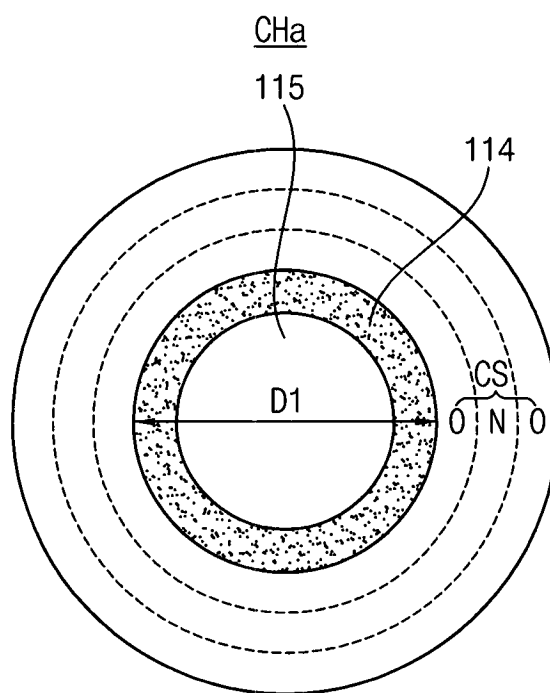


FIG. 9B

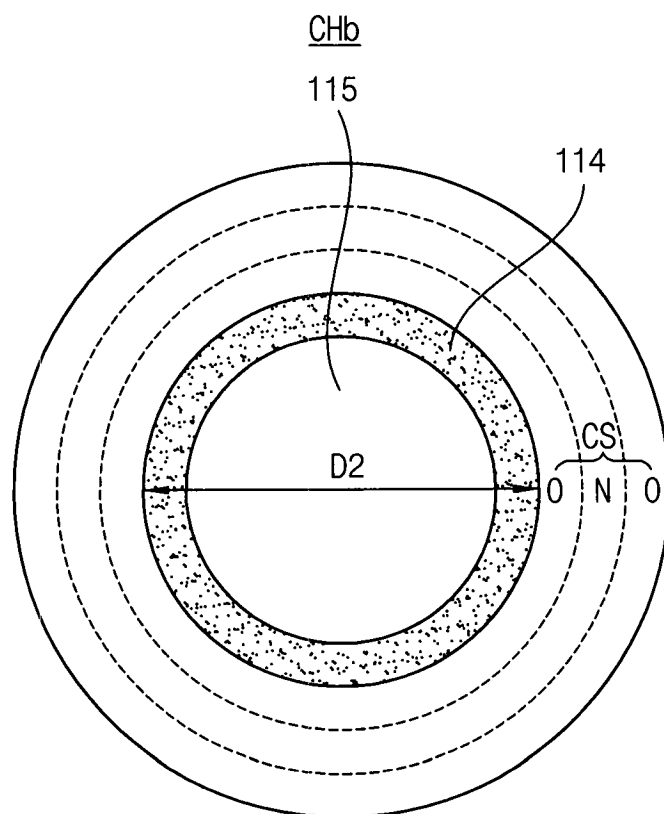


FIG. 10

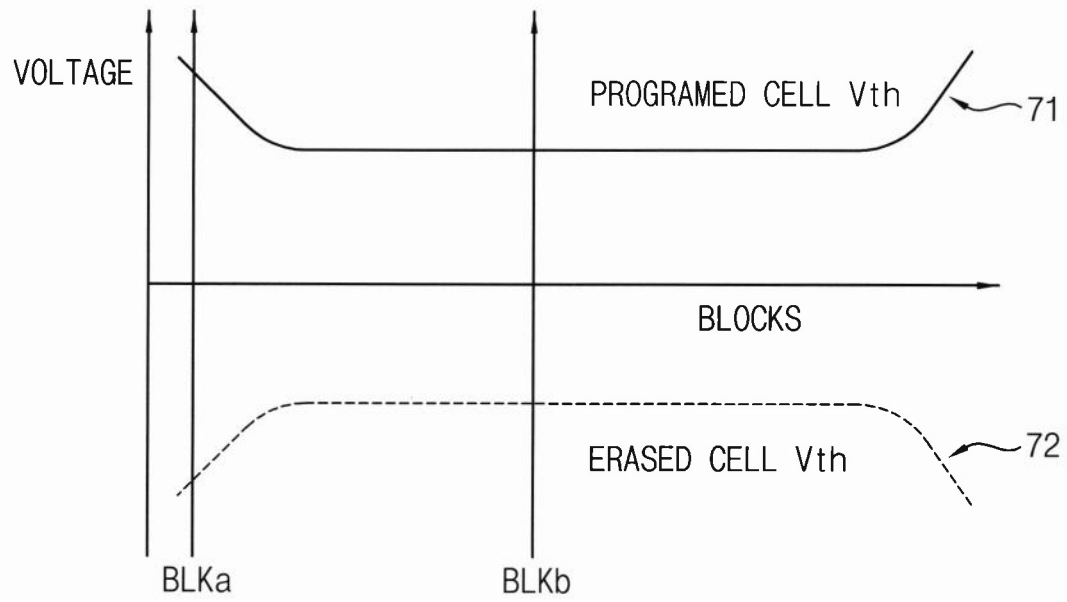


FIG. 11

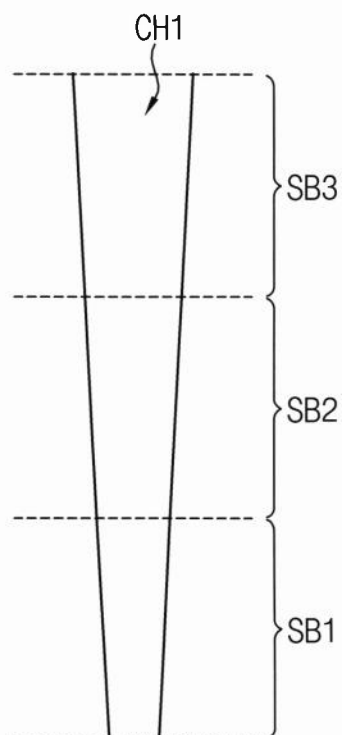


FIG. 12

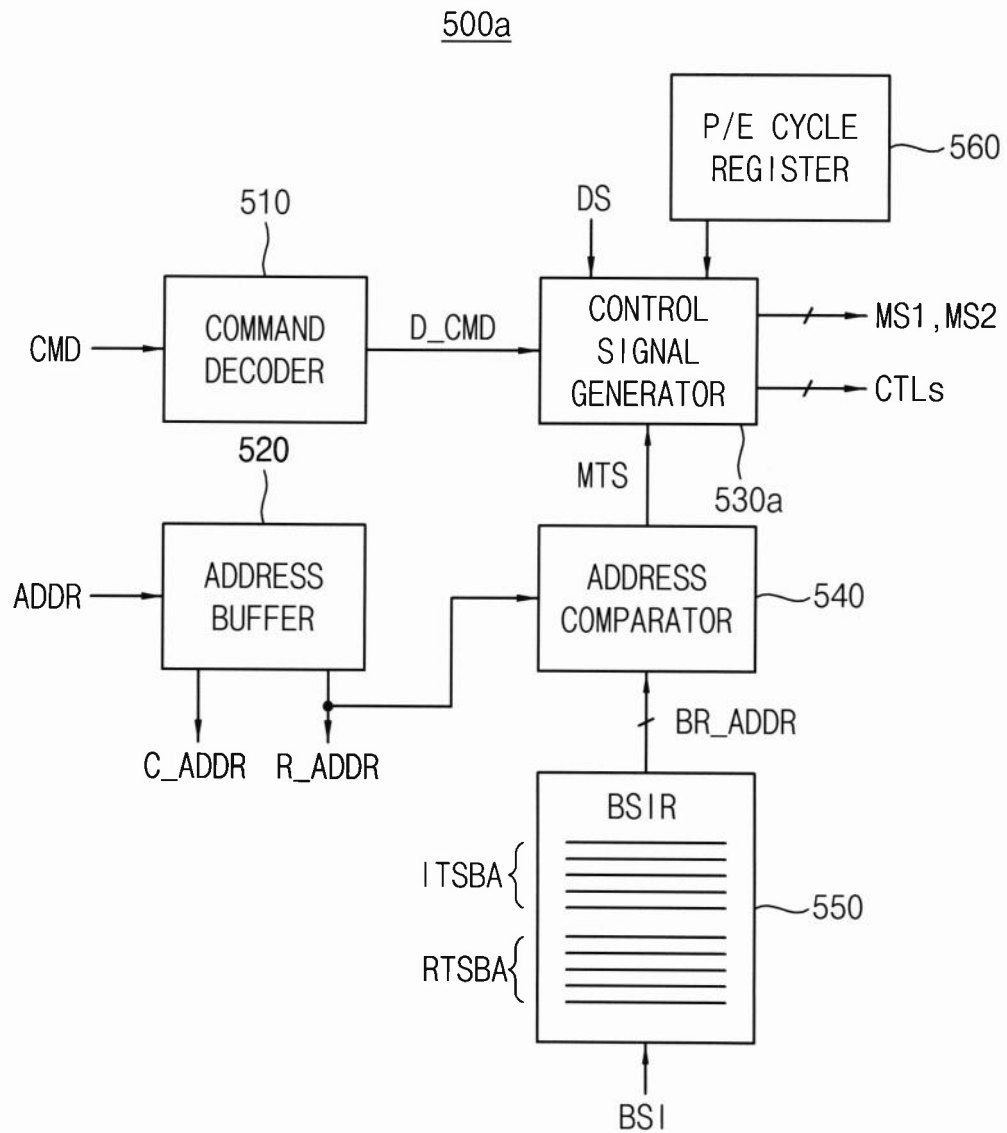


FIG. 13

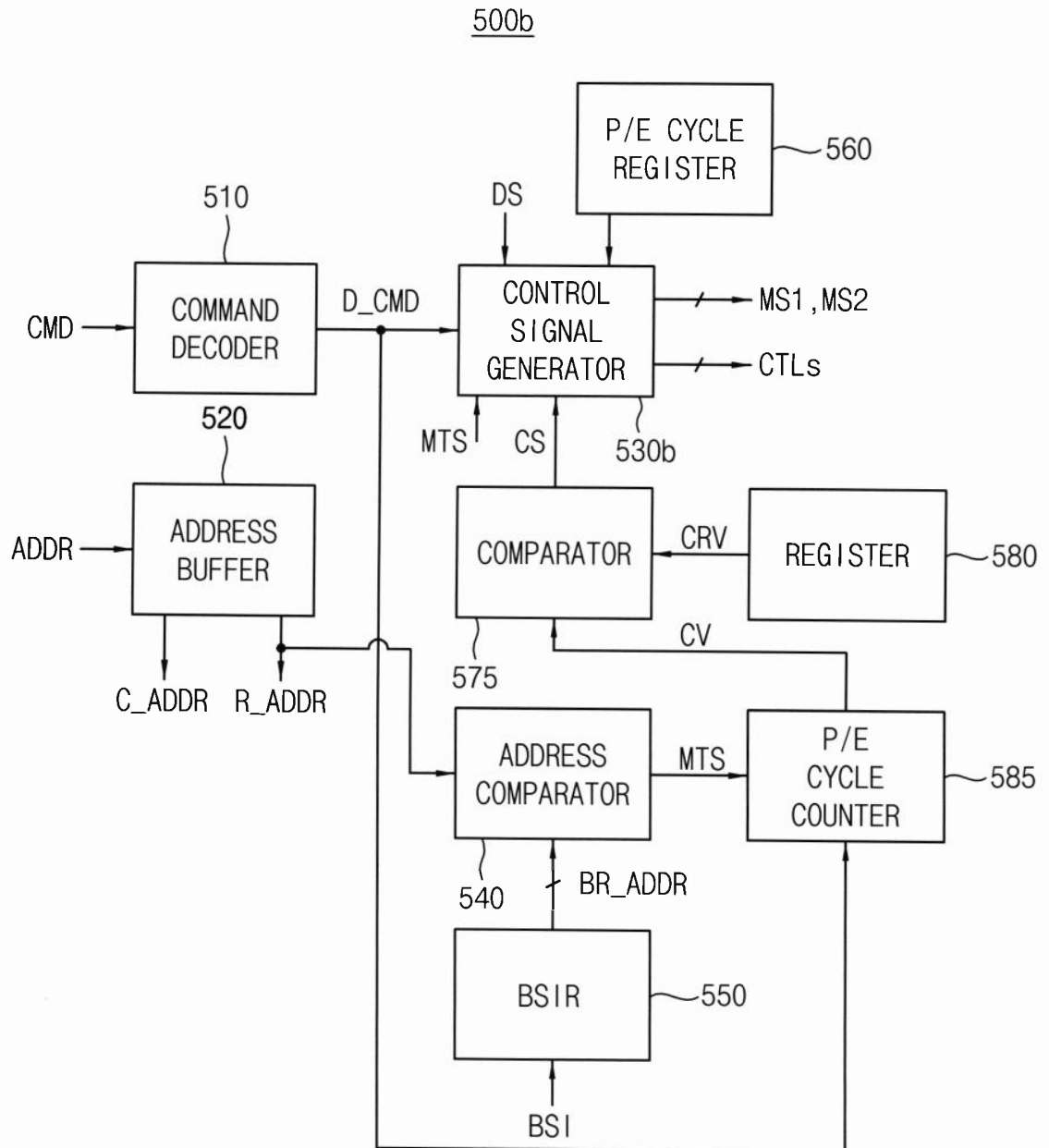


FIG. 14

700

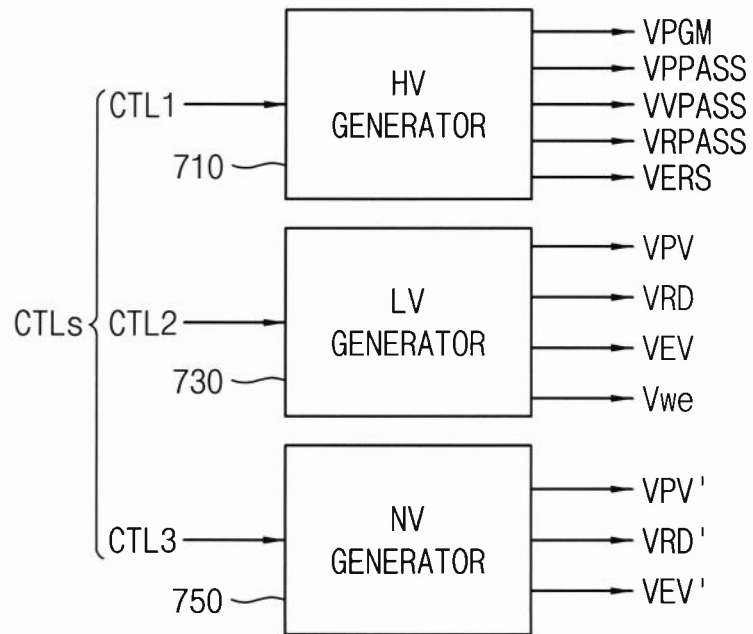


FIG. 15

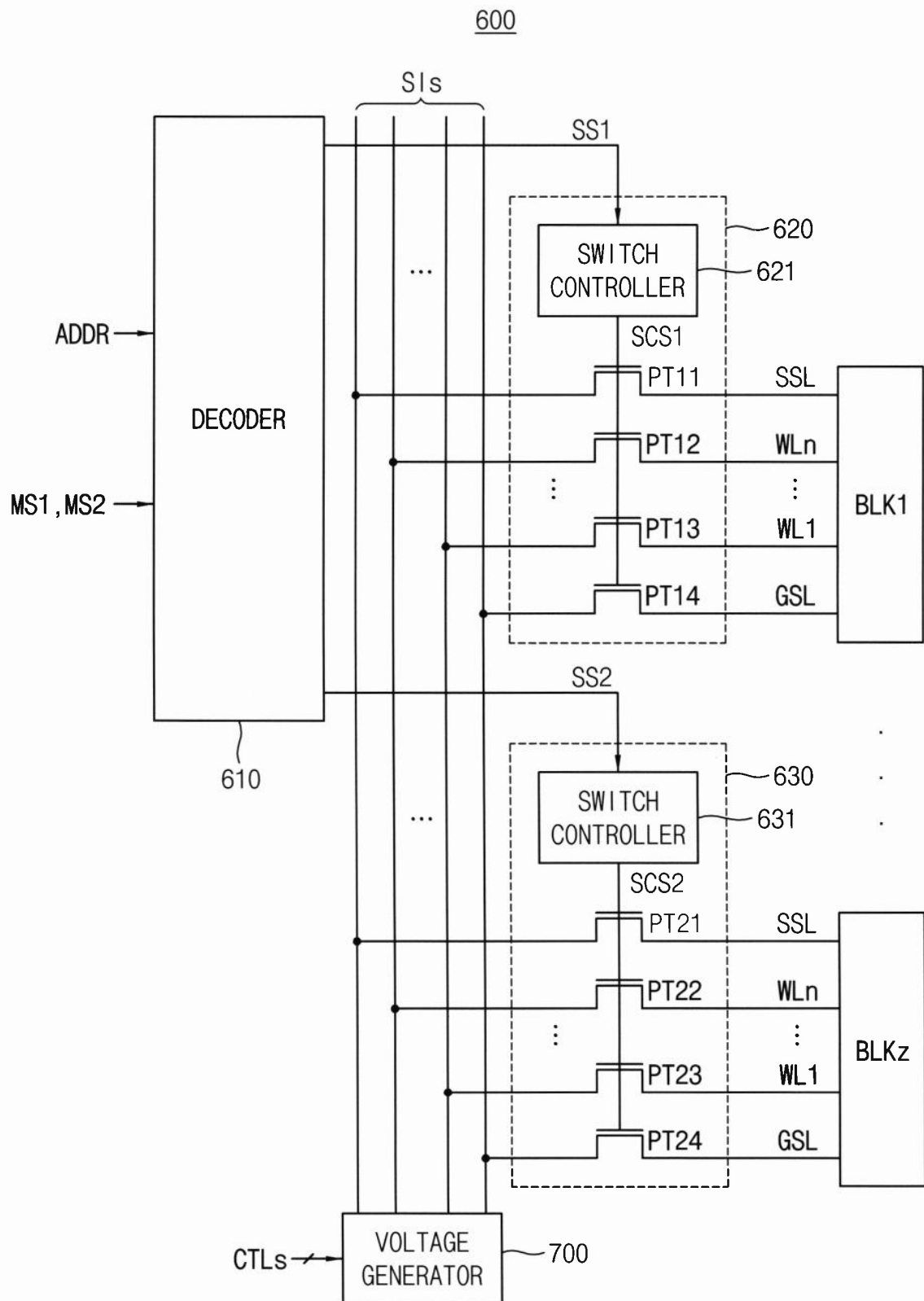


FIG. 16

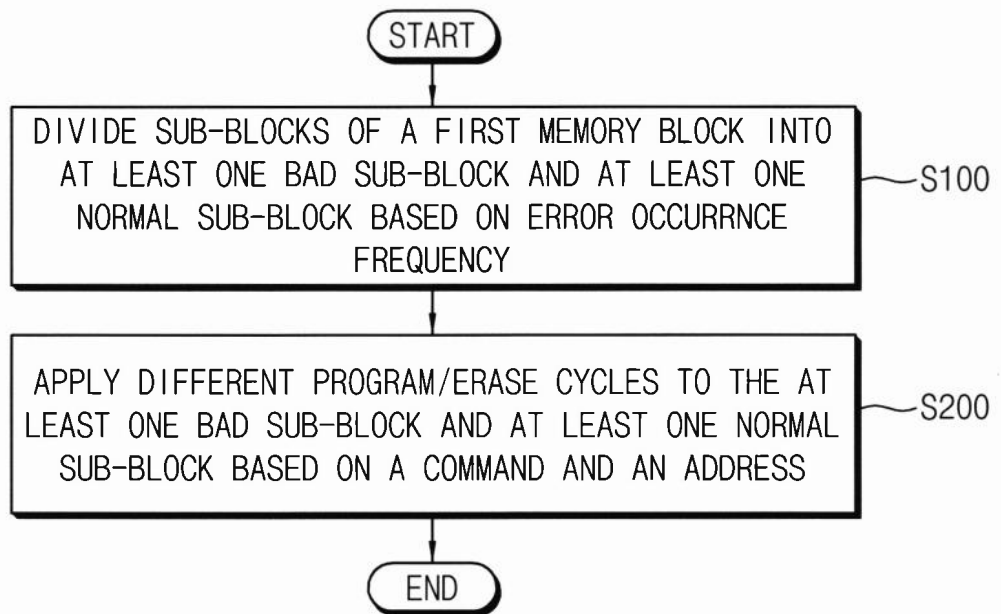


FIG. 17

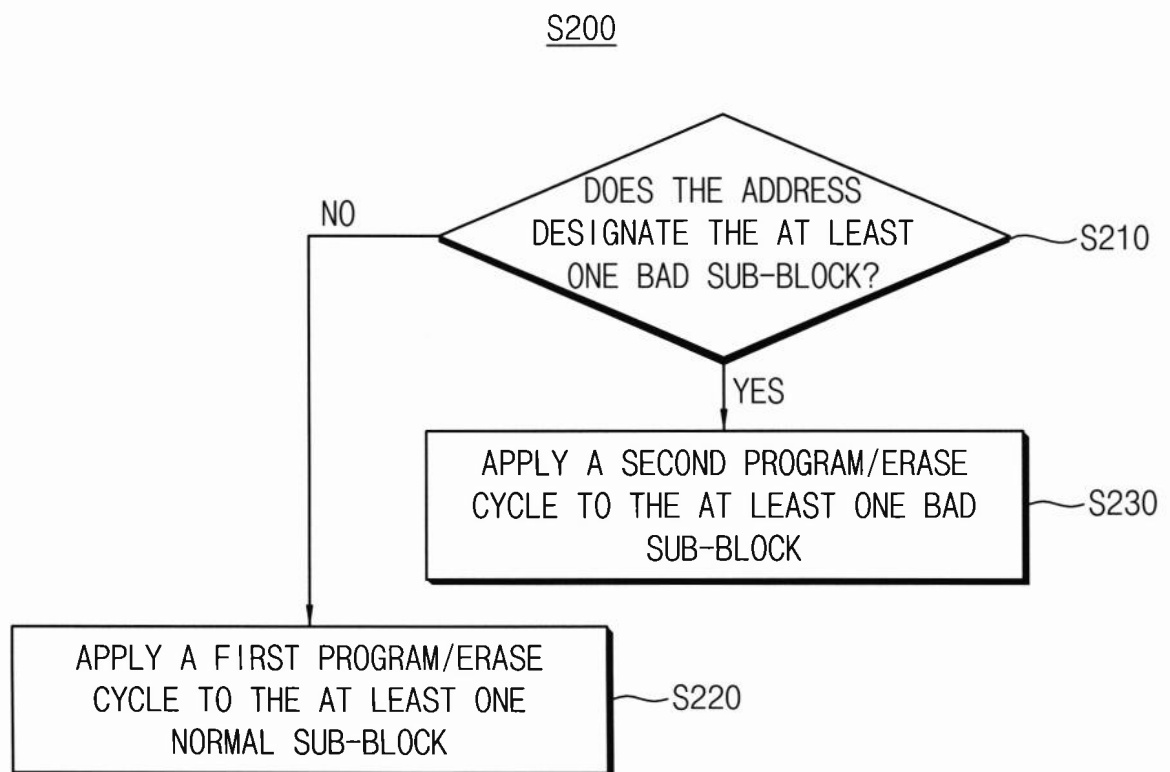


FIG. 18

S230

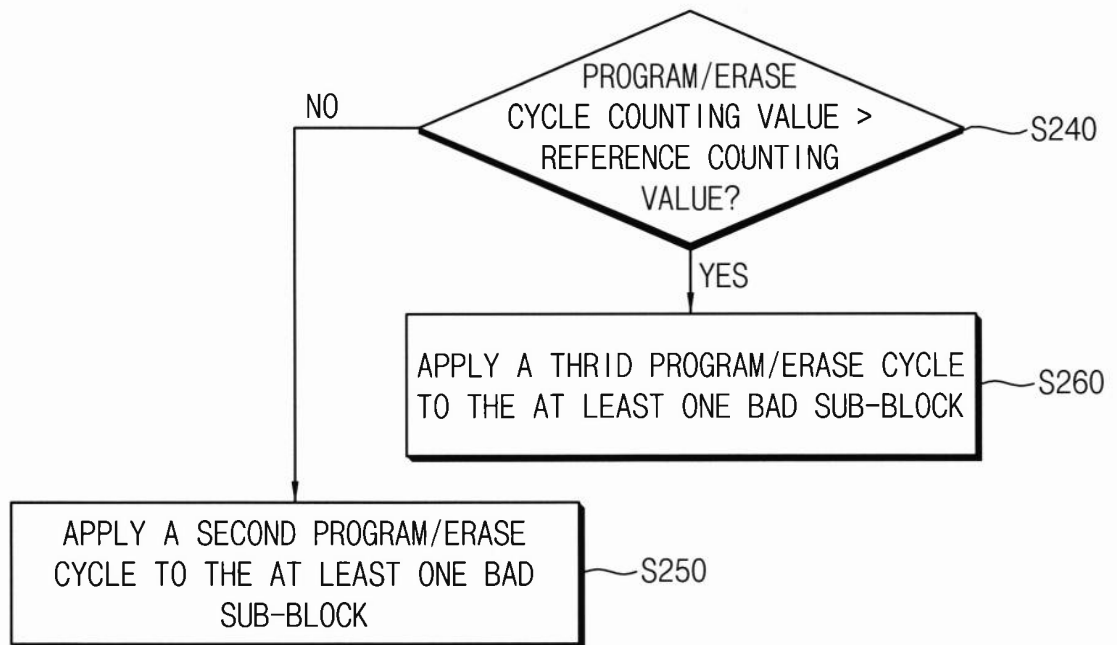


FIG. 19A

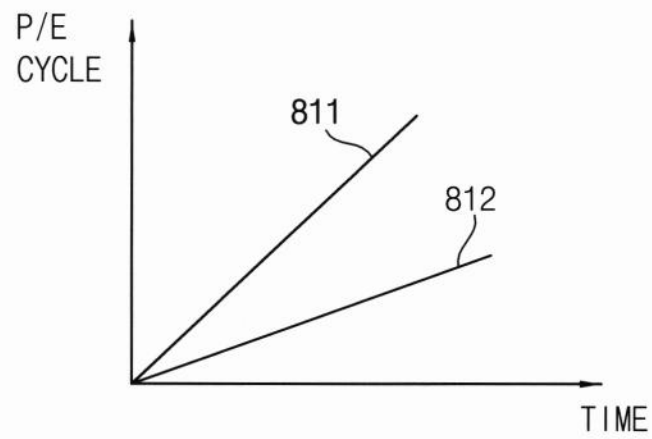


FIG. 19B

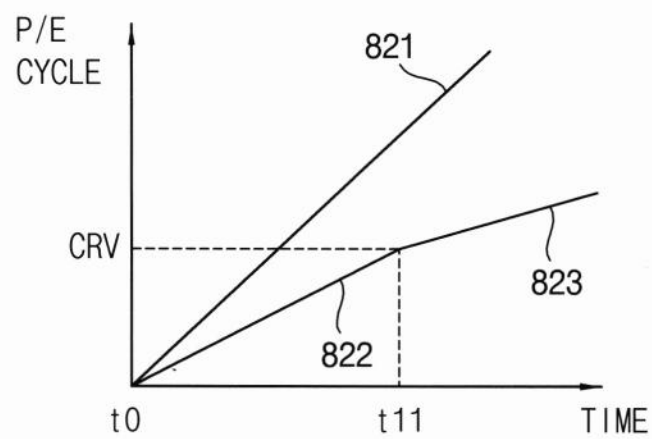


FIG. 20

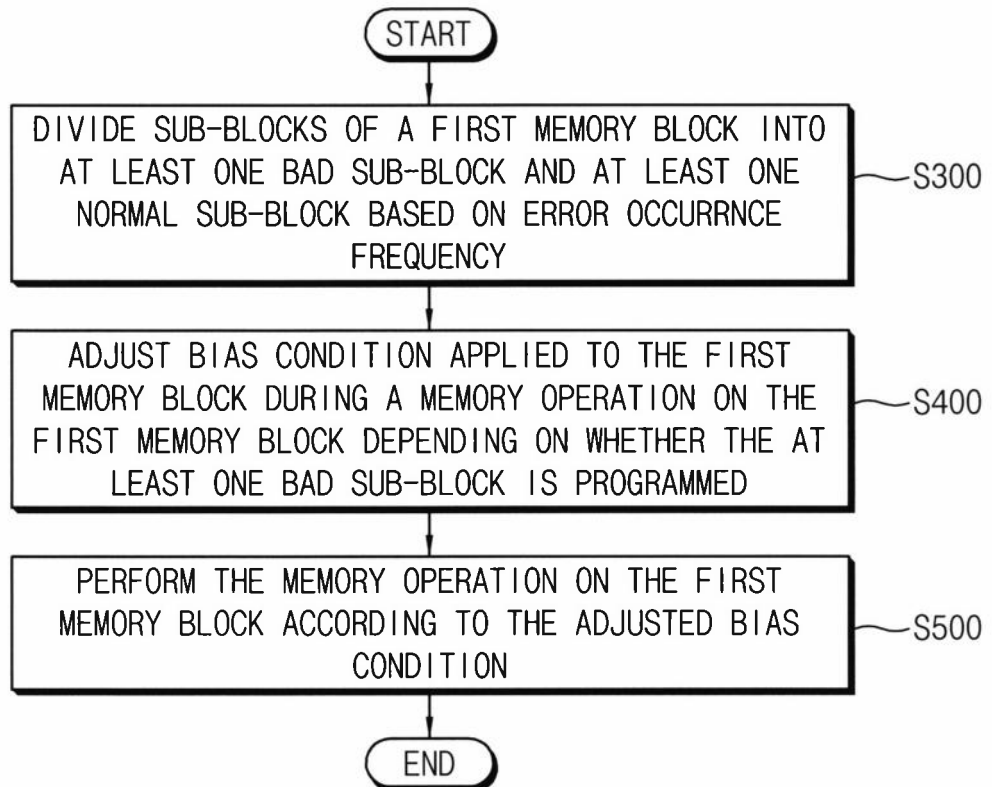


FIG. 21A

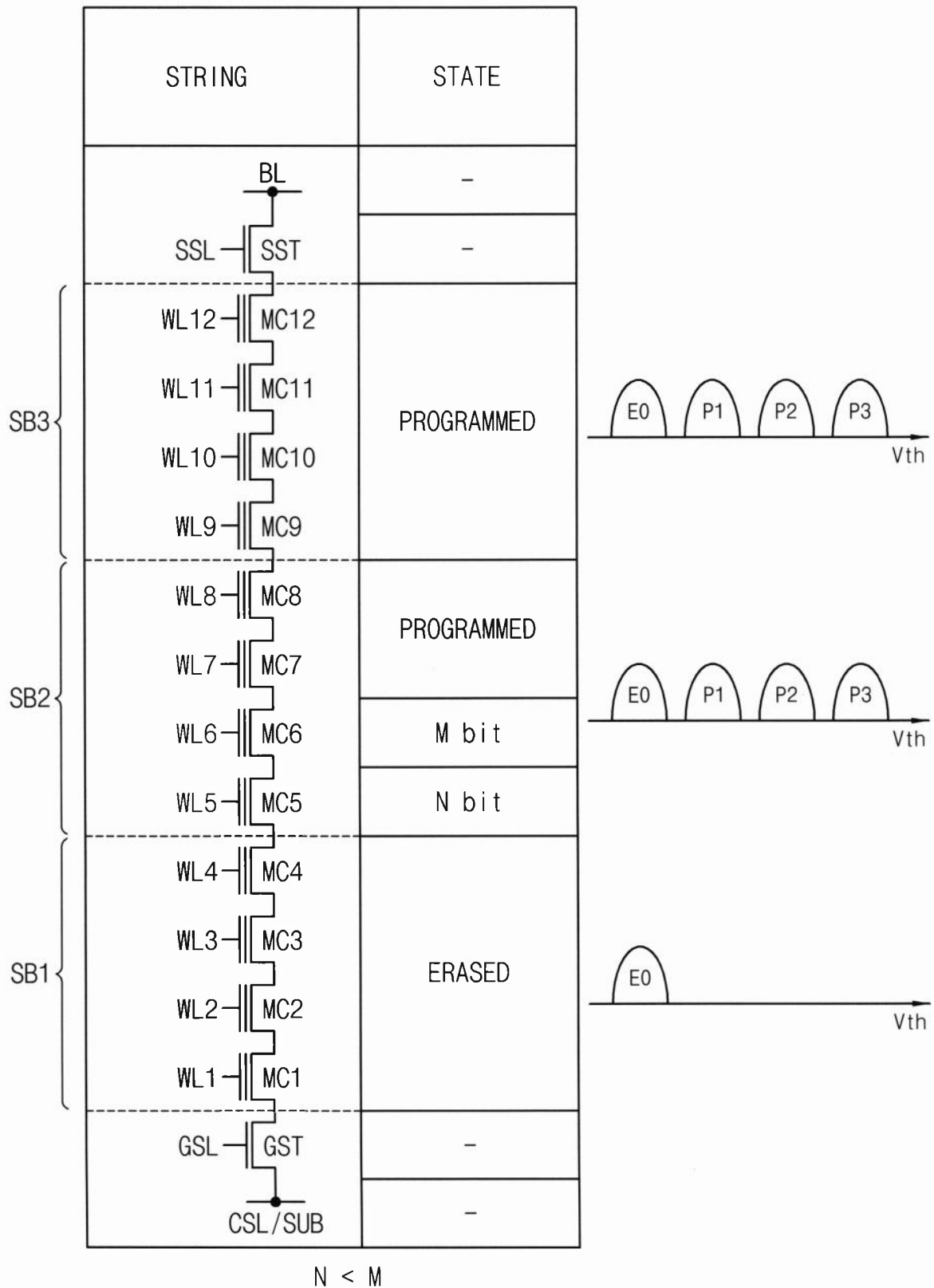


FIG. 21B

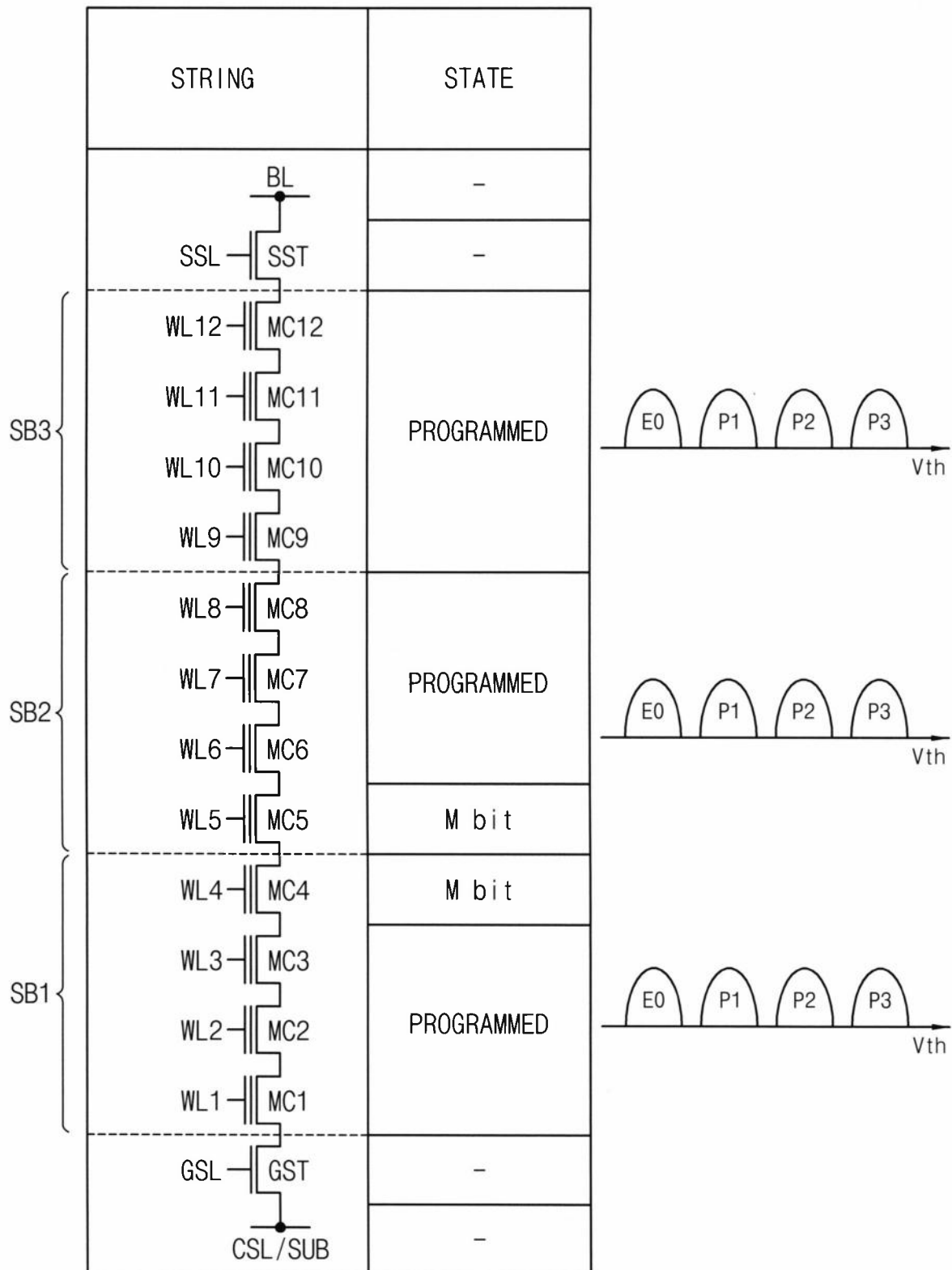


FIG. 22A

STRING		ERASE BIAS	
		MB1 ERASE	
BL SSL — SST SB3 { WL12 — MC12 WL11 — MC11 WL10 — MC10 WL9 — MC9 SB2 { WL8 — MC8 WL7 — MC7 WL6 — MC6 WL5 — MC5 SB1 { WL4 — MC4 WL3 — MC3 WL2 — MC2 WL1 — MC1 GSL — GST CSL/SUB		FLOAT	
		FLOAT	
		Vwe1	
		Vwe1	
		Vwe1	
		Vwe1	
		Vwe1	
		Vwe1	
		Vwe1	
		Vwe2	
		Vwe2	
		Vwe2	
		Vwe2	
		FLOAT	
		VERS	

$$V_{we1} < V_{we2} < V_{ERS}$$

FIG. 22B

	STRING	ERASE BIAS
		MB1 ERASE
SB3 SB2 SB1	BL SSL—SST	FLOAT
		FLOAT
	WL12—MC12	Vwe1
	WL11—MC11	Vwe1
	WL10—MC10	Vwe1
	WL9—MC9	Vwe1
	WL8—MC8	Vwe1
	WL7—MC7	Vwe1
	WL6—MC6	Vwe1
	WL5—MC5	Vwe1
	WL4—MC4	Vwe1→FLOAT AT T1
	WL3—MC3	Vwe1→FLOAT AT T1
	WL2—MC2	Vwe1→FLOAT AT T1
	WL1—MC1	Vwe1→FLOAT AT T1
	GSL—GST	FLOAT
	CSL/SUB	VERS

$V_{we1} < V_{ERS}$

FIG. 23A

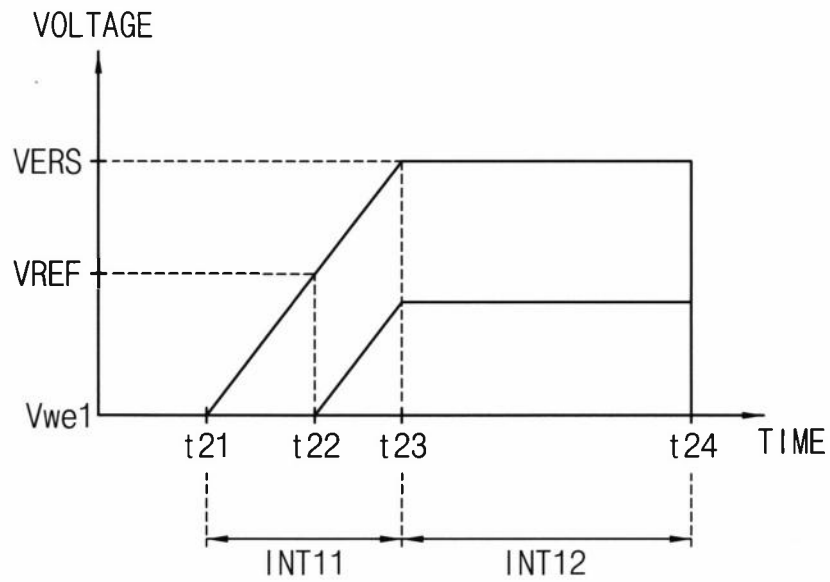


FIG. 23B

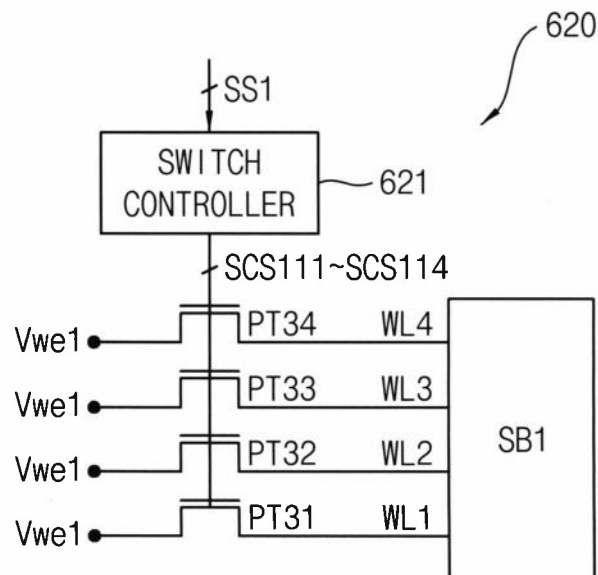


FIG. 24

STRING		ERASE BIAS	
		MB1 ERASE	
BL SSL—SST WL12—MC12 WL11—MC11 WL10—MC10 WL9—MC9 WL8—MC8 WL7—MC7 WL6—MC6 WL5—MC5 WL4—MC4 WL3—MC3 WL2—MC2 WL1—MC1 GSL—GST CSL/SUB SB3 SB2 SB1	FLOAT	FLOAT	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	Vwe1	Vwe1	
	FLOAT		VERS1→VERS2 AT T2

$$V_{we1} < VERS1 < VERS2$$

FIG. 25

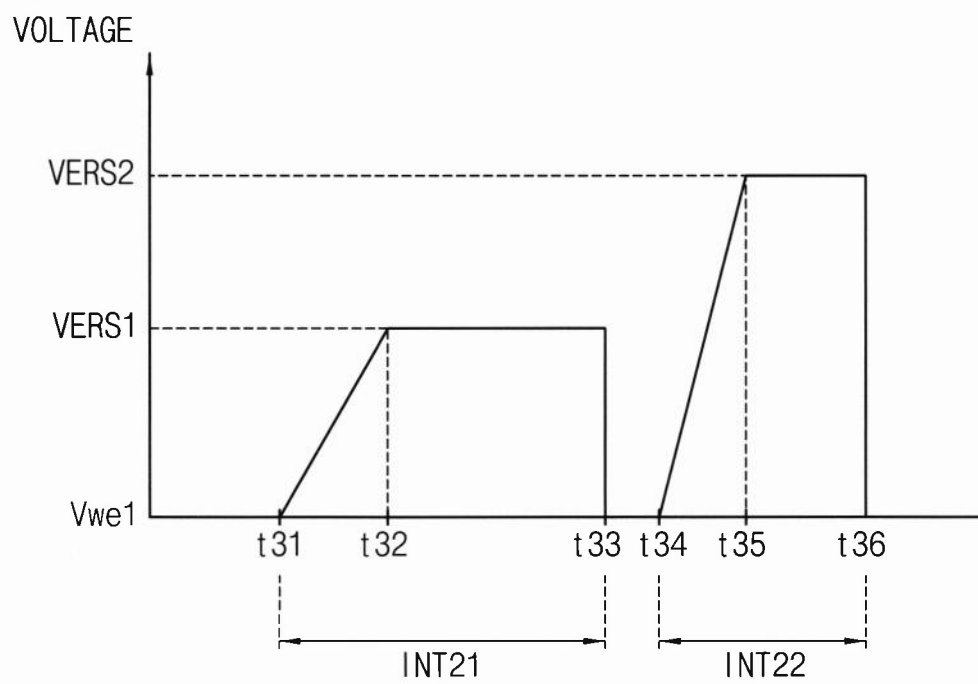


FIG. 26A

STRING		READ BIAS
		WL6 READ
BL SSL — SST SB3 WL12 — MC12 WL11 — MC11 WL10 — MC10 WL9 — MC9 SB2 WL8 — MC8 WL7 — MC7 WL6 — MC6 WL5 — MC5 SB1 WL4 — MC4 WL3 — MC3 WL2 — MC2 WL1 — MC1 GSL — GST CSL / SUB		-
		VRPASS11
		VRPASS11
		VRPASS11
		VRPASS11
		VRPASS11
		VRPASS11
		VRD
		VRPASS11
		VRPASS12
		VRPASS12
		VRPASS12
		VRPASS12
		VRPASS11
		GND

VRPASS12 < VRPASS11

FIG. 26B

STRING		READ BIAS
		WL6 READ
		-
		VRPASS21
SB3	WL12-MC12	VRPASS21
	WL11-MC11	VRPASS21
	WL10-MC10	VRPASS21
	WL9-MC9	VRPASS21
SB2	WL8-MC8	VRPASS21
	WL7-MC7	VRPASS21
	WL6-MC6	VRD
	WL5-MC5	VRPASS21
SB1	WL4-MC4	VRPASS22
	WL3-MC3	VRPASS22
	WL2-MC2	VRPASS22
	WL1-MC1	VRPASS22
GSL-GST		VRPASS21
CSL/SUB		GND

VRPASS22 < VRPASS21

FIG. 27

