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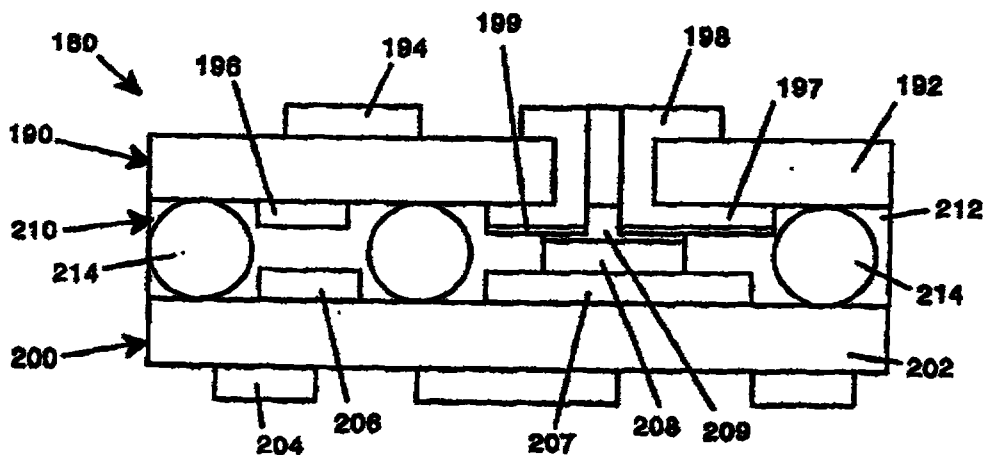
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(71) Applicant (for all designated States except US): LITRONIC INDUSTRIES, INC. [US/US]; 2950 Redhill Avenue, Costa Mesa, CA 92626 (US).			
(72) Inventor; and (75) Inventor/Applicant (for US only): POMMER, Richard, J. [US/US]; 32266 Via Del Nido, Trabuco Canyon, CA 92679 (US).			
(74) Agents: NOBLES, Kimberly, G. et al.; Blakely, Sokoloff, Taylor & Zafman, 7th floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025-1026 (US).			

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(54) Title: PRINTED CIRCUIT MULTILAYER ASSEMBLY AND METHOD OF MANUFACTURE THEREFOR



(57) Abstract

A printed circuit assembly and method of making the same utilize in one embodiment an adhesive layer including a plurality of non-conductive "gauge particles" disposed within a non-conductive adhesive. When the adhesive layer is disposed between opposing printed circuit layers (be they insulating substrates, conductive layers, or other layers), individual gauge particles (44, 174) are interposed or sandwiched at various points between the layers such that the diameters of the particles control the layer separation throughout overlapping areas of thereof, thereby permitting careful control over layer separation. A printed circuit assembly and method of making the same utilize in another embodiment an interlayer interconnecting technology incorporating conductive posts that are deposited on one of a pair of contact pads formed on opposing printed circuit boards and thereafter bonded to the other in the pair of contact pads during lamination. Fusible material may be utilized in the conductive posts (208, 209) to facilitate mechanical bonding to a contact pad, and the posts project through a dielectric layer disposed between the printed circuit boards, thereby forming the electrical connections between the boards at discrete locations.