FIG. 1

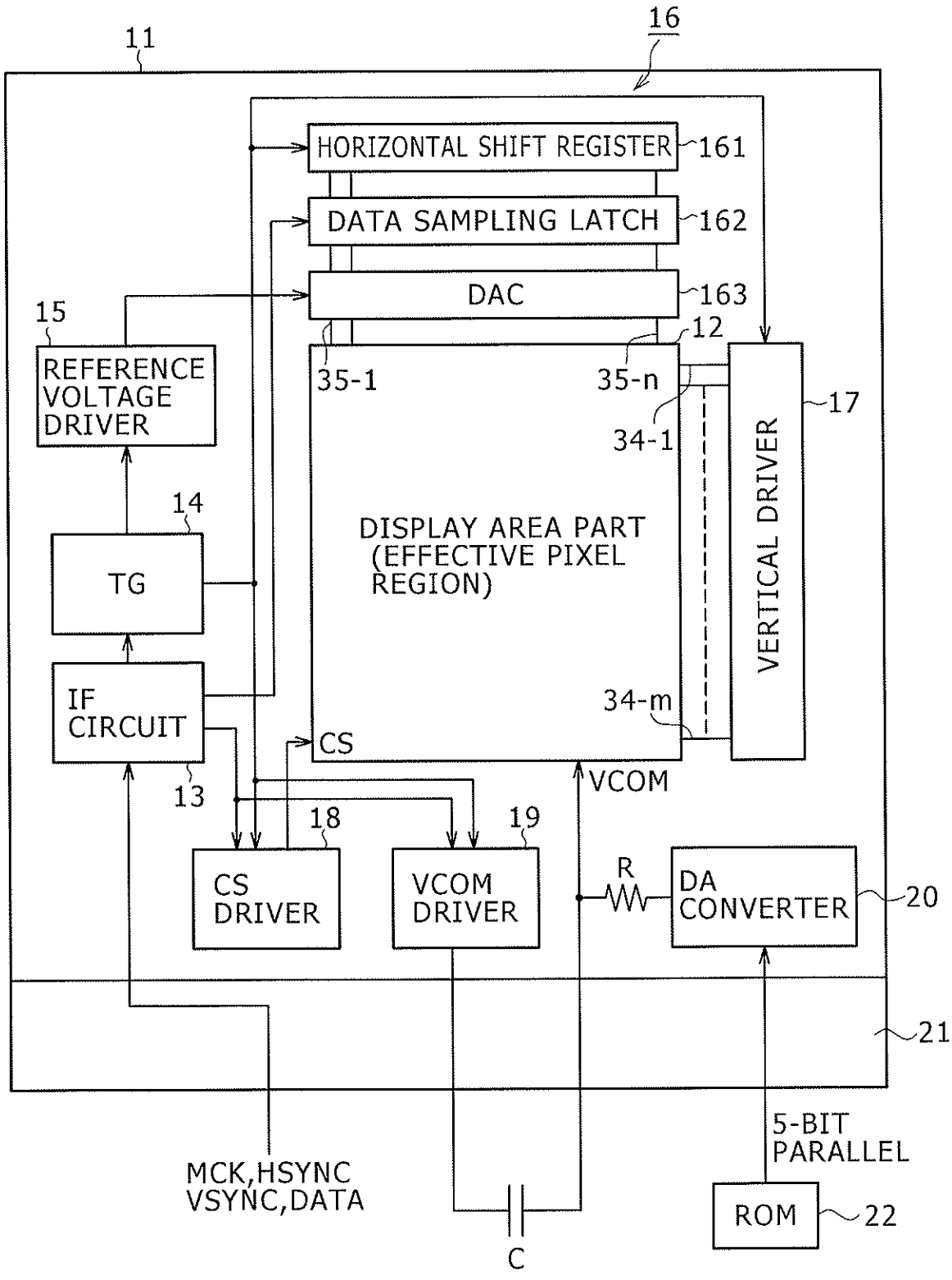

11
16
HORIZONTAL SHIFT REGISTER
161
DATA SAMPLING LATCH
162
DAC
163
15
12
REFERENCE VOLTAGE DRIVER
35-1
35-n
34-1
17
VERTICAL DRIVER
14
DISPLAY AREA PART (EFFECTIVE PIXEL REGION)
TG
IF CIRCUIT
34-m
CS
VCOM
13
18
19
CS DRIVER
VCOM DRIVER
R
DA CONVERTER
20
21
5-BIT PARALLEL
MCK,HSYNC VSYNC,DATA
ROM
22
C

FIG. 2

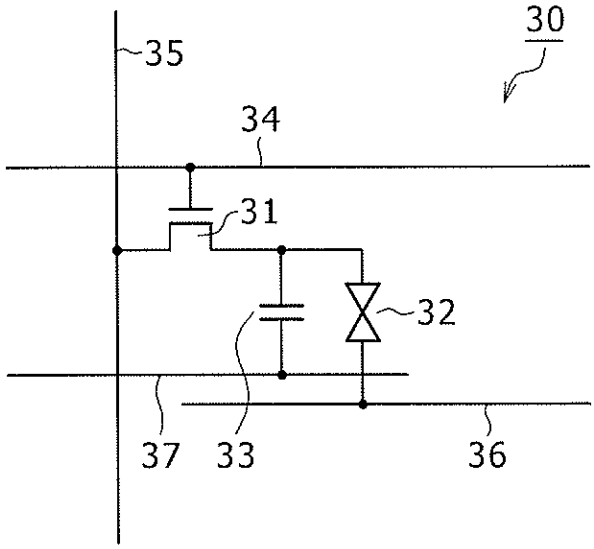

30
35
34
31
32
37
33
36

FIG. 3

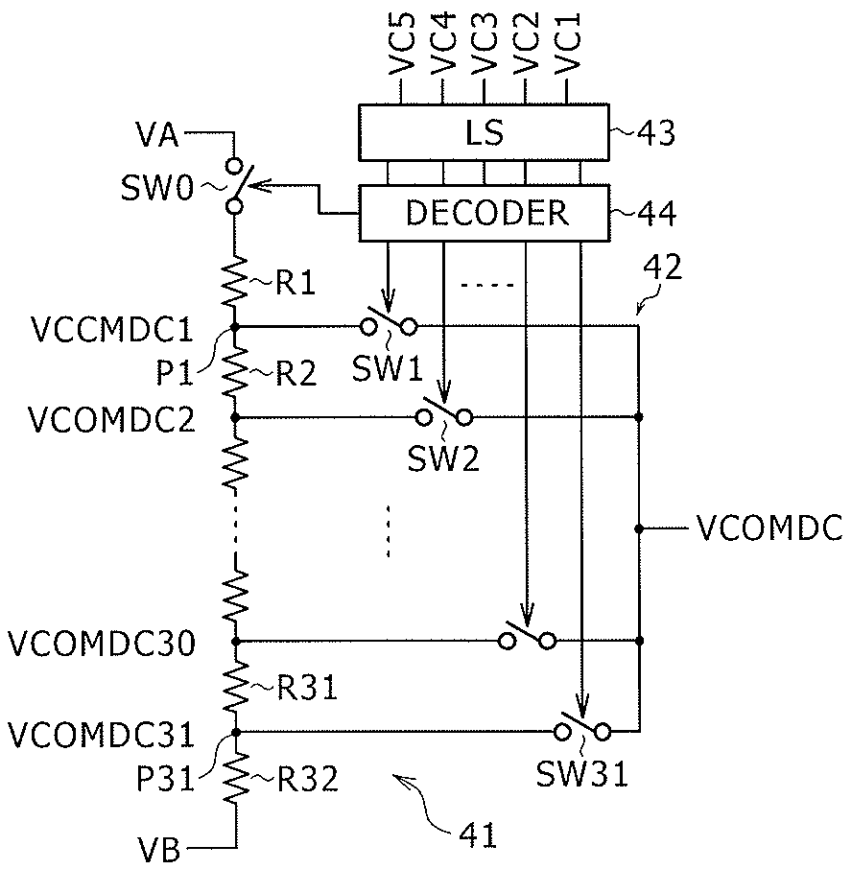

VC5
VC4
VC3
VC2
VC1
LS
43
VA
SW0
DECODER
44
42
R1
VCCMDC1
P1
R2
SW1
VCOMDC2
SW2
VCOMDC
VCOMDC30
R31
VCOMDC31
P31
R32
SW31
VB
41

FIG. 5

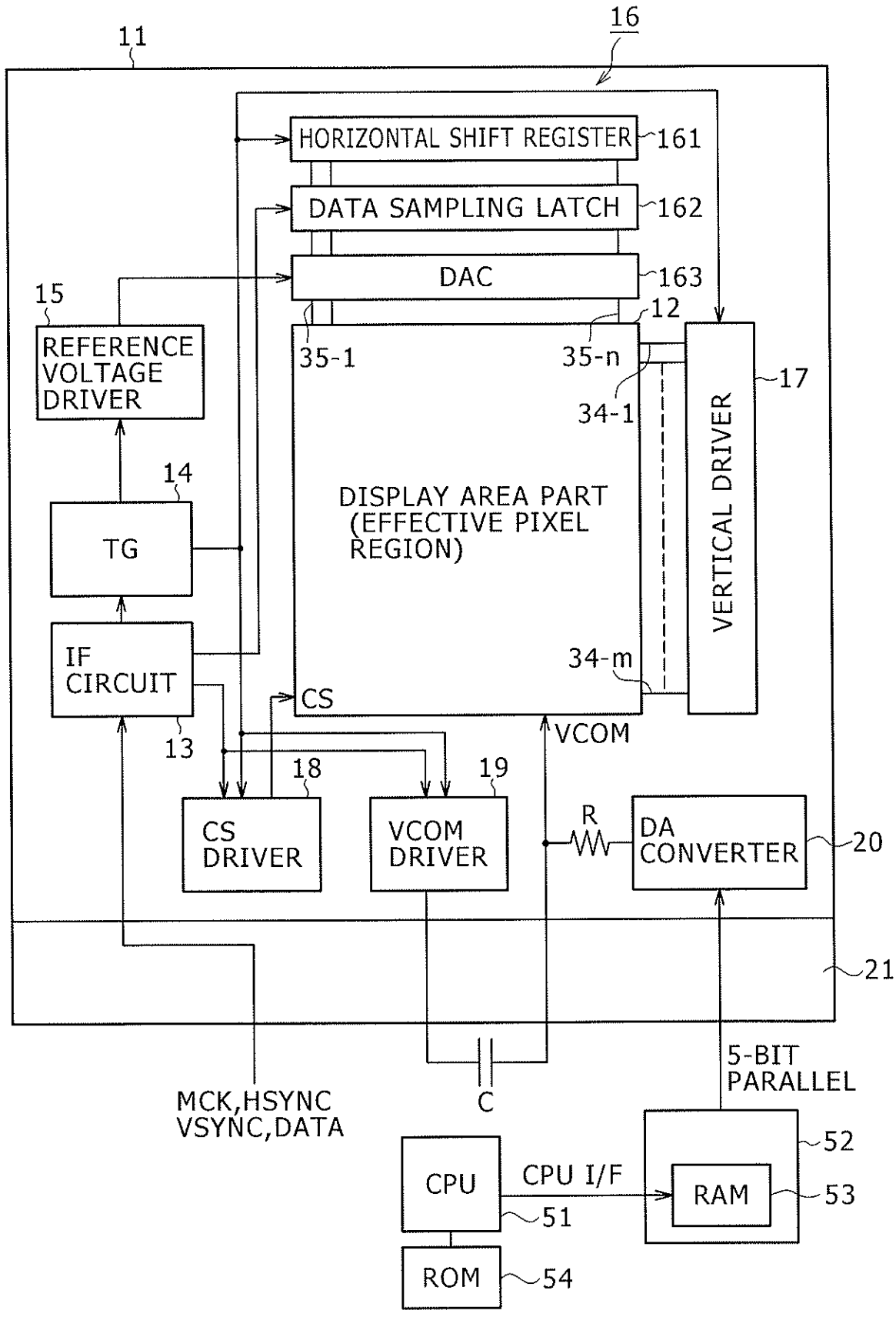

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CS DRIVER
VCOM DRIVER
R
DA CONVERTER
20
21
5-BIT PARALLEL
MCK,HSYNC VSYNC,DATA
C
52
CPU
CPU I/F
RAM
53
51
ROM
54

FIG.6

