

### Abstract

A video system comprises a video display (244) having a wide format display ratio for displaying a video signal. A signal processor (304) has an interpolator (337) and a first in first out line memory (356) having asynchronous write and read ports for selectably expanding and compressing a picture represented by data in the video signal. The picture is cropped to define a subset of the picture for display by controlling writing of the data into the line memory. A microprocessor (340) provides control signals with selectable time durations and selectable phases relative to a synchronizing component of said video signal for selecting boundaries of the subset of the picture for display. The microprocessor can select the time durations and the phases responsive to user commands.

FIGURE 9

This Sheet Has Been Replaced

