

PROTECTION ARRANGEMENT FOR AN AUDIO OUTPUT CHANNELField of the Invention

5 The invention concerns a protection arrangement for an audio output channel, and particularly a protection arrangement for detecting when a sustained DC component capable of damaging a loudspeaker is developed at the output of a audio power amplifier.

Background of the Invention

10 Normally the average or DC component of an audio signal applied to a loudspeaker is negligible. However, a failure within the power amplifier driving the loudspeaker may cause a sustained DC component to be applied to the loudspeaker. A loudspeaker can be damaged when a sustained DC signal is applied to it. Accordingly, an audio output channel may include a so-called DC detector coupled to the output of the power amplifier for generating a fault indicating signal when a sustained DC signal is developed. A variety of actions can be taken for protecting the loudspeaker in response to the fault signal. For example the equipment containing the audio channel may be turned off. U.S. patent application serial number 571,726, entitled "Shutdown System for a Television Receiver", filed for R. E. Morris, Jr. and G. H. Johnson on August 27, 1990, and assigned to the same assignee as the present application, discloses a protection arrangement for a loudspeaker of a television receiver including a DC detector and provisions for turning the receiver off when a DC fault is detected.

Summary of the Invention

15 An aspect of the invention concerns the recognition that it is desirable to detect the occurrence of certain other fault conditions in an audio output channel. More specifically, it has been recognized that the loss of one of the positive and negative power supply voltages for the audio power amplifier may cause a DC component to be coupled to the loudspeaker which may not be



detected by a conventional DC detector and which may damage the power amplifier. Another aspect of the invention concerns a DC detector which itself is capable of detecting either (1) the presence of a sustained DC component at the output of an audio power amplifier, or (2) the loss of one of the power supply voltages for the power amplifier.

These and other aspects of the invention will be described with reference to the accompanying Drawing.

10 Brief Description of the Drawing

In the Drawing:

Figure 1 is a schematic diagram of an audio output channel including a protection arrangement constructed in accordance with the invention; and

Figures 2a and 2b are schematic diagrams of respective voltage reference sources utilized in the protection arrangement shown in Figure 1.

The same or similar elements shown in the various Figures of the Drawing are identified by the same or similar reference designations.

Detailed Description of the Drawing

25 The left (L) and right (R) stereophonic audio output channels, for example, of a television receiver, are shown in Figure 1. Since the two channels are identical, except for the signals they process, only the left channel will be described in detail. A significant portion of the circuitry of each of the channels is incorporated within three integrated circuits (ICs). The ICs are indicated by rectangles and their input and output terminals are indicated by circles.

30 An audio power amplifier 1L amplifies a low level left audio signal (L) received at an input to produce a high level audio output signal suitable for driving a loudspeaker 3L coupled to an output. Amplifier 1L is incorporated within an IC 5L which receives a positive supply voltage ($+V_C$) from a positive voltage supply and a negative supply voltage ($-V_C$) from a negative



voltage supply 9 at respective voltage supply terminals. IC 5L is also connected to ground through a respective terminal. Various external elements, including a feedback path, are not shown since they are not relevant to the description of the invention. The
 5 μ PC1188H audio power amplifier IC available from NEC Corporation is suitable for use as IC 5L.

As earlier noted, loudspeaker 3L may be damaged if a sustained DC signal is applied to it. To prevent such a condition, a DC detector comprising a low pass filter 11L, including a resistor
 10 13L and a capacitor 15L, and a so-called "window comparator" 17L, including two voltage comparators 19L and 21L and associated voltage reference sources 23 and 25, are coupled to the output of amplifier 1L.

The input of low pass filter 11L is coupled to the
 15 output of amplifier 1L. The output of low pass filter 11L, at the junction of resistor 13L and capacitor 15L, is coupled to the inverting (-) input of voltage comparator 19L and to the non-inverting (+) input of voltage comparator 21L. The values of resistor 13L and capacitor 15L of low pass filter 11L are selected
 20 to prevent any AC components of the audio signal from being coupled to the inputs of voltage comparators 17L and 19L and to allow only a sustained DC voltage to be developed at the output of low pass filter 11L.

The non-inverting (+) input of voltage comparator 19L
 25 is coupled to positive reference voltage source 23 which supplies a reference voltage $+V_R$ and the inverting (-) input of voltage comparator 21L is coupled to negative reference voltage source 25 which supplies a reference voltage $-V_R$. Window comparator 17L, comprising voltage comparators 19L and 21L, determines whether
 30 or not the DC voltage developed at the output of low pass filter 11L is within a relatively small range between positive reference voltage $+V_R$ and negative voltage reference $-V_R$, or is outside of the range. Voltage comparators 19L and 21L (as well as voltage comparators 19R and 21R of the "right" audio output channel) are
 35 incorporated within an IC 27. A positive supply voltage for IC 27 is derived from the positive supply voltage $+V_C$ by a voltage divider 29 including a resistors 31 and 33, filter capacitor 35 and positive reference voltage supply 23. A negative supply voltage



for IC 27 is derived from negative supply voltage $-V_C$ by a voltage divider 37 including a resistors 39 and 41, filter capacitor 43 and negative reference voltage supply 25. The output signals of voltage comparators 19L and 21L will have either a high level
5 near the positive supply voltage developed by voltage divider 29 on a low level dependent on the negative supply voltage developed by voltage divider 37 depending on which one of the respective two input signals has the greater magnitude. The LM 324A operational amplifier IC available from National
10 Semiconductor Corporation is suitable for use as IC 27.

The outputs of voltage comparators 19L and 21L are coupled through respective resistors 45L and 47L to the base of a NPN transistor 49 configured as a common emitter amplifier. The collector of transistor 49 is coupled through a "pull-up" resistor 51
15 to a source of positive supply voltage and also to a control unit 53. Control unit 53, which may comprise a microprocessor, controls various functions of television receiver, including its "on/off" state in response to user initiated command signals generated by a keyboard (not shown). In connection with the "on/off" function of
20 the television receiver, control unit 53 generates an "on/off" control signal which is coupled to positive voltage supply 7 and negative voltage supply 9. Control unit 53 also controls voltage supplies 7 and 9 in response to a fault indicating signal generated by the DC detector comprising low pass filter 11L, window
25 comparator 17L and transistor 49.

The average value of an audio signal over any significant time period is zero. Therefore, under normal operating conditions, the magnitude of the DC voltage developed at the output of low pass filter 11L at the junction of resistor 13L and
30 capacitor 15L is substantially zero. Under these conditions, neither one of reference voltages $+V_R$ or $-V_R$ associated with respective ones of voltage comparators 19L and 21L is exceeded and a slightly negative low level is produced at the outputs of each one of comparators 19L and 21L. The level is sufficiently
35 low to ensure that transistor 49, which is normally non-conductive ("cut-off"), remains non-conductive. As a result, control unit 53 continues to keep the television receiver in the "on" state.



If a fault occurs within amplifier 1L, such as when one of the output transistors of amplifier 1L (which, for example, may be arranged in a so-called "push-pull" configuration) fails causing the respective supply voltage to be coupled to the output of amplifier 1L, a sustained DC voltage is developed at the output of low pass filter 11L and is coupled to window comparator 17L. Voltage comparator 19L will generate a high level at its output if the DC voltage is positive and voltage comparator 21L will generate a high level at its output if the DC voltage is negative. The high level is coupled to the base of transistor 49 and causes transistor 49 to be rendered conductive ("turned-on"). Control unit 53 responds to the low level produced at the collector of conductive transistor 49 by generating an "off" control signal. The "off" control signal causes voltage supplies 7 and 9 to be turned off, thereby removing the DC voltage from loudspeaker 3L.

In addition to the detection of the application of a sustained DC voltage to loudspeaker 3L, it is desirable to detect the loss of negative supply voltage $-V_R$ while positive supply voltage $+V_R$ is present. The reason for this will now be explained. The semiconductor devices, resistors and other elements which comprise amplifier 1L are formed on and supported by a substrate 55L. Negative supply voltage $-V_R$ is coupled to substrate 55L so that semiconductor junctions between various elements and the substrate are reverse biased, thereby ensuring that the elements are electrically isolated. The desired electrical isolation is lost if negative supply voltage $-V_R$ is lost. This may cause a positive DC voltage to be developed at the output of amplifier 1L, which may damage loudspeaker 3L. The loss of negative supply voltage $-V_R$ may also damage IC 5L. In view of such possibility, the manufacturer of the μ PC1188H audio power amplifier IC, suitable for use as IC 5L, recommends that both the positive and negative supply voltages be applied at the same time, or that the negative supply voltage be applied before the positive supply voltage. However, such, precautions cannot prevent possible damage should negative voltage supply 9 fail.

While window comparator 17L may detect the presence of a positive DC voltage at the output of amplifier due to the loss of negative supply voltage $-V_R$, the magnitude of the DC



voltage is unpredictable and may be below reference voltage $+V_R$ and may therefore go undetected in this manner.

Advantageously, window comparator 17L operates in another manner to detect the absence of negative supply voltage $-V_R$, as follows:

5 A voltage comparator such as the LM324A may be, and usually is, operated between a positive supply voltage and ground potential. In that case, the lowest level to which the output of the voltage comparator will be driven is slightly above
10 ground potential. If negative supply voltage $-V_C$ is removed from IC 27, ground potential will be coupled to the "negative" supply voltage inputs of voltage comparators 19L and 21L instead of negative supply voltage $-V_C$ by virtue of paths to ground provided by reference voltage source 25 shown in Figure 2b and various
15 other loads of negative voltage supply 9 which are not shown. Accordingly, if negative supply voltage $-V_C$ is lost, the lowest level to which the outputs of voltage comparators 19L and 21L will be able to be driven is slightly above ground potential. That level is above the conduction threshold of transistor 49 and therefore
20 insufficient to keep transistor 49 non-conductive (cutoff). As a result, transistor 49 is rendered conductive (turned on) and a low level fault signal is developed at its collector. Control unit 53 generates an "off" control signal in response to the low level fault signal causing positive voltage supply 7, as well as the rest of the
25 television receiver, is to be turned off.

Thus, by utilizing both positive and negative supply voltages, window comparator 17L is capable of detecting the loss of negative supply voltage $-V_C$ as well as detecting the development of a sustained DC signal at the output of power
30 amplifier 1L. It is noted that although a fault will be indicated when negative supply voltage $-V_C$ is lost, no fault will be indicated if both negative supply voltage $-V_C$ and positive voltage $+V_C$ are absent. As a result, it is possible to turn the receiver on for troubleshooting tests.

35 The utilization of both positive and negative supply voltages rather than only a single polarity supply voltage is also advantageous since it permits positive and negative reference voltages relatively close to ground potential to be utilized. This in



turn permits a DC fault to be detected more quickly. In a protection arrangement, such as is disclosed in the Morris and Johnson patent application referred to above, in which only a single polarity supply voltage is utilized, a reference voltage which is approximately one-half of the magnitude of the supply voltage is utilized to permit both positive and negative DC voltages to be detected. As a result, the voltage across the capacitor of the low pass filter of the DC detector must charge to approximately, one-half of the magnitude of the supply voltage before a DC fault can be detected. The present protection arrangement utilizing both positive and negative supply voltages avoids such an undesirable delay because positive and negative reference voltages $+V_R$ and $-V_R$ can have much lower magnitudes than the magnitude of the single reference voltage of a protection arrangement utilizing a single polarity supply voltage.

Implementations of voltage reference sources 23 and 25 are shown in Figures 2a and 2b, respectively. Voltage reference 23 includes resistors 57a and 59a connected in series between resistor 33 and ground and a NPN transistor 61a having its base connected to the junction of resistors 57a and 59a, its collector connected to the junction of resistors 33 and 57a and its emitter connected to ground. The magnitude of reference voltage $+V_R$ is determined by the ratio of resistors 57a and 59a. The implementation of reference voltage source 23 shown in Figure 2a has a relatively lower output impedance compared with a conventional voltage divider and is therefore capable of supplying a reference voltage to more comparison circuits. This is particularly desirable when there are several audio output channels, for example, left and right audio output channels for both internal and external loudspeakers. The implementation of reference voltage source 23 shown in Figure 2a is also capable of supplying a reference voltage with a lower magnitude, for example 2 volts, compared with a Zener diode. The implementation of voltage reference 25 shown in Figure 2b is very similar to the implementation of voltage reference 23 shown in Figure 2a and therefore corresponding elements are identified with the the same reference numbers, except that the letter "a" has been replaced with the letter "b". The major difference



between the implementation of reference voltage source 25 and reference voltage source 23 is that the connections of the emitter and collector of NPN transistor 61b are reversed with respect to the connections of the emitter and collector of transistor 61a
5 because of the different polarities which are involved.

The present invention has been described by way of example with reference to a particular embodiment.

Modifications to the described embodiment and other embodiments may become apparent by those skilled in the art.

10 These modifications and other embodiments are intended to be within the scope of the invention defined by one or more of the following Claims.

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Claims

1. Apparatus comprising:
a source (7) of a first supply voltage;
5 a source (9) of a second supply voltage having a polarity opposite to that of said first supply voltage;
a source (L) of an audio signal;
an audio power amplifier (1L,1R) normally receiving said first and second supply voltages at respective power supply input
10 terminals, receiving said audio input signal at an input and providing an audio output signal at an output terminal;
means for coupling a loudspeaker (3L,3R) to said output terminal of said audio amplifier (1L,1R); characterized by
a detector (17L,17R) coupled to said output of said
15 amplifier for generating a signal indicative of a fault condition when either or both of (1) a DC signal is produced at said output of said amplifier (1L,1R) or (2) one of said supply voltages is absent.
2. The apparatus recited in Claim 1, characterized in that:
20 said detector (1L,1R) normally receives said first and second supply voltages at respective power supply input terminals and is responsive to the absence of one of said supply voltages at the respective power supply terminal of said detector (17L,17R) for
25 generating said fault signal.
3. The apparatus recited in Claim 2, characterized in that:
a significant portion of said amplifier is incorporated
within an integrated circuit (1L,1R) including a substrate (55L,55R)
on which semiconductor devices are supported, said second supply
30 voltage is negative and is coupled to said substrate (55L,55R), the absence of said second supply voltage tending to cause a DC signal to be produced at said output of said amplifier (1L,1R).

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4. Apparatus comprising:

a source (7) of a first supply voltage;

a source (9) of a second supply voltage having a polarity opposite to that of said first supply voltage;

5 a source (L) of an audio signal;

an audio power amplifier (1L,1R) normally receiving said first and second supply voltages at respective power supply input terminals, receiving said audio input signal at an input and providing an audio output signal at an output terminal;

10 means for coupling a loudspeaker (3L,3R) to said output terminal of said audio amplifier (1L,1R);

a detector (17L,17R) normally receiving said first and second supply voltages at respective power supply input terminals and being coupled to said output of said amplifier (1L,1R) for
15 generating a signal indicative of a fault condition when either or both of (1) a DC signal is produced at said output of said amplifier (1L,1R) or (2) one of said supply voltages is absent.

5. The apparatus recited in Claim 4, characterized in that:

20 said detector (17L,17R) includes a low pass filter (11L,11R) having an input coupled to the output of said amplifier (1L,1R) and an output; a first comparator (19L,19R) having an input which is coupled to the output of said low pass filter (11L,11R) and which generates a first output signal at an output when the

25 output signal of said low pass filter exceeds a first voltage threshold; a second comparator (21L,21R) having an input which is coupled to the output of said low pass filter (11L,11R) and which generates a second output signal at an output when the output signal of said low pass filter exceeds a second voltage threshold having a polarity

30 opposite to that of said first voltage threshold; means (49,53) responsive to either of said first and second output signals for producing said fault signal; at least one of said first and second output signals also being generated in response to the absence of one of said first and second supply voltages.

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6. The apparatus recited in Claim 4, characterized in that:
said means (17L,17R) for producing said fault signal is a
threshold device having an output at which said fault signal is
generated when a signal coupled to an input exceeds a given
5 threshold, each of a said first and second signals having a level which
exceeds said given threshold; and

including at least one comparator which is unable to
generate an output signal which does not exceed said given threshold
when one of said first and second supply voltage is absent.

10

7. The apparatus recited in Claim 4, characterized in that:
a significant portion of said amplifier (1L,1R) is
incorporated within an integrated circuit (5L,5R) including a
15 substrate (55L,55R) on which semiconductor devices are supported,
said second supply voltage is coupled to said substrate (55L,55R), the
absence of said second supply voltage tending to cause a DC signal to
be produced at said output of said amplifier (1L,1R).

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8. The apparatus recited in Claim 7, characterized in that:
said second supply voltage is negative.



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9. Apparatus comprising:

a source (7) of a positive supply voltage;

a source (9) of a negative supply voltage having a polarity opposite to that of said first supply voltage;

5 a source (L) of an audio signal;

an audio power amplifier (1L,1R) normally receiving said positive and negative supply voltages at respective power supply input terminals, receiving said audio input signal at an input and providing an audio output signal at an output terminal; a significant
10 portion of said amplifier being incorporated within an integrated circuit (5L,5R) including a substrate (55L,55R) on which semiconductor devices are supported, said negative supply voltage being coupled to said substrate (55L,55R), the absence of said second supply voltage tending to cause a DC signal to be produced at said
15 output of said amplifier;

means for coupling a loudspeaker (3L,3R) to said output terminal of said audio amplifier (1L,1R); characterized by

a detector (17L,17R) receiving said positive and negative supply voltages at respective power supply input terminals and
20 being coupled to said output of said amplifier (1L,1R) for generating a signal indicative of a fault condition when either or both of (1) a DC signal is produced at said output of said amplifier (1L,1R) or (2) said negative supply voltage is absent.



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10. Apparatus comprising:

a source (7) of a first supply voltage;

a source (9) of a second supply voltage having a polarity opposite to that of said first supply voltage;

5 a source (L) of an audio signal;

an audio power amplifier (1L,1R) normally receiving said first and second supply voltages at respective power supply input terminals, receiving said audio input signal at an input and providing an audio output signal at an output terminal;

10 means for coupling a loudspeaker (3L,3R) to said output terminal of said audio amplifier (1L,1R); characterized by

a detector (17L,17R) coupled to said output of said amplifier (1L,1R) for generating a signal indicative of a fault condition when either or both of (1) a DC signal is produced at said output of said amplifier (1L,1R) or (2) one of said supply voltages is absent;

said detector (17L,17R) including a low pass filter (11L,11R) having an input coupled to the output of said amplifier (1L,1R) and an output; a first comparator (19L,19R) normally receiving said first and second supply voltages at respective supply voltage terminals and having an input coupled to the output of said low pass filter (11L,11R) and which generates a first output signal above a given level at an output when the output signal of said low pass filter (11L,11R) exceeds a first threshold; a second comparator (21L,21R) normally receiving said first and second supply voltages at respective supply voltage terminals and having an input coupled to the output of said low pass filter (11L,11R) and which generates a second output signal having a level which exceeds said given level at an output when the output signal of said low pass filter (11L,11R) exceeds a second threshold having a polarity opposite to that of said first voltage threshold; and a threshold device having an output at which said fault signal is generated when a signal coupled to an input has a level which exceeds said given level; at least one of said comparators (19L,19R,21L,21R) being unable to generate an output signal which does not exceed said given level when one of said first and second supply voltage is absent.

